

CUSTOMER SERVICE BULLETIN

Radio Shack® CUSTOMER SERVICE BULLETIN

400 Atrium—One Tandy Center, Fort Worth, Texas 76102

Bulletin No.	4910-19
Description	TRSDOS
Model	Model II
Stock No.	26-4910 Version 2.0a
Date	Dec. 15, 1981
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Problem

We are now using several suppliers of the FDC controller for the TRS 80 Model II. Some of these chips have different timing constraints from the original chip. This has caused some machines to demonstrate problems when using the new chips, specifically the Synertek chip. TRSDOS 2.0a is currently sensitive to these timing differences.

Solution

Apply the following patches if your machine is demonstrating I/O problems with disk (lockups requiring system reset). We hesitate to recommend applying these patches to a system that is working properly, however, there should be no effect at all in applying these patches to any system with any of the chips in current use.

1. Make a backup copy of your TRSDOS diskette.
2. At TRSDOS READY apply the following patches.

```
PATCH SYSRES/SYS A=0E54 F=3ED8D3E4CD6C0EDBE7 C=F33ED8D3E43ED0D3E4
PATCH SYSRES/SYS A=0E5D F=DBE43ED0D3E4CD6C0E C=FB000000CD6C0EDBE7
PATCH FORMAT A=2D19 F=3ED8D3E4CD2F2DDBE7 C=F33ED8D3E43ED0D3E4
PATCH FORMAT A=2D22 F=DBE43ED0D3E4CD2F2D C=FB000000CD2F2DDBE7
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