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TRICEP



# TRICEP

TRICEP is a supermicro designed and optimized for four to eight users. It utilizes a blend of industry standards to produce a powerful architecture at a remarkably low price.

At the heart of the system is Motorola's 68000, the most popular of the new generation of 32-bit microprocessors. It offers increased number crunching performance and the ability to directly address enormous amounts of memory.

Add to that the power of a UNIX System V port that has been optimized for the 68000 chip. TRICEP's UNIX is sub-licensed from Unisoft Systems and is fully compatible with Bell Labs' System V.

The entire TRICEP system is built around an S-100 bus motherboard that ensures compatibility with other manufacturers meeting the IEEE 696 standards.

The name TRICEP refers to the three point DMA architecture that gives the system its power. The Direct Memory Access capability of the hard, floppy, and I/O controllers frees the main 68000 CPU to do central processing while the controllers take care of the peripheral requirements of the individual users.

The TRICEP slave architecture offers the option of 80188 slave processor boards that allow individual users compatibility with the MS-DOS operating system and its considerable library of business applications software.

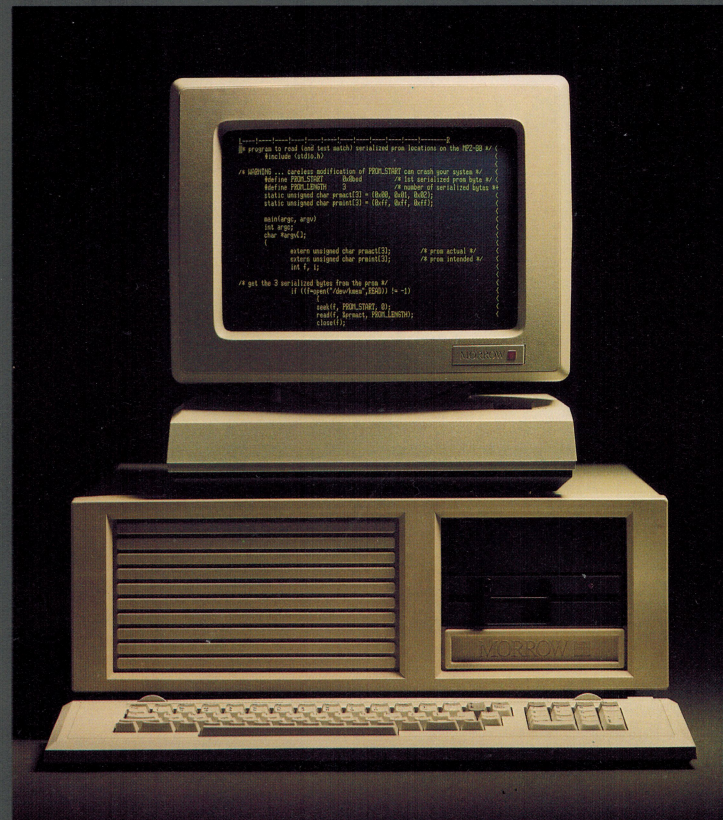


## SOFTWARE DEVELOPERS:

For the software development group, TRICEP is a powerful low-cost Supermicro they can use to enter the UNIX System V applications development market.

The growing popularity of UNIX is producing the same sort of software development explosion that fueled the take-off of 8- and 16-bit micros. Every software group that wants to play a part, will need a powerful fully featured system for their development work.

TRICEP is bundled with all of the critical System V utilities that are often stripped out of low cost UNIX systems. Furthermore, the Unisoft port used in TRICEP adds enhancements like record-locking and IEEE floating-point capability, as well as a Modem based system update and verification utility. An optimizing C compiler comes with the system and other optional languages include BASIC, COBOL, FORTRAN 77, Ada and Pascal.





## SYSTEMS INTEGRATION:

To the system integrator, OEM or VAR, TRICEP provides a great framework on which to build value.

The adoption of UNIX as the standard operating system for 32-bit micros will speed the development of applications packages. Meanwhile much important software development has taken place in environments other than UNIX.

The TRICEP slave architecture will provide sub-environments, the first of which will allow access to the MS-DOS applications base.

The systems integrator with software equity in larger systems will appreciate the ability to port specialized programs from the mini and mainframe segments of the marketplace.

The OEM that specializes in adding hardware value will appreciate the flexibility of TRICEP's IEEE 696/S-100 standard bus.

## OFFICE AUTOMATION:

For the small to medium size business, TRICEP is the key to office automation with the multi-user/multi-tasking power of a mini at a fraction of the cost.

Multi-user applications allow a number of users to share and work with the same database. Whether it's the law firm where paralegals and lawyers needs to work with the same boilerplate files, or the accounting and personnel departments that need to have shared access to personnel files — a multi-user system means shared access to files, programs and peripherals.

TRICEP gives a business access to the rapidly expanding library of multi-user UNIX applications. In addition it allows the utilization of existing MS-DOS business applications software.

The most important point for the business person is that TRICEP offers all these features at a very affordable price.





**T**he TRICEP architecture is specifically designed to run the most powerful operating system in the world . . . UNIX System V.

The heart of the system is a CPU board featuring the 16/32-bit MC68000 microprocessor running at 8 or 10 MHz, and a MC68451 Memory Management Unit.

The DMA Hard Disk Controller uses an 8X300 microprocessor at 7 MHz and controls up to four 5¼" hard disk drives (ST506 compatible). TRICEP comes with a standard 16 Megabyte drive (average access time of 85 milliseconds) or the optional 32 Megabyte drive (35-45 milliseconds) for a maximum hard disk storage of 128 MB.

The DMA Floppy Disk Controller uses a Z80 microprocessor at 4 MHz and controls up to four 5¼" and/or four 8" floppy drives. On board firmware supports 8" and 5¼" soft or hard sectored drives

The DMA Serial Input/Output Controller uses an 8085 microprocessor at 5 MHz and offers four RS232C ports that communicate with serial peripherals at speeds up to 19.2K BAUD. TRICEP can support a second DMA I/O board to expand the system to eight users.

The motherboard offers fourteen S-100 slots as outlined by the IEEE 696 standard. The motherboard has an onboard programmable interrupt controller (8259), a real time clock (NEC 1990) and a Centronics parallel port.

256K dynamic RAM boards provide TRICEP's high speed memory (DRAMs with 150 nsec access time for 8 MHz systems and 120 nsec for 10 MHz systems). TRICEP is shipped standard with two boards providing 512K RAM, but the system is expandable to 2 Megabytes.

TRICEP's slave processor architecture features an MS-DOS board with an 80188 microprocessor at 8 MHz. The board comes with 128K of dual port RAM (expandable to 512K) that is directly accessible by any of the intelligent controllers on the system. The slave processor boards can free the 68000 CPU of up to 80% of its application tasks. TRICEP will support up to 7 slave processor boards.

TRICEP's cabinet provides 2 switched convenience outlets, an AC power breaker, front panel reset-Key switch, rear panel connector access, forced air cooling, steel construction and a rackmount option. Dimensions: 19" wide x 21" deep x 7.5" high. Power Supply: 250 watt average output, FCC class A operation certified, 110/220 switch option, 3.0 AMPS max. at 95-128 VAC, 1.5 AMPS max. at 190-265 VAC.

