



Technical Newsletter

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IBM 4341 Processor Channel Characteristics

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This Technical Newsletter provides replacement pages for the subject publication. These replacement pages remain in effect for subsequent versions unless specifically altered. Pages to be inserted and/or removed are:

Front cover, Preface

If you are inserting pages from different Newsletters and *identical* page numbers are involved, always use the page with the latest date (shown in the change-page notice at the top of the page). The page with the latest date contains the most complete information.

A change to the text or to an illustration is indicated by a line to the left of the change.

Summary of Amendments

Provides a new cover page and preface page to specify that the text, charts, and appendixes in this manual apply to both the 4341 Processor Model Group 1 and Model Group 10.

Note: Please file this cover letter at the back of the manual to provide a record of changes.



GA24-3671-3
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Systems

**IBM 4341 Processor
Model Groups 1 and 10
Channel Characteristics**

IBM

Preface

This manual describes how to check the effects of loads imposed on the channels of the IBM 4341 Processor Model Groups 1 and 10. (All calculations, charts, and appendixes apply to both Model Group 1 and Model Group 10.) With this information, programmers and systems analysts can verify that a proposed configuration of input/output (I/O) devices can work satisfactorily with the 4341 Processor.

The sections present 4341 channel organization and implementation, a description of subchannels and unit control words, the procedure for channel interference determination, concurrent I/O capabilities, block-multiplexer and byte-multiplexer channel loading, specific device considerations, and channel evaluation tables.

Before using this manual, you should have a thorough understanding of input/output operations for the 4341 as described in:

- *IBM 4341 Processor Model Group 1 Functional Characteristics and Processor Complex Configurator*, Order No. GA24-3672
- *IBM 4341 Processor Model Groups 10 and 11 Functional Characteristics and Processor Complex Configurators*, GA24-3797
- *IBM System/370 Principles of Operation*, GA22-7000
- *IBM 4300 Processors Principles of Operation for ECPS:VSE Mode*, GA22-7070

When testing for data overrun on the byte-multiplexer channel, two special worksheets are required:

- *IBM 4341 Byte-Multiplexer Channel Preliminary Worksheet*, GX24-3746
- *IBM 4341 Processor Channel Load Sum Worksheet*, GX24-3670

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This is a reprint of GA24-3671-2 incorporating changes released in Technical Newsletter GN24-0888, dated September 10, 1980.

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