

TURBO CUBE

REFERENCE ONLY

DECEMBER 2, 1991

SHEET INDEX

- SHEET 2 CPU : TMC : EPROM : PLL : CONFIG : ADB
- SHEET 3 PC : RTC : LED
- SHEET 4 DRAM : BUS MULTIPLEXER : IMPED RESISTORS
- SHEET 5 MONOCHROME DISPLAY ELECTRONICS
- SHEET 6 NEXT BUS INTERFACE
- SHEET 7 KMS INTERFACE : LASER PRINTER INTERFACE
- SHEET 8 ETHERNET
- SHEET 9 DSP
- SHEET 10 SCC
- SHEET 11 SCSI
- SHEET 12 FLOPPY
- SHEET 13 BYPASS CAPACITORS
- SHEET 14 THIS SHEET INTENTIONALLY LEFT BLANK
- SHEET 15 MECHANICAL : TEST CIRCUIT
- SHEET 16 DATA BUS IMPEDANCE CONTROL RESISTORS
- SHEET 17 030 BUS IMPEDANCE CONTROL RESISTORS

3 ALL INDUCTANCE IS IN MICROHENRIES
2 ALL CAPACITANCE IS IN MICROFARADS
1 ALL RESISTANCE IS IN OHMS, 1/8W +/-1%
NOTES, UNLESS OTHERWISE SPECIFIED

Originator	KPG	unreadable date			
Approved	APP				
				Document # PN46434AA	
			Title SCHEMATIC, PCB, TURBOCUBE	Sheet 1 of 17	