Processor Technology 3P+S as Altair 88-2SIO
(and RS-232, 9600 baud, DCE, 8N1, 3-wire interface)

Jumper area A (I/O address) - Put serial port at I/O address 10h/11h
   A7, 6, 5, 3, 2 connect to G (middle column, ground)
   A4 connect to V (right column, 5v)

Jumper area B (port order) - Put serial port first
   Connect L to R

Jumper area C (word size, parity, etc.) - Settings hardwired instead of by register
   Connect R to C

Jumper area D (current loop enable/disable) - Disable current loop
   Connect C to R

Jumper area E (baud rate) - Select 9600 baud, 0001 0111 1111
   In left to right order, G is 1st row down, V is 2nd row down
   GGGV GVVV VVVV

Jumper area G (status register/input) - Map receive and transmit ready to match 2SIO
   Connect RIN to 1
   Connect RDA to C0
   Connect TBE to C1

Jumper area H (word size, parity, etc.) - Set 8N1 for word size, parity, stop bits
   Connect SBS to 1st row down (ground)
   All others open

Jumper area J (transmit connections) - Connect transmit data output
   Connect 4 to 4th row

Output connectors (card edge on top of board)
   J2-K to DB-25 pin 2 (terminal output to computer input)
   J1-R to DB-25 pin 3 (terminal input to computer output)
   J1-11 to DB-25 pin 7 (signal ground)