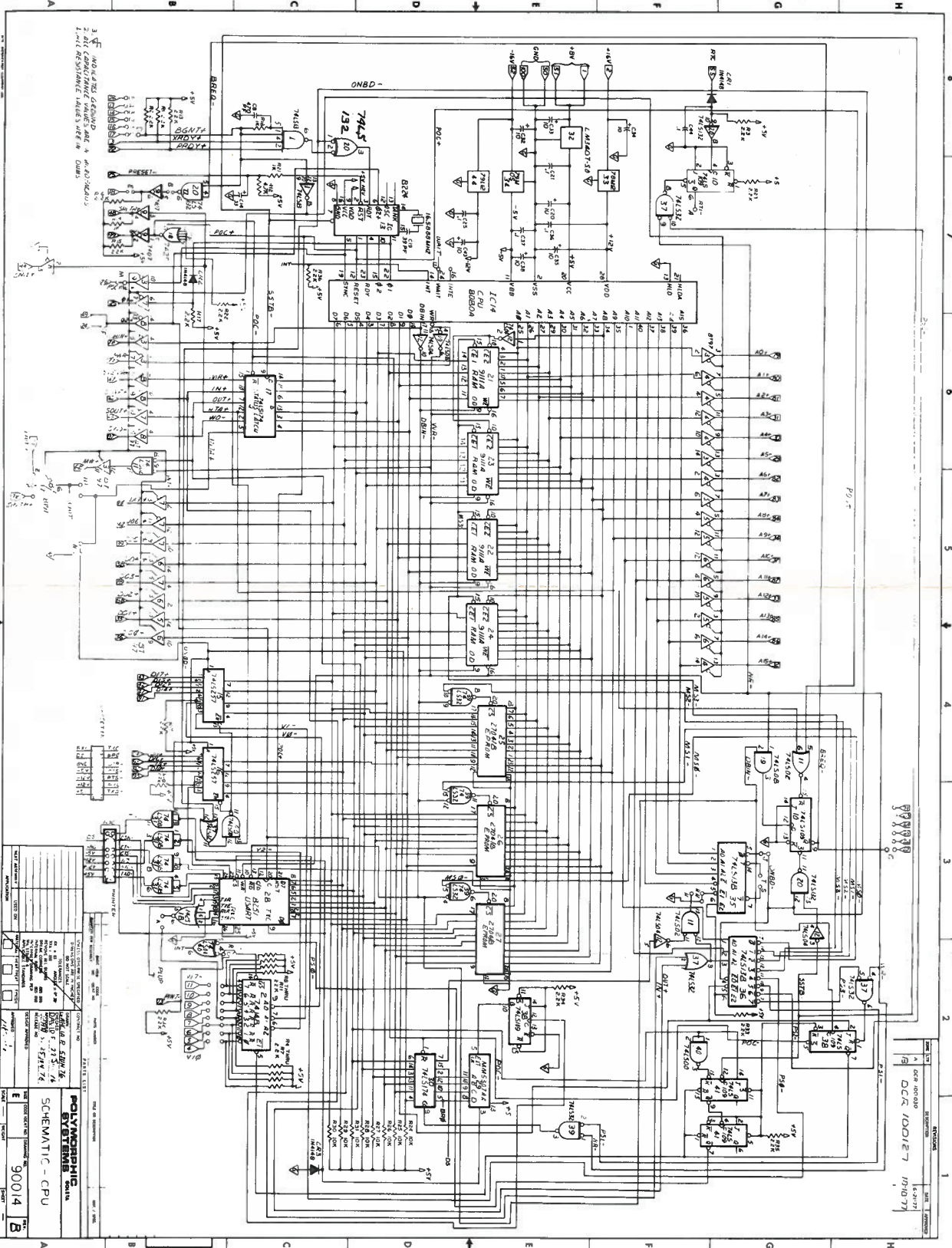


3. $\frac{1}{2}$ INDICATES GROUND
 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS
 1. ALL RESISTANCE VALUES ARE IN OHMS

POLYCORPORATION
 SYSTEMS
 SCHEMATIC - CPU
 30014

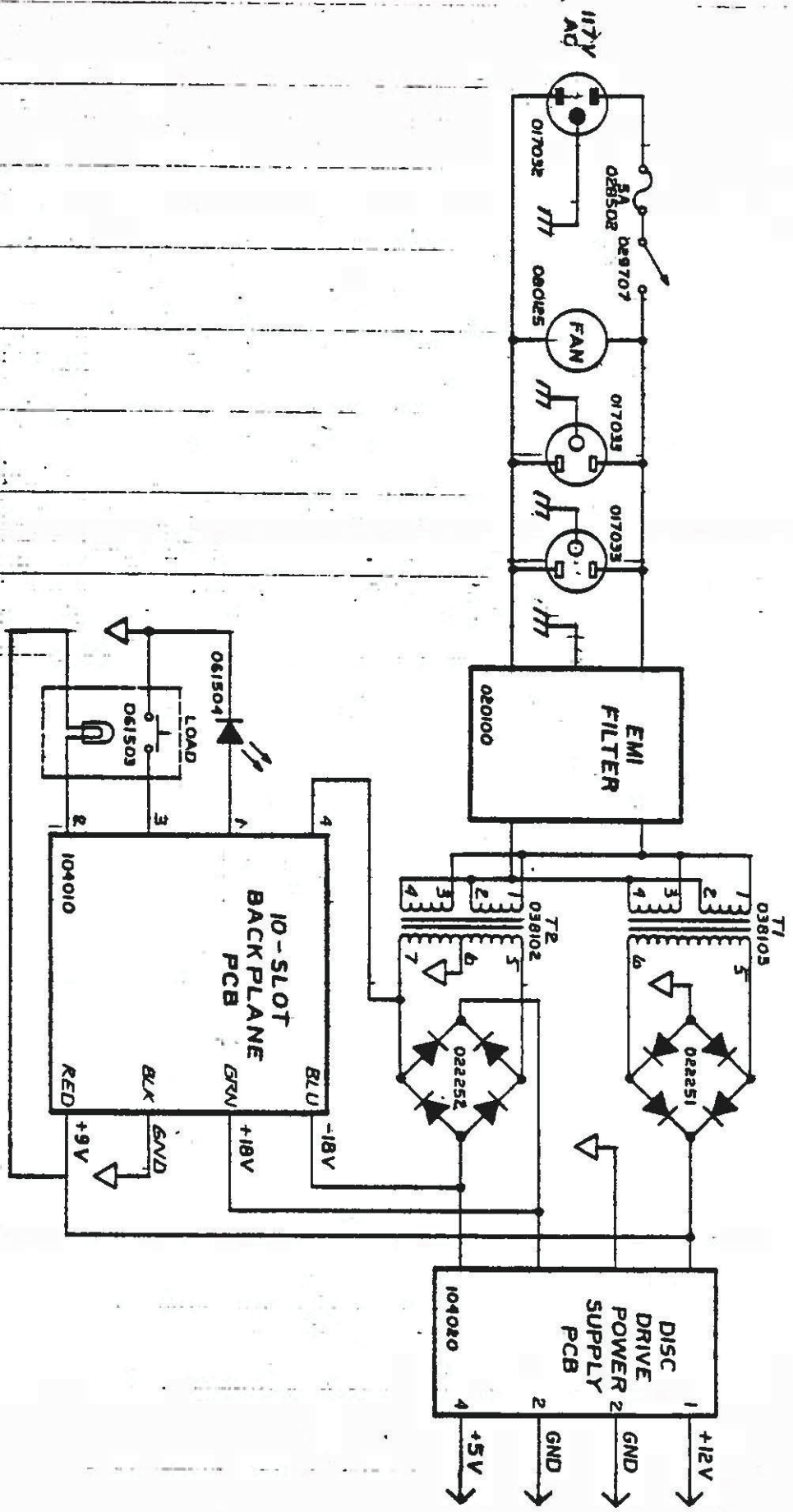
100-100-0000
 DCR 100127
 1/10/77



3. F. INDICATED GROUND
 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS
 1. UNLESS OTHERWISE INDICATED IN CIRCUIT

DATE	10/17/77
DESIGNED BY	W. J. BROWN
CHECKED BY	W. J. BROWN
APPROVED BY	W. J. BROWN
PROJECT	POLYMERMIC SYSTEMS
DESCRIPTION	SCHEMATIC - CPU
REV.	1
QTY.	1
1	1
2	1
3	1
4	1
5	1
6	1
7	1
8	1
9	1
10	1
11	1
12	1
13	1
14	1
15	1
16	1
17	1
18	1
19	1
20	1
21	1
22	1
23	1
24	1
25	1
26	1
27	1
28	1
29	1
30	1
31	1
32	1
33	1
34	1
35	1
36	1
37	1
38	1
39	1
40	1
41	1
42	1
43	1
44	1
45	1
46	1
47	1
48	1
49	1
50	1
51	1
52	1
53	1
54	1
55	1
56	1
57	1
58	1
59	1
60	1
61	1
62	1
63	1
64	1
65	1
66	1
67	1
68	1
69	1
70	1
71	1
72	1
73	1
74	1
75	1
76	1
77	1
78	1
79	1
80	1
81	1
82	1
83	1
84	1
85	1
86	1
87	1
88	1
89	1
90	1
91	1
92	1
93	1
94	1
95	1
96	1
97	1
98	1
99	1
100	1

DATE: 10/17/77
 DRAWN: W. J. BROWN
 CHECKED: W. J. BROWN
 APPROVED: W. J. BROWN
 PROJECT: POLYMERMIC SYSTEMS
 DESCRIPTION: SCHEMATIC - CPU
 REV: 1
 QTY: 1
 1 1
 2 1
 3 1
 4 1
 5 1
 6 1
 7 1
 8 1
 9 1
 10 1
 11 1
 12 1
 13 1
 14 1
 15 1
 16 1
 17 1
 18 1
 19 1
 20 1
 21 1
 22 1
 23 1
 24 1
 25 1
 26 1
 27 1
 28 1
 29 1
 30 1
 31 1
 32 1
 33 1
 34 1
 35 1
 36 1
 37 1
 38 1
 39 1
 40 1
 41 1
 42 1
 43 1
 44 1
 45 1
 46 1
 47 1
 48 1
 49 1
 50 1
 51 1
 52 1
 53 1
 54 1
 55 1
 56 1
 57 1
 58 1
 59 1
 60 1
 61 1
 62 1
 63 1
 64 1
 65 1
 66 1
 67 1
 68 1
 69 1
 70 1
 71 1
 72 1
 73 1
 74 1
 75 1
 76 1
 77 1
 78 1
 79 1
 80 1
 81 1
 82 1
 83 1
 84 1
 85 1
 86 1
 87 1
 88 1
 89 1
 90 1
 91 1
 92 1
 93 1
 94 1
 95 1
 96 1
 97 1
 98 1
 99 1
 100 1



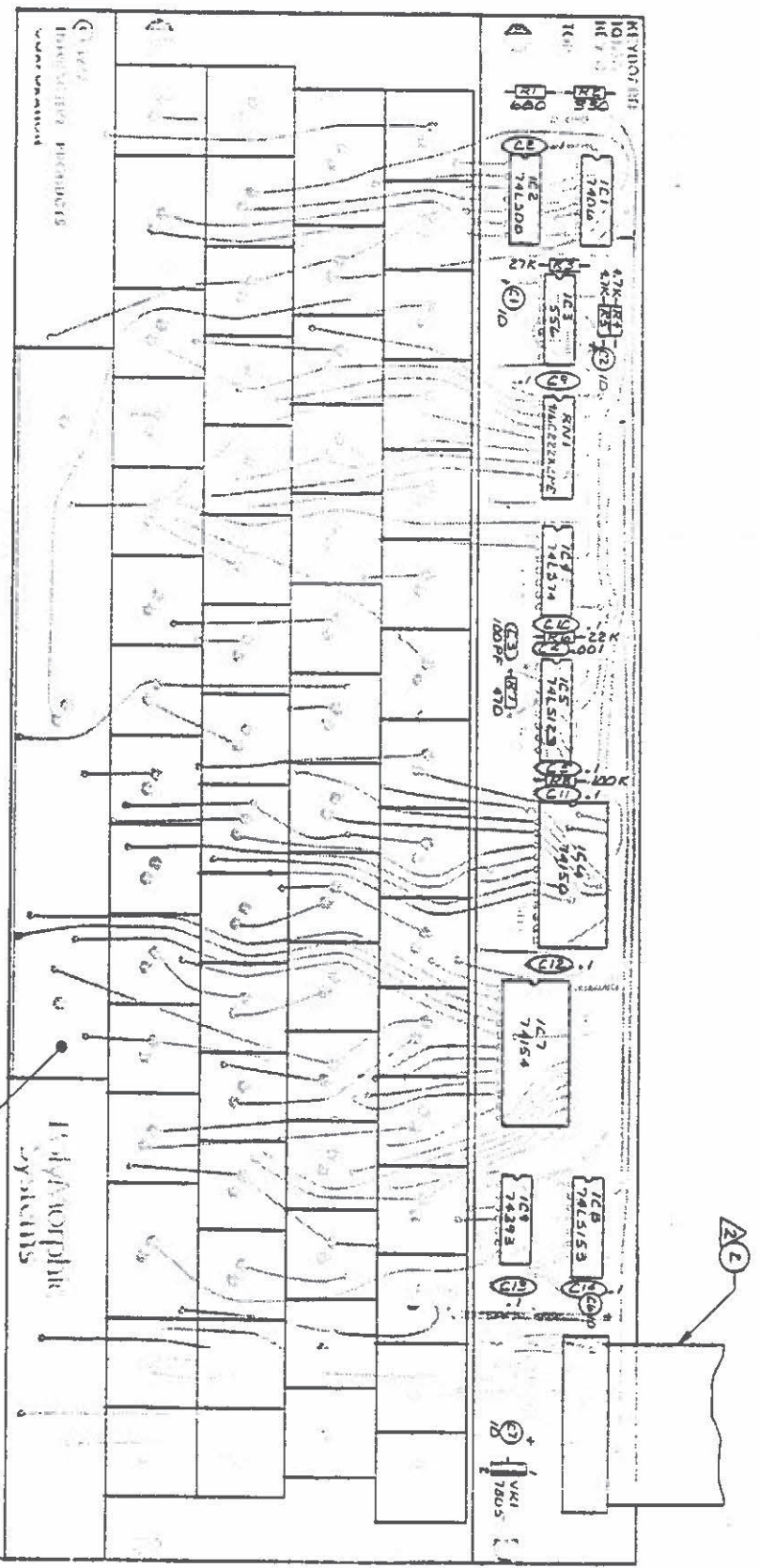
© 1977 INTERACTIVE PRODUCTS CORPORATION

UNLESS OTHERWISE SPECIFIED		DATE
IN REPEAT DRAWING PER AND YIELD		6-22-77
DRAWING ARE IN INCHES AND DECIMALS		
SHEET NUMBER		
BREAK SHEET BOOK		
XX ±	Y	
MATERIAL		
FINISH		
OWN	ARAKI, L.	
CHK		
APP	<i>P.A.D.</i>	7-6-77
DATE		
USED ON		
NEXT ASST		

POLYMORPHIC SYSTEMS GOLETA	
SCHEMATIC - SYSTEM 8813 INTERCONNECT	
SIZE	C
DRAWING NUMBER	104402
REV	

NOTES: UNLESS OTHERWISE SPECIFIED

REV	DESCRIPTION	DATE	BY
A	REDESIGNED OLS APPROX 1	6/27/77	JTT
B	REVISED DRAWING	5/13/77	JTT
C	OCR MODIFIED	part 1/77	JTT



DO NOT RE-INSTALL AFTER 11 MIN SOLDER OPERATION. SOCKET NOT REQUIRED FOR KVI

NOTES: UNLESS OTHERWISE SPECIFIED

SEE PART'S LIST FOR PARTS LIST

INTERACTIVE PRODUCTS CORPORATION

POLYMORPHIC SYSTEMS

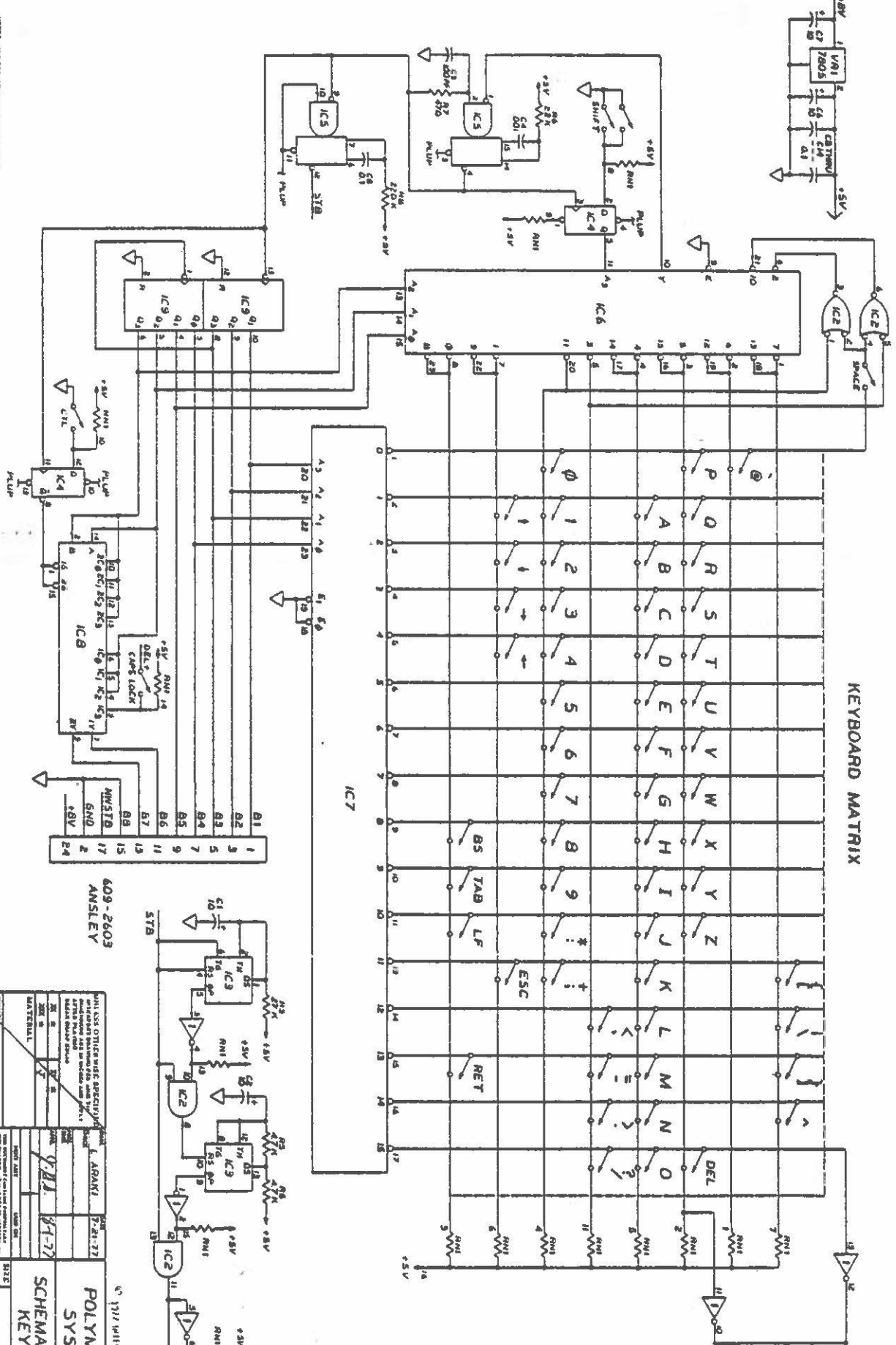
ASSY - KEYBOARD II

REV	DESCRIPTION	DATE	BY
A	REDESIGNED OLS APPROX 1	6/27/77	JTT
B	REVISED DRAWING	5/13/77	JTT
C	OCR MODIFIED	part 1/77	JTT

DATE	BY	CHKD	APP'D
12/1/77	J.P.B.	J.P.B.	J.P.B.

TITLE	DRAWING NUMBER	REV
ASSY - KEYBOARD II	0271011	B

SCALE	SHEET	OF
1/1	1	1



IC	DESCRIPTION
1	7406
2	74LS08
3	556
4	74LS74
5	74LS29
6	74LS0
7	74LS4
8	74LS153
9	74LS93

609-2603 ANSLEY

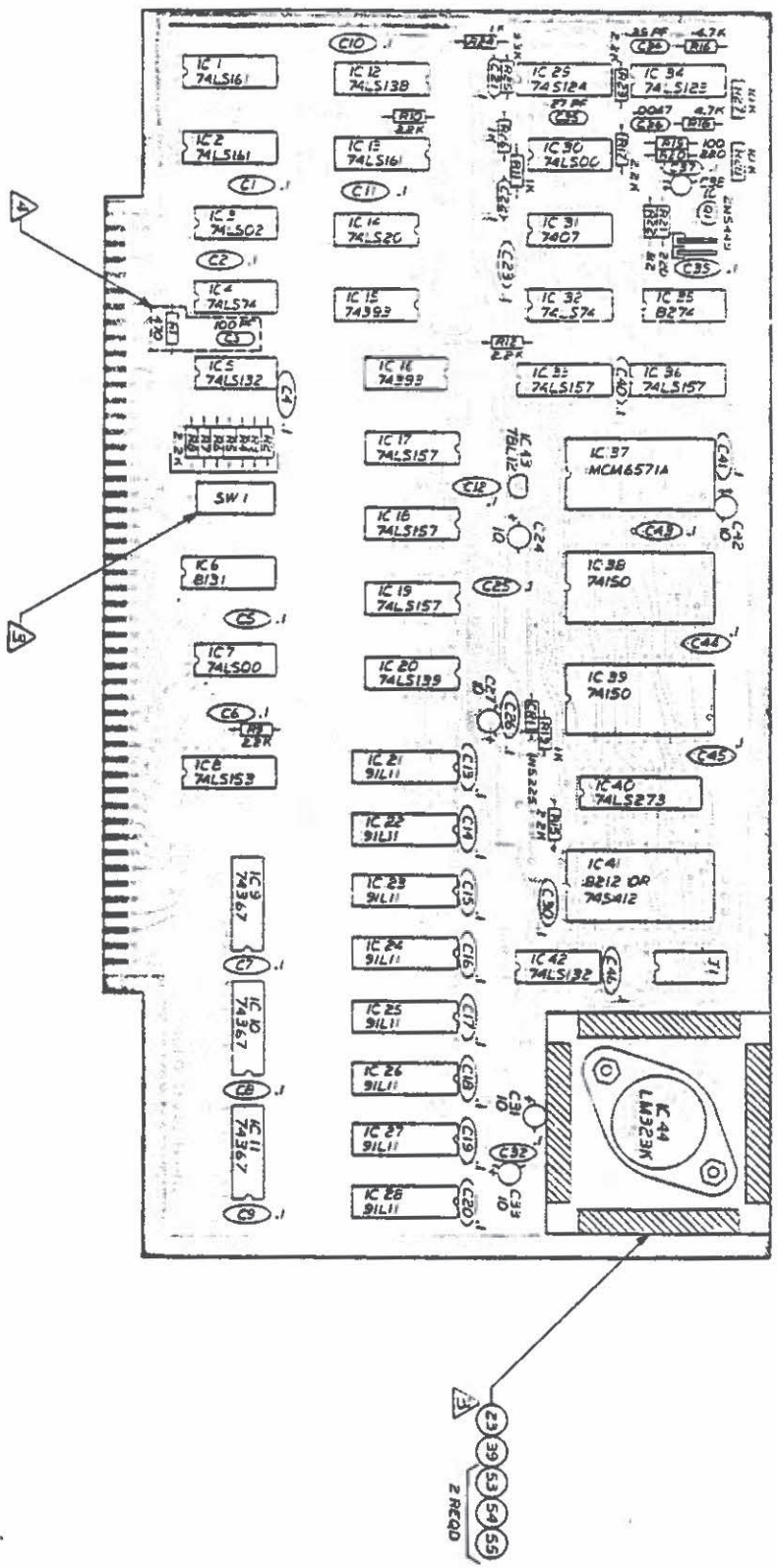
© 1977 INTELLECTUAL PROPERTY CORPORATION

POLYMORPHIC SYSTEMS

SCHEMATIC - KEYBOARD II

DATE: 102 202

REV: 1



- 4 OMIT HI AND C3 FOR USE WITH DISK SYSTEM
- 3 TO BE INSTALLED AFTER FLOW SOLDER OPERATION.
- 2. MODIFY PER DRAWING 105255.
- 1. TO BE USED ON BOARDS UP TO REV 1.2

NOTE: UNLESS OTHERWISE SPECIFIED

POLYMORPHIC SYSTEMS	
ASSY - VIDEO BOARD, 1.2	
DATE	10-13-75
BY	10-13-75
CHKD	10-13-75
APP'D	10-13-75
REV	8
SIZE	D
SCALE	1:1
DRW NO	001026
REV	8
DATE	10-13-75
BY	10-13-75
CHKD	10-13-75
APP'D	10-13-75

8
A
001040

7

6

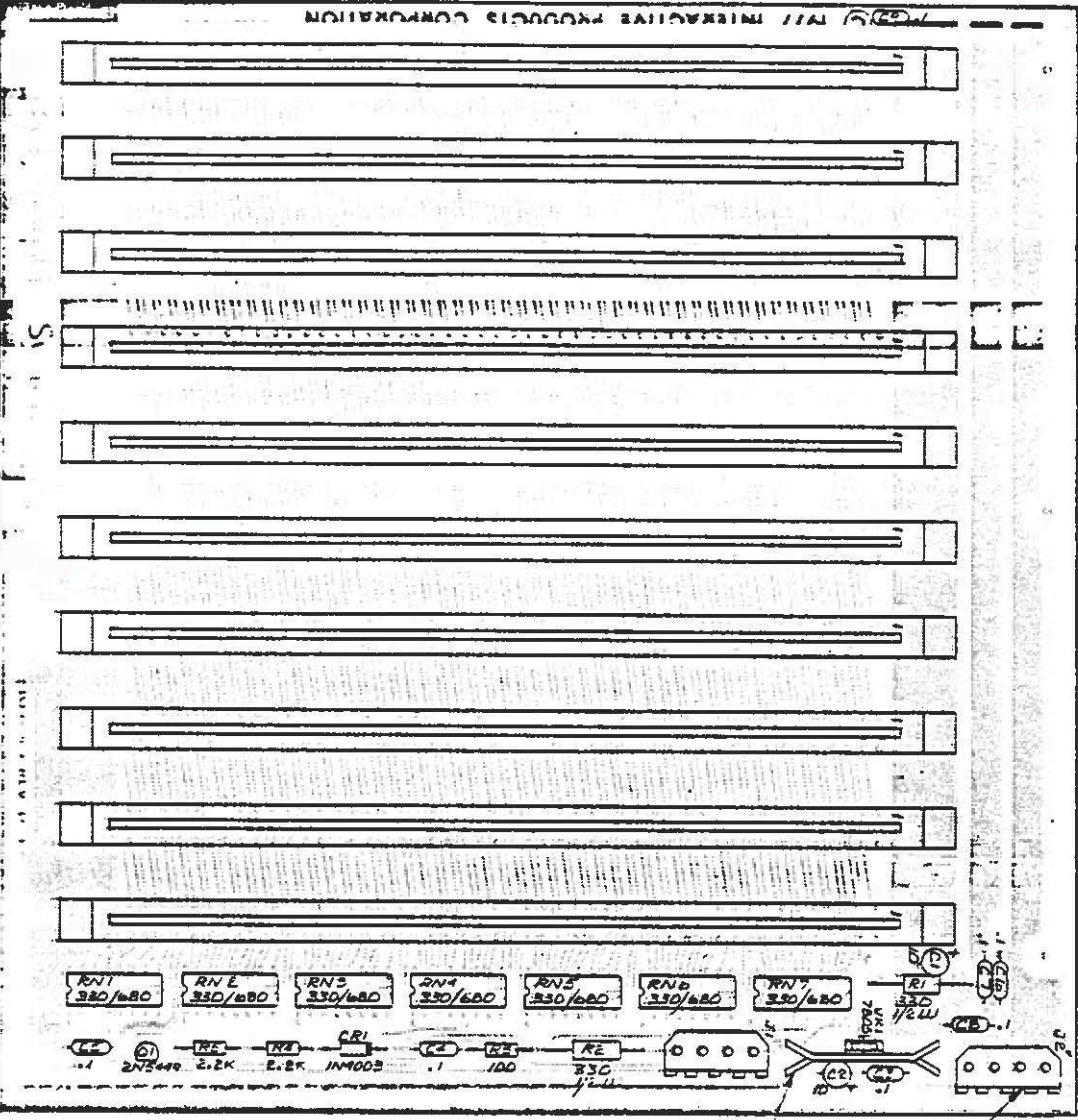
5

4

3

2

1



⑩ E READ
⑨ ⑪ ⑫ ⑬ ⑭ ⑮ ⑯

TO BE INSTALLED AFTER
FLUOR SOLDER OPERATION.
MODIFY PER 104443.
RESISTOR NETWORKS (RVI THROUGH R) TO BE
INSTALLED AFTER BACKPLANE TEST
R013; UNLESS OTHERWISE SPECIFIED

8
7
6
5
4
3
2
1

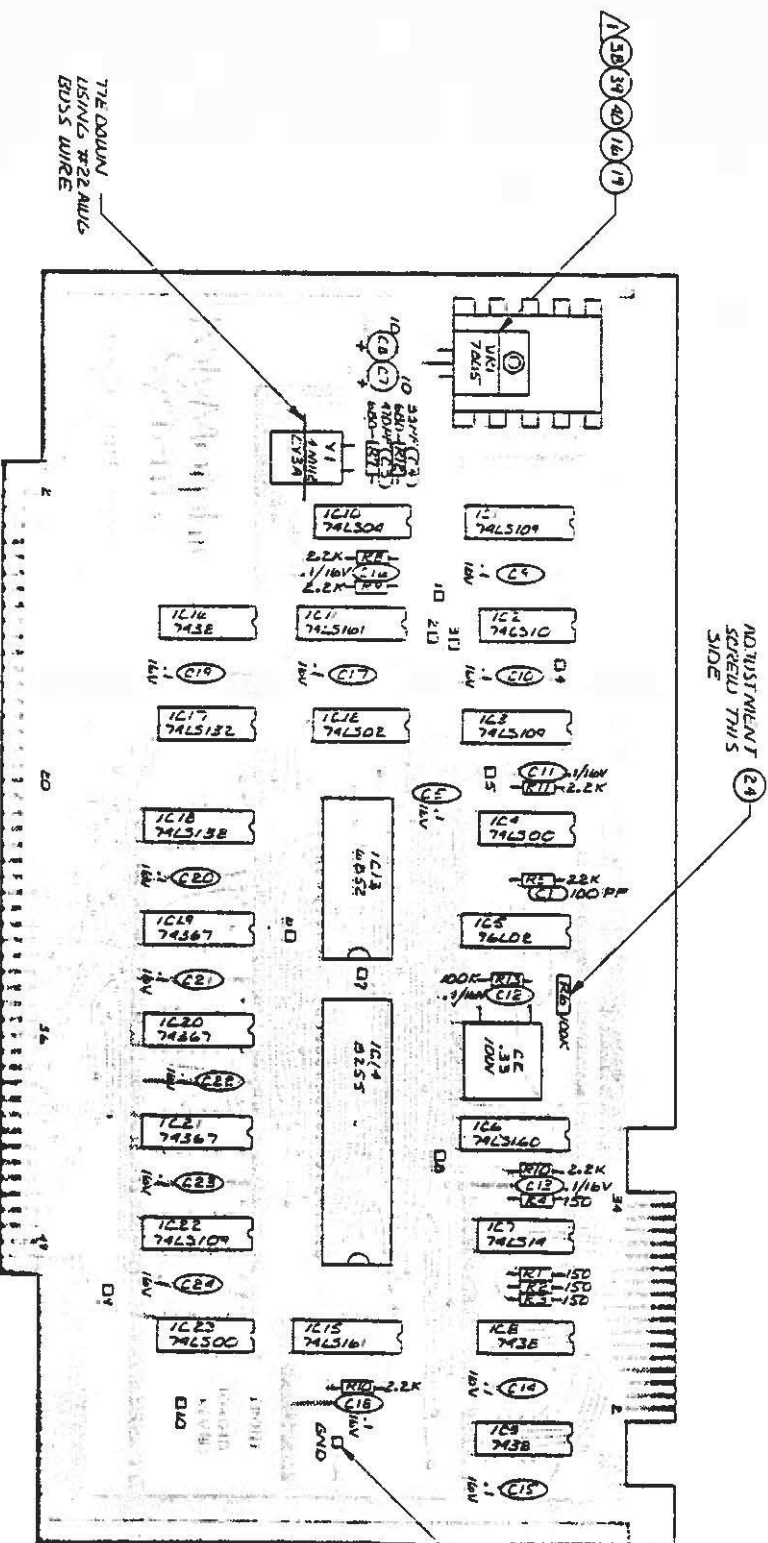
REV	DESCRIPTION	DATE	BY
1	DGR 100137	4/27/71	JR
2	DK 100139 100113 100180	5/27/71	JR

SEE PARTS LIST 00-9040

POLYMORPHIC SYSTEMS	
AS3Y-SYS BA13 BACKPLANE	DRAWING NUMBER 00-9040
REV A	REV B

DATE: 6/20/71
BY: E.E.A.
CHECKED: E.E.A.
APPROVED: E.E.A.

UNLESS OTHERWISE SPECIFIED:
DIMENSIONS ARE IN INCHES AND DECIMALS THEREOF ARE IN THOUSANDS.
DIMENSIONS ARE TO CENTER UNLESS OTHERWISE SPECIFIED.
TOLERANCES ARE:
FRACTIONS: ±.005
DECIMALS: ±.005
DIMENSIONS NOT SPECIFIED: ±.005



THE DOWN
 USING #22 AWG
 BUS WIRE

ADJUSTMENT
 SCREW THIS
 SIDE

11 PLACES
 INSTALL FROM
 FAR SIDE

2 MIDWAY PER DUB 10-9-41.

TO BE INSTALLED AFTER
 FLOW SOLDERING AFTER.

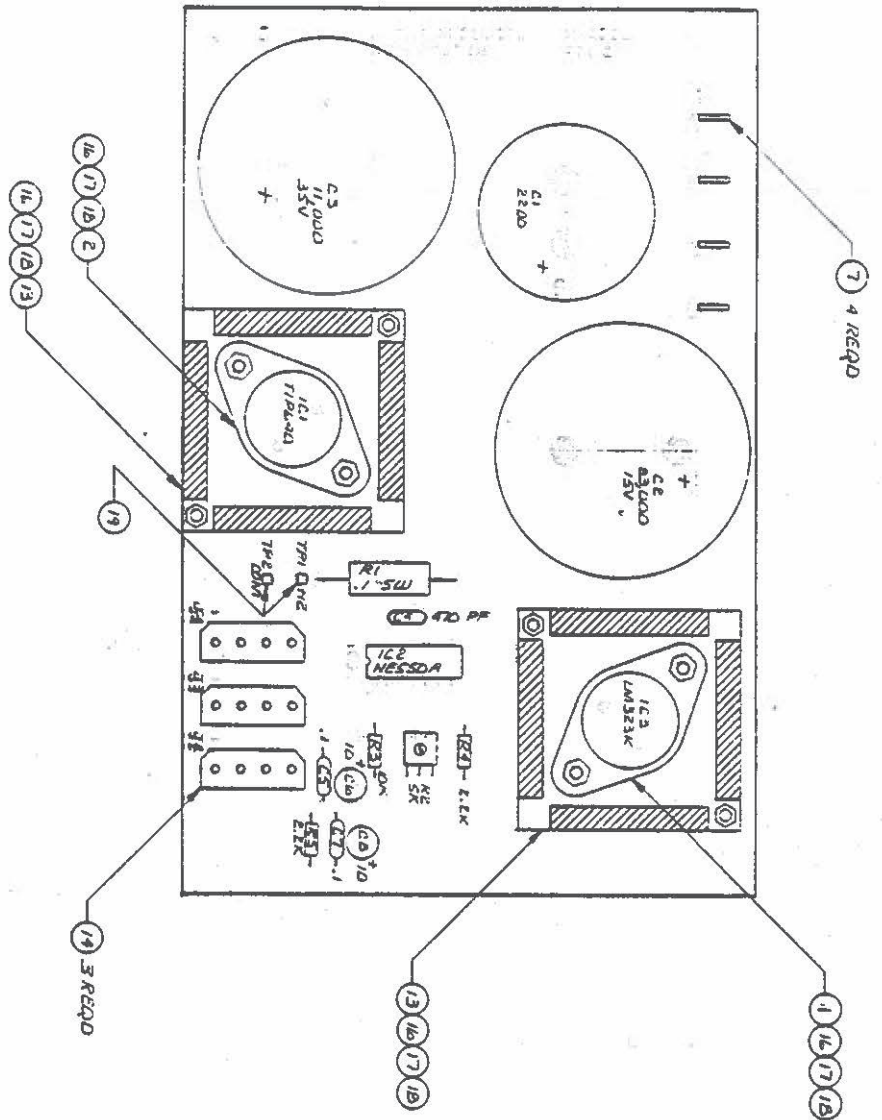
NOTES: UNLESS OTHERWISE SPECIFIED

© 1971 INTRACTIVE PRODUCTS CORPORATION
 SEE PARTS LIST 00-4010

POLYGRAPHIC SYSTEMS

ASSY-DISC CONTROLLER

PARTS LIST THIS LIST IS THE ONLY AUTHORITY FOR THE IDENTIFICATION OF PARTS AND THE IDENTIFICATION OF THE PARTS TO BE USED IN THE ASSEMBLY OF THIS PRODUCT.	
DRAWING NUMBER 00-4010	SHEET 08
DATE 10/63	REV. 08
SCALE 1/2" = 1"	SHEET 08



UNLESS OTHERWISE SPECIFIED

MIL-STD-883C METHOD 2000		MIL-STD-883C METHOD 2000	
TEST	TIME	TEST	TIME
1000	1000	1000	1000
2000	2000	2000	2000
3000	3000	3000	3000
4000	4000	4000	4000
5000	5000	5000	5000
6000	6000	6000	6000
7000	7000	7000	7000
8000	8000	8000	8000
9000	9000	9000	9000
10000	10000	10000	10000

REV	REV	REV	REV
1	2	3	4
DATE	DATE	DATE	DATE
11/18/75	11/18/75	11/18/75	11/18/75
BY	BY	BY	BY
J. J. BROWN	J. J. BROWN	J. J. BROWN	J. J. BROWN
CHECKED	CHECKED	CHECKED	CHECKED
J. J. BROWN	J. J. BROWN	J. J. BROWN	J. J. BROWN
APPROVED	APPROVED	APPROVED	APPROVED
J. J. BROWN	J. J. BROWN	J. J. BROWN	J. J. BROWN

REV	REV	REV	REV
1	2	3	4
DATE	DATE	DATE	DATE
11/18/75	11/18/75	11/18/75	11/18/75
BY	BY	BY	BY
J. J. BROWN	J. J. BROWN	J. J. BROWN	J. J. BROWN
CHECKED	CHECKED	CHECKED	CHECKED
J. J. BROWN	J. J. BROWN	J. J. BROWN	J. J. BROWN
APPROVED	APPROVED	APPROVED	APPROVED
J. J. BROWN	J. J. BROWN	J. J. BROWN	J. J. BROWN

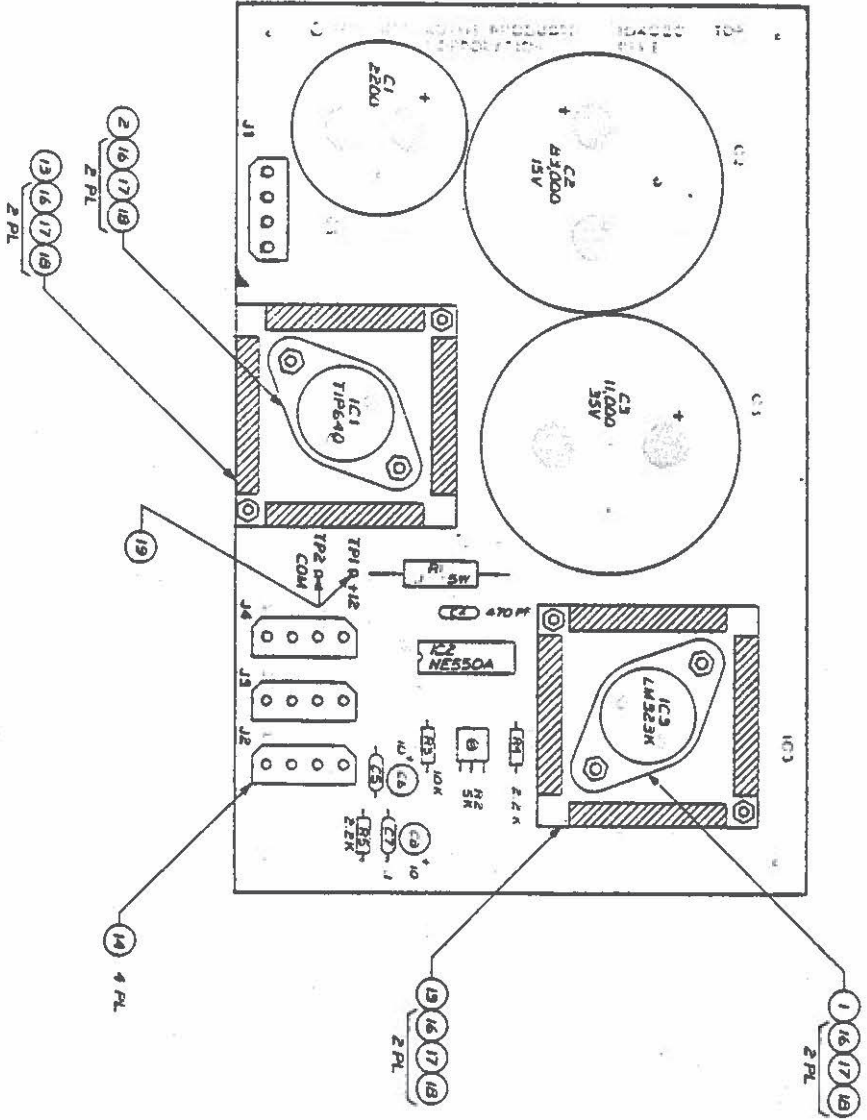
REV	REV	REV	REV
1	2	3	4
DATE	DATE	DATE	DATE
11/18/75	11/18/75	11/18/75	11/18/75
BY	BY	BY	BY
J. J. BROWN	J. J. BROWN	J. J. BROWN	J. J. BROWN
CHECKED	CHECKED	CHECKED	CHECKED
J. J. BROWN	J. J. BROWN	J. J. BROWN	J. J. BROWN
APPROVED	APPROVED	APPROVED	APPROVED
J. J. BROWN	J. J. BROWN	J. J. BROWN	J. J. BROWN

SEE PART'S LIST DD4020

ADLYNORPHIC SYSTEMS

ASSY-3V5 0813 POWER SUPPLY

DD4020 SHEET OF 1

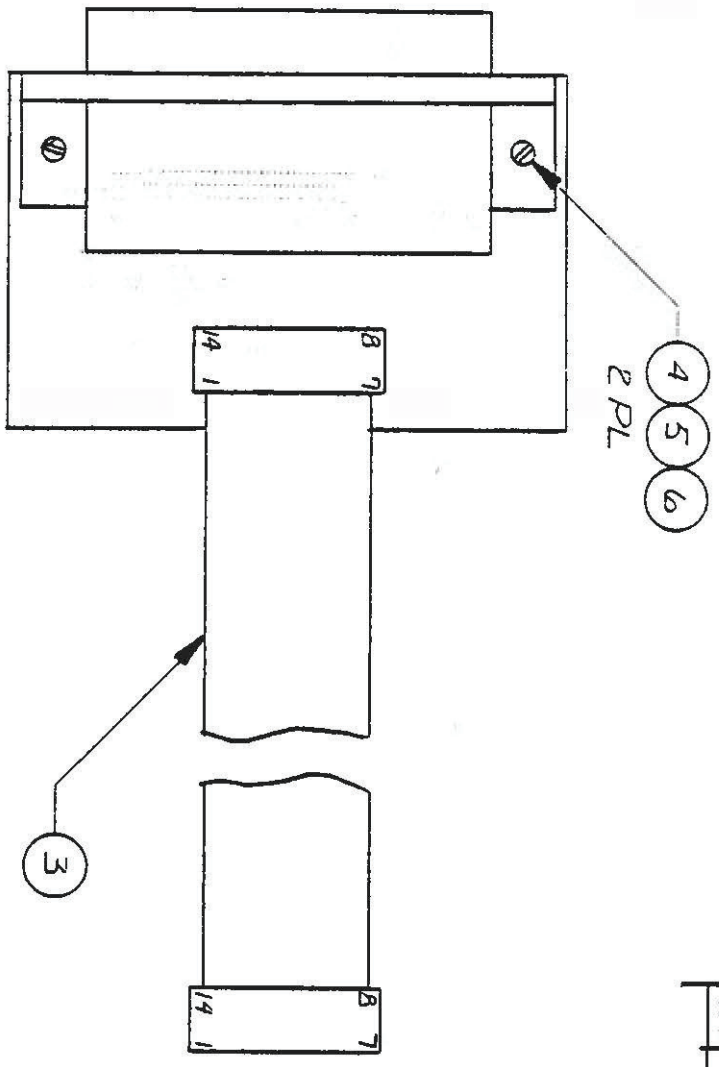


NOTES: UNLESS OTHERWISE SPECIFIED

POLYMORPHIC SYSTEMS.		ASSY - SYS 8813 POWER SUPPLY	
DESIGNED BY: L. ARAKI	DATE: 11-8-77	REV: A	REV: A
CHECKED BY: JPL	DATE: 11-8-77	DRIVING NUMBER: 004020	SCALE: 2:1
MATERIAL:		DRAWING NUMBER: 004020	
FINISH:		DATE: 11-8-77	

DRAWING NO. 001027

REVISIONS			
REV.	DESCRIPTION	DATE	APP.



SEE PARTS LIST 001027

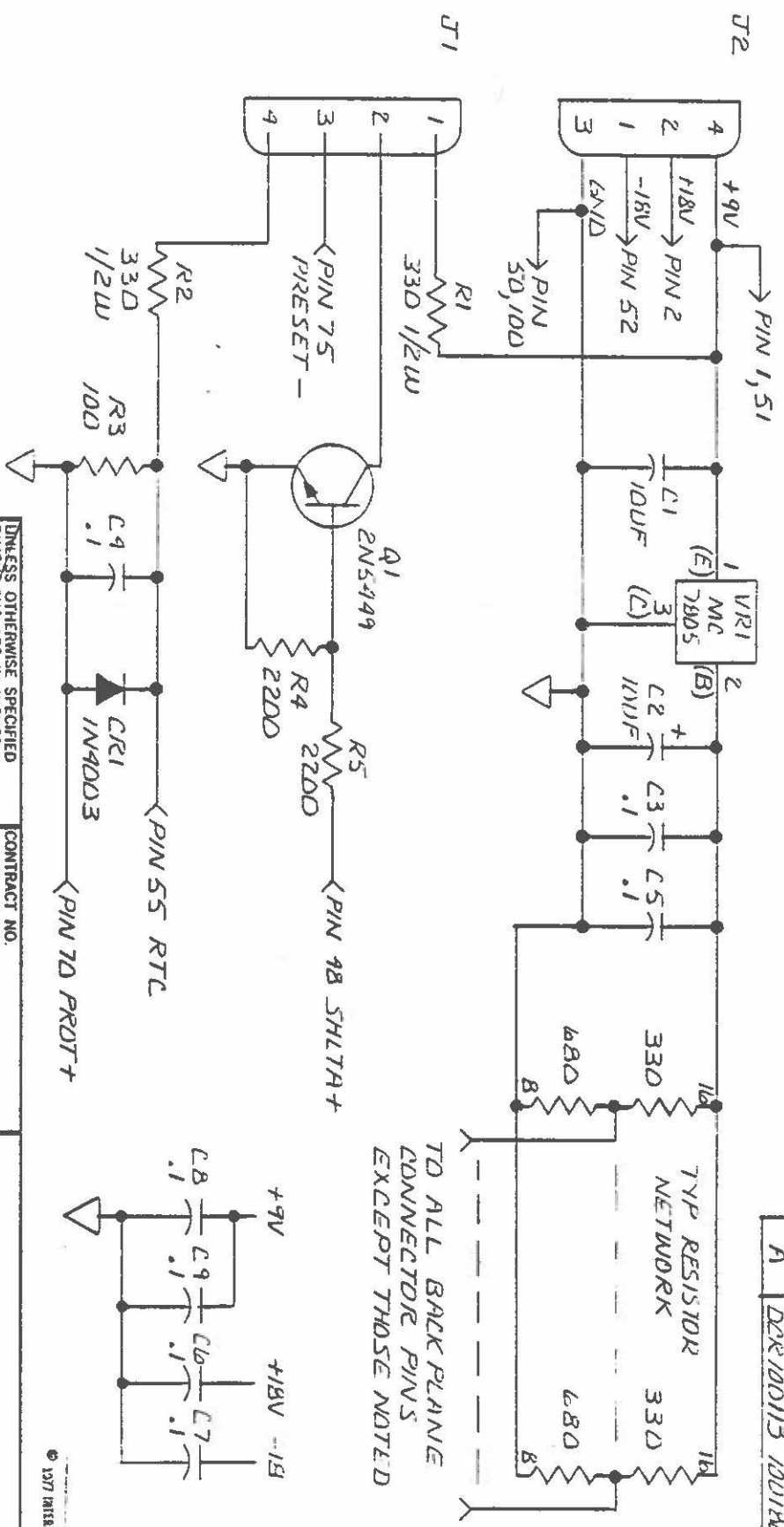
POLYMORPHIC SYSTEMS

ASSY-PARALLEL BD

UNLESS OTHERWISE SPECIFIED INTERPRET DRAWING PER ANSI Y14 DIMENSIONS ARE IN INCHES AND APPLY AFTER PLATING BREAK SHARP EDGES		DRW'G <i>R. Sanchez</i>	DATE 6-17-77	THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION. IT MAY NOT BE REPRODUCED OR TRANSMITTED TO OTHER DOCUMENTS WITHOUT THE WRITTEN PERMISSION OF THE MANUFACTURER.
XX ±	X ±	CHECK F.E.A.	DATE 6-30-77	
XXX ±	Y	APP'G <i>J.A.A.</i>	DATE 7-5-77	NEXT ASSY
MATERIAL		USED ON		
FINISH		SIZE		
		B		
		DRAWING NUMBER		REV
		001027		
		SCALE	2/1	SHEET
				OF

NOTES: UNLESS OTHERWISE SPECIFIED

REV	DESCRIPTION	DATE	APPR
A	DR 100115 10U1HD	6-19-77	022



© 1977 INTERACTIVE PRODUCTS CORPORATION

POLYMORPHIC SYSTEMS

SCHEMATIC - 10 SLOT BACKPLANE

APPROVALS		DATE
DRAWN	<i>R. Sandoz</i>	3-5-77
CHECKED		
APPR.	<i>P. R. K.</i>	10/6

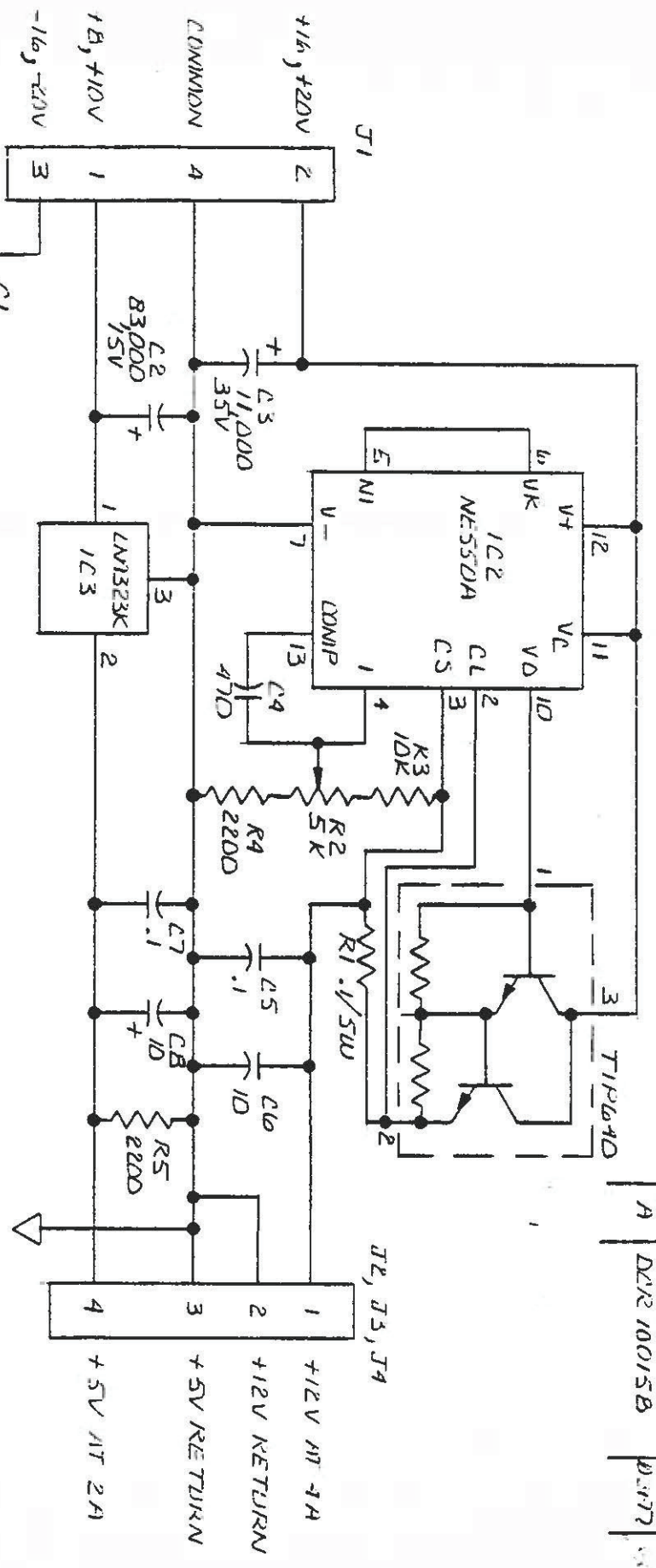
DIMENSIONS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE:		
FRACTIONS	DECIMALS	ANGLES
±	.XX ±	±
MATERIAL	XXX ±	
FINISH		

DO NOT SCALE DRAWING

SIZE	CODE IDENT NO.	DRAWING NO.	REV
B		104041	A

SHEET OF

REV	DATE	BY
A	02/10/15 B	DJH



© 1977 INTEL CORPORATION

POLYMORPHIC SYSTEMS

SCHEM-FLOPPY POWER SUPPLY

CONTRACT NO. _____

APPROVALS: *R. Smith* DATE: 4-5-77

DRAWN BY: *R. Smith* CHECKED BY: _____

DATE: 02/10/15

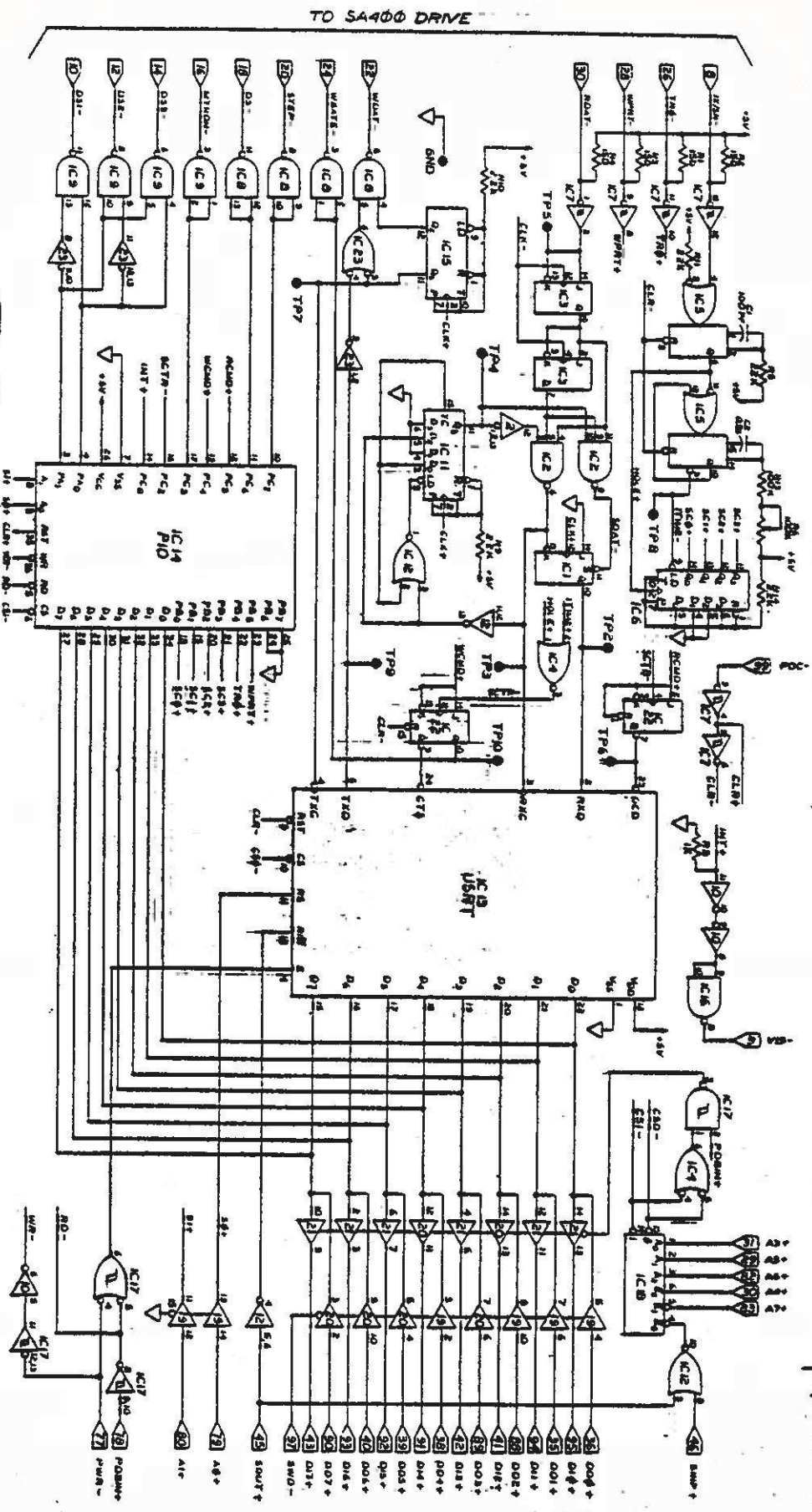
SIZE: B CODE IDENT NO. 104021 DRAWING NO. 104021

SCALE: _____ SHEET OF _____

DO NOT SCALE DRAWING

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES. FRACTIONS ARE IN INCHES. DECIMALS ARE IN THOUSANDS.

FINISH: _____

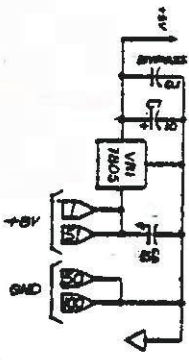
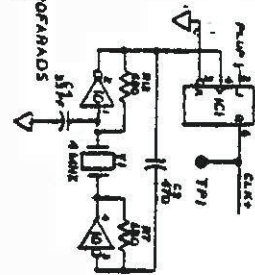


TO SA400 DRIVE

1. ALL CAPACITANCE VALUES ARE IN MICROFARADS

2. ALL RESISTANCE VALUES ARE IN OHMS

NOTES: UNLESS OTHERWISE SPECIFIED



IC1	74LS100	IC14	74LS100
IC2	74LS100	IC15	74LS100
IC3	74LS100	IC16	74LS100
IC4	74LS100	IC17	74LS100
IC5	74LS100	IC18	74LS100
IC6	74LS100	IC19	74LS100
IC7	74LS100	IC20	74LS100
IC8	74LS100	IC21	74LS100
IC9	74LS100	IC22	74LS100
IC10	74LS100	IC23	74LS100
IC11	74LS100	IC24	74LS100
IC12	74LS100	IC25	74LS100

IC1	74LS100	IC14	74LS100
IC2	74LS100	IC15	74LS100
IC3	74LS100	IC16	74LS100
IC4	74LS100	IC17	74LS100
IC5	74LS100	IC18	74LS100
IC6	74LS100	IC19	74LS100
IC7	74LS100	IC20	74LS100
IC8	74LS100	IC21	74LS100
IC9	74LS100	IC22	74LS100
IC10	74LS100	IC23	74LS100
IC11	74LS100	IC24	74LS100
IC12	74LS100	IC25	74LS100

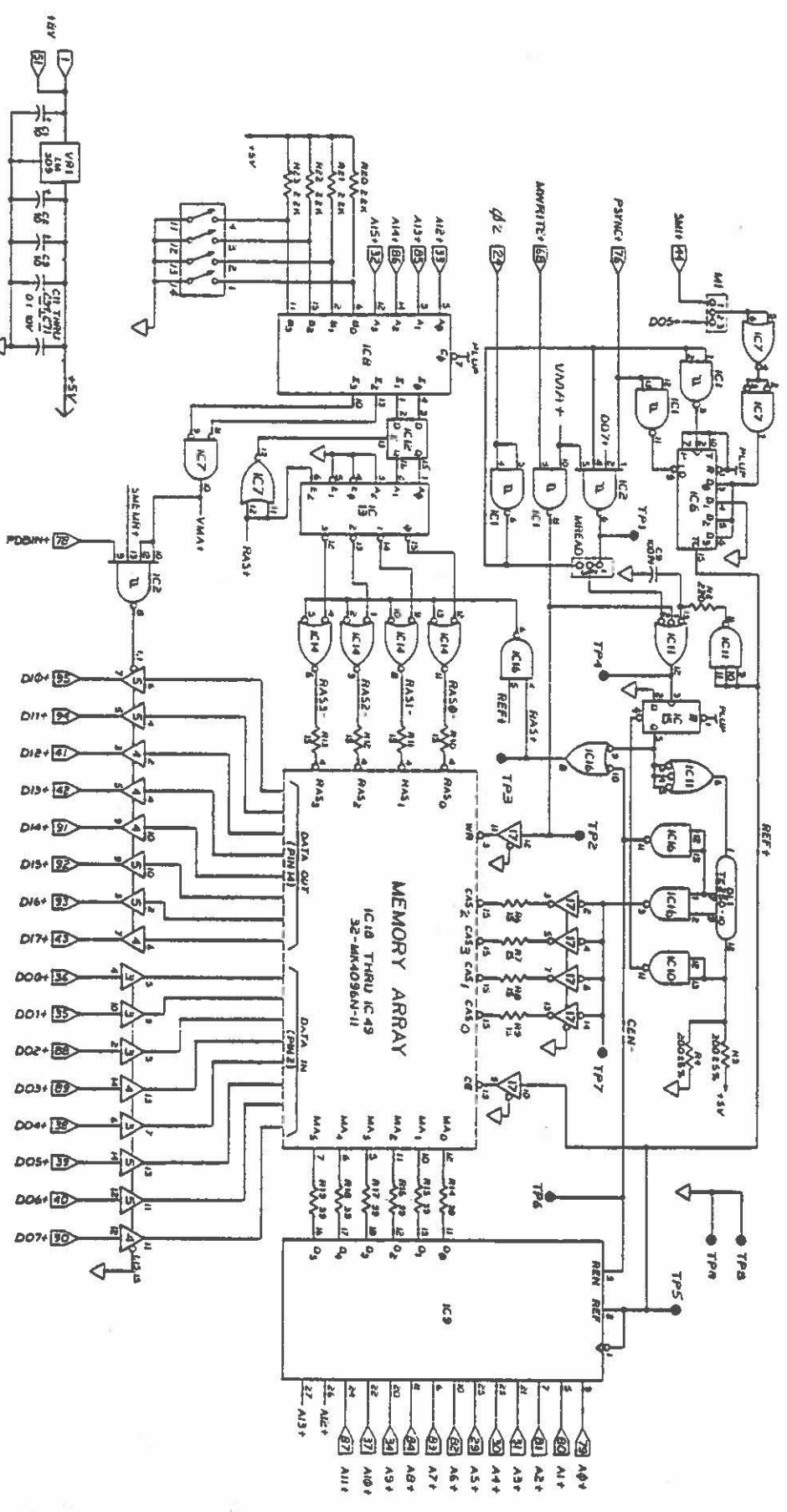
IC1	74LS100	IC14	74LS100
IC2	74LS100	IC15	74LS100
IC3	74LS100	IC16	74LS100
IC4	74LS100	IC17	74LS100
IC5	74LS100	IC18	74LS100
IC6	74LS100	IC19	74LS100
IC7	74LS100	IC20	74LS100
IC8	74LS100	IC21	74LS100
IC9	74LS100	IC22	74LS100
IC10	74LS100	IC23	74LS100
IC11	74LS100	IC24	74LS100
IC12	74LS100	IC25	74LS100

© 1977 INTERACTIVE PRODUCTS CORPORATION

POLYMORPHIC SYSTEMS

SCHEMATIC - DISC CONTROLLER

REV	1
DATE	10-4-77
DESIGNED BY	DAVID R. BROWN
CHECKED BY	DAVID R. BROWN
DATE	10-4-77
APP'D	DAVID R. BROWN
DATE	10-4-77



MEMORY ARRAY
IC18 THRU IC49
32-M4096N-11

MEMORY REFERENCE
IC9

ADDRESS DECODE
IC1

DATA DECODE
IC2

DATA DECODE
IC3

DATA DECODE
IC4

DATA DECODE
IC5

DATA DECODE
IC6

DATA DECODE
IC7

DATA DECODE
IC8

DATA DECODE
IC9

DATA DECODE
IC10

DATA DECODE
IC11

DATA DECODE
IC12

DATA DECODE
IC13

DATA DECODE
IC14

DATA DECODE
IC15

DATA DECODE
IC16

DATA DECODE
IC17

DATA DECODE
IC18

DATA DECODE
IC19

DATA DECODE
IC20

DATA DECODE
IC21

DATA DECODE
IC22

DATA DECODE
IC23

DATA DECODE
IC24

DATA DECODE
IC25

DATA DECODE
IC26

DATA DECODE
IC27

DATA DECODE
IC28

DATA DECODE
IC29

DATA DECODE
IC30

DATA DECODE
IC31

DATA DECODE
IC32

DATA DECODE
IC33

DATA DECODE
IC34

DATA DECODE
IC35

DATA DECODE
IC36

DATA DECODE
IC37

DATA DECODE
IC38

DATA DECODE
IC39

DATA DECODE
IC40

DATA DECODE
IC41

DATA DECODE
IC42

DATA DECODE
IC43

DATA DECODE
IC44

DATA DECODE
IC45

DATA DECODE
IC46

DATA DECODE
IC47

DATA DECODE
IC48

DATA DECODE
IC49

DATA DECODE
IC50

DATA DECODE
IC51

DATA DECODE
IC52

DATA DECODE
IC53

DATA DECODE
IC54

DATA DECODE
IC55

DATA DECODE
IC56

DATA DECODE
IC57

DATA DECODE
IC58

DATA DECODE
IC59

DATA DECODE
IC60

DATA DECODE
IC61

DATA DECODE
IC62

DATA DECODE
IC63

DATA DECODE
IC64

DATA DECODE
IC65

DATA DECODE
IC66

DATA DECODE
IC67

DATA DECODE
IC68

DATA DECODE
IC69

DATA DECODE
IC70

DATA DECODE
IC71

DATA DECODE
IC72

DATA DECODE
IC73

DATA DECODE
IC74

DATA DECODE
IC75

DATA DECODE
IC76

DATA DECODE
IC77

DATA DECODE
IC78

DATA DECODE
IC79

DATA DECODE
IC80

DATA DECODE
IC81

DATA DECODE
IC82

DATA DECODE
IC83

DATA DECODE
IC84

DATA DECODE
IC85

DATA DECODE
IC86

DATA DECODE
IC87

DATA DECODE
IC88

DATA DECODE
IC89

DATA DECODE
IC90

DATA DECODE
IC91

DATA DECODE
IC92

DATA DECODE
IC93

DATA DECODE
IC94

DATA DECODE
IC95

DATA DECODE
IC96

DATA DECODE
IC97

DATA DECODE
IC98

DATA DECODE
IC99

DATA DECODE
IC100

DATA DECODE
IC101

DATA DECODE
IC102

DATA DECODE
IC103

DATA DECODE
IC104

DATA DECODE
IC105

DATA DECODE
IC106

DATA DECODE
IC107

DATA DECODE
IC108

DATA DECODE
IC109

DATA DECODE
IC110

DATA DECODE
IC111

DATA DECODE
IC112

DATA DECODE
IC113

DATA DECODE
IC114

DATA DECODE
IC115

DATA DECODE
IC116

DATA DECODE
IC117

DATA DECODE
IC118

DATA DECODE
IC119

DATA DECODE
IC120

DATA DECODE
IC121

DATA DECODE
IC122

DATA DECODE
IC123

DATA DECODE
IC124

DATA DECODE
IC125

DATA DECODE
IC126

DATA DECODE
IC127

DATA DECODE
IC128

DATA DECODE
IC129

DATA DECODE
IC130

DATA DECODE
IC131

DATA DECODE
IC132

DATA DECODE
IC133

DATA DECODE
IC134

DATA DECODE
IC135

DATA DECODE
IC136

DATA DECODE
IC137

DATA DECODE
IC138

DATA DECODE
IC139

DATA DECODE
IC140

DATA DECODE
IC141

DATA DECODE
IC142

DATA DECODE
IC143

DATA DECODE
IC144

DATA DECODE
IC145

DATA DECODE
IC146

DATA DECODE
IC147

DATA DECODE
IC148

DATA DECODE
IC149

DATA DECODE
IC150

DATA DECODE
IC151

DATA DECODE
IC152

DATA DECODE
IC153

DATA DECODE
IC154

DATA DECODE
IC155

DATA DECODE
IC156

DATA DECODE
IC157

DATA DECODE
IC158

DATA DECODE
IC159

DATA DECODE
IC160

DATA DECODE
IC161

DATA DECODE
IC162

DATA DECODE
IC163

DATA DECODE
IC164

DATA DECODE
IC165

DATA DECODE
IC166

DATA DECODE
IC167

DATA DECODE
IC168

DATA DECODE
IC169

DATA DECODE
IC170

DATA DECODE
IC171

DATA DECODE
IC172

DATA DECODE
IC173

DATA DECODE
IC174

DATA DECODE
IC175

DATA DECODE
IC176

DATA DECODE
IC177

DATA DECODE
IC178

DATA DECODE
IC179

DATA DECODE
IC180

DATA DECODE
IC181

DATA DECODE
IC182

DATA DECODE
IC183

DATA DECODE
IC184

1:17 INTERACTIVE DRAWING

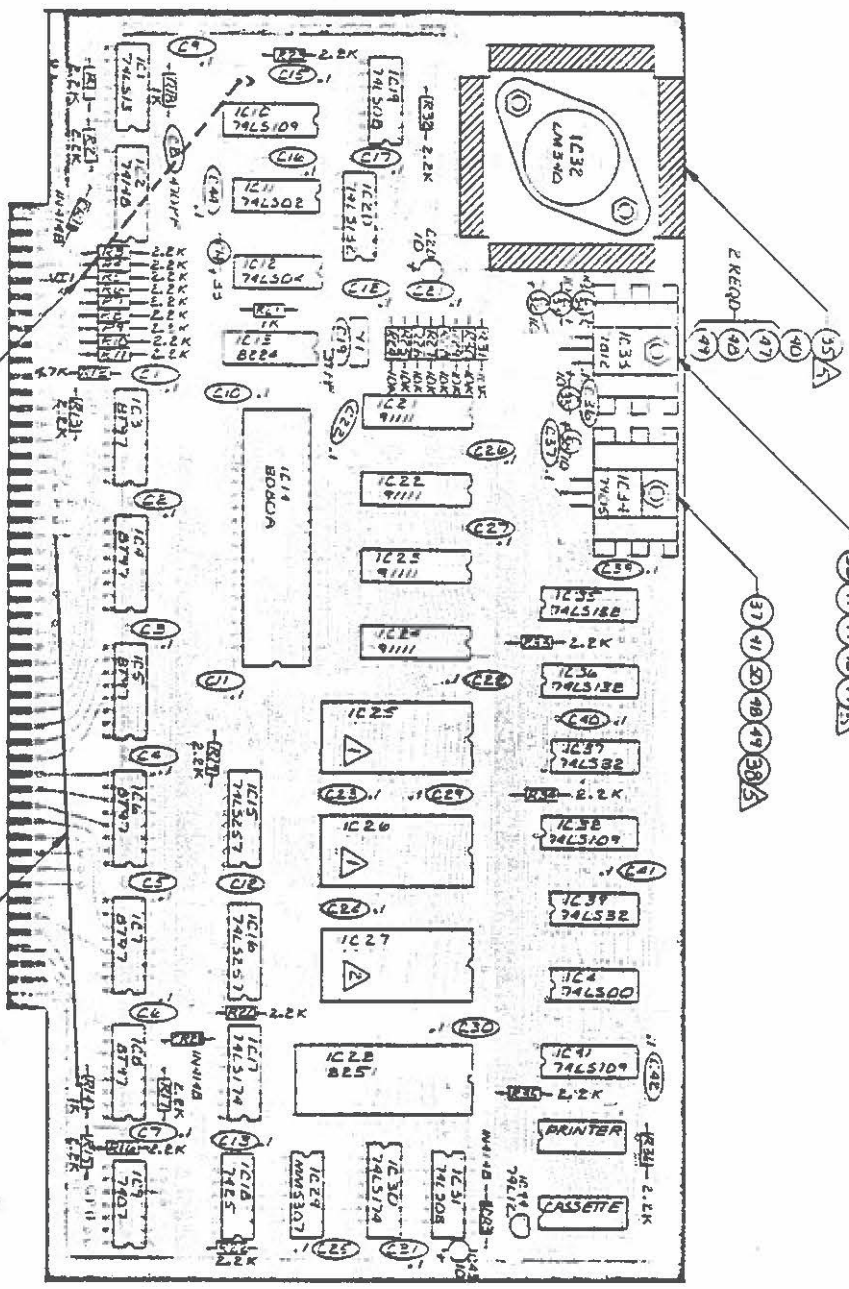
POLYMORPHIC SYSTEMS
16K RAM

DATE: 10/11/77
DRAWN BY: J. L. ARANKI
CHECKED BY: J. L. ARANKI

105201

SHEET 7 OF 7

REV	DATE	BY
A	02R 100115	9491
B	02R 100115	0477



1. TO BE INSTALLED AFTER FLUX SOLDER DISTRIBUTION
2. MOUNT PER DATA 104920, 104933, 105252.
3. TO BE USED FOR BOARD UP TO REV. 0.31
4. MOUNT: 4.0 MOUNTIK P/N 035020.
5. FOR DISC SYSTEM:
 - A. IC25 ROOF ROOM 400 P/N 035025
 - B. IC26 ROOF ROOM 600 P/N 035030

NOTES: UNLESS OTHERWISE SPECIFIED

THIS IS AN OUTLINE DRAWING SPECIFICALLY FOR THE PURPOSE OF IDENTIFYING THE LOCATION OF THE BOARD IN THE SYSTEM. IT IS NOT TO BE USED FOR FABRICATING THE BOARD.	
DRAWING NUMBER 0010162	SHEET OF 1
HYUNDAIC SYSTEMS H35V-CPU BOARD	
DATE 1/27/77	DRAWN BY S. B. S.
CHECKED BY S. B. S.	APPROVED BY S. B. S.

© 1977 HYUNDAIC SYSTEMS CORPORATION