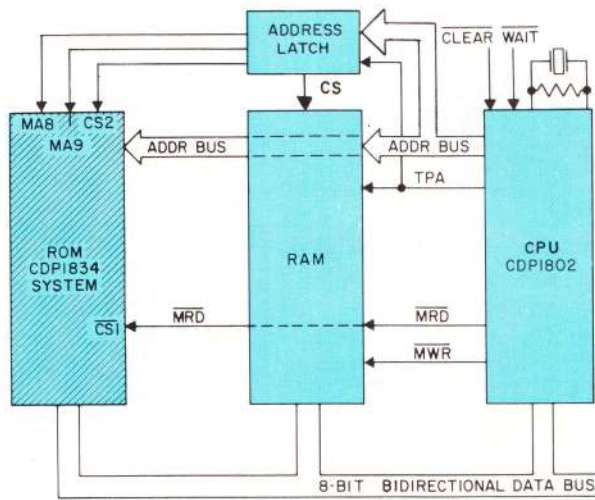# CDP1832, CDP1834 Mask-Programmable ROM's

The CDP1832 and the CDP1834 DO NOT have on-board address latches, but are designed primarily as pin-for-pin replacements for industry types 2704 and 2708 respectively.

A typical system with an external address latch that is common with most large systems is shown here.
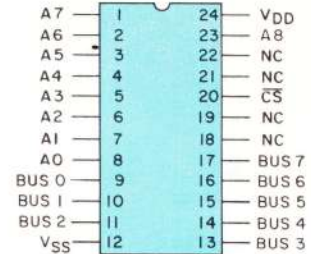
- Pin-Compatible with industry types 2704 and 2708
- Accepts standard memory address decoding
- Polarity options on chip selects

Typical System

92CM-32226

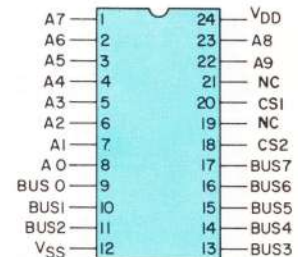### CDP1832 Terminal-Assignment Diagram

TOP VIEW
NC = NO CONNECTION

92CS-27579R2

CDP1834 Terminal-Assignment Diagram

TOP VIEW
NC = NO CONNECTION
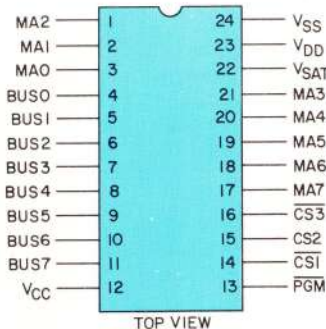
92CS-28727RI

CDP18U42 256x8 UV Eraseable PROM

The CDP18U42 is a static CMOS programmable read-only memory (PROM), organized as 256 words by 8 bits. It interfaces directly with any CDP1800 microprocessor and has several chip-select inputs for expansion.

The CDP18U42 is ideally suited for experimentation and other uses where rapid design changes are necessary. It allows the user to design such a system using all-CMOS components.

- Single voltage supply (during read) 4-6.5V
- Bulk UV erase to all "0" in 5 to 20 minutes
- Programmed to "1" with 20V, 10 ms (typ.)

- Fast access time: 700 ns, typ.
- Fast programming time: 2.5 sec. typ.
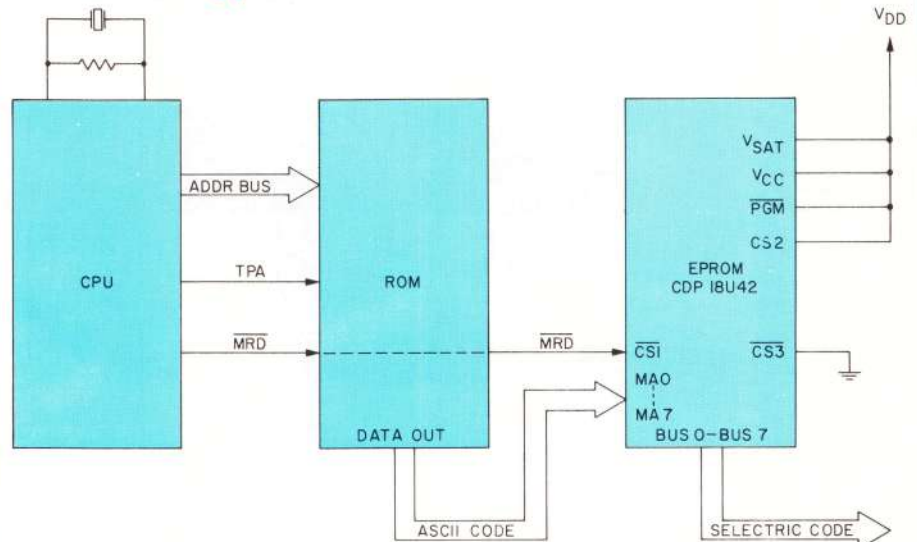- Functional replacement for 1702 UV eraseable PROM

Terminal-Assignment Diagram

TOP VIEW

92CS-31960

Code Converter Application

92CM-34320

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