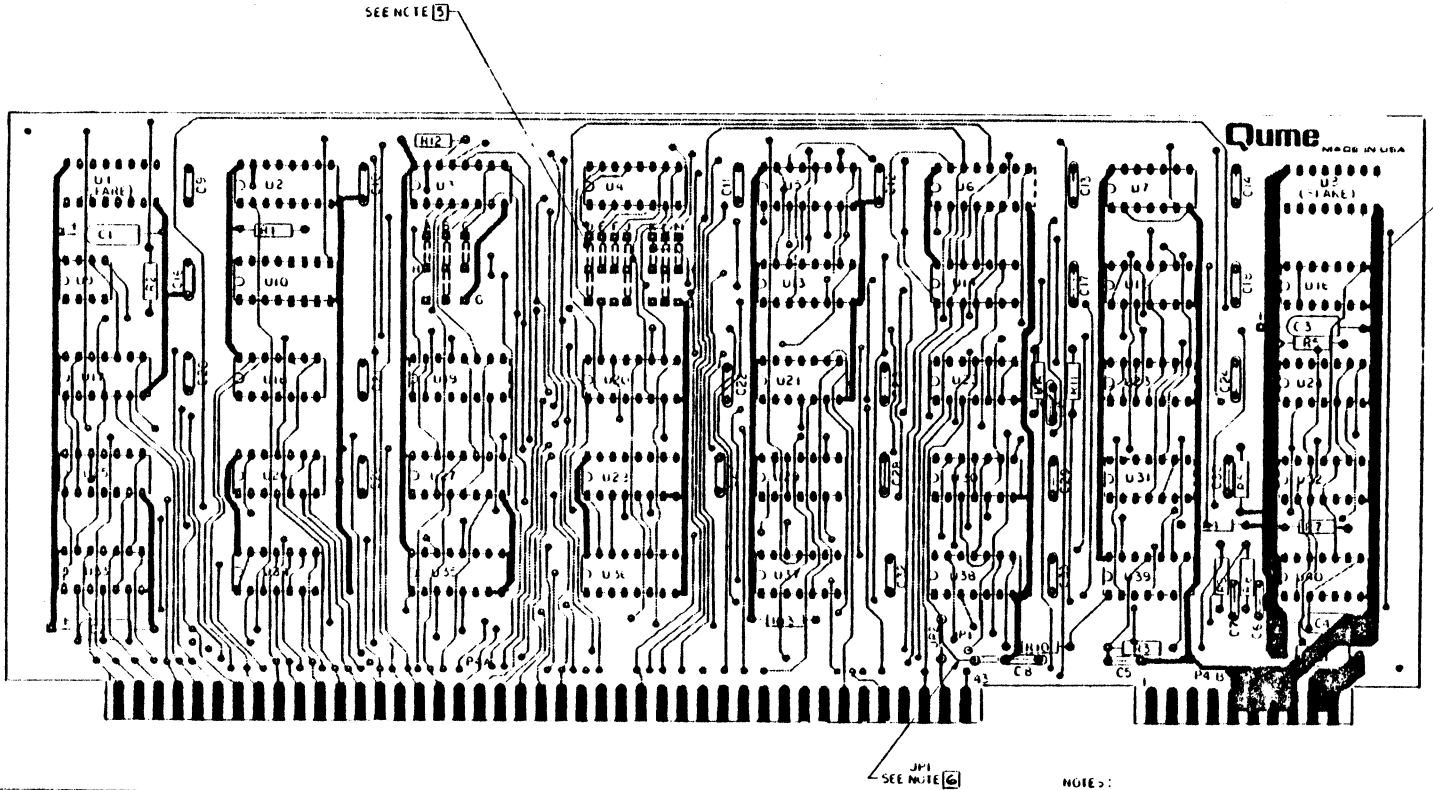

DASH No. CHART				
ASSY (ASSY No.)	-01 (SH 2)	-02 (SH 3)	-03 (SH 5)	-04 (SH 4)
DESCR.	PARALLEL	SEQUENTIAL	PARALLEL DUAL DRIVE	SEQUENTIAL DUAL DRIVE
U6	—	74174	—	74174
U19	—	74174	—	74174
U16	—	74LS08	—	74LS08
U23	—	74LS74	—	74LS74
U32	—	74LS74	—	74LS74
U40	—	74LS74	—	74LS74
U31	—	74LS86	—	74LS86
U39	—	74LS14	—	74LS14
U24	—	74LS00	—	74LS00
JPA-JPM	OPEN	CLOSED	OPEN	CLOSED
JP1	CLOSED	CLOSED	OPEN	OPEN
JP2	OPEN	OPEN	CLOSED	CLOSED

LAST USED REF DESIG.
U40, C23, R12, L1, JP2

NOTES:
UNLESS OTHERWISE SPECIFIED:

1. ALL RESISTORS ARE IN OHMS, 1/4W, 5%.
2. CAPACITORS C1, C2, C3 AND C4 ARE 4.7 MFD, 10V, 20%.
3. ALL CAPACITORS ARE 0.02 MFD, 50V, 20%
4. SHEET 3 IS FOR PARALLEL LOGIC OPTION
   SHEET 4 IS FOR SEQUENTIAL LOGIC OPTION
   SEE DASH NO. CHART.
5. ALL JUMPERS AS SHOWN ARE USED ON SEQUENTIAL -02 VERSON ONLY.
6. FOR DUAL DRIVE OPTION CUT JUMPER "JP1" INSTALL JUMPER "JP2" (90672-03, -04)
7. THIS SCHEMATIC VALID FOR 90672-01 REV B PCN 0743
   90672-02 REV B PCN 0743
   90672-03 REV B PCN 0743
   90672-04 REV B PCN 0743

C	0801			S. MINYEN	6-22-77	Qume	
A	—		0793	F. SHAH	9-12-77	SCHEMATIC	
						SM3/TT75 COORD. LOGIC	
						SIZE D	90670-XX C