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Service Division

80-011

Test Jumper Array Is Required To Perform CE Alignment On D3 and D3E Disk Drives

The logic PCBA's used on D3's or D3E's have two circuits for detecting index pulses from the removable pack. The first circuit feeds J125, pin 5, and detects the index pulse on multi-notched packs. J125 pin 5 is jumpered to J125 pin 16 to obtain index pulses on mechanically sectored and electronically sectored units that are using multi-notched packs. The second circuit feeds J125, pin 14, and detects the index pulse on index-only packs or the sector pulses on mechanically sectored units using multi-notched packs.

Top-load CE alignment packs have only one notch. Therefore, when trying to perform the index-to-data adjustment on top-load mechanically or electronically sectored units, no index pulse will be available at the exerciser or upper index test points.

To service these units, make a test jumper array as shown in Figure 1.

Remove the existing J125 array on the logic PCBA and install the test array. Proceed with the alignment procedure, as described in the Operation and Maintenance Manual. After completing the adjustment, do not forget to remove the test array and reinstall the original array in J125.

The test array must be used on top-load units with model types D3xxx-E0xx and D3xxx-MQxx; for these units, use the upper index test point on the logic PCBA with the test array installed.

The test array is *not* required on model types D3xxx-10xx; for these units, simply use the upper index test point on the logic PCBA with the original array installed in J125.

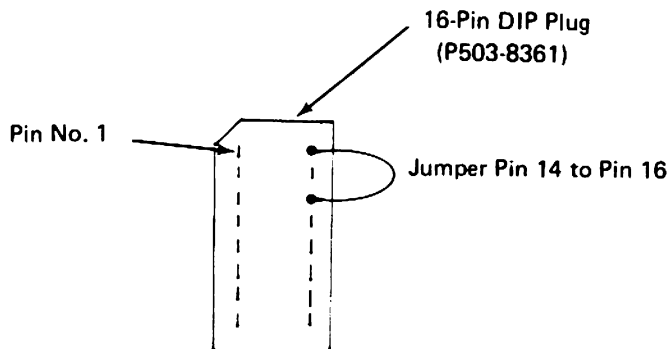


Figure 1. J125/Test Jumper Array

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