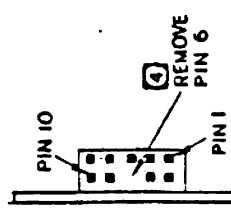
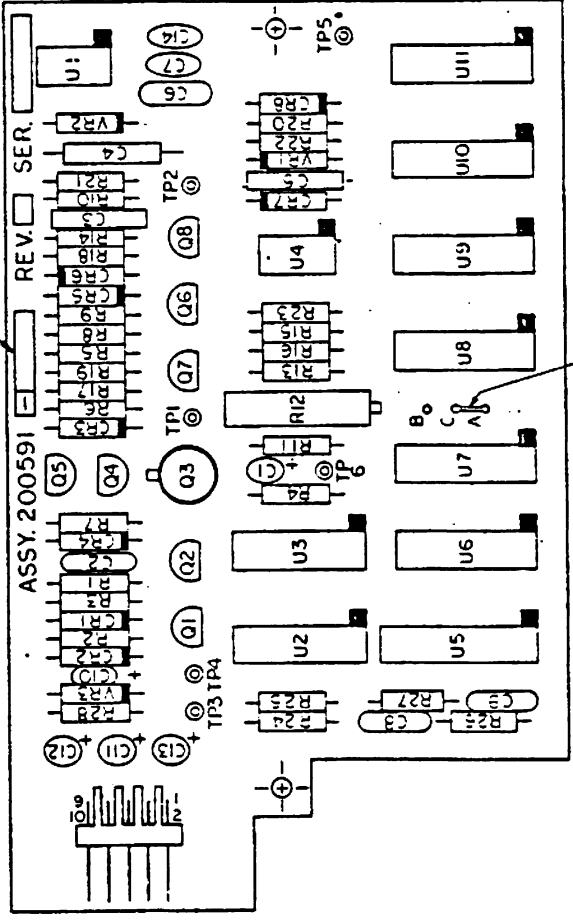


REV	DESCRIPTION	DATE	APPR
X1	EXP RELEASE	1/7	11/1/75
A	MFG RELEASE	NO CHG.	1/11
B	ECO 233		
C	ECO 271		
D	ECO 321		
E	ECO 378		
F	ECO 502		
G	ECO 537		
H	ECO 580		
I	ECO 576		



- ④ REMOVE KEYING PINS PRIOR TO INSTALLATION ON PCB.
- 3. SCHEMATIC REF 200592.
- ② THE JUMPER BETWEEN A & C IS AN ETCHED CONDUCTOR. WHEN ANY OTHER JUMPER LOCATION IS REQUIRED - (SEE TABULATED M/L) CUT AWAY ETCH TRACE AND ADD JUMPER WIRE.
- ① MARK DASH VERSION, REVISION AND SERIAL NUMBER .J2 HIGH BLACK CHARACTERS IN AREA SHOWN. UNLESS OTHERWISE SPECIFIED:

PART NO. TO BE 200591 -XXX

NOTES UNLESS SPECIFIED	DRAWN	5/2/75	PerSci Inc
1. TOLERANCES .XX ±	CHECK		
2. BREAK ALL SHARP EDGES APPROX .010	APPR.		
3. MASH SURFACES ✓	MATERIAL		
4. ALL DIMS IN INCHES.	FINISH		
MODEL NO.	SCALE	2:1	SIZE
270,277,288,299A2/B	DO NOT SCALE THIS DRAWING	C	200591 (CHARTED)
NEXT ASSY 200131	WEIGHT		
200597, 200800, 200801	SHEET 1 OF 1		

ASSEMBLY, PCB PHASE LOCKED FM DATA SEPARATOR	CODE 120
	CITY/STATE 1