

**SCHEMATIC OF THE  
HEATHKIT®  
SERIAL I/O AND CASSETTE  
INTERFACE CARD  
MODEL H8-5**

**NOTES:**

1. ALL RESISTORS ARE 1/4 WATT UNLESS MARKED OTHERWISE. RESISTOR VALUES ARE IN OHMS (K=1,000; M=1,000,000).
2. ALL CAPACITORS ARE IN  $\mu$ F UNLESS MARKED OTHERWISE.
3.  $\nabla$  THIS SYMBOL INDICATES CIRCUIT BOARD GROUND.
4.  $\square$  THIS SYMBOL INDICATES THE SYSTEM BUS CONNECTION.

5.  $\square$  THIS SYMBOL INDICATES WHERE A WIRE IS SOLDERED TO THE CIRCUIT BOARD.
6.  $\square$  THIS SYMBOL POINTS TO THE ONLY PLACE WHERE A VOLTAGE SOURCE CONNECTS.
7.  $\circ$  THIS SYMBOL INDICATES A DC VOLTAGE TAKEN WITH A HIGH INPUT IMPEDANCE VOLTMETER FROM THE POINT INDICATED TO CHASSIS GROUND. VOLTAGES MAY VARY  $\pm 20\%$ .

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