

IDENTIFICATION

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PRODUCT CODE: MAINDEC=08=DJEXB=A=D  
PRODUCT NAME: 2K TO 32K PDP-8A PROCESSOR EXERCISER  
DATE CREATED: JANUARY 15, 1975  
MAINTAINER: DIAGNOSTIC GROUP  
AUTHOR: BRUCE HANSEN

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## 1.0

### ABSTRACT

THE 2K TO 32K PDP-8A PROCESSOR EXERCISER IS A PROGRAM TO CHECK THE EXECUTION OF MEMORY REFERENCE INSTRUCTIONS AND OPERATE INSTRUCTIONS. IN A 2K TO 32K PDP-8A COMPUTER, THE PROGRAM ALSO CHECKS FOR DATA TRANSMISSIONS, AND INTERRUPTS IF A OPTIONS 1 MODULE (M8316) IS CONNECTED IN LOOP BACK MODE AND THE PROGRAM IS INITIALIZED TO TEST THE OPTION 1 MODULE. ALL INSTRUCTIONS, ADDRESSES, MEMORY DATA, AC DATA, MQ DATA AND LINK DATA ARE GENERATED FROM A RANDOM NUMBER GENERATOR.

THE PROGRAM RELOCATES ITSELF A PAGE AT A TIME, UP AND DOWN, WITHIN ANY 2K TO 4K MEMORY FIELD. IF THE COMPUTER CONTAINS MORE THAN 4K OF MEMORY, THE PROGRAM WILL RELOCATE UP AND DOWN BETWEEN MEMORY FIELDS. AT LEAST 2K OF MEMORY IS REQUIRED IN THE LAST EXTENDED MEMORY FIELD FOR THE PROGRAM TO RELOCATE INTO IT.

THE PROGRAM CAN ALSO BE RUN ON THE PDP-8A APT TEST LINE.

## 2.0

### REQUIREMENTS

#### 2.1

### HARDWARE

THE FOLLOWING HARDWARE IS REQUIRED FOR THE EXECUTION OF THIS PROGRAM.

PROCESSOR(S): PDP-8A/E/F/M

MEMORY: 2K OF MEMORY TO 32K OF MEMORY IN ANY INCREMENTS OF 1K.

OPTIONS: IF GREATER THAN 4K OF MEMORY, A M837 OR M8317 MODULE (MEMORY EXTENSION/TIME SHARE CONTROL) IS REQUIRED.

IF DATA TRANSMISSIONS AND INTERRUPTS ARE TO BE TESTED, A PDP-8A OPTION BOARD #1 (M8316) IS REQUIRED (PDP-8A ONLY).

SPECIAL: IF A PDP-8A OPTION BOARD #1 IS TO BE TESTED, THE FOLLOWING HARDWARE IS REQUIRED:  
A. ONE QUAD EXTENDER MODULE  
B. ONE BC08R-01 CABLE  
C. ONE EIA TEST CONNECTOR (OPTIONAL)  
D. THREE TERMINI-POINT JUMPERS.

## 2.2

### STORAGE

THE PROGRAM INITIALLY OCCUPIES LOCATIONS 0000 TO 0155 AND 0200 TO 3722. LOCATIONS 0156 TO 0176 ARE RESERVED FOR THE RIM LOADER ON COMPUTERS CONTAINING LESS THAN 4K OF MEMORY. THE PROGRAM USES LOCATIONS 0000 TO 0155 AND 3600 TO 3722 FOR PROGRAM INITIALIZATION. ONCE THE PROGRAM HAS BEEN STARTED, THESE LOCATIONS AND THE LOCATIONS RESERVED FOR THE RIM LOADER WILL BE DESTROYED. ALL LOCATIONS OUTSIDE THE PROGRAM AREA

IN THE PROGRAM FIELD AND ANY OTHER FIELD, IF SELECTED, ARE USED AS A TEST AREA. THE TEST AREA IS INITIALLY FILLED WITH HALTS AND REFILLED AFTER EVERY 4096 TEST INSTRUCTIONS HAVE BEEN EXECUTED. IF THE PROGRAM IS ALLOWED TO RELOCATE, IT WILL RELOCATE UP AND BACK WITHIN A 2K TO 4K MEMORY FIELD AND UP AND DOWN BETWEEN MEMORY FIELD IF MORE THAN 4K.

2.3 PREREQUISITE SOFTWARE  
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PDP-8A CPU TEST  
PDP-8A MEMORY TESTS  
KM8-A OPTION TEST #2 - IF PDP-8A OPTION BOARD #2 IN SYSTEM  
DKC8-AA OPTION TEST #1 - IF PDP-8A OPTION BOARD #1 IN SYSTEM

3.0 RESTRICTIONS  
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3.1 HARDWARE RESTRICTIONS  
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- A. MINIMUM OF 2K OF MEMORY TO A MAXIMUM OF 32K OF MEMORY
- B. IF A PDP-8A OPTION BOARD #1 IS TO BE USED WITH THE PROGRAM, THE OPTION BOARD MUST BE CONNECTED IN LOOP BACK FOR THE 12 BIT PARALLEL I/O AND THE SERIAL LINE UNIT.

3.2 SOFTWARE RESTRICTIONS  
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ALL THE SOFTWARE RESTRICTIONS LISTED BELOW PLUS FIELD AND MEMORY SIZE MUST BE ADHERED TO, OTHERWISE, THERE IS NO GUARANTY WHAT WILL HAPPEN TO THE PROGRAM.

- A. ONCE THE PROGRAM HAS BEEN STARTED, THE PROGRAM LOADER(S) WILL BE DESTROYED.
- B. ONCE THE PROGRAM HAS BEEN STARTED AND IT WAS INITIALIZED TO RUN WITH OR WITHOUT THE HARDWARE FRONT PANEL SWITCH REGISTER, THE PROGRAM CAN NOT BE RE-INITIALIZED UNLESS THE PROGRAM IS RELOADED.
- C. BEFORE EACH PROGRAM START, LOCATION 0021 IN THE PROGRAM FIELD MUST BE INITIALIZED FOR THE AMOUNT OF MEMORY TO BE TESTED, LOCATION 0021 INITIALLY IS PRESET TO 0001 (NO FRONT PANEL SWITCH REGISTER AND 2K OF MEMORY).
- D. ONCE THE PROGRAM HAS RELOCATED INTO ANOTHER MEMORY AREA, AND IT IS DESIRED TO CHANGE MEMORY SIZE, MEMORY SIZE CANNOT BE DECREASED BELOW THE 1K SEGMENT THAT THE PROGRAM IS LOCATED IN.
- E. IF THE FRONT PANEL SWITCH REGISTER WAS SELECTED, ALWAYS STOP THE PROGRAM BY SETTING THE SWITCH REGISTER TO 0400. THIS IS DONE TO INSURE THAT THE PROGRAM IS NOT IN THE PROCESS OF RELOCATING, FOR THOSE SYSTEMS WITHOUT A FRONT PANEL, IT IS BEST TO RELOAD THE PROGRAM.

F. IF THE PROGRAM WAS INITIALIZED TO ANY 3K FIELD, THE PROGRAM SHOULD ALSO BE RUN AT 2K. THE REASON FOR THIS IS THAT IN ANY 3K MEMORY FIELD, ADDRESSES 2000 TO 3777 WILL NOT BE TESTED.

#### 4.0 STANDARD TEST PROCEDURE

IF THE PDP-8A OPTION BOARD #1 IS TO BE USED IN CONJUNCTION WITH THE PROGRAM DO STEP A BELOW, OTHERWISE GO TO STEP B.

A. DO THE FOLLOWING STEPS FOR THE INITIAL HARDWARE SETUP OF THE PDP-8A OPTION BOARD #1:

1. TURN THE AC POWER OFF ON THE COMPUTER
2. UNPLUG THE PDP-8A OPTION BOARD #1 (M8316) FROM THE COMPUTER
3. PLUG THE QUAD EXTENDER INTO THE SLOT THE M8316 OCCUPIED
4. CONNECT BERG SOCKETS J4 AND J5 ON THE M8316 MODULE IN PARALLEL USING THE BC08R-01 CABLE. IN CABLING THE BC08R-01 CABLE, MAKE SURE THAT THE LETTERING IS FACING UPWARDS ON ONE END OF THE CABLE AND DOWNWARDS ON THE OTHER END.
5. SET ALL THE SWITCHES ON THE M8316 MODULE TO THE OFF POSITION, AND THEN SET S1-5, S1-6, AND S1-8 TO THE ON POSITION.
6. PLUG THE M8316 MODULE INTO THE QUAD EXTENDER.
7. TURN THE AC POWER BACK ON TO THE COMPUTER,
8. GO TO STEP B.

B. IF THE COMPUTER CONTAINS AT LEAST 4K OF MEMORY, LOAD THE PAPER TAPE, MAINDEC-08-DJEXB-A-PB, USING THE STANDARD BINARY LOADER TECHNIQUE AND THEN GO TO STEP D. IF THE COMPUTER CONTAINS LESS THAN 4K OF MEMORY, DEPOSIT THE APPROPRIATE RIM LOADER INTO THE LOCATIONS LISTED BELOW IN FIELD 0.

LOCATION	LOW SPEED RIM	HIGH SPEED RIM
0156	6032	6014
0157	6031	6011
0160	5357	5357
0161	6036	6016
0162	7106	7106
0163	7006	7006
0164	7510	7510
0165	5357	5374
0166	7006	7006
0167	6031	6011
0170	5367	5367
0171	6034	6016
0172	7420	7420
0173	3776	3776
0174	3376	3376
0175	5356	5357

C. PLACE THE PAPER TAPE, MAINDEC-08-DJEXB-A-PM, INTO THE DESIRED READER. "LOAD ADDRESS" TO 0156, PRESS "INIT" AND THEN "RUN". WHEN THE TAPE HAS BEEN LOADED, HALT THE COMPUTER AND GO TO THE NEXT STEP.

D. IF THE PDP-8A OPTION BOARD #1 IS TO BE USED IN CONJUNCTION WITH THE PROGRAM, DO THE ITEMS LISTED BELOW, IF THE PDP-8A OPTION BOARD #1 IS NOT USED, GO TO PARAGRAPH 4.2, RUN PDP-8A PROCESSOR EXERCISER.

1. WITHOUT TURNING POWER OFF ON COMPUTERS CONTAINING RAM MEMORY, TURN OFF THE TELETYPE AND UNPLUG THE TELETYPE CABLE FROM THE J3 BERG SOCKET ON THE M8316 MODULE IF CONNECTED.
2. NOW USING THE TERM1-POINT JUMPERS, JUMPER THE APPROPRIATE PINS ON THE J3 BERG SOCKET ON THE M8316 MODULE TO THE DESIRED LOOP BACK LISTED BELOW.
  - A. EIA LOOP BACK - PIN F TO PIN J AND PIN E TO PIN M OR USE THE EIA TEST CONNECTOR IF AVAILABLE.
  - B. 20MA LOOP BACK - PIN E TO PIN H, PIN K TO PIN KK, AND PIN S TO PIN AA,
3. GO TO PARAGRAPH 4.2, RUN PDP-8A PROCESSOR EXERCISER

4.1 CHANGING IOT CODES

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NOT APPLICABLE

4.2 RUN PDP-8A PROCESSOR EXERCISER

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THE PROGRAM MUST "ALWAYS" BE INITIALIZED AT ANY PROGRAM RESTART, ONCE THE PROGRAM IS INITIALIZED TO RUN WITH OR WITHOUT THE HARDWARE FRONT PANEL SWITCH REGISTER, IT CANNOT BE REINITIALIZED AGAIN UNLESS THE PROGRAM IS RELOADED.

IN ORDER TO FIND, INITIALIZE, START AND STOP THE PROGRAM DO THE FOLLOWING STEPS.

- A. IF THE PROGRAM WAS JUST LOADED, THE PROGRAM WILL RESIDE IN THE FIRST 2K OF FIELD 0 AND THE STARTING ADDRESS WILL BE 0200. IF THIS WAS THE CASE GO TO STEP B TO INITIALIZE THE PROGRAM, TO FIND THE PROGRAM AND STARTING ADDRESS OF THE PROGRAM, ONCE THE PROGRAM HAS BEEN STARTED, DO THE FOLLOWING STEPS.
  1. LOAD EXTENDED ADDRESS TO FIELD 0
  2. LOAD ADDRESS TO ADDRESS 0005 AND EXAMINE THAT LOCATION
  3. THE CONTENTS OF LOCATION 0005 WILL CONTAIN THE STARTING ADDRESS OF THE PROGRAM AND THE FIELD THAT THE PROGRAM IS LOCATED IN, THE CONTENTS OF ADDRESS 0005 WILL BE IN THE FOLLOWING FORMAT SA0F. SA EQUALS THE MOST SIGNIFICANT SIX BITS OF THE STARTING ADDRESS, F EQUALS THE FIELD THAT THE PROGRAM IS LOCATED IN, SA02 WILL BE THE NEW STARTING ADDRESS OF THE PROGRAM
  4. LOAD THE INSTRUCTION AND DATA FIELD TO THE FIELD THAT THE PROGRAM IS LOCATED IN (OBTAINED FROM STEP 3 ABOVE).
  5. GO TO STEP B TO INITIALIZE THE PROGRAM
- B. THE PROGRAM WHEN FIRST LOADED IS INITIALIZED TO THE FOLLOWING CONDITIONS; NO HARDWARE FRONT PANEL SWITCH REGISTER, NO PDP-8A OPTION BOARD #1 TESTING, AND MEMORY SIZE OF 2K. TO CHANGE THE INITIAL CONFIGURATION OR IF THE PROGRAM IS TO BE RESTARTED, LOAD ADDRESS TO 0021

IN THE PROGRAM FIELD, NOW DEPOSIT INTO THIS LOCATION  
THE HARDWARE CONFIGURATION AND MEMORY SIZE TO BE TESTED  
FROM THE TABLES BELOW.

HARDWARE CONFIGURATION BITS 0 AND 1 = LOCATION 0021

BIT 0=0 USE LOCATION 0020 AS A PSEUDO SWITCH REGISTER  
BIT 0=1 USE THE HARDWARE FRONT PANEL SWITCH REGISTER  
BIT 1=0 DO NOT TEST THE PDP-8A OPTION BOARD #1  
BIT 1=1 TEST THE PDP-8A OPTION BOARD #1

MEMORY SIZE = BITS 7-11 = LOCATION 0021

MEMORY LOC 21 MEMORY LOC 21 MEMORY LOC 21 MEMORY LOC 21

1K	N/A	9K	0010	17K	0020	25K	0030
2K	0001	10K	0011	18K	0021	26K	0031
*3K	0002	*11K	0012	*19K	0022	*27K	0032
4K	0003	12K	0013	20K	0023	28K	0033
5K	0004	13K	0014	21K	0024	29K	0034
6K	0005	14K	0015	22K	0025	30K	0035
*7K	0006	*15K	0016	*23K	0026	*31K	0036
8K	0007	16K	0017	24K	0027	32K	0037

\*REFER TO STEP F IN SOFTWARE RESTRICTIONS, PARAGRAPH 3.2.

IF BIT 0 OF LOCATION 0021 WAS SET TO A 0, LOAD  
ADDRESS TO LOCATION 0020 AND DEPOSIT INTO THIS  
LOCATION THE SWITCH REGISTER SETTING DESIRED  
(NORMALLY ALL ZEROES).

- C. TO START THE PROGRAM LOAD ADDRESS TO THE ADDRESS OBTAINED  
IN STEP A ABOVE, SET THE SWITCH REGISTER TO THE DESIRED  
SWITCH SETTING (NORMALLY ZEROES), NOW PRESS "INIT" AND  
THEN "RUN", THE PROGRAM SHOULD NOW RUN.
- D. TO STOP THE PROGRAM "ALWAYS" SET THE SWITCH REGISTER  
OR PSEUDO SWITCH REGISTER WHICHEVER SELECTED TO 0400,  
FAILURE TO DO THIS MAY DESTROY THE PROGRAM WHEN IT  
IS RESTARTED, THE PROGRAM WILL HALT AFTER 4096  
TEST INSTRUCTIONS HAVE BEEN EXECUTED IF THE  
SWITCH REGISTER WAS SET TO 0400. RESETTING THE  
SWITCH REGISTER AND PRESSING "INIT" AND THEN "RUN"  
WILL CAUSE THE PROGRAM TO CONTINUE TESTING.
- E. THE PROGRAM SHOULD RUN UNTIL STOPPED BY THE OPERATOR,  
IF THE PROGRAM HALTED AND THE SWITCH REGISTER WAS NOT  
SET TO 0400, REFER TO THE SECTION ON ERRORS.
- F. RUN THIS PROGRAM FOR 30 MINUTES. A PROGRAM PASS  
WILL RANGE BETWEEN 3 SECONDS TO 14 SECONDS DEPENDING  
ON MEMORY SIZE AND CYCLE TIME.
- G. TO RESTART THE PROGRAM, DO STEPS A, B AND C.

## 5.0 ERRORS

ALL ERRORS DETECTED BY THE PROGRAM WILL RESULT IN AN ERROR HALT, REFER TO THE SECTION ON ERROR HALTS TO DETERMINE IF THE ERROR WAS A FAILURE DUE TO A MEMORY REFERENCE INSTRUCTION, OPERATE INSTRUCTION, SERIAL LINE UNIT DATA ERROR, PARALLEL I/O DATA ERROR, ILLEGAL INTERRUPT, BATTERY EMPTY, INACTIVE DEVICE, OR A RELOCATION ERROR.

### 5.1 ERROR HALTS

TO DETERMINE WHAT TYPE OF ERROR WAS DETECTED BY THE PROGRAM, REFERENCE THE ERROR HALTS LISTED BELOW AND GO TO THE PARAGRAPH DESCRIBING THE ERROR AND FOR THE ERROR RECOVERY. ANY ERROR HALTS WHICH OCCUR AND DO NOT CORRESPOND TO ANY OF THE ADDRESSES LISTED BELOW, ARE CATASTROPHIC ERRORS, THESE ERRORS ARE PROBABLY DUE TO EXECUTION OF A INSTRUCTION TO THE WRONG ADDRESS OR FIELD. THE HEADERS FOR THE ERROR HALTS LISTED BELOW ARE DEFINED AS FOLLOWS:

MIR ERR =	MEMORY REFERENCE INSTRUCTION ERROR (AND-TAD-ISZ-DCA-JMS-JMP)
OPR ERR =	OPERATE INSTRUCTION ERROR
SLU D. ERR =	SERIAL LINE UNIT DATA ERROR
P. I/O D. ERR =	12 BIT PARALLEL I/O DATA ERROR
ILL. INT =	ILLEGAL INTERRUPT ERROR
INACT. DEV. =	INACTIVE DEVICE ERROR
BAT EMP =	BATTERY EMPTY
SAF0 =	STARTING ADDRESS AND FIELD PROGRAM IS LOCATED IN
ROLL UP =	RELOCATION ERROR WHILE ROLLING UP IN A MEMORY FIELD
ROLL DOWN =	RELOCATION ERROR WHILE ROLLING DOWN IN A MEMORY FIELD
SWAP FIELDS =	RELOCATION ERROR DURING RELOCATION TO ANOTHER FIELD
SAF0 =	SA IS THE STARTING ADDRESS OF THE PROGRAM (SA00) AND F IS DETERMINED BY THE OPERATOR FOR THE FIELD WHICH THE PROGRAM HALTED IN. SAF0 SHOULD AGREE WITH ADDRESS 0005 IN FIELD ZERO.

MRI ERR	OPR ERR	SLU D. ERR	P. I/O D. ERR	SAF0
1337	1742	3213	3251	02F0
1537	2142	3413	3451	04F0
1737	2342	3613	3651	06F0
2137	2542	4013	4051	10F0
2337	2742	4213	4251	12F0
2537	3142	4413	4451	14F0
2737	3342	4613	4651	16F0
3137	3542	5013	5051	20F0
3337	3742	5213	5251	22F0
3537	4142	5413	5451	24F0
3737	4342	5613	5651	26F0
4137	4542	6013	6051	30F0
4337	4742	6213	6251	32F0
4537	5142	6413	6451	34F0
4737	5342	6613	6651	36F0
5137	5542	7013	7051	40F0
5337	5742	7213	7251	42F0
5537	6142	7413	7451	44F0

ILL. INT	INACT DEV	BAT EMP	SAF0
3132	3323	3311	02F0
3332	3523	3511	04F0
3532	3723	3711	06F0
3732	4123	4111	10F0
4132	4323	4311	12F0
4332	4523	4511	14F0
4532	4723	4711	16F0
4732	5123	5111	20F0
5132	5323	5311	22F0
5332	5523	5511	24F0
5532	5723	5711	26F0
5732	6123	6111	30F0
6132	6323	6311	32F0
6332	6523	6511	34F0
6532	6723	6711	36F0
6732	7123	7111	40F0
7132	7323	7311	42F0
7332	7523	7511	44F0

#### RELOCATION ERRORS

ROLL UP	ROLL DOWN	SWAP FIELDS	SAF0
0233	3565	0466	N/A
0433	3765	0666	N/A
0633	4165	1066	N/A
1033	4365	1266	N/A
1233	4565	1466	N/A
1433	4765	1666	N/A
1633	5165	2066	N/A
2033	5365	2266	N/A
2233	5565	2466	N/A
2433	5765	2666	N/A
2633	6165	3066	N/A
3033	6365	3266	N/A
3233	6565	3466	N/A
3433	6765	3666	N/A
3633	7165	4066	N/A
4033	7365	4266	N/A
4233	7565	4466	N/A
4433	7765	4666	N/A

5.2 ERROR PRINTOUTS

NOT APPLICABLE

5.3 MEMORY REFERENCE INSTRUCTION ERRORS

THE PROGRAM WILL HALT AT ADDRESS XX37 FOR MEMORY REFERENCE INSTRUCTION (AND-TAD-ISZ-DCA-JMS-JMP) ERRORS. XX37 WILL BE A COMMON HALT FOR ALL ITEMS LISTED BELOW IN THE TABLE. RECORD THE CONTENTS OF THE AC OF THIS HALT INTO THE FIRST ITEM IN THE TABLE AND THEN PRESS "RUN", CONTINUE WITH THE SEQUENCE UNTIL EACH ITEM IN THE TABLE BELOW IS FILLED.

MEMORY REFERENCE INSTRUCTION INFORMATION TABLE

HALT #	ADDRESS	CONTENTS OF AC	DESCRIPTION
HALT #1	XX37		FIELD THAT PROGRAM PUT INSTRUCTION IN
HALT #2	XX37		INSTRUCTION RETURNED FROM THIS FIELD AFTER EXECUTION OF INSTRUCTION
HALT #3	XX37		EXPECTED PC RETURN FROM INSTRUCTION
HALT #4	XX37		ACTUAL PC RETURN FROM INSTRUCTION
HALT #5	XX37		ADDRESS WHERE INSTRUCTION WAS PLACED
HALT #6	XX37		TEST INSTRUCTION - THE INSTRUCTION WHICH WAS EXECUTED
HALT #7	XX37		REFERENCE ADDRESS - ADDRESS WHICH THE INSTRUCTION WILL REFERENCE, OR IF THE INSTRUCTION IS INDIRECT, THIS ADDRESS WILL CONTAIN THE INDIRECT ADDRESS.
HALT #8	XX37		INDIRECT ADDRESS - THIS IS THE INDIRECT ADDRESS WHICH THE TEST INSTRUCTION WILL REFERENCE, N/A FOR DIRECT ADDRESSING INSTRUCTIONS.
HALT #9	XX37		INITIAL MEMORY DATA - MEMORY DATA WHICH IS PUT INTO REFERENCE ADDRESS OR INDIRECT ADDRESS IF INSTRUCTION IS DIRECT OR INDIRECT, N/A FOR JMP OR JMS INSTRUCTIONS.
HALT #10	XX37		FINAL MEMORY DATA - CONTENTS OF REFERENCE ADDRESS OR INDIRECT ADDRESS AFTER EXECUTION OF INSTRUCTION. FOR A JMP INSTRUCTION, THIS NUMBER SHOULD BE EQUAL TO A CIF X, FOR

\* JMS INSTRUCTION, THIS NUMBER  
SHOULD EQUAL THE INSTRUCTION  
ADDRESS (HALT #5) PLUS 1.

HALT #11 XX37

THE CONTENTS OF THE AC  
BEFORE THE EXECUTION OF THE INSTRUCTION

HALT #12 XX37

THE CONTENTS OF THE AC AFTER  
THE EXECUTION OF THE TEST INSTRUCTION

HALT #13 XX37

THE STATE OF THE LINK, BEFORE  
THE EXECUTION OF THE INSTRUCTION

HALT #14 XX37

THE STATE OF THE LINK, AFTER THE  
EXECUTION OF THE TEST INSTRUCTION.

HALT #15 XX37

THE CONTENTS OF THE MQ BEFORE  
THE TEST INSTRUCTION IS EXECUTED

HALT #16 XX37

THE CONTENT OF THE MQ AFTER  
THE EXECUTION OF THE TEST INSTRUCTION.

THE IS THE END OF THE MEMORY REFERENCE INSTRUCTION ERROR INFORMATION.  
REFER TO MEMORY REFERENCE TEST INSTRUCTION SETUP SECTION,  
PARAGRAPH 5.4, TO DETERMINE THE TYPE OF ERROR.

TO LOOP ON THIS ERROR, SET THE SWITCH REGISTER OR  
PSEUDO SWITCH REGISTER WHICHEVER SELECTED TO 7000 AND  
PRESS "INIT" AND THEN "RUN", THE PROGRAM IS NOW IN A  
LOOP, LOOPING ON THE SAME CONDITIONS.

#### 5.4 MEMORY REFERENCE TEST INSTRUCTION SETUP

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TO DETERMINE THE TYPE OF ERROR, THE OPERATOR MUST UNDERSTAND  
THE TEST INSTRUCTION SETUP, THE TEST INSTRUCTIN SETUPS  
ARE BROKEN UP INTO GROUPS WHICH ARE LISTED AND DESCRIBED  
BELOW.

##### A. AND'S THROUGH DCA'S DIRECT ADDRESSING MODE

1. INSTRUCTION SETUP IS PUT IN SOME RANDOM FIELD
2. LOCATION 4 OF THIS RANDOM FIELD CONTAINS THE RETURN  
POINTER TO THE PROGRAM,
3. THE CONTENTS OF THE LINK, AC AND MQ CONTAINS SOME RANDOM NUMBER
4. THE PROGRAM JUMPS TO THE INSTRUCTION ADDRESS +1
5. INSTRUCTION ADDRESS +1 = CIF TO PROGRAM FIELD,
6. INSTRUCTION ADDRESS = THE TEST INSTRUCTION
7. INSTRUCTION ADDRESS +1 = JMS I 4 - RETURN TO PROGRAM
8. INSTRUCTION ADDRESS +2 = JMS I 4 - RETURN TO PROGRAM
9. REFERENCE ADDRESS = INITIAL MEMORY DATA = LOCATION THE INSTRUCTION  
WILL EXECUTE.

##### B. AND'S THROUGH DCA'S INDIRECT ADDRESS MODE

1. INSTRUCTION SETUP IS PUT IN SOME RANDOM FIELD
2. LOCATION 4 OF THIS FIELD CONTAINS THE RETURN POINTER TO THE PROGRAM
3. THE CONTENTS OF THE LINK, AC, AND MQ CONTAINS SOME RANDOM NUMBER
4. THE PROGRAM JUMPS TO THE INSTRUCTION ADDRESS +1
5. INSTRUCTION ADDRESS +1 = CIF TO PROGRAM FIELD

6. INSTRUCTION ADDRESS = THE TEST INDIRECT INSTRUCTION
7. INSTRUCTION ADDRESS +1 = JMS I 4 - RETURN TO PROGRAM
8. INSTRUCTION ADDRESS +2 = JMS I 4 - RETURN TO PROGRAM
9. REFERENCE ADDRESS = INDIRECT ADDRESS - THE ADDRESS THE INSTRUCTION WILL REFERENCED
10. INDIRECT ADDRESS = INITIAL MEMORY DATA - THE LOCATIONS THE INSTRUCTION WILL EXECUTE

C. JMP'S = DIRECT ADDRESSING MODE

1. INSTRUCTION SETUP IS PUT IN SOME RANDOM FIELD
2. LOCATION 4 OF THIS RANDOM FIELD CONTAINS THE RETURN POINTER TO THE PROGRAM,
3. THE CONTENTS OF THE LINK, AC, AND MQ CONTAIN SOME RANDOM NUMBER
4. THE PROGRAM JUMPS TO THE INSTRUCTION ADDRESS
5. INSTRUCTION ADDRESS = THE TEST JUMP INSTRUCTION
6. REFERENCE ADDRESS = CIF TO PROGRAM FIELD. TEST INSTRUCTION JUMPS TO HERE
7. REFERENCE ADDRESS +1 = JMS I 4 - RETURN TO PROGRAM
8. REFERENCE ADDRESS +2 = JMS I 4 - RETURN TO PROGRAM

D. JMP'S = INDIRECT ADDRESS MODE

1. INSTRUCTION SETUP IS PUT IN SOME RANDOM FIELD
2. LOCATIONS 4 OF THIS RANDOM FIELD CONTAINS THE RETURN POINTER TO THE PROGRAM,
3. THE CONTENTS OF THE LINK, AC, AND MQ CONTAINS SOME RANDOM NUMBER,
4. THE PROGRAM JUMPS TO THE INSTRUCTION ADDRESS
5. INSTRUCTION ADDRESS = THE TEST JMP INDIRECT INSTRUCTION
6. REFERENCE ADDRESS = CONTAINS THE INDIRECT ADDRESS
7. INDIRECT ADDRESS = CIF TO PROGRAM FIELD
8. INDIRECT ADDRESS +1 = JMS I 4 - RETURN TO PROGRAM
9. INDIRECT ADDRESS +2 = JMS I 4 - RETURN TO PROGRAM

E. JMS'S = DIRECT ADDRESS MODE

1. INSTRUCTION SETUP IS PUT IN SOME RANDOM FIELD
2. LOCATION 4 OF THIS FIELD CONTAINS THE RETURN POINTER TO THE PROGRAM.
3. THE CONTENTS OF THE LINK, AC, AND MQ CONTAINS SOME RANDOM NUMBER,
4. THE PROGRAM JUMPS TO THE INSTRUCTION ADDRESS
5. INSTRUCTION ADDRESS = THE TEST JMS INSTRUCTION
6. REFERENCE ADDRESS = SHOULD CONTAIN INSTRUCTION ADDRESS +1 AFTER EXECUTION OF TEST INSTRUCTION
7. REFERENCE ADDRESS +1 = CIF TO PROGRAM FIELD
8. REFERENCE ADDRESS +2 = JMS I 4 - RETURN TO PROGRAM
9. REFERENCE ADDRESS +3 = JMS I 4 - RETURN TO PROGRAM

F. JMS'S = INDIRECT ADDRESS MODE

1. INSTRUCTION SETUP IS PUT IN SOME RANDOM FIELD
2. LOCATION 4 OF THIS FIELD CONTAINS THE RETURN POINTER TO PROGRAM,
3. THE CONTENTS OF THE LINK, AC AND MQ CONTAINS SOME RANDOM NUMBER,
4. THE PROGRAM JUMPS TO THE INSTRUCTION ADDRESS
5. INSTRUCTION ADDRESS = THE TEST JMS INDIRECT INSTRUCTION
6. REFERENCE ADDRESS = INDIRECT ADDRESS
7. INDIRECT ADDRESS = SHOULD CONTAINS THE INSTRUCTION ADDRESS +1 AFTER EXECUTION OF INSTRUCTION
8. INDIRECT ADDRESS +1 = CIF TO PROGRAM FIELD
9. INDIRECT ADDRESS +2 = JMS I 4 - RETURN TO PROGRAM
10. INDIRECT ADDRESS +3 = JMS I 4 - RETURN TO PROGRAM.

## OPERATE INSTRUCTION ERRORS

THE PROGRAM WILL HALT AT ADDRESS XX42 FOR ALL OPERATE INSTRUCTION ERRORS. XX42 WILL BE A COMMON HALT FOR ALL IDEMS LISTED BELOW IN THE TABLE. RECORD THE CONTENTS OF THE AC OF THIS HALT INTO THE FIRST IDEM IN THE TABLE AND THEN PRESS "RUN". CONTINUE WITH THIS SEQUENCE UNTIL EACH IDEM IN THE TABLE BELOW IS FILLED.

### OPERATE INSTRUCTION INFORMATION TABLE

HALT #	ADDRESS	CONTENTS OF AC	DESCRIPTION
HALT #1	XX42		FIELD THAT PROGRAM PUT INSTRUCTION IN
HALT #2	XX42		INSTRUCTION RETURNED FROM THIS FIELD AFTER EXECUTION OF INSTRUCTION
HALT #3	XX42		EXPECTED PC RETURN FROM INSTRUCTION
HALT #4	XX42		ACTUAL PC RETURN FROM INSTRUCTION
HALT #5	XX42		ADDRESS WHERE INSTRUCTION WAS PLACED
HALT #6	XX42		TEST INSTRUCTION - THE INSTRUCTION WHICH WAS EXECUTED
HALT #7	XX42		THE CONTENTS OF THE AC BEFORE THE INSTRUCTION WAS EXECUTED
HALT #8	XX42		THE SIMULATED RESULTS OF THE AC, AS CALCULATED BY THE PROGRAM, OF WHAT THE AC SHOULD BE AFTER THE EXECUTION OF THE TEST OPERATE INSTRUCTION
HALT #9	XX42		THE CONTENTS OF THE AC AFTER THE EXECUTION OF THE TEST INSTRUCTION.
HALT #10	XX42		THE CONTENTS OF THE LINK BEFORE THE TEST OPERATE INSTRUCTION WAS EXECUTED,
HALT #11	XX42		THE SIMULATED RESULTS OF THE LINK AFTER THE TEST INSTRUCTION WAS EXECUTED AS CALCULATED BY THE PROGRAM
HALT #12	XX42		THE CONTENT OF THE LINK AFTER THE EXECUTION OF THE TEST INSTRUCTION
HALT #13	XX42		THE CONTENTS OF THE MQ BEFORE THE EXECUTION OF THE TEST INSTRUCTION
HALT #14	XX42		THE SIMULATED RESULTS OF THE MQ, AFTER EXECUTION OF THE TEST OPERATE INSTRUCTION AS CALCULATED BY THE PROGRAM.

## HALT #15

THE CONTENTS OF THE MQ AFTER  
THE EXECUTION OF THE TEST INSTRUCTION.

THIS IS THE END OF THE OPERATE INSTRUCTION ERROR INFORMATION.  
ERRORS ENCOUNTERED UNDER THIS SECTION MAY BE DUE TO THE  
EXECUTION OF THE TEST OPERATE INSTRUCTION OR THE SIMULATION  
OF TEST INSTRUCTION DONE BY THE PROGRAM. REFER TO PARAGRAPH  
5.6 FOR OPERATE TEST INSTRUCTION SETUP.

TO LOOP ON A OPERATE TEST INSTRUCTION ERROR, SET THE SWITCH  
REGISTER OR PSEUDO SWITCH REGISTER WHICHEVER SELECTED TO 7000  
AND PRESS "INIT" AND THEN "RUN". THE PROGRAM IS NOW  
IN A LOOP, LOOPING ON THE SAME CONDITIONS.

## 5.6 OPERATE TEST INSTRUCTION SETUP

TO DETERMINE THE TYPE OF ERROR, THE OPERATOR MUST UNDERSTAND  
THE TEST INSTRUCTION SETUP. THE OPERATE TEST INSTRUCTION  
SETUP IS LISTED BELOW.

- A. BEFORE THE EXECUTION OF THE TEST OPERATE INSTRUCTION, THE  
PROGRAM SIMULATES THE RESULTS OF THE EXECUTION OF  
THE TEST OPERATE INSTRUCTION UPON THE LINK, AC, AND MQ
- B. THE INSTRUCTION SETUP IS PLACED IN SOME RANDOM FIELD
- C. LOCATION 4 OF THIS RANDOM FIELD CONTAINS THE RETURN  
POINTER TO THE PROGRAM.
- D. THE CONTENTS OF THE LINK, AC AND MQ CONTAINS SOME RANDOM  
NUMBER.
- E. THE PROGRAM JUMPS TO THE INSTRUCTION ADDRESS +1,
- F. INSTRUCTION ADDRESS +1 = CIF TO PROGRAM FIELD
- G. INSTRUCTION ADDRESS +2 = THE TEST OPERATE INSTRUCTION
- H. INSTRUCTION ADDRESS +3 = JMS I 4 - RETURN TO PROGRAM.
- I. INSTRUCTION ADDRESS +4 = JMS I 4 - RETURN TO PROGRAM.

## 5.7 SERIAL LINE UNIT DATA ERRORS

THE PROGRAM WILL HALT AT ADDRESS XX13 FOR ALL SERIAL LINE  
UNIT DATA ERRORS. TO FIND OUT THE WORD THAT WAS  
TRANSMITTED AND THE WORD THAT WAS READ, DO THE FOLLOWING:

- A. LOAD ADDRESS TO XX32, WHERE XX IS THE MOST SIGNIFICANT  
SIX BITS OF THE SERIAL LINE UNIT DATA ERROR HALT  
ADDRESS. EXAMPLE - IF THE PROGRAM HALTED AT 3213, LOAD  
ADDRESS TO 3232.
- B. EXAMINE ADDRESS XX32 FOR THE WORD TRANSMITTED
- C. EXAMINE NEXT ADDRESS XX33 FOR THE WORD RECEIVED

- D. IF IT IS DESIRED TO LOOP ON THIS DATA PATTERN, DO THE FOLLOWING STEPS, OTHERWISE, GO TO STEP E TO CONTINUE TESTING.
1. LOAD ADDRESS TO THE ERROR HALT ADDRESS, CHANGE THE CONTENTS OF THIS ADDRESS FROM 7402 TO 7300,
  2. LOAD ADDRESS TO XX21, WHERE XX IS THE MOST SIGNIFICANT SIX BITS OF THE SERIAL LINE UNIT DATA ERROR HALT ADDRESS. EXAMPLE - IF THE PROGRAM HALTED AT 3213, LOAD ADDRESS TO 3221, NOW CHANGE XX21 FROM 3232 TO 7300,
  3. NOW SUBTRACT 0200 FROM THE MOST SIGNIFICANT 6 BITS OF THE ERROR HALT ADDRESS, USING THE NEW NUMBER AND ADDING 0045 ONTO IT TO FORM A NEW NUMBER OF XX45, LOAD ADDRESS TO XX45, EXAMPLE - IF PROGRAM HALTED AT 3213, LOAD ADDRESS TO 3045, NOW CHANGE XX45 FROM 3753 TO 7300,
  4. GO TO NEXT STEP E.
- E. TO CONTINUE TESTING OR IF STEPS 1, 2 AND 3 ABOVE WERE DONE TO LOOP ON SAME DATA PATTERN, LOAD ADDRESS TO ONE ADDRESS MORE THAN THE SERIAL LINE UNIT ERROR HALT ADDRESS (XX13+1=XX14), SET THE SWITCH REGISTER OR PSEUDO SWITCH REGISTER, WHICHEVER SELECTED, TO 1000 TO INHIBIT PROGRAM RELOCATION, DO NOT PRESS "INIT" BUT PRESS "RUN". IF STEPS 1, 2 AND 3 ABOVE WERE DONE, THE PROGRAM WILL RUN TRANSMITTING THE SAME DATA PATTERN AS DETECTED BY THE ERROR, IF THE ABOVE STEPS WERE NOT DONE, THE PROGRAM WILL RUN UNTIL ANOTHER DATA ERROR IS ENCOUNTERED.
- F. IF IT IS DESIRED TO CHANGE THE CONTENTS OF LOCATIONS LISTED IN STEPS 1, 2 AND 3 OF STEP D ABOVE, BACK TO THERE ORIGINAL CONTENTS, STOP THE COMPUTER BY SETTING THE SWITCH REGISTER TO 1400, NOW DO STEPS 1, 2 AND 3 IN STEP D ABOVE IN REVERSE, BY REPLACING THE 7300'S WITH THE ORIGINAL CONTENTS, NOW GO TO PARAGRAPH 4,2 TO RESTART THE PROGRAM,

#### 5.8 12 BIT PARALLEL I/O DATA ERRORS

---

THE PROGRAM WILL HALT AT ADDRESS XX51 FOR ALL 12 BIT PARALLEL I/O DATA ERRORS, TO FIND OUT THE WORD THAT WAS TRANSMITTED AND THE WORD THAT WAS READ, DO THE FOLLOWING STEPS.

- A. LOAD ADDRESS TO XX34, WHERE XX IS THE MOST SIGNIFICANT SIZE BITS OF THE 12 BIT PARALLEL I/O DATA ERROR HALT ADDRESS, EXAMPLE - IF THE PROGRAM HALTED AT ADDRESS 3251, LOAD ADDRESS TO 3234,
- B. EXAMINE ADDRESS XX34 FOR THE WORD TRANSMITTED
- C. EXAMINE NEXT ADDRESS XX35 FOR THE WORD READ
- D. IF IT IS DESIRED TO LOOP ON THIS DATA PATTERN, DO THE FOLLOWING STEPS, OTHERWISE GO TO STEP E TO CONTINUE TESTING.
  1. LOAD ADDRESS TO THE ERROR HALT ADDRESS (XX51) AND CHANGE THE CONTENTS FROM 7402 TO 7300,

2. LOAD ADDRESS TO XX76, WHERE XX IS THE MOST SIGNIFICANT SIX BITS OF THE ERROR HALT ADDRESS, EXAMPLE - IF PROGRAM HALTED AT ADDRESS 3251, LOAD ADDRESS TO 3276, NOW CHANGE THE CONTENT OF ADDRESS XX76 FROM 3234 TO 7300.
  3. NOW SUBTRACT 0200 FROM THE MOST SIGNIFICANT SIX BITS OF THE ERROR HALT ADDRESS, USING THIS NEW NUMBER AND ADDING 0047 ONTO IT TO FORM A NEW NUMBER OF XX47, LOAD ADDRESS TO XX47. EXAMPLE - IF PROGRAM HALTED AT 3251, LOAD ADDRESS TO 3047, NOW CHANGE XX47 FROM 3754 TO 7300.
  4. GO TO NEXT STEP E.
- E. TO CONTINUE TESTING OR IF STEPS 1, 2 AND 3 WERE DONE ABOVE TO LOOP ON SAME DATA PATTERN, LOAD ADDRESS TO ONE ADDRESS MORE THAN THE ERROR HALT ADDRESS (XX51+1=XX52), SET THE SWITCH REGISTER OR PSEUDO SWITCH REGISTER, WHICHEVER SELECTED, TO 1000 TO INHIBIT PROGRAM RELOCATION, DO NOT PRESS "INIT" BUT PRESS "RUN". IF STEPS 1, 2 AND 3 ABOVE WERE DONE, THE PROGRAM WILL RUN TRANSMITTING THE SAME DATA PATTERN AS DETECTED BY THE ERROR. IF THE ABOVE STEPS WERE NOT DONE, THE PROGRAM WILL RUN UNTIL ANOTHER DATA ERROR IS ENCOUNTERED.
- F. IF IT IS DESIRED TO CHANGE THE CONTENTS OF LOCATIONS LISTED IN STEPS 1, 2 AND 3 OF STEP D ABOVE, BACK TO THERE ORIGINAL CONTENTS, STOP THE COMPUTER BY SETTING THE SWITCH REGISTER TO 1400, NOW DO STEPS 1, 2 AND 3 IN STEP D ABOVE IN REVERSE, BY REPLACING THE 7300'S WITH THE ORIGINAL CONTENTS, NOW GO TO PARAGRAPH 4,2 TO RESTART THE PROGRAM.

#### 5.9 ILLEGAL INTERRUPT ERRORS

---

THE PROGRAM WILL HALT AT ADDRESS XX32 FOR A ILLEGAL INTERRUPT. THIS ERROR HALT WILL OCCUR IF THE PROGRAM INTERRUPTED WITHOUT ONE OF THE FOLLOWING FLAGS BEING SET ON THE PDP-8A OPTION 1 MODULE, SLU XMIT/RECEIVE FLAG, PARALLEL I/O DATA READY FLAG, REAL TIME CLOCK FLAG, OR AC LOW FLAG, THE ERROR MAY ALSO BE CAUSED BY A FLAG GETTING CLEARED ON A INTERRUPT, OR A FLAG FAILING TO SKIP IN THE SKIP CHAIN, TO RECOVER FROM THIS ERROR, RESTART THE PROGRAM (PARAGRAPH 4,2), IF THE ERROR STILL EXISTS, USE A SCOPE TO SEE WHAT OTHER FLAG IS SET BESIDES THE FLAGS ON THE PDP-8A OPTION BOARD#1 MODULE.

#### 5.10 INACTIVE DEVICE ERROR

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- A. THE PROGRAM WILL HALT AT XX23 FOR ANY PDP-8A OPTION BOARD #1 INACTIVE DEVICE ERROR, A INACTIVE DEVICE ERROR IS DEFINED AS ANY DEVICE (SERIAL LINE UNIT, PARALLEL I/O, REAL TIME CLOCK) THAT HAS NOT INTERRUPTED WITHIN A GIVEN TIME, TO FIND OUT WHAT DEVICE OR FLAG IS INACTIVE DO THE FOLLOWING:
1. SUBTRACT 100 FROM THE MOST SIGNIFICANT SIX BITS OF THE ERROR HALT ADDRESS AND THEN ADD 0026 ON IT TO FORM A ADDRESS OF XX26, NOW LOAD ADDRESS TO THIS NUMBER, THIS NUMBER WILL BE USED IN THE NEXT STEP, EXAMPLE - IF THE PROGRAM HALTED AT 3323, LOAD ADDRESS TO 3226.

2. NOW EXAMINE EACH OF THE FOLLOWING ADDRESSES, TO FIND OUT WHICH FLAG OR DEVICE IS INACTIVE, A LOCATION LISTED BELOW THAT IS SET TO ALL ONES IS THE INACTIVE DEVICE  
XX26 = SERIAL LINE UNIT'S TRANSMIT FLAG  
XX27 = SERIAL LINE UNIT'S RECEIVE FLAG  
XX30 = 12 BIT PARALLEL I/O DATA READY FLAG  
XX31 = REAL TIME CLOCK FLAG,
- B. TO RECOVER FROM THIS ERROR, RESTART THE PROGRAM (PARAGRAPH 4.2). IF THE PROBLEM STILL EXISTS, USE A SCOPE OR RUN THE PDP-8A OPTION TEST #1 PROGRAM.

#### 5.11 BATTERY EMPTY ERRORS

-----

THE PROGRAM WILL HALT AT ADDRESS XX11, IF A PDP-8A OPTION BOARD #2 IS INSTALLED AND IF THE SIGNAL AC LOW L IS LOW AND THE BATTERY EMPTY F/F IS SET, THIS ERROR IS CAUSED BY LOSING POWER TO THE COMPUTER, WHICH RESULTS IN DRAINING THE BATTERIES, TO RECOVER FROM THIS HALT, ALLOW THE BATTERIES TIME TO RECHARGE AND THEN RELOAD THE PROGRAM.

#### 5.12 RELOCATION ERRORS

-----

ALL ERRORS WHICH ARE ENCOUNTERED BY MOVING THE PROGRAM UP OR DOWN IN A MEMORY FIELD OR BETWEEN FIELDS ARE CATASTROPHIC AND THE PROGRAM MUST BE RELOADED.

#### 6.0 SWITCH REGISTER SETTINGS

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##### 6.1 NORMAL OPERATING SWITCHES

-----

SR2=1 (1000) = INHIBIT PROGRAM RELOCATION  
SR3=1 (0400) = HALT THE PROGRAM AT THE COMPLETION OF A PROGRAM PASS ONLY IF SR1=0

##### 6.2 ERROR RELATED SWITCHES

-----

SR0=1 (4000) = INHIBIT ERROR HALTS FOR MEMORY REFERENCE OR OPERATE ERRORS,  
SR1=1 (2000) = LOOP ON TEST CONDITIONS FOR MEMORY REFERENCE OR OPERATE INSTRUCTIONS,  
SR2=1 (1000) = INHIBIT PROGRAM RELOCATION

#### 7.0 REVISIONS

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FIRST SUBMISSION OF THE PROGRAM

## PROGRAM DESCRIPTION

THE 2K TO 32K PDP-8A PROCESSOR EXERCISER CHECKS THE EXECUTION OF ALL MEMORY REFERENCE AND OPERATE INSTRUCTIONS IN ALL FIELDS AND ADDRESSES. ALL INSTRUCTIONS, FIELDS, AND DATA ARE SELECTED FROM A RANDOM NUMBER GENERATOR, THE PROGRAM FILLS MEMORY WITH HALTS AFTER EVERY 4096 TEST INSTRUCTIONS HAVE BEEN EXECUTED. IF A OPTION #1 MODULE (M8316) IS SELECTED AND CONNECTED IN LOOP BACK MODE, THE MODULE WILL BE TESTED IN INTERRUPT MODE. DATA TRANSMISSIONS AND INTERRUPTS ARE TESTED ON BOTH THE SERIAL LINE UNIT AND THE 12 BIT PARALLEL I/O. ALSO THE REAL TIME CLOCK INTERRUPT IS TESTED. AFTER EVERY 4096 TEST INSTRUCTIONS HAVE BEEN EXECUTED, THE PROGRAM RELOCATES ITSELF, A PAGE AT A TIME, "UP AND DOWN" WITHIN ANY 2K TO 4K MEMORY FIELD. ONCE THE PROGRAM HAS RELOCATED "UP AND DOWN" WITHIN A SPECIFIC MEMORY FIELD, IT WILL RELOCATE UP INTO THE NEXT FIELD IF MORE THAN 4K OF MEMORY EXISTS AND THE NEXT FIELD CONTAINS AT LEAST 2K, THIS PROCEDURE WILL CONTINUE UNTIL THE LAST MEMORY FIELD IS ENTERED, THEN THE PROGRAM WILL RELOCATE ITSELF DOWN A FIELD AT A TIME UNTIL FIELD ZERO IS REACHED, THEN THE ENTIRE SEQUENCE IS REPEATED. REFER TO THE FOLLOWING PARAGRAPHS FOR MORE DETAILED INFORMATION.

REFER TO PARAGRAPH 5.4 FOR MEMORY REFERENCE TEST INSTRUCTION SETUP, THE PROGRAM VERIFIES THE EXECUTION OF ALL MEMORY REFERENCE INSTRUCTIONS (AND-TAD-ISZ-DCA-JMS-JMP) FOR THE FOLLOWING:

- A. THE INSTRUCTION RETURNED TO THE PROGRAM FROM THE CORRECT FIELD
- B. THE INSTRUCTION RETURNED TO THE PROGRAM FROM THE CORRECT ADDRESS
- C. CORRECT ADDRESSING MODES:
  - 1. DIRECT AND INDIRECT ADDRESSING
  - 2. SAME PAGE AND PAGE 0 ADDRESSING
  - 3. AUTO INDEX ADDRESSING
- D. THE CORRECT MEMORY AND AC DATA AFTER THE EXECUTION OF THE TEST INSTRUCTION.
- E. THE LINK DOESN'T CHANGE FOR THE FOLLOWING INSTRUCTIONS AND, ISZ, DCA, JMS AND JMP
- F. THE MQ DOESN'T CHANGE,

REFER TO PARAGRAPH 5.6 FOR OPERATE TEST INSTRUCTION SETUP, THE PROGRAM SIMULATES THE EXECUTION OF THE TEST "OPERATE" INSTRUCTION AND VERIFIES THE HARDWARE EXECUTION OF THAT SAME OPERATE INSTRUCTION FOR THE FOLLOWING:

- A. THE INSTRUCTION RETURNED TO THE PROGRAM FROM THE CORRECT FIELD
- B. THE INSTRUCTION RETURNED TO THE PROGRAM FROM THE CORRECT ADDRESS
- C. AC DATA RETRUNED EQUALS THE SIMULATED AC DATA
- D. THE LINK DATA RETURNED EQUALS THE SIMULATED LINK DATA
- E. THE MQ DATA RETURNED EQUALS THE SIMULATED MQ DATA

THE OPTION #1 MODULE IS EXERCISED IN INTERRUPT MODE IF SELECTED. THE PROGRAM WHEN FIRST STARTED AND AFTER EACH PROGRAM RELOCATION GENERATES RANDOM DATA FOR THE SERIAL LINE UNIT AND THE 12 BIT PARALLEL I/O. THE INTERRUPT ENABLE FLIP-FLOPS ARE THEN SET FOR THE SERIAL LINE UNIT, THE 12 BIT PARALLEL I/O, AND THE REAL TIME CLOCK; THE RANDOM DATA IS THEN TRANSMITTED ON THE SERIAL LINE UNIT AND THE 12 BIT PARALLEL I/O. THE PROGRAM THEN TURNS THE INTERRUPT ON AND JUMPS TO THE MAIN PART OF THE PROGRAM TO GENERATE AND TEST MEMORY REFERENCE AND OPERATE INSTRUCTIONS. WHEN A INTERRUPT OCCURS, THE PROGRAM DOES THE FOLLOWING:

- A. SAVE THE AC, LINK, AND THE INTERRUPTED PC
- B. THE PROGRAM DOES ONE OF THE FOLLOWING DEPENDING ON THE FLAG SET
  1. SLU XMIT FLAG = CLEAR XMIT FLAG. GO TO STEP C
  2. SLU RECV FLAG = CLEAR RECV FLAG=COMPARE XMIT DATA WITH DATA READ=GENERATE NEW RANDOM DATA AND TRANSMIT IT. GO TO STEP C.
  3. PARALLEL I/O = CLEAR DATA READY FLAG=CHECK DATA ACCEPTED IN=COMPARE DATA. GO TO STEP C.
  4. RTC FLAG = CLEAR REAL TIME CLOCK FLAG=GENERATE RANDOM DATA FOR PARALLEL I/O AND TRANSMIT IT. GO TO STEP C.
  5. AC LOW FLAG = CLEAR THE FLAG=CHECK BATTERY EMPTY F/F IF SET PROGRAM HALTS; IF NOT GO TO STEP C
- C. THE PROGRAM CHECKS ALL DEVICES TO BE ACTIVE, RESTORES THE LINK, THE AC, ISSUES A RMF INSTRUCTION AND RETURNS TO THE PROGRAM WHERE IT WAS INTERRUPTED FROM.
- D. WHEN THE PROGRAM IS READY TO BE RELOCATED, THE PROGRAM WAITS FOR THE FLAGS AND THEN TURNS THE INTERRUPT OFF.

9.0 FLOWCHARTS  
-----

NONE

10. LISTING  
-----  
ATTACHED

```

/2K TO 32K PDP-8A PROCESSOR EXERCISER
/
/MAINDEC=0B=DJEXB=A=L
/
/COPYRIGHT 1974, DIGITAL EQUIPMENT CORPORATION
/
/PROGRAMMERI BRUCE HANSEN

7421 MQL=7421
7701 ACL=7701
7604 LAS=7604
7402 HLT=7402
6160 SIMCLR=6160 /CLEAR SIMULATOR LOGIC
6244 RMF=6244
6035 KIE=6035
6087 CAF=6087 /CLEAR ALL FLAGS
6101 SBE=6101 /SKIP ON BATTERY EMPTY
6102 SPL=6102 /SKIP ON AC LOW
6103 CAL=6103 /CLEAR AC LOW F/F
6135 CLLE=6135 /SET INT ENA ON REAL TIME CLOCK IF DATA BIT 11 ON A 1
6136 CLCL=6136 /CLEAR REAL TIME CLOCK FLAG
6137 CLSK=6137 /SKIP ON REAL TIME CLOCK FLAG

6570 OBST=6570 /SKIP ON DATA ACCEPTED CLEAR IT AND DATA AVAILABLE
6571 OBSK=6571 /SKIP ON DATA READY
6572 OBRD=6572 /READ THE 12 BIT PARALLEL I/O REGISTER IN TO THE AC
6573 OBCF=6573 /CLEAR DATA READY-SET DATA ACCEPTED
6574 OBDT=6574 /LOAD THE 12 BIT PARALLEL I/O BUFFER AND TRANSMIT
6575 OBBE=6575 /SET PARALLEL I/O INTERRUPT ENABLE F/F
6576 OBCF=6576 /CLEAR PARALLEL I/O INTERRUPT ENABLE F/F
6577 OBSD=6577 /ISSUE A STROBE PULSE

0000 *0

0000 2000 Z
0001 6202 CIF 20/XX
0002 5403 JMP I INT
0003 3102 INT, INTERS
0004 2000 RETPNT, Z
0005 2200 STRFLD, BCN /MRI AND OPR RETURN POINTER
                           /STARTING ADDRESS AND FIELD PROGRAM IS LOCATED IN

0010 *10
0010 0000 AUTO10, Z
0011 0000 AUTO11, Z

0020 *20
0020 0000 SWITCH, Z
0021 0001 OP1SEL, 2001
0022 0000 OP2SEL, 2000


```

## /SWITCH REGISTER SETTINGS

```

/SR0#1 INHIBIT ERROR HALT
/SR1#1 LOOP ON ERROR OR TEST CONDITIONS
/SR2#1 INHIBIT PROGRAM RELOCATION
/SR3#1 HALT AFTER EXECUTION OF A PROGRAM PASS(4296 TEST INSTRUCTIONS)

```

```

/LOCATIONS 0005 TO 0177 WILL BE OVERLAYED ONCE THE PROGRAM HAS BEEN STARTED,
/IF THE PROGRAM HAS BEEN SETUP TO RUN WITH OR WITHOUT THE FRONT PANEL
/SWITCH REGISTER, IT CANNOT BE REINITIALIZED AGAIN. THE ONLY WAY TO
/CHANGE THE FRONT PANEL STATUS IS TO RELOAD THE PROGRAM AND REINITIALIZE IT.

/THE FOLLOWING ROUTINE WILL CHANGE "TAD (I) SAVSNR" TO LAS, IF THE
/OPERATOR SET BIT 0 OF LOCATION 21 TO A ONE.

```

```

0023 0000 PATCH: 0
0024 1136 TAD K5771
0025 3540 DCA I L0C200
0026 1137 TAD K5772
0027 3541 DCA I L0C201
0030 7340 CLA CLL CMA
0031 1023 TAD PATCH
0032 3023 DCA PATCH
0033 6162 SIMCLR
0034 1021 TAD OP1SEL /GET THE HARDWARE CONFIGURATION
0035 7700 SMA CLA /IS THE FRONT PANEL SWITCH REGISTER TO BE USED?
0036 5052 JMP PATCH1 /CHECK FOR ACT LINE
0037 1142 TAD MM6
0040 3143 DCA LASCNT
0041 1144 TAD LASTAB
0042 3145 DCA PATMOV
0043 1545 TAD I PATMOV
0044 3146 DCA PATMV1
0045 1147 TAD KLAS
0046 3546 DCA I PATMV1
0047 2145 ISE PATMOV
0050 2143 ISE LASCNT
0051 5043 JMP *5
0052 1022 PATCH1, TAD OP2SEL /CHECK FOR THE ACT LINE BIT
0053 7700 SMA CLA /IS IT SET ?
0054 5423 JMP I PATCH /NO RETURN TO THE PROGRAM
0055 1125 TAD OVLAY
0056 3010 DCA AUTO10
0057 1126 TAD MRIOVR
0060 3011 DCA AUTO11
0061 4103 JMS HOV0VR /GO OVERLAY FIRST 5 LOCATIONS OF ERROR
0062 1127 TAD OVRLY1
0063 3010 DCA AUTO10
0064 1130 TAD OPROVR
0065 3011 DCA AUTO11
0066 4103 JMS HOV0VR /GO OVERLAY FIRST 5 LOCATIONS OF ERROR
0067 1132 TAD K7610 /PUT SKIP UNCONDITIONALLY IN OPERR+1
0070 3531 DCA I OPRSKP /IN ORDER TO GO TO ERROPR
0071 1021 TAD OP1SEL /GET THE HARDWARE CONFIGURATION

```

/2K TO 32K PDP-8A PROCESSOR EXERCISER PAL10 V142 21-DEC-74 14:34 PAGE 1-2

```

0072 0134 AND CON37 /MASK OFF MEMORY SIZE
0073 1135 TAD MIN37 /CHECK TO SEE IF 32K SELECTED
0074 7640 SZA CLA /IS THERE 32K SELECTED?
0075 5502 JMP I PATCHC /NO, GO TO NEXT BUFFER TO GET NEXT OVERLAY
0076 7240 CLA CMA /SUBTRACT 1K FROM 32K
0077 1021 TAD OP1SEL
0100 3021 DCA OP1SEL /SAVE MEMORY SIZE AS 31K
0101 5502 JMP I PATCHC /CONTINUE THE OVERLAY FOR ACT LINE
0102 3600 PATCHC, PATCH2

0103 0000 MOVOVR, 0
0104 1133 TAD HS
0105 3145 DCA PATHOV
0106 1410 TAD I AUTO10
0107 3411 DCA I AUTO11
0108 2145 ISZ PATMOV
0111 5106 JMP ,+3
0112 5503 JMP I MOVOVR

0113 6002 AEROV1, IOF
0114 6272 CIF 70
0115 1767 1767
0116 5717 5717
0117 6520 6520

0122 6002 AEROV2, IOF
0121 6272 CIF 70
0122 1745 1745
0123 5712 5712
0124 6520 6520

0125 0112 OVRLAY, AEROV1+1
0126 1312 HRIOVR, ERROR-1
0127 0117 OVRLY1, AEROV2+1
0130 2305 OVROR, ERRORP+1

0131 2745 DPRSKP, DPRERR+1
0132 7610 K7610, SK CLA
0133 7773 HS, +5
0134 0037 CON37, 37
0135 7741 MIN37, +37

0136 5771 K5771, 5771
0137 5772 K5772, 5772
0140 0200 LOC200, SG4
0141 3201 LOC201, SG4+1
0142 7772 MN6, +6
0143 7772 LASCNT, +6
0144 0150 LASTAB, TABLAS
0145 0000 PATMOV, 0
0146 3000 PATMVI, 0
0147 7604 KLAS, LAS

0150 0252 TABLAS, LPONT

```

/2K TO 32K PDP-8A PROCESSOR EXERCISER PAL10 V142 21-DEC-74 14:34 PAGE 1-3

```

0151 2274 XCNT+3
0152 0301 ARRANG+4
0153 1342 LOOPSH
0154 2737 LPSH2
0155 2744 DPRERR

0200 *200
/
0202 0000 BGN, 0/JMS PATCH/JMP I XBGREN
0201 0000 0/JMS PATCH/JMP I XBGCON

0202 0000 CHANGE, 0
0203 1602 TAD I CHANGE /GET THE WORD TO MODIFY
0204 7450 SNA /IS IT EQUAL TO ZERO
0205 5602 JMP I CHANGE /YES ALL DONE MODIFYING
0206 1212 TAD SUBADD /SUBTRACT OR ADD 200
0207 3602 DCA I CHANGE /RESTORE THE MODIFIED WORD
0210 2202 ISZ CHANGE
0211 5203 JMP ,+6 /GET THE NEXT WORD TO MODIFY

0212 0000 SUBADD, 0
0213 0000 DIRFLG, 0
0214 0200 LOWLIM, 200
0215 7400 4400, +400
/
0216 1245 SWAP1, TAD SZPRG /ROUTINE TO SWAP PROGRAM UP
0217 3202 DCA CHANGE /SAVE PROGRAM SIZE
0220 1375 TAD XENOPR /MODIFIED END OF PROGRAM
0221 3246 DCA CNTR2
0222 1201 TAD M200
0223 1375 TAD XENDPR
0224 3247 DCA CNTR3
0225 1647 MOVUP, TAD I CNTR3 /GET ACTUAL END OF PROGRAM
0226 3646 DCA I CNTR2
0227 1647 TAD I CNTR3 /COMPARE THE WORD THAT WAS RELOCATED
0230 7041 CIA
0231 1646 TAD I CNTR2
0232 7640 SEA CLA
0233 7402 WLT /COMPARE ERROR DURING RELOCATION
0234 7040 CMA
0235 1247 TAD CNTR3
0236 3247 DCA CNTR3
0237 7040 CMA
0240 1246 TAD CNTR2
0241 3246 DCA CNTR2
0242 2202 ISZ CHANGE
0243 5225 JMP MOVUP
0244 5776 JMP I PSCTN

0245 4401 SZPRG, BGN=PRSEND+1
0246 0000 CNTR2, 0
0247 0000 CNTR3, 0
0250 0400 K400, 400
0251 7500 M200, +200

```

```

0292 1336 LPENT, TAD SAVSWR/LAS           /LOOP ON INSTRUCTION IF SR1 =1
0293 7004 RAL
0294 7700 SMA CLA
0295 5271 JMP XCNT
0296 1762 RESET, TAD I XINSTR
0297 8298 AND K40B
0298 7658 SNA CLA
0299 5763 JMP I XNTIND
0300 1762 TAD I XINSTR
0301 2014 AND LOWLIM
0302 7649 SEA CLA
0303 1764 TAD I XASAVA
0304 1765 TAD I XASAVB
0305 3766 DCA I XREFAD
0306 5767 JMP I XLOOP
0307 2247 XCONT, 156 CNTRS
0308 5770 JMP I RSCNTX
0309 4760 JMS I WAIT
0310 1336 TAD SAVSWR/LAS
0311 7006 RTL
0312 7004 RAL
0313 7712 SPA CLA
0314 7402 HLT
0315 1335 TAD SAVSWR/LAS
0316 7005 RTL
0317 7710 SPA CLA
0318 5775 JMP I RSCNT
0319 6224 ARRANG, RIF
0320 7041 CIA
0321 1755 TAD I XFLD
0322 7640 SZA CLA
0323 7248 CLA CMA
0324 7450 SNA
0325 1773 TAD I XUPERL
0326 3774 DCA I HIGHLM
0327 1213 TAD DIRFLG
0328 7640 SZA CLA
0329 5761 JMP I XROLBK
0330 1375 ROLLUP, TAD XENDPR
0331 7048 DMA
0332 1774 TAD I HIGHLM
0333 7650 SNA CLA
0334 5761 JMP I XROLBK
0335 5327 JMP SETFLG
0336 7249 CLA CMA
0337 3213 SETFLG, DCA DIRFLG
0338 1213 TAD DIRFLG
0339 7648 SEA CLA
0340 1215 TAD M400
0341 1214 TAD LOWLIM
0342 3212 DCA SUBADD
0343 5355 JMP ACHNG

```

/NO,RESET DATA IN REFERANCE ADDRESS  
 /YES,REGENERATE REFERANCE ADDRESS  
 /MASK OUT PAGE BIT

/BUMP PASS COUNTER  
 /IF OPTION 1 SELECTED WAIT FOR FLAGS  
 /CHECK SR3 TO HALT AFTER A PROGRAM PASS

/SR3=1 HALT AT END OF A PROGRAM PASS  
 /DO NOT RELOCATE IF SR2=1

/GET HOME FIELD  
 /IS IT EQUAL TO LAST FIELD  
 /NO,PROGRAM IS IN A 4K FIELD  
 /YES, IN LAST FIELD GET UPPER LIMITS

/SAVE UPPER LIMIT  
 /IS THE PROGRAM ROLLING UP OR ROLLING BACK

/THE PROGRAM IS ROLLING BACK  
 /GET END OF PROGRAM AND COMPARE IT  
 /WITH HIGH LIMITS

/THIS IS NEEDED FOR A 1K FIELD OTHER THAN 0  
 /NO,SET REVERSE FLAG  
 /-1 IF GOING REVERSE; 0 IF FORWARD  
 /ROLLING UP OR ROLLING BACK?

/ROLLING BACK IF DIRECTIONN FLAG = -1  
 /ROLLING UP IF FLAG = 0  
 /SAVE 200 OR -200

```

0336 0000 SAVSWR, 0
/
0337 0000 F2INIT, 0
0338 6201 CDF 00
0339 6224 RIF
0340 1340 TAD F2INIT+1
0341 7001 IAC
0342 3745 DCA I CIFFD0
0343 0001 CIFFD0, INT-2
0344 7248 CLA CMA
0345 1745 TAD I CIFFD0
0346 3352 DCA .#2
0347 4797 JMS I SETINT
0348 7402 HLT/CDF
0349 5737 JMP I F2INIT

```

/CHANGE DATA FIELD TO FIELD 2  
 /READ THE INSTRUCTION FIELD  
 /GET THE CDF INSTRUCTION  
 /MAKE IT A CIF TO PROGRAM FIELD  
 /PUT IT IN LOCATION 1 OF FIELD 2

/SET THE AC TO ALL ONE'S  
 /CHANGE CIF BACK TO CDF PROGRAM FIELD  
 /PUT IT IN NEXT LOCATION

TO PROGRAM FIELD  
 /RETURN TO PROGRAM

```

0350 1144 XFLD, FLDLIM
0351 2137 SETINT, INTSET
0352 3357 WAIT, WAITEN
0353 8401 XROLBK, ROLBAK
0354 2746 XINSTR, INSTR
0355 2625 XNTIND, NOTIND
0356 1146 XASAVA, ASAVA
0357 1147 XASAVB, ASAVB
0358 0747 XREFAD, REFAD
0359 8622 XLOOP, LOOP#1+1
0360 1081 RSCNTX, GENFLD
0361 3027 XBRGAN, BGNCON+1
0362 3026 XBGCON, BGNCON
0363 1550 XUPERL, UPLIM
0364 1145 HIGHLM, HGHLIM
0365 3576 XENDPR, PRSEND
0366 3424 RSCNT, STARTP
0367 0000 0

```

/

```

0368 0400 #400
0369 5351 JMP ACHNG

```

/

```

0370 1367 ROLBAK, TAD BEGIN
0371 7841 CIA
0372 1770 TAD I XLWLM
0373 7648 SZA CLA
0374 5771 JMP I RTFLGR
0375 3772 DCA I RTFLG
0376 1773 TAD I MAXFLD
0377 7650 SNA CLA
0378 5774 JMP I RTFLGF
0379 1300 TAD FLDFLG

```

/GET BEGINNING OF PROGRAM AND COMPARE IT  
 /WITH THE LOW LIMIT

/IS IT EQUAL  
 /NO,ROLL THE PROGRAM BACK  
 /SET DIRECTION FLAG TO FORWARD  
 /IS THE PROGRAM LIMIT ONLY 2K-4K

/YES, DO NOT SWAP BUT ROLL THE PROGRAM UP  
 /SWAP THE PROGRAM UP OR DOWN

```

0413 7640      SZA CLA
0414 5222      JMP SWAPON
0415 6224      SWAPUP, RIP
0416 1361      TAD K10
0417 7041      CIA
0418 1773      TAD I MAXFLD
0419 5753      JMP I CSHPUP
0420 6224      SHAPON, RIP
0421 7450      SNA
0422 5215      JMP SWAPUP
0423 1363      TAD M10
0424 7640      SZA CLA
0425 5232      JMP SFLOFG=1
0426 3308      DCA FLDFLG
0427 5236      JMP ,+5
0428 7242      CLA CNA
0429 3308      SFLOFG, DCA FLDFLG
0430 1360      TAD FLDFLG
0431 7640      SZA CLA
0432 1362      TAD M20
0433 3276      DCA NEWDFA+1
0434 6224      RIF
0435 1361      TAD K10
0436 3276      DCA NEWDFA+1
0437 1362      TAD B6201
0438 3287      DCA NEWDTF
0439 6224      RIF
0440 1362      TAD B6201
0441 6224      DCA SWPFLD
0442 1276      TAD NEWDTF
0443 1362      TAD B6201
0444 3287      DCA NEWDTF
0445 6224      RIF
0446 1362      TAD B6201
0447 3263      DCA SWPFLD
0448 1257      TAD NEWDTF
0449 3275      DCA NEWDTF
0450 1775      SWPUP, TAD I XSIZE
0451 3276      DCA NEWDTF
0452 1778      TAD I XLNLIM
0453 3384      DCA RETHR
0454 1784      TAD I RETHR
0455 7402      NEWDTF, HLT/CDF
0456 3794      DCA I RETHR
0457 7402      TAD I RETHR
0458 7041      CIA
0459 7402      SWPFLD, HLT/CDF
0460 1784      TAD I RETHR
0461 7640      SZA CLA
0462 7402      HLTFL, HLT
0463 2304      IS2 RETHR
0464 2276      IS2 NEWDTF
0465 5236      JMP NEWDTF-1
0466 2257      IS2 NEWDTF
0467 1257      TAD NEWDTF
0468 3276      DCA ,+2
0469 7402      NEWDTF, HLT/CDF
0470 7402      HLT/CDF
0471 5776      JMP I XGO

```

```

2580 8000      FLDFLG, 2
2581 8010      K10, 10
2582 7760      M20, =23
2583 7770      M10, =10
/
0504 2000      RETHR, 0
0505 3327      DCA FILALL
0506 6214      R0F
0507 3325      DCA RETFLD
0510 7402      HLT/CDF
0511 7701      AC_
0512 3756      DCA I RTM0
0513 7010      RAR
0514 3757      DCA I RTLINK
0515 1768      TAD I BINSTR
0516 7005      RTL
0517 7005      RTL
0518 0343      AND B7
0519 1324      TAD BDOTST
0520 3323      DCA ,+1
0521 8000      2
/
0524 5761      BDOTST, JMP I TSTINS
0525 0000      RETFLD, 8
0526 6201      B6201, 6201
/
ROUTINE TO FILL THE WHOLE FIELD WITH HALTS
0527 0000      FILALL, 2
0528 3304      DCA RETHR
0529 1754      TAD I XSTFLD
0530 1326      TAD B6201
0531 3340      DCA CDHLT1
0532 6224      RIF
0533 1326      TAD B6201
0534 3344      DCA CDHLT2
0535 1266      TAD HLTFL
0536 7402      CDHLT1, HLT/CDF
0537 3784      DCA I RETHR
0538 2384      IS2 RETHR
0539 0007      B7, 7
0540 7402      CDHLT2, HLT/CDF
0541 2755      IS2 I BLIMIT
0542 5337      JMP ,+7
0543 3727      JMP I FILALL
/
0551 e551      /
0552 4752      AACNNG, JMS I XCHNGE
/
0553 0202      XCHANGE, CHANGE
0554 1116      CSWPUP, DECSWP
0555 0247      XSTFLD, CNTRS
0556 1145      BLIMIT, HGHLIM

```

```

0556 2753 RTMDD, HOMONE
0557 2751 RTLINK, LTKON
0560 0746 SINSTR, INSTR
0561 1281 TSTING, ANDTST
0562 1223 TSTIN1, TADTST
0563 1234 TSTIN2, ISETST
0564 1255 TSTIN3, DCATST
0565 1267 TSTIN4, JNSTST
0566 1304 TSTIN5, JNPTST
0567 0280 BEGIN, BOV
0570 0214 XLMHIM, LOWLIM
0571 0236 RTFLGR, SETFLG=1
0572 0213 RTFLG, DIRFLG
0573 1144 MAXFLD, FLDLIM
0574 0227 RTFLGF, SETFLG
0575 0245 XSIZE, SZPRG
0576 3424 XGO, STARTP
0577 0000 Z

2680 4600 /
2680 5366 JMP ACHO
2681 3350 LOOPID, DCA INAD /SAVE THIS WORD AS INDIRECT ADDRESS
2682 1360 TAD K7770 /CHECK FOR AUTO-INDEX
2683 1347 TAD REFAD
2684 7510 SPA /WAS IT LESS THAN 10
2685 5211 JMP NOTAUT /YES, NOT AUTO-INDEX
2686 7161 CIA STL
2687 1343 TAD A7
2688 7630 SEL CLA /WAS IT WITHIN AUTO BOUNDARY
2689 7610 NOTAUT, SKP CLA /AND, NOT AUTO-INDEX
2690 7342 TAD INAD /AUTO INDEX, SUBTRACT 1 FROM INDIRECT ADDRESS
2691 1350 CLA CLL CMA
2692 3310 DCA SETRET /SAVE INDIRECT ADDRESS
2693 1354 TAD RANFLD
2694 1356 TAD K6281 /CHANGE TO A RANDOM DATA FIELD
2695 3220 DCA .+1
2696 7402 HLT/COF
2697 1318 TAD SETRET /GET INDIRECT ADDRESS
2698 3747 DCA I REFAD /PUT INDIRECT ADDRESS INTO REF ADD
2699 1350 TAD INAD /MAKE REFAD=INAD
2700 3347 DCA REFAD
2701 7330 NOTIND, CLA CLL CML RAR
2702 1346 TAD INSTR /WHAT TYPE OF INSTR
2703 7632 SEL CLA /IT WAS A JMP OR JMS
2704 5265 JHP JMPJMS
2705 1354 TAD RANFLD
2706 1356 TAD K6281
2707 3234 DCA .+1
2708 7402 HLT/COF /GET INITIAL MEMORY DATA AND PUT IT IN
2709 1351 TAD DATATH /REF ADD OR INDIRECT ADD FOR AND THROUGH DCA
2710 3747 DCA I REFAD /SUBTRACT 1 FROM INSTRUCTION ADDRESS
2711 7240 OPRINT, CLA CMA

```

```

0640 1345 TAD ADDRS /AND SAVE IT
0641 3344 DCA HOMCIF
0642 5224 RTF
0643 1357 TAD K6282 /SET UP HOME INSTRUCTION FIELD
2644 3744 DCA I HOMCIF /IN INSTRUCTION ADDRESS+.1 FOR AND=DCA
2645 7301 CLA CLL IAC
2646 1345 TAD ADDRS
2647 4310 JMS SETRET /SETUP RETURN, INSTR ADD+.1=4480 FOR AND=DCA
/LOCATION 0 CONTAINS RETURN POINTER
/PUT INSTRUCTION IN INSTRUCTION ADDRESS

0650 1346 NOTIJ, TAD INSTR /GET THE RANDOM HQ DATA
0651 3745 DCA I ADDRS /AND LOAD IT INTO THE HQ
2652 1355 TAD SAVLNK /SAFETY CLEAR THE AC IN CASE HQ DOESN'T
0653 7104 CLL RAL /MAKE UP A CIF TO A RANDOM FIELD
0654 1353 TAD HQDATA
0655 7421 HQW
0656 7200 CLA
0657 1354 TAD RANFLD
0658 1357 TAD K6282
0659 3263 DCA .+2 /GET THE AC DATA INTO THE AC
0660 1352 TAD DATAHR /D.F. HAS BEEN CHANGED NOW CHANGE I.F.
0661 7402 HLT/CIF /GO EXECUTE INSTRUCTION IN RANDOM FIELD
0662 1352 JMP I HOMCIF
0663 7402
0664 5744

/////////////////////////////////////////////////////////////////////////
//FOR AND'S THROUGH DCA'S DIRECTS THE INSTRUCTION SETUP IS AS FOLLOWS:
/
//SOME RANDOM FIELD
//LOCATION 4 OF THIS FIELD EQUALS RETURN POINTER TO PROGRAM FIELD
//THE AC EQUALS SOME RANDOM NUMBER
//INST ADD+.1= CIF TO PROGRAM FIELD
//INST ADD = TEST INSTRUCTION
//INST ADD+.1= JMS I 4
//INST ADD+.2= JMS I 4
/
//REF ADD = INITIAL MEMORY DATA, THIS IS THE LOC THE INST WILL REFERENCE
/////////////////////////////////////////////////////////////////////////
//FOR AND'S THROUGH DCA'S INDIRECTS THE INST SETUP IS AS FOLLOWS
/
//SOME RANDOM FIELD
//LOCATION 4 OF THIS FIELD EQUALS RETURN POINTER TO PROGRAM FIELD
//THE AC EQUALS SOME RANDOM NUMBER
//INST ADD+.1= CIF TO HOME FIELD
//INST ADD = TEST INSTRUCTION
//INST ADD+.1= JMS I 4
//INST ADD+.2= JMS I 4
/
//REF ADD = INDIRECT ADDRESS
/
//IND ADD = INITIAL MEMORY DAA
/////////////////////////////////////////////////////////////////////////
0665 1346 JMPJHS, TAD INSTR /GET THE INSTRUCTION
0666 7086 RTL /IS IT A JMP OR JMS?
0667 7780 SMA CLA

```

/2K TO 32K PDP-8A PROCESSOR EXERCISER PAL10 V142 21-DEC-74 14:34 PAGE 1-10

```

8670 7081 IAC
8671 1347 TAD REFAD
8672 3310 DCA SETRET
8673 1354 TAD RANDF
8674 1356 TAD K6281
8675 3276 DCA ,+1
8676 7422 HLT/CDF
8677 6224 RIF
8700 1357 TAD K6282
8701 3710 DCA ,1 SETRET
8702 7001 IAC
8703 1310 TAD SETRET
8704 4310 JHS SETRET
8705 1345 TAD ADDRS
8706 3344 DCA HOMCIF
8707 5250 JMP NOTJJ
/////////////////////////////////////////////////////////////////////////
//THE INSTRUCTION SETUP FOR JMP DIRECTS IS AS FOLLOWS:
//
//SOME RANDOM FIELD
//LOC 4 OF THIS FIELD EQUALS RETURN POINTER TO PROGRAM FIELD
//THE AC EQUALS SOME RANDOM NUMBER
//INST ADD =JMP INSTRUCTION
//
//REF ADD =CIF TO PROGRAM FIELD
//REF ADD+1 =JMS I 4
//REF ADD+2 =JMS I 4
/////////////////////////////////////////////////////////////////////////
//THE INSTRUCTION SETUP FOR JMP INDIRECTS IS AS FOLLOWS:
//
//SOME RANDOM FIELD
//LOC 4 OF THIS FIELD EQUALS RETURN POINTER TO PROGRAM FIELD
//THE AC EQUALS SOME RANDOM NUMBER
//INST ADD =JMP INDIRECT INSTRUCTION
//
//REF ADD =INDIRECT ADDRESS
//
//IND ADD =CIF TO PROGRAM FIELD
//IND ADD+1 =JMS I 4
//IND ADD+2 =JMS I 4
/////////////////////////////////////////////////////////////////////////
//THE INSTRUCTION SETUP FOR JMS DIRECTS IS AS FOLLOWS
//
//SOME RANDOM FIELD
//LOC 4 OF THIS FIELD EQUALS RETURN POINTER TO PROGRAM FIELD
//THE AC EQUALS SOME RANDOM NUMBER
//INST ADD =JMS DIRECT INSTRUCTION
//
//REF ADD =SOME UNKNOWN NUMBER
//REF ADD+1 =CIF TO PROGRAM FIELD
//REF ADD+2 =JMS I 4

```

/2K TO 32K PDP-8A PROCESSOR EXERCISER PAL10 V142 21-DEC-74 14:34 PAGE 1-11

```

//REF ADD+3 =JMS I 4
/////////////////////////////////////////////////////////////////////////
//THE INSTRUCTION SETUP FOR JMS INDIRECTS IS AS FOLLOWS
//
//SOME RANDOM FIELD
//LOC 4 OF THIS FIELD EQUALS RETURN POINTER TO PROGRAM FIELD
//THE AC EQUALS SOME RANDOM NUMBER
//INST ADD =JMS INDIRECT INSTRUCTION
//
//REF ADD =INDIRECT ADDRESS
//
//IND ADD =SOME UNKNOWN NUMBER
//IND ADD+1=CIF TO PROGRAM FIELD
//IND ADD+2=JMS I 4
//IND ADD+3=JMS I 4
/////////////////////////////////////////////////////////////////////////
//THIS ROUTINE SETS UP LOC 3 IN SOME FIELD FOR RETURN POINTER
//TO THE PROGRAM AND ALSO SETS UP THE JMS I 0'S AFTER THE EXECUTION OF THE
//INSTRUCTION,
8710 0000 SETRET, 0
8711 3362 DCA JMSLOC
8712 7381 CLA CLL, IAC
8713 1362 TAD JMSLOC
8714 3363 DCA JMSLOC
8715 1364 TAD KJMS
8716 3762 DCA I JMSLOC
8717 1364 TAD KJMS
8720 3763 DCA I JMSLOC
8721 1376 TAD JNSRET
8722 3761 DCA I FLCRET
8723 5710 JMP I SETRET
8724 4773 ERROR2: JMS I ZGETWO
8725 4770 JMS I YHALT /FINAL MEMORY DATA
8726 1352 TAD DATAHR
8727 4770 JMS I YHALT /AC DATA BEFORE EXECUTION OF INSTR
8728 1774 TAQ I BFIND
8731 4770 JMS I YHALT /AC DATA RETURNED
8732 1355 TAQ SAVLNK
8733 4770 JMS I YHALT /INITIAL LINK BEFORE EXEC OF INSTR
8734 1771 TAQ I FLINK /
8735 4770 JMS I YHALT /LINK AFTER EXEC OF INSTR
8736 1353 TAQ MODATA
8737 4770 JMS I YHALT /INITIAL MQ DATA
8740 1772 TAQ I FMODAT
8741 4770 JMS I YHALT /MQ DATA AFTER EXEC OF INSTR
8742 5775 JMP I ZCNT /BUMP COUNTER AND RETURN
8743 0007 47, 7
8744 0008 HOMCIF, 8 /ADDRESS OF THE HOME CIF
8745 0000 ADDRS, 8 /THE ADDRESS OF THE INSTRUCTION

```

/2K TO 32K PDP-8A PROCESSOR EXERCISER PAL10 V142 21-DEC-74 14134 PAGE 1-12

```

0746 0000 INSTR, 0
0747 0000 REFAO, 0
0750 0000 INDAO, 0
0751 0000 DATATH, 0
0752 0000 DATAHR, 0
0753 0000 MODATA, 0
0754 0000 RANFLD, 0
0755 0000 SAVLNK, 0
0756 6281 K6201, 6281
0757 6202 K6202, 6202
0760 7770 K7770, 7770
0761 0084 FLDRET, 4
0762 0000 JNSLOC, 0
0763 0000 JNSLOD, 0
0764 4484 KJMS, JMS I 4
/
0766 0766 /
0766 4767 ACHG, JMS I ARERNG /ROUTINE TO ULTER ADDRESSES
/
0767 2282 ARERNG, CHANGE
0770 1336 YHALT, HALT
0771 2751 FLINK, LINKDN
0772 2753 FNODAT, MODONE
0773 2112 ZGETND, GETNO
0774 2527 ZPIND, FILALL
0775 8252 ZQNT, LPQNT
0776 2584 JMSRET, RETHR
0777 0000 0
/
1000 1000 e1000
1000 5350 JMP A1CHG
/
1001 4763 GENFLD, JMS I ARANDY /GET A RANDOM FIELD
1002 0325 AND K70 /MASK WORD FOR FIELD BITS
1003 0327 AND FLDSMK /MASK WORD FOR FIELD
1004 1338 TAD CONFLD /CONSTRAINT WORD FOR FIELD
1005 0325 AND K70
1006 3756 DCA I FLDTRAN
1007 1756 TAD I FLDTRAN
1008 7041 CIA
1009 1344 TAD FLDLIM
1010 7510 SPA
1011 5281 JHP GENFLD
1012 5281 S2A CLA
1013 5281 /WITHIN LIMITS ?
1014 7640 S2A CLA
1015 7240 CLA CHA
1016 7450 SNA
1017 1767 TAD I XUPLIM
1018 3345 DCA HGHLIM
1019 1345 TAD HGHLIM
1020 7041 CIA
1021 1326 TAD ADD11
1022 3760 DCA I XBNDCN
1023 3760 /SAVE THE NUMBER FOR CHECKING BOUNDRIES
1024 3760

```

/2K TO 32K PDP-8A PROCESSOR EXERCISER PAL10 V142 21-DEC-74 14134 PAGE 1-13

```

1025 4763 MEMDAT, JMS I ARANDY /GENERATE RANDOM MEMORY DATA FOR AND>DCA
1026 0335 AND MDTMSK /MASK WORD FOR MEMORY DATA
1027 1336 TAD CONMOT /CONSTRAINT WORD
1028 3771 DCA I ADATAT /SAVE IT
1029 4763 ACODATA, JMS I ARANDY /GENERATE RANDOM AC DATA
1030 0337 AND ACOMSK /MASK WORD
1031 1348 TAD CONACD /CONSTRAINT WORD
1032 3772 DCA I ADATAH /SAVE THE AC DATA WORD
1033 1348 RAR /MOVE THE LINK INTO AC BIT 0
1034 3772 DCA I LNKS4V /SAVE THE LINK
1035 7810 DCA I LNKS4V /GENERATE RANDOM HQ DATA
1036 3774 GENMOD, JMS I ARANDY /MASK WORD FOR HQ DATA
1037 4763 AND HQOMSK /CONSTRAINT WORD FOR HQ DATA
1038 0341 TAD CONMOD /SAVE THE HQ DATA WORD
1039 1342 DCA I AMODAT /GENERATE RANDOM ADDRESS FOR INSTRUCTION
1040 3773 GENADD, JMS I ARANDY /MASK OFF ADDRESS BITS FOR THIS FIELD
1041 4763 AND HGHLIM /MASK WORD FOR INSTRUCTION ADDRESS
1042 3773 AND ADMMSK /CONSTRAINT WORD
1043 4763 TAD COVADR /IS IT WITHIN LIMITS
1044 0345 AND HGHLIM /NO, TRY AGAIN
1045 0331 AND ADRMSK /THIS IS THE INSTRUCTIONS ADDRESS
1046 1332 TAD COVADR
1047 4764 JMS I ABNRY1
1048 5243 JHP GENADD
1049 3766 DCA I AADDRS
1050 1766 TAD I AADDRS
1051 0325 AND CONST1
1052 3446 DCA ASAVA
1053 4755 GENINS, JMS I XGENT1 /SAVE PAGE BITS FOR FORMING REFERENCE ADDRESS
1054 4755 JMS I ABNRY1 /GENERATE RANDOM INSTRUCTION
1055 4755 JHP CONST1 /IS IT WITHIN LIMITS
1056 4755 /NO, TRY AGAIN
1057 5385 JMS I ASAME1 /COMPARE TO ADDRS
1058 4761 JMS I ASAME1 /STORE EQUAL OR TO CLOSE TRY AGAIN
1059 3770 DCA I AREFAD /PUT COF HOME FIELD INTO INSTRUCTION RETURN
1060 6214 RDF
1061 1323 TAD C6281
1062 3765 DCA I XRETHR
1063 1767 TAD I AINSTR
1064 0324 AND A400
1065 7650 SNA CLA
1066 4763 GENIND, JMS I ARANDY /PUT COF HOME FIELD INTO INSTRUCTION RETURN
1067 4763 AND HGHLIM /INSTR = INSTRUCTION TO TEST
1068 0345 AND INDMSK /WAS INSTR INDIRECT
1069 2333 TAD CONIND /NO, NOT INDIRECT GO SETUP TEST CONDITIONS
1070 7650 JMS I ANTINO /GENERATE RANDOM INDIRECT ADDRESS
1071 5775 AND HGHLIM /MASK OFF ADDRESS BITS FOR THIS FIELD
1072 4763 AND HGHLIM /MASK WORD FOR INDIRECT ADDRESS
1073 0345 TAD CONIND /CONSTRAINT WORD FOR INDIRECT
1074 2333 JMS I ABNRY1 /IS IT WITHIN BOUNDARIES
1075 1334 JMS I ABNRY1 /NO, TRY AGAIN
1076 4764 JMS I ASAME1 /COMPARE TO ADDRS
1077 5312 JHP CONST2 /TRY AGAIN
1078 4764 JMS I ASAME1 /TRY AGAIN
1079 5312 JHP CONST2 /TRY AGAIN
1080 4764 JMS I ASAME2 /TRY AGAIN
1081 5312 JHP CONST2 /TRY AGAIN
1082 4764 JMS I ASAME2 /TRY AGAIN
1083 5312 JHP CONST2 /TRY AGAIN
1084 5776 JHP I ALOPID /GO SETUP TEST CONDITIONS
/
1105 7600 CONST1, 7600
1106 1343 TAD CONFLG
1107 7648 S2A CLA
1110 5243 JHP GENADD

```

/2K TO 32K PDP-8A PROCESSOR EXERCISER PAL10 V142 21-DEC-74 14134 PAGE 1-14

```

1111 5255      JMP GENINS
1112 1343      CONST2, TAD CONFLG
1113 7710      SPA CLA
1114 5243      JMP GENADD
1115 5272      JMP GENINO
1116 7510      DECSWP, SPA
1117 5752      JMP I FLOFGR
1120 7650      SNA CLA
1121 5754      JMP I CHK1KF
1122 5753      JMP I FLOFGF
1123 6261      C6201, 6231
1124 8480      A4E0, 408
1125 0070      K70, 70
1126 0011      ADD11, 11
1127 7777      FLDNSK, 7777
1130 0000      CONFLD, 0
1131 7777      ADRNSK, 7777
1132 0000      CONADR, 0
1133 7777      INDMSK, 7777
1134 0000      CONIND, 0
1135 7777      MDTMSK, 7777
1136 0000      CONMDT, 0
1137 7777      ACDSK, 7777
1140 0000      CONACD, 0
1141 7777      MODNSK, 7777
1142 0000      CONHOD, 0000
1143 0000      CONFLG, 0
1144 0000      FLDLIM, 0
1145 0000      KGHLIH, 0
1146 0000      ASAVAL, 0
1147 0000      ASAVB, 0
1150          e1150
1150 4751      A1CHG, JMS I A1RRNG
1151 0202      A1RRNG, CHANGE
1152 0432      FLDGFR, SFLDGFG=1
1153 0433      FLDGFG, SFLDGFG
1154 2555      CHK1KF, FLCHK
1155 1601      XGENTI, INSGEN
1156 0754      FLDRAW, RANFLD
1157 1590      XUPLIM, UPRLIM
1160 1551      XBNDCN, BNDCON
1161 1463      ASAME1, SAME1
1162 1473      ASAME2, SAME2
1163 1461      ARANDY, RANDY
1164 1435      ABNRY1, BNDRY1
1165 0510      XRETHR, RETHR4
1166 3745      AADRS, ADRS
1167 3766      AINSTR, INSTR

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/2K TO 32K PDP-8A PROCESSOR EXERCISER PAL10 V142 21-DEC-74 14:34 PAGE 1-15

```

1170 3747      AREFAD, REFAD
1171 3751      ADATAT, DATAHTH
1172 0752      ADATAH, DATAHTH
1173 0753      ANDDAT, MQDATA
1174 3755      LNKSAV, SAVLNK
1175 3625      ANTIND, NOTIND
1176 0601      ALQPID, LOOPID
1177 3000      Z
1200          e1200
1200 5347      JMP BCHNG           /GO ULTER
1201 4755      ANDTST, JMS I TSTPC
1202 1775      TAD I BDATH
1203 0776      AND I BDATHR
1204 7041      CIA
1205 1772      TAD I DATFN
1206 7640      COMPAR, SEA CLA
1207 5313      JMP ERROR
1208 1760      TAD I LINKSV
1209 7041      CIA
1210 1761      TAD I LINKRT
1211 7640      SEA CLA
1212 5313      JMP ERROR
1213 1763      TAD I MODAT
1214 7041      CIA
1215 1762      TAD I DONEHQ
1216 7640      SEA CLA
1217 5313      JMP ERROR
1218 5765      JMP I BLPCNT
1223 4755      TADTST, JMS I TSTPC
1224 7340      CLA CLL CHA
1225 2775      AND I BDATH
1226 1776      TAD I BDATHR
1227 7041      CIA
1228 1772      TAD I DATFN
1229 7640      SEA CLA
1230 5313      JMP ERROR
1231 5765      JMP I BLPCNT
1234 7301      ISZTST, CLA CLL IAC
1235 1775      TAD I BDATH
1236 7650      SNA CLA
1237 7001      IAC
1238 4755      JMS I TSTPC
1239 1776      TAD I BDATHR
1240 7041      CIA
1241 1772      TAD I DATFN
1242 7640      SEA CLA
1243 5313      JMP ERROR
1244 1774      TAD I BREFAD
1245 3756      DCA I XBSAVA

```

1250 4757 JMS I XGETWD  
 1251 7041 CIA  
 1252 7001 IAC  
 1253 1775 TAO I BDATH  
 1254 5206 JMP COMPAR /DID ISE WORK  
 /  
 1255 4755 OCATST, JMS I TSTPC /CHECK PC FROM RETURN  
 1256 1774 TAO I BREPAD  
 1257 3756 DCA I XBSAVA  
 1260 4757 JMS I XGETWD  
 1261 7041 CIA  
 1262 1776 TAO I BDATHR  
 1263 7640 SZA CLA /DID DCA WORK  
 1264 5313 JMP ERROR /DCA FAILED  
 1265 1772 TAO I DATPN /DIO AC CLEAR ON DCA  
 1266 5206 JMP COMPAR /?  
 /  
 1267 4755 JMSTST, JMS I TSTPC /CHECK PC FROM RETURN  
 1270 1776 TAO I BDATHR  
 1271 7041 CIA  
 1272 1772 TAO I DATFN  
 1273 7643 SZA CLA /DID JMS CHANGE AC  
 1274 5313 JMP ERROR /JMS CHANGED AC  
 1275 1774 TAO I BREPAD  
 1276 3756 DCA I XBSAVA  
 1277 4757 JMS I XGETWD  
 1300 7041 CIA  
 1301 7001 IAC  
 1302 1766 TAO I BADRS  
 1303 5206 JMP COMPAR /DID JMS WORK  
 /  
 1304 4755 JMPTST, JMS I TSTPC /CHECK PC FROM RETURN  
 1305 1776 TAO I BDATHR  
 1306 7041 CIA  
 1307 1772 TAO I DATFN  
 1310 5206 JMP COMPAR /DID JMP AFFECT THE AC  
 /  
 1311 0000 POSAVE, 0  
 1312 0200 0200, 200  
 1313 5342 ERROR, JMP LOOPSW /CHECK SR0 TO INHIBIT ERROR HALT  
 1314 1764 TAO I XRNFLD  
 1315 4336 JMS HALT /FIELD THAT INSTRUCTION WAS PUT IN  
 1316 1773 TAO I XRETFL  
 1317 4336 JMS HALT /PROGRAM RETURNED FROM THIS FIELD  
 1320 1311 TAO POSAVE  
 1321 4336 JMS HALT /EXPECTED PC RETURN  
 1322 1754 TAO I RETURN  
 1323 4336 JMS HALT /ACTUAL PC RETURN  
 1324 1766 TAO I BADRS  
 1325 4336 JMS HALT /INSTRUCTION ADDRESS  
 1326 1767 TAO I FINSTR  
 1327 4336 JMS HALT /INSTRUCTION  
 1330 1767 TAO I FINSTR

1331 0312 AND C200  
 1332 7640 SZA CLA  
 1333 1770 ERRPSR, TAO I ZASAVA  
 1334 1771 TAO I ZASAVB  
 1335 5753 JMP I XERROR /GET REST OF ERROR INFORMATION  
 /  
 1336 0000 HALT, 0  
 1337 7482 HALT  
 1340 7200 CLA  
 1341 5736 JMP I HALT /  
 /  
 /INHIBIT ERROR HALT IF SR0 IS SET TO A ONE  
 1342 1751 LOOPSH, TAO I SWRSAV/LAS /CHECK THE SWITCH REGISTER  
 1343 7700 SMA CLA /IS IT SET  
 1344 5314 JMP ERROR+1 /NO GO HALT ON ERROR WITH INFO IN AC  
 1345 5752 JMP I ERRRET /GO CHECK LOOP ON INSTRUCTION SWITCH  
 /  
 1347 \*1347 /  
 1347 4750 BOHNG, JMS I BRERNG /  
 1350 0202 BRERNG, CHANGE  
 1351 0336 SWRSAY, SAVSR  
 1352 0252 ERRRET, LPONT  
 1353 1415 XERROR, ERROR1  
 1354 2504 RETURN, RETHR  
 1355 2873 TSTPC, PCST  
 1356 2145 XBSAVA, BSAVA  
 1357 2112 XGETWD, GETWD  
 1360 0755 LINKSV, SAVLNK  
 1361 2751 LINKRT, LINKDN  
 1362 2753 DONEQ, HDONE  
 1363 0753 MODAT, HDATA  
 1364 0754 XRNFLD, RANFLD  
 1365 0252 BLPCNT, LPONT  
 1366 2745 BADRS, ADDRS  
 1367 2746 FINSTR, INSTR  
 1370 1146 ZASAVA, ASAVA  
 1371 1147 ZASAVB, ASAVB  
 1372 8527 DATPN, FILALL  
 1373 8525 XRETFL, RETFLD  
 1374 8747 BREPAD, REPAD  
 1375 0751 BDATH, DATAFH  
 1376 0752 BDATHR, DATAHR  
 1377 0000 /  
 1400 \*1400 /  
 1400 5362 JMP COHNG /  
 1401 0000 RANDY, 0  
 1402 7301 CLA CLL IAE  
 1403 1343 TAO RAND

```

1404 1344      TAD RAN2
1405 7186      CLL RTL
1406 3343      DCA RANI
1407 1344      TAD RAN2
1408 7822      RTR
1409 1343      TAD RANI
1410 3344      DCA RAN2
1411 1344      RANDY1: TAD RAN2
1412 5601      JMP I RANDY
/
1413 3774      ERROR1: DCA I CREFAD
1414 1774      TAD I CREFAD
1415 4765      JMS I XHALT      /REFERENCE ADDRESS
1416 1771      TAD I ZINDAO
1417 4765      JMS I XHALT      /INDIRECT ADDRESS IF ANY
1418 1767      TAD I CDATAT
1419 4765      JMS I XHALT      /INITIAL MEMORY DATA
1420 1756      TAD I ZINSTR
1421 8347      AND C400
1422 7650      SNA CLK
1423 5232      JMP ,+3
1424 1771      TAD I ZINDAO
1425 3774      DCA I CREFAD
1426 1774      TAD I CREFAD
1427 3770      DCA I ZBSAVA
1428 5772      JMP I XERR2      /GO GET REST OF INFORMATION

/THIS SECTION OF THE SUBROUTINE CHECKS FOR ILLEGAL ADDRESSES WHICH
ARE AS FOLLOWS: 0000 - 0006 AND UPPER TEST AREA LIMIT, +1 AND -2.

1430 0000      BNDRY1: 0
1431 3354      DCA CSAVB
1432 1384      TAD CSAVB      /GET THE NUMBER
1433 1345      TAD MM7      /SUBTRACT 7 FROM IT
1434 7100      CLL      /CLEAR OUT THE LINK
1435 1351      TAD BNDCON      /ADD IN BOUNDARY CONSTANT=6012,4012,2012,2012
1436 7630      SEL CLA      /ILLEGAL ADDRESS, RETURN TO RANDOM NUMBER GENERATOR
1437 5635      JMP I BNDRY1

/THIS SECTION OF SUBROUTINE CHECKS FOR ILLEGAL ADDRESS WHICH ARE
/ THE PROGRAM AREA -3 TO PROGRAM END +1

1440 7346      BNDOOK1: CLA CLL CMA RTL
1441 1376      TAD PRBCG
1442 7041      CIA
1443 1354      TAD CSAVB
1444 7510      SPA
1445 5297      JMP BNDOOK2
1446 7161      CIA STL
1447 1352      TAD PRCSIZ
1448 7620      SNL CLA
1449 5635      JMP I BNDRY1
1450 2235      BNDOOK2: ISZ BNDRY1

/BNDOOK2, ISZ BNDRY1

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1451 0354      CLA CLL CMA
1452 5635      AND CSAVB
1453 5635      JMP I BNDRY1
/
1454 0000      SAME1: 0
1455 3355      DCA CSAVC
1456 1775      TAD I CADORS
1457 3353      DCA CSAVA
1458 4303      JMS TSAME
1459 2263      ISZ SAME1
1460 1355      TAD CSAVC
1461 5663      JMP I SAME1
/
1462 0000      SAME2: 0
1463 3355      DCA CSAVC
1464 1774      TAD I CREFAD
1465 3353      DCA CSAVA
1466 4303      JMS TSAME
1467 2273      ISZ SAME2
1468 1355      TAD CSAVC
1469 5673      JMP I SAME2
/
1470 0000      TSAME: 0
1471 7344      CLA CLL CMA RAL
1472 1355      TAD CSAVC
1473 7041      CIA
1474 1353      TAD CSAVA
1475 7510      SPA
1476 5320      JMP INSOK
1477 7161      CIA STL
1478 1386      TAD CS
1479 7822      SNL CLA
1480 2303      ISZ TSAME
1481 7420      SNL
1482 2303      ISZ TSAME
1483 7300      INSOK, CLA CLL
1484 5703      JMP I TSAME
/
1485 0000      LIMITS: 0
1486 1921      TAD OP1SEL      /GET MEMORY SIZE FROM HARDWARE CONFIGURATION
1487 0346      AND K37      /MASK OFF MEMORY BITS
1488 7104      CLL RAL
1489 3350      DCA UPRLIM
1490 1350      TAD UPRLIM
1491 0360      AND C70
1492 3773      DCA I XFIDLM
1493 1350      TAD UPRLIM
1494 0357      AND C7
1495 7112      CLL RTR
1496 7822      RTR
1497 1361      TAD C1777
1498 3358      DCA UPRLIM
1499 1828      TAD SWITCH
1500 3764      DCA I SAVESW

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1542 5722 JMP I LIMITS

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1543 1234 RAN1, 1234
1544 5678 RAN2, 5678
1545 7771 MH7, 7
1546 0837 K37, 37
1547 0408 C408, 408
1550 0008 UPRLIN, 0
1551 0008 BNDCON, 0
1552 3402 PRGSIZ, PRGEND=4=BSN
1553 0000 CSAVA, 0
1554 0000 CSAVB, 0
1555 0000 CSAVC, 0
1556 0005 CB, 0005
1557 0007 C7, 7
1558 0078 C78, 78
1561 1777 C1777, 1777
/
1562 *1562
1562 4763 CCHNG, JMS I CRERNG /ROUTINE TO ULTER
/
1563 2222 CRERNG, CHANGE
1564 0336 SAVESW, SAVSWR
1565 1336 XHALT, HALT
1566 2746 ZINSTR, INSTR
1567 0751 COATAT, DATATH
1570 2145 BBSAVA, BSVA
1571 0750 ZINDAD, INDAD
1572 0724 XERR2, ERROR2
1573 1144 XFLDLH, FLDLIM
1574 0747 CREFAD, REFDAD
1575 0745 CADDR3, ADDRS
1576 0200 PRGBG, BGV
1577 0000 0
/

```

/RANDOM OPERATES=GROUP 1 + GROUP2 + AND MQ OPERATES

1602 \*1600
1602 5347 JMP FCHNG

```

1601 0000 INSGEN, 0 /ROUTINE TO GENERATE A RANDOM INSTRUCTION
1602 4755 JMS I BRANDY /GO GENERATE A RANDOM NUMBER
1603 0242 AND INSMASK /MASK WORD FOR INSTRUCTION
1604 1243 TAD CONINS /CONSTRAINT WORD FOR INSTRUCTION
1605 3754 DCA I EINSTR /SAVE THE INSTRUCTION
1606 6201 CDF 00 /CHANGE DATA FIELD TO FIELD 0
1607 6224 RIF /READ THE INSTRUCTION FIELD
1610 1356 TAD START /GET THE STARTING ADDRESS
1611 3612 DCA I ADDR55 /PUT FIELD AND STARTING ADDRESS INTO LOC 5

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1612 0005 ADDR55, STRFLD /ADDRESS 5 OF FIELD 0 = STARTING ADDRESS AND PRG FIELD
1613 6224 RIF /READ THE INSTRUCTION FIELD
1614 1206 TAD ADDRESS-4/GET THE CDF INSTRUCTION
1615 3216 DCA ,+1 /PUT CDF TO PROGRAM FIELD IN NEXT LOCATION
1616 7402 HLT/CDF /CHANGE OF BACK TO PROGRAM FIELD
1617 1754 TAD I EINSTR /CHECK TO SEE IF IT WAS A IOT
1620 0244 AND K7000
1621 1245 TAD M6000
1622 7450 SNA
1623 0222 JMP INSGEN+1 /IT WAS A IOT REGENERATE A NEW INSTRUCTION
1624 1244 TAD K7000 /IS IT AN OPERATE INSTRUCTION
1625 7650 SNA CLA
1626 5256 JMP OPRBGN /YES IT WAS AN OPERATE
1627 1351 TAD MRIPNT /GET THE RETURN POINTER FOR MRI INSTRUCTIONS
1630 3793 DCA I EJMSRT /SAVE IT
1631 1754 TAD I EINSTR /NOT A IOT OR OPERATE
1632 0246 AND K177 /CREATE A REFERENCE ADDRESS
1633 3781 DCA I AASAVB
1634 1754 TAD I EINSTR /GET THE INSTR
1635 0247 AND A208 /PAGE ZERO OR SAME PAGE
1636 7640 SNA CLA
1637 1760 TAD I AASAVA
1640 1761 TAD I AASAVB
1641 5601 JMP I INSGEN /RETURN AND CHECK IT

1642 7777 INSHSK, 7777
1643 0000 CONINS, 0
1644 7000 K7000, 7000
1645 2000 M6000, +6000
1646 0177 K177, 177
1647 0200 A200, 200
1650 2400 B400, +00
1651 0014 A14, 14
1652 7764 NEG14, +14
1653 0001 A1, 1
1654 0200 BP0, 0
1655 7721 K7721, 7721

1656 1352 OPRBGN, TAD OPRPNT /GET THE RETURN POINTER FOR DPR INSTRUCTIONS
1657 3753 DCA I EJMSRT /SAVE IT
1660 1754 TAD I EINSTR
1661 0250 AND B400
1662 7640 SNA CLA
1663 5272 JMP ILLOP2
1664 1754 TAD I EINSTR /OP1-CHECK BITS 8 AND 9 TO BE ON A ONE
1665 0251 AND A14
1666 1252 TAD NEG14
1667 7630 SNA CLA
1670 0202 JMP INSGEN+1 /ILLEGAL-REGENERATE A NEW INSTRUCTION
1671 5306 ILLM043 /GO SETUP RANDOM AQ AND MQ DATA

1672 1754 ILLOP2, TAD I EINSTR /IS THE INSTR A MQ OR OP2 INSTR
1673 0253 AND A1
1674 7640 SNA CLA
1675 5303 JMP ILLNO /INSTR IS A MQ INSTR CHECK FOR ILLEGAL INSTR

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1676 1754 TAD I EINSTR /IS THE INSTR A OSR OR HLT
1677 0254 AND BP6
1700 7440 SEA
1701 5202 JNP IN8GEN+1 /INSTR IS A OSR OR HLT REGENERATE
1702 5306 JNP ILLMQ+3 /GO SET UP SIMULATED AC DATA AND HQ
1703 1754 ILLHQ: TAD I EINSTR /GET THE INSTRUCTION
1704 8295 AND K7721 /MASK OUT FOR LEGAL HQ INSTRUCTIONS
1705 3754 DCA I EINSTR /AND SAVE IT

1706 1762 TAD I XDATAH
1707 3763 DCA I XSINAC /PUT INITIAL WORD IN SIMULATED AC
1710 1764 TAD I XSVLNK
1711 3765 DCA I XSMLNK /PUT INITIAL LINK IN SIMULATED LINK
1712 1757 TAD I INTMQD /GET THE RANDOM HQ DATA
1713 3766 DCA I XSIMHQ /PUT INITIAL HQ DATA IN SIMULATED HQ
1714 7326 CLA CLL RTL /SET UP INSTRUCTION RETURN POINTER
1715 1773 TAD I OADDRS /GET THE INSTRUCTION ADDRESS AND ADD 2
1716 3767 DCA I XEXPRT /SET UP EXPECTED RETURN UNLESS A SKIP
1717 6214 RDP /READ THE DATA FIELD
1720 1333 TAD 06281 /ADD IN THE CDP INSTRUCTION
1721 3774 DCA I XRTOPF /SET UP A LOC TO RETURN TO OWN DATA FIELD
1722 1754 TAD I EINSTR /IS THE INSTR A OP1 OR OP2
1723 0250 AND B400
1724 7650 SNA CLA
1725 5770 JMP I XSHOP1 /OP1 GO SIMULATE THE INSTRUCTION
1726 1754 TAD I EINSTR /IS THE INSTR A HQ INSTR
1727 0253 AND AL
1730 7650 SNA CLA
1731 5771 JMP I XSMOP2 /OP2 GO SIMULATE THE INSTRUCTION
1732 5772 JMP I XSMQI /HQ GO SIMULATE THE HQ INSTR

1733 6201 06281, 6201

1734 1766 OPERR1: TAD I XSIMHQ /GET THE SIMULATED HQ
1735 4341 JMS HLTOPR
1736 1775 TAD I GHQDN /GET THE FINAL HQ
1737 4341 JMS HLTOPR
1740 5776 JMP I GLPSW2 /GO LOOK AT SRB TO LOOP ON INSTR

1741 2080 HLTOPR, 2
1742 7402 HLT
1743 7300 CLA CLL
1744 5741 JMP I HLTOPR
/

1747 #1747
/
1748 4750 FCHNG, JMS I FRERN
/
1750 0202 FRERN, CHANCE
1751 2504 MRJPNTR, RETHR

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1752 2675 OPRPNT, OPRRET
1753 0776 ZJMSRT, JMSRET
1754 0746 EINSTR, INSTR
1755 1401 BRANDY, RANDY
1756 0200 START, BGN
1757 0753 INTMQD, HQDATA
1760 1146 AASAVA, ASAVA
1761 1147 AASAVB, ASAVB
1762 3752 XDATAH, DATAHR
1763 2755 XSIMAC, SIMAC
1764 3755 XSVLNK, SAVLNK
1765 2756 XSMLNK, SIMLNK
1766 2757 XSIMHQ, SIMHQ
1767 2754 XEXPRT, EXPRET
1770 0001 XSHOP1, SIMOP1
1771 2201 XSMOP2, SIMOP2
1772 2252 XSMQ1, SIMQ1
1773 0745 OADDRS, ADDRS
1774 2785 XRTOPF, RETTOPF
1775 2753 GHQDN, HQDONE
1776 2737 GLPSW1, LPSW1
1777 0000 2
/

2000 82000
/
2200 5347 JMS GCWNG
/
/BEGINNING OF OPERATE GROUP ONE SIMULATION
2001 1762 SIMOP1: TAD I CINSTR /GET THE INSTRUCTION
2002 0271 AND POS200 /IS BIT 4 SET TO CLEAR THE AC
2003 7640 SEA CLA
2004 3773 DCA I OSIMAC /YES, CLEAR OUT THE SIMULATED AC
2005 1762 TAD I CINSTR /GET THE INSTRUCTION
2006 0267 AND K100 /IS BIT 5 SET TO CLEAR THE LINK
2007 7640 SEA CLA
2010 3774 DCA I OSMLNK /YES, CLEAR THE SIMULATED LINK
2011 1762 TAD I CINSTR /GET THE INSTRUCTION
2012 0256 AND K40 /IS BIT 6 SET TO COMPLEMENT THE AC
2013 7640 SEA CLA
2014 4753 JMS I XSMCMA /YES GO SIMULATE A CMA
2015 1762 TAD I CINSTR /GET THE INSTR
2016 0245 AND K20 /IS BIT 7 SET TO COMPLEMENT THE LINK
2017 7640 SEA CLA
2020 4764 JMS I XSMQML /YES, GO SIMULATE A CML
2021 1762 TAD I CINSTR /GET THE INSTRUCTION
2022 0261 AND K1 /IS BIT 11 SET TO INCREMENT THE AC
2023 7640 SEA CLA
2024 4745 JMS I XSMIAC /YES GO SIMULATE IAC
2025 1762 TAD I CINSTR /GET THE INSTRUCTION
2026 0262 AND K2 /IS BIT 10 SET TO RTR OR RTL
2027 7640 SEA CLA

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```

2030 5242      JHP SIMTWO /YES GO CHECK TO SEE WHICH ONE
2031 1762      TAD I CINSTR /GET THE INSTRUCTION
2032 8264      AND K14 /IS IT A ROTATE LEFT OR RIGHT
2033 1272      TAD NEG10 /RAR?
2034 7450      SNA
2035 4766      JMS I XSMRMR /YES GO SIMULATE A ROTATE RIGHT
2036 1263      TAD K4 /NO,RAL?
2037 7650      SNA CLA
2038 4767      JMS I XSMRAL /YES,GO SIMULATE A ROTATE LEFT
2039 5254      JHP OPRSET /GO TEST THE INSTRUCTION

2042 1762      SIMTWO, TAD I CINSTR /GET THE INSTRUCTION
2043 0264      AND K14 /BIT 8 AND 9 = 0
2044 7450      SNA
2045 4770      JMS I XSMBSW /YES,GO SIMULATE A BYTE SWAP
2046 1272      TAD NEG10 /RTR?
2047 7450      SNA
2048 4771      JMS I XSMRTR /YES, GO SIMULATE A ROTATE TWICE RIGHT
2049 1263      TAD K4 /RTL?
2050 7650      SNA CLA
2051 4772      JMS I XSMRTL /YES,GO SIMULATE A ROTATE TWICE LEFT

2054 1776      OPRSET, TAD I OFIELD
2055 1270      TAD 06201
2056 3257      DCA ,+1
2057 7402      HLT/CDF /CHANGE TO THE RANDOM DATA FIELD
2058 5775      JMP I INTOPR /GO SETUP THE OPERATE INSTRUCTION

2061 2091      K1, 1
2062 2082      K2, 2
2063 0004      K4, 4
2064 0014      K14, 14
2065 0020      K20, 20
2066 0040      K40, 40
2067 0100      K100, 100
2068 6281      D6201, 6281
2069 8280      POS200, 200
2070 7770      NEG10, -10

/
2073 0000      PCTST, 0
2074 7001      LAS
2075 1754      TAD I XJMSLC
2076 3755      DCA I XPGSAV
2077 1755      TAD I XPGSAV
2100 7041      CIA
2101 1756      TAD I XRETPC
2102 7640      SZA CLA
2103 5761      JMP I MRIERR
2104 1757      TAD I FLOXRN
2105 7041      CIA
2106 1760      TAD I FLXRET
2107 7640      SZA CLA
2108 5761      JMP I MRIERR
2109 5673      JMP I PCTST

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/
2112 0000      GETRD, 0
2113 1757      TAD I FLOXRN
2114 1344      TAD A6221
2115 3316      DCA ,+1
2116 7402      HLT/CDF
2117 1745      TAD I BSAVA
2120 3345      DCA BSAVA
2121 6224      RIF
2122 1344      TAD A6221
2123 3324      DCA ,+1
2124 7402      HLT/CDF
2125 1345      TAD BSAVA
2126 5712      JMP I GETRD

/
2127 0000      RANCON, 0
2130 1752      TAD I ZCNFLG
2131 7650      SNA CLA
2132 5727      JMP I RANCON
2133 1753      TAD I XWDMOV
2134 7402      HLT
2135 7684      LAS
2136 5727      JMP I RANCON

/
2137 0000      INTSET, 0
2140 1351      TAD XINT
2141 3743      DCA I TINT
2142 5737      JMP I INTSET
2143 0003      TINT, INT
/
2144 6281      A6201, 6281
2145 8280      BSAVA, 0
/
2147 #2147
/
2147 4750      GCHNG, JMS I GRERNG
/
2150 0292      GRERNG, CHANGE
2151 3102      XINT, INTERS
2152 1143      ZCNFLG, COVFLG
2153 3573      XWDMOV, HDWDX
2154 0762      XJMSLC, JMSLOC
2155 1311      XPGSAV, PCSAVE
2156 0584      XRETPC, RETHR
2157 0754      FLOXRN, RANFLD
2158 0525      FLXRET, RETFLD
2161 1313      MRIERR, ERROR
2162 0746      CINSTR, INSTR
2163 2401      XSMCMA, SIMCMA
2164 2426      XSMCML, SIMCML

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2163 2435 XSMIAC, SIMIAC
2166 2442 XSMRAR, SIMRAR
2167 2461 XSMRAL, SIMRAL
2170 2477 XSMBSH, SIMBSW
2171 2520 XSMRTR, SIMRTR
2172 2537 XSMRTL, SIMRTL
2173 2755 OSIMAC, SIMAC
2174 2756 OSMLNK, SIMLNK
2175 0637 INTOPR, OPRINT
2176 0754 OFIELD, RANFLD
2177 0000 J

2200 2220 /
2200 5343 JHP HCHNG
/

```

## /BEGINNING OF OPERATE GROUP 2 SIMULATION

```

2201 3251 SIMOP2, DCA SKPFLG /CLEAR THE SKIP FLAG
2202 1745 SHACKX, TAD I DINSTR
2203 0243 AND Z100
2204 7650 SNA CLA
2205 5211 JHP SZACHK
2206 4746 JHS I XSMSHA
2207 5211 JHP SZACHK
2210 5226 JHP SETSKP
2211 1745 SZACHK, TAD I DINSTR
2212 0244 AND Z40
2213 7650 SNA CLA
2214 5220 JHP SNLCHK
2215 4747 JHS I XSMSZA
2216 5220 JHP SNLCHK
2217 5226 JHP SETSKP
2220 1745 SNLCHK, TAD I DINSTR
2221 0245 AND Z20
2222 7650 SNA CLA
2223 5227 JHP COMCHK
2224 4750 JHS I XSMSNL
2225 5227 JHP COMCHK
2226 2251 SETSKP, ISZ SKPFLG
2227 1745 COMCHK, TAD I DINSTR
2230 0250 AND POS10
2231 7640 SNA CLA
2232 7240 CLA CHA
2233 1251 TAD SKPFLG
2234 7640 SNA CLA
2235 2751 ISZ I ZEXPRY
2236 1745 TAD I DINSTR
2237 0246 AND Z20
2240 7640 SNA CLA
2241 3752 DCA I XACSIM
2242 5753 JHP I ZSETOP /GO SETUP AND TEST INSTR

```

```

2243 J108 Z100, 100
2244 0040 Z40, 40
2245 0020 Z20, 20
2246 0200 Z200, 200
2247 0320 Z320, 320
2250 0010 POS10, 10
2251 0000 SKPFLG, 0

```

## /BEGINNING OF OPERATE GROUP 2 HQ INSTRUCTION SIMULATION

```

2252 1745 SIMMOI, TAD I DINSTR /GET THE INSTRUCTION
2253 0247 AND Z320 /MASK OUT FOR LEGAL BITS 4,5 &7
2254 7450 SNA
2255 5753 JHP I ZSTOP /INSTRUCTION IS A NOP
2256 1304 TAD NEG20 /SUBTRACT 20
2257 7450 SNA
2260 5754 JHP I XSHMQL /GO SIMULATE A MQL
2261 1305 TAD M60
2262 7450 SNA
2263 5755 JHP I XSHMQA /GO SIMULATE A MQA
2264 1304 TAD NEG20
2265 7450 SNA
2266 5756 JHP I XSHSWP /GO SIMULATE A SWP
2267 1305 TAD M60
2270 7450 SNA
2271 5757 JHP I XSMCLA /GO SIMULATE A CLA
2272 1304 TAD NEG20
2273 7450 SNA
2274 5760 JHP I XSMCAM /GO SIMULATE A CAM
2275 1305 TAD M60
2276 7450 SNA
2277 5761 JHP I XSMACL /GO SIMULATE A ACL
2300 1304 TAD NEG20
2301 7650 SNA CLA
2302 5762 JHP I XCLSWP /GO SIMULATE A SWP, CLA
2303 7402 HLT /NONE OF THE ABOVE
2304 7760 NEG20, +20
2309 7720 M60, +60

```

```

/ERRPRP, TAD I GRANL /GET THE RANDOM DATA FIELD
2387 4764 JMS I CPRHLT
2318 1765 TAD I GOPRET /GET THE ACTUAL RETURN FIELD
2311 4764 JMS I GPRHLT
2312 1751 TAD I ZEXPRY /GET THE EXPECTED RETURN PC
2313 4764 JMS I OPRHLT
2314 1766 TAD I GACTRY /GET THE ACTUAL RETURN PC
2315 4764 JMS I OPRHLT
2316 1767 TAD I GADDRS /GET THE INSTRUCTION ADDRESS
2317 4764 JMS I OPRHLT
2320 1745 TAD I DINSTR /GET THE INSTRUCTION
2321 4764 JMS I OPRHLT
2322 1770 TAD I GOATAH /GET THE INITIAL AC DATA
2323 4764 JMS I OPRHLT
2324 1752 TAD I XACSIM /GET THE SIMULATED AC

```

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```

2325 4764      JMS I OPRHLT
2326 1771      TAD I GOATAD /GET THE FINAL AC
2327 4764      JMS I OPRHLT
2330 1772      TAD I GSVLNK /GET THE INITIAL LINK
2331 4764      JMS I OPRHLT
2332 1773      TAD I GSMLNK /GET THE SIMULATED LINK
2333 4764      JMS I OPRHLT
2334 1774      TAD I GLNKDN /GET THE FINAL LINK
2335 4764      JMS I OPRHLT
2336 1775      TAD I GMODAT /GET THE INITIAL MD DATA
2337 4764      JMS I OPRHLT
2340 5776      JMP I OPRERR1

/
2343 #2343
/
2343 4744      HCHNG; JMS I HRERNG
/
2344 0202      HRERNG, CHANGE
2345 0746      DINSTR, INSTR
2346 2611      XSHSMA, SIMSMA
2347 2681      XSMSZA, SIMSZA
2350 2626      XSMSNL, SIMSNL
2351 2754      ZEXPRIT, EXPRET
2352 2755      XACSIM, SIMAC
2353 2094      ZSETOP, OPSET
2354 2640      XSMGML, SIMMOL
2355 2644      XSMGMA, SIMMQA
2356 2652      XSMSPW, SIMSPWP
2357 2661      XSMCLA, SIMCLAA
2360 2663      XSMCAH, SIMCAH
2361 2666      XSMACL, SIMACL
2362 2671      XCLSWP, CLASWP
2363 0754      GRANFL, RANFLD
2364 1741      OPRHLT, HLTOPR
2365 2752      GOPRET, OPRET
2366 2675      GACTRT, OPRET
2367 0745      GADDRS, ADDRS
2370 0752      GDATAH, DATAHR
2371 2750      GDATAAD, DATAON
2372 0755      GSVLNK, SAVLNK
2373 2756      GSMLNK, SIMLINK
2374 2751      GLNKDN, LINKDN
2375 0753      GHODAT, MDQDATA
2376 1734      OPRERR1, OPRERR1
2377 0000      0

2400 #2400
/
2400 5367      JMP     ICNNG
/

```

/ROUTINE TO SIMULATE A COMPLEMENT

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```

2401 2000      SIMOMA, 0
2402 1224      TAD    M14
2403 3225      DCA    CNT
2404 3226      DCA    SIMCML
2405 1775      TAD I BSIMAC
2406 7104      CLL    RAL
2407 3235      DCA    SIMIAC
2410 7420      SIZ   SIMCML
2411 2226      TAD    SIMCML
2413 2225      ISE    CNT
2414 5216      JMP    ,+2
2415 5222      JMP    ENOCMA
2416 7184      CLL    RAL
2417 3226      DCA    SIMCML
2420 1235      TAD    SIMIAC
2421 5286      JMP    SIMOMA+5
2422 3775      ENOCMA; DCA I BSIMAC
2423 5601      JMP    I SIMOMA

2424 7764      M14, -M14
2425 2000      CNT, 0

/ROUTINE TO SIMULATE A CML
2426 3000      SIMCML, 0
2427 1776      TAD I BSMLNK /GET THE SIMULATED LINK
2430 7650      SNA    CLA /IS IT A 0
2431 1234      TAD I K4000 /YES, MAKE IT A ONE
2432 3776      DCA I BSMLNK /SAVE IT
2433 5626      JMP I SIMCML

2434 4000      K4000, 4000

/ROUTINE TO SIMULATE A IAC
2435 0000      SIMIAC, 0
2436 2775      ISE 1 BSIMAC /BUMP THE SIMULATED AC
2437 5241      JMP    ,+2
2440 4226      JMS    SIMCML
2441 5635      JMP I SIMIAC

/ROUTINE TO SIMULATE A RAR
2442 0200      SIMRAR, 0
2443 7300      CLA    CLL /CLEAR OUT A LINK AND THE AC
2444 1776      TAD I BSMLNK /GET THE SIMULATED LINK
2445 7284      RAL    /PUT IT IN THE LINK
2446 1268      TAD    NEG6
2447 3225      DCA    CNT
2450 1773      TAD I BSIMAC /GET THE SIMULATED AC
2451 7006      RTL    /ROTATE 12 PLACES TO THE LEFT
2452 2225      ISE    CNT
2453 5281      JMP    ,+2
2454 3775      DCA I BSIMAC /SAVE THE SIMULATED ROTATE

```

2453 7010 RAR /GET THE LINK  
 2455 3776 DCA I BSHLNK /SAVE THE LINK  
 2457 5642 JMP I SIMRAR /RETURN

2460 7772 NEG6, #6

/ROUTINE TO SIMULATE A RAL

2461 0000 SIMRAL: B  
 2462 7300 CLA CLL  
 2463 1776 TAD I BSHLNK /GET THE SIMULATED LINK  
 2464 7004 RAL /PUT IT IN THE LINK  
 2465 1260 TAD NEG6  
 2466 3225 DCA CNT  
 2467 1775 TAD I BSIMAC /GET THE SIMULATED AC  
 2470 7012 RTR /ROTATE IT RIGHT 12 TIMES  
 2471 2225 ISZ CNT  
 2472 5270 JMP ,2  
 2473 3775 DCA I BSIMAC /SAVE THE SIMULATED ROTATE  
 2474 7010 RAR  
 2475 3776 DCA I BSHLNK /SAVE THE SIMULATED LINK  
 2476 5661 JMP I SIMRAL /RETURN

/ROUTINE TO SIMULATE A BYTE SWAP

2477 0000 SIMBSW: B  
 2502 7300 CLA CLL  
 2501 1776 TAD I BSHLNK /GET THE SIMULATED LINK  
 2502 7010 RAR  
 2503 7812 RTR  
 2504 7812 RTR  
 2505 1775 TAD I BSIMAC /GET THE SIMULATED AC  
 2506 0317 AND K7700  
 2507 1775 TAD I BSIMAC /GET IT AGAIN  
 2512 7006 RTL  
 2511 7006 RTL  
 2512 7006 RTL  
 2513 3775 DCA I BSIMAC /SAVE THE SIMULATED BYTE SWAP  
 2514 7010 RAR  
 2515 3776 DCA I BSHLNK /SAVE THE LINK  
 2516 5677 JMP I SIMBSW /RETURN  
 2517 7700 K7700, 7700

/ROUTINE TO SIMULATE RTR

2522 0000 SIMRTR: B  
 2521 7300 CLA CLL  
 2522 1776 TAD I BSHLNK /GET THE SIMULATED LINK  
 2523 7004 RAL /PUT IT IN THE LINK  
 2524 1336 TAD M13  
 2525 3225 DCA CNT /SETUP A COUNTER FOR 11 RAL'S  
 2526 1775 TAD I BSIMAC /GET THE SIMULATED AC  
 2527 7004 RAL  
 2528 2225 ISZ CNT

2531 5327 JMP ,2  
 2532 3775 DCA I BSIMAC /SAVE THE SIMULATED ROTATED AC  
 2533 7010 RAR  
 2534 3776 DCA I BSHLNK /SAVE THE SIMULATED LINK  
 2535 5720 JMP I SIMRTR /RETURN

2536 7765 M13, #13

/ROUTINE TO SIMULATE RTL

2543 0000 SIMRTL: B  
 2543 7300 CLA CLL  
 2541 1776 TAD I BSHLNK /GET THE SIMULATED LINK  
 2542 7004 RAL /AND PUT IT IN THE LINK  
 2543 1336 TAD M13  
 2544 3225 DCA CNT /SET UP A COUNTER TO DO 11 RAR'S  
 2545 1775 TAD I BSIMAC /GET THE SIMULATED AC  
 2546 7010 RAR  
 2547 2225 ISZ CNT  
 2550 5346 JMP ,2  
 2551 3775 DCA I BSIMAC /SAVE THE SIMULATED ROTATED AC  
 2552 7010 RAR  
 2553 3776 DCA I BSHLNK /SAVE THE SIMULATED ROTATED LINK  
 2554 5737 JMP I SIMRTL

/  
 2555 1771 FLGCHK: TAD I UPERL /IS THE LAST FIELD = 1K  
 2556 1365 TAD M1777  
 2557 7648 SZA CLA  
 2558 5772 JMP I TSFLDF /NO GO SWAP IT UP  
 2560 6224 RIF /READ THE INSTRUCTION FIELD  
 2562 7640 SZA CLA /IS IT EQUAL TO FIELD #  
 2563 5773 JMP I TSWPDN /NO,DO SWAP THE PROGRAM DOWN  
 2564 5774 JMP I ROLFLG /YES,DO NOT SWAP FIELDS BUT ROLL UP

2565 6001 M1777, #1777

/  
 2567 #2567

2567 4770 ICHNG: JMS I IRERNNG

2570 0202 IRERNNG: CHANGE  
 2571 1558 UPERL: UPRLM  
 2572 0433 TSFLDF: SFLDFG  
 2573 0422 TSWPDN: SWAPDN  
 2574 0327 ROLFLG: SETFLG  
 2575 2755 BSIMAC: SIMAC  
 2576 2756 BSHLNK: SIMLNK  
 2577 0000 0

/  
 2600 #2600

2680 5364 JMP /

## /ROUTINE TO SIMULATE A SEA

```

2681 0800 SIMSEA; 0
2682 7240 CLA CMA
2683 1770 TAD I CSIMAC
2684 3223 DCA ACUTST
2685 2223 ISZ ACUTST
2686 5691 JMP I SIMSEA
2687 2201 ISZ SIMSEA
2688 5691 JMP I SIMSEA

```

## /ROUTINE TO SIMULATE A SMA

```

2689 0800 SIMSMA; 0
2690 1770 TAD I CSIMAC
2691 0225 AND D4000
2692 1224 TAD K7777
2693 3223 DCA ACUTST
2694 2223 ISZ ACUTST
2695 5221 JMP ,+2
2696 5611 JMP I SIMSMA
2697 2211 ISZ SIMSMA
2698 5611 JMP I SIMSMA
2699 0800 ACUTST, 0
2700 7777 ,+1
2701 4000 D4000, 4000

```

## /ROUTINE TO SIMULATE A SNL

```

2702 0800 SIMSNL; 0
2703 1771 TAD I CSHLNK
2704 0225 AND D4000
2705 1224 TAD K7777
2706 3223 DCA ACUTST
2707 2223 ISZ ACUTST
2708 5236 JMP ,+2
2709 5626 JMP I SIMSNL
2710 2226 ISZ SIMSNL
2711 5626 JMP I SIMSNL

```

## /ROUTINE TO SIMULATE A MQL

```

2712 1770 SIMMQL, TAD I CSIMAC /GET THE SIMULATED AC
2713 3772 DCA I CSIMMQ /PUT IT IN THE SIMULATED MQ
2714 3770 DCA I CSIMAC /CLEAR OUT THE SIMULATED AC
2715 5773 JMP I COPRST /GO EXECUTE THE INSTRUCTION

```

## /ROUTINE TO SIMULATE HQA

```

2716 1772 SIMMQA, TAD I CSIMMQ /GET THE SIMULATED MQ
2717 7040 CMA /COMPLEMENT THE RESULTS

```

```

2720 0770 AND I CSIMAC /MASK RESULTS WITH SIMULATED AC
2721 1772 TAD I CSIMMQ /INCLUSIVE OR THE SIMULATED HQ
2722 3770 DCA I CSIMAC /THE SIMULATED AC = INCLUSIVE OR OF HQ & AC
2723 5773 JMP I COPRST /GO EXECUTE THE INSTR.

```

## /ROUTINE TO SIMULATE A SWP

```

2724 1770 SIMSWP, TAD I CSIMAC /GET THE SIMULATED AC
2725 3223 DCA ACUTST /AND SAVE IT
2726 1772 TAD I CSIMMQ /GET THE SIMULATED MQ
2727 3770 DCA I CSIMAC /AND PUT IT IN THE SIMULATED AC
2728 1223 TAD ACUTST /GET THE SIMULATED AC
2729 3772 DCA I CSIMMQ /AND PUT IT IN THE SIMULATED MQ
2730 5773 JMP I COPRST /GO EXECUTE THE INSTRUCTION

```

## /ROUTINE TO SIMULATE A CLA

```

2731 3770 SIMCLA, DCA I CSIMAC /CLEAR THE SIMULATED AC
2732 5773 JMP I COPRST /GO EXECUTE THE INSTRUCTION

```

## /ROUTINE TO SIMULATE A CAM

```

2733 3770 SIMCAM, DCA I CSIMAC /CLEAR THE SIMULATED AC
2734 3772 DCA I CSIMMQ /CLEAR THE SIMULATED MQ
2735 5773 JMP I COPRST /GO EXECUTE THE INSTRUCTION

```

## /ROUTINE TO SIMULATE A ACL

```

2736 1772 SIMACL, TAD I CSIMMQ /GET THE SIMULATED MQ
2737 3770 DCA I CSIMAC /PUT IT IN THE SIMULATED AC
2738 5773 JMP I COPRST /GO EXECUTE THE INSTR.

```

## /ROUTINE TO SIMULATE A CLA,SWP

```

2739 1772 CLASHP, TAD I CSIMMQ /GET THE SIMULATED MQ
2740 3770 DCA I CSIMAC /PUT IT IN THE SIMULATED AC
2741 3772 DCA I CSIMMQ /CLEAR THE SIMULATED MQ
2742 5773 JMP I COPRST /GO EXECUTE THE INSTRUCTION

```

```

2743 0000 OPRRET, 0 /RETURN HERE FROM OPERATE INSTRUCTION
2744 3350 DCA DATADN /SAVE THE FINAL AC
2745 7010 RAR
2746 3351 DCA LINKDN /SAVE THE FINAL LINK
2747 6214 RDF /GET THE RANDOM DATA FIELD
2748 3352 DCA OPRETF /SAVE THE DF FROM OPERATE RETURN
2749 7781 ACL /GET THE FINAL MQ DATA
2750 3353 DCA MDNONE /SAVE IT
2751 7402 RETTDF, HLTA/GDF /CHANGE OF BACK TO PROGRAM FIELD
2752 1775 CPRCOM, TAD I XFIELD /GET THE EXPECTED INSTRUCTION FIELD
2753 7041 CIA
2754 1352 TAD OPRETF /GET THE FIELD INSTRUCTION RETURNED FROM
2755 7640 S2A CLA
2756 5344 JMP OPRERR /PROGRAM RETURNED FROM THE WRONG FIELD
2757 1354 TAD EXPRET /GET THE EXPECTED RETURN PC

```

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```

2714 7041 CIA
2715 1275 TAD OPRRET /GET THE ACTUAL RETURN PC
2716 7640 SZA CLA
2717 9344 JHP OPRERR /EXPECTED PC DOES NOT AGREE WITH ACTUAL
2720 1357 TAD SIMHQ /GET THE SIMULATED HQ
2721 7041 CIA
2722 1353 TAD HQDONE /GET THE ACTUAL HQ
2723 7640 SZA CLA
2724 9344 JHP OPRERR /ERROR, ACTUAL HQ DOES NOT EQUAL SIMULATED HQ
2725 1356 TAD SIMLNK /GET THE SIMULATED LINK
2726 7041 CIA
2727 1351 TAD LINKDN /GET THE ACTUAL LINK
2730 7640 SZA CLA
2731 9344 JHP OPRERR /ERROR SIMULATED AND ACTUAL LINK ARE NOT EQUAL
2732 1355 TAD SIMAC /GET THE SIMULATED AC
2733 7041 CIA
2734 1350 TAD DATAON /GET THE ACTUAL AC RETURNED
2735 7640 SZA CLA
2736 9344 JHP OPRERR /SIMULATED AND ACTUAL AC DO NOT AGREE
2737 1766 LPSK0: TAD I SRSAV/LAS /*S SET TO LOOP ON THE INSTRUCTION
2740 7004 RAL
2741 7710 SPA CLA
2742 5774 JMP I XINGN /YES GO LOOP ON THE INSTRUCTIN
2743 5776 JMP I XXCNT /GO BUMP INSTRUCTION COUNTER

2744 1766 OPRERR, TAD I SRSAV/LAS /CHECK SR0=1 TO INHIBIT ERROR HALT
2745 7710 SPA CLA /IS IT SET
2746 5337 JMP I LPSON /YES, CHECK LOOP SWITCH
2747 5767 JMP I DERRR /NO, GO HALT WITH ERROR INFORMATION IN AC

2750 8000 DATAON, 0
2751 8000 LINKDN, 0
2752 8000 OPRRET, 0
2753 8000 HQDONE, 0
2754 8000 EXPRET, 0
2755 8000 SIMAC, 0
2756 8000 SIMLNK, 0
2757 8000 SIMHQ, 0
2764 /*2764
2764 4765 JHNG, JMS I JRERNG
2765 8202 JRERNG, CHANG
2766 8336 SRSAV, SAVSR
2767 2386 OERROR, ERRORP
2770 2755 CSIMAC, SIMAC
2771 2756 CSIMLNK, SIMLNK
2772 2757 CSIMHQ, SIMHQ
2773 2054 COPRST, OPRSET
2774 1606 XINGN, INSGEN+5
2775 2754 XFIELD, RANFLD
2776 2271 XXCNT, XCNT
2777 8000 0

```

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```

3000 /*3000
3000 5345 JMP KC4NG
3000 /*ROUTINE TO FILL MEMORY WITH HALTS AROUND THE PROGRAM
3001 1771 FILRND, TAD I CONTLM /GET THE UPPER LIMIT COUNTER
3002 3225 DCA TEMP /SAVE IT
3003 1772 TAD I FLDCNT /CHECK TO SEE IF IT IS FIELD 0
3004 7650 SNA CLA /IS IT FIELD 0?
3005 1223 TAD MM4 /YES, SUBTRACT 4 FROM THE BEGINNING ADDRESS
3006 1373 TAD ABON
3007 7241 CIA /NEGATE THE NUMBER FOR A COUNTER
3010 3771 DCA I CONTLM /SAVE IT
3011 1772 TAD I FLDCNT /CHECK TO SEE IF IT IS FIELD 0
3012 7690 SNA CLA /IS IT FIELD 0?
3013 1224 TAD KKK4 /YES, START FILLING FIELD 0 AT ADDRESS 4
3014 4774 JMS I ZFILL /FILL THE FIRST HALF OF PROGRAM FIELD
3015 1225 TAD TEMP /GET THE UPPER LIMIT COUNTER
3016 1375 TAD ENDOFP /ADD END OF PROGRAM TO IT
3017 3771 DCA I CONTLM /SAVE THIS NUMBER AS THE COUNTER
3020 1375 TAD ENDOFP /GET THE ADDRESS TO START FILLING MEMORY
3021 4774 JMS I ZFILL /WITH HALTS
3022 5776 JMP I XADD1 /RETURN FOR NEXT FIELD

3023 7774 MM4, #4
3024 0004 KK4, 4
3025 0000 TEMP, 0

3026 7240 BGNCON, DCA CMA /CONSTRAINT STARTING ADDRESS
3027 3750 DCA I XCNFLG /RANDOM STARTING ADDRESS
3030 4751 JMS I XLIMIT /SETUP MEMORY LIMITS
3031 1021 TAD OPSEL
3032 3234 DCA SELOP1
3033 5762 JMP I CONSET /RETURN TO PROGRAM TO SETUP MASK AND CONSTRAINT WORDS

3034 0000 SELOP1, 0
3035 0000 SETOP1, 0 TAD SELOP1
3036 1234 RAL /CHECK TO SEE IF OPTION 1 WAS SELECTED
3037 7004 SMA CLA /WAS IT?
3040 7700 JMS I SETOP1 /NO, JUST RUN MRI AND OPR
3041 9635 TAD KILL
3042 3301 DCA
3043 4752 JMS I OPRAND /START SETTING UP OPTION 1
3044 9300 AND KK377 /MASK BITS 4-13
3045 3753 DCA I SLUXMT /SAVE THE RANDOM NUMBER FOR SLU
3046 4752 JMS I OPRAND /GENERATE A RANDOM NUMBER FOR PARALLEL I/O
3047 3754 DCA I PIOXMT /SAVE THE WORD FOR THE PARALLEL I/O
3050 7240 CLA CMA /SET ALL PROGRAM FLAGS TO INACTIVE STATE
3051 3755 DCA I FLGXMT /SLU XMIT FLAG
3052 7240 CLA CMA
3053 3756 DCA I FLGREC /SLU RECEIVE FLAG

```

```

3054 7240      CLA     CHA
3055 3757      DCA I  FLGPIO /PARALLEL I/O FLAG
3056 7240      CLA     CHA
3057 3760      DCA I  FLGRTC /REAL TIME CLOCK FLAG
3060 1277      TAD     MMSS
3061 3761      DCA I  DVINAC /SETUP A DEVICE INACTIVE COUNTER
3062 7301      CLA DLL IAC /SET DATA 11 TO A ONE
3063 6935      KIE
3064 6135      CLLE   /SET SLU INTERRUPT ENABLE
3065 7300      CLA     CLL
3066 6575      DBSE   /SET PARALLEL I/O INT ENABLE
3067 1753      TAD I  SLUXMT /GET THE WORD TO BE TRANSMITTED BY SLU
3070 6046      TLS
3071 7200      CLA
3072 1754      TAD I  PIDXMT /GET THE WORD TO BE TRANSMITTED BY PARALLEL I/O
3073 6574      DTO
3074 7301      CLA DLL IAC /SET A PROGRAM FLAG TO SIGNIFY TO RTC WHEN
3075 3763      DCA I  PIORDY /TO TRANSMIT ANOTHER CHARACTER ON PARALLEL I/O
3076 5635      JMP I  SETOPI /RETURN TO PROGRAM

3077 7723      MM55, .55
3100 0377      KK377, 377
3101 0000      KILL, 0

/INTERRUPT SERVICE ROUTINE

3102 3341      INTERS, DCA INTAC /SAVE THE AC
3103 7010      RAR
3104 3342      DCA INTLNK /SAVE THE LINK
3105 1743      TAD I ADDRS2 /GET THE INTERRUPT PC
3106 3344      DCA INTRET /SAVE IT
3107 6224      RIF
3108 1345      TAD KKCDF /ADD CDF INSTRUCTION TO BITS 6-8
3111 3312      DCA .41 /PUT CDF TO PROGRAM FIELD IN NEXT LOCATION
3112 7402      HLTCDF /TO PROGRAM FIELD
3113 6041      TSF /SKIP ON SLU XMIT FLAG
3114 7410      SKP
3115 5765      JMP I SERXMT /GO SERVICE SLU XMIT FLAG
3116 6031      KSF /SKIP ON SLU RECEIVE FLAG
3117 7410      SKP
3120 5766      JMP I SERREC /GO SERVICE SLU RECEIVE FLAG
3121 6571      DBSK /SKIP ON PARALLEL I/O DATA READY FLAG
3122 7410      SKP
3123 5767      JMP I SERPIO /GO SERVICE PARALLEL I/O
3124 6137      CLSK /SKIP ON REAL TIME CLOCK FLAG
3125 7410      SKP
3126 5770      JMP I SERRTC /GO SERVICE REAL TIME CLOCK FLAG
3127 6192      SP, /SKIP ON AC LOW F/F
3128 7410      SKP
3131 5764      JMP I POWERF /POWER FAILURE GO CLEAR AC LOW AND RETURN
3132 7402      HLT /ILLEGAL INTERRUPT
3133 1342      RETPRG, TAD INTLNK /GET THE LINK
3134 7104      CLL RAL /RESTORE IT
3135 1341      TAD INTAC /RESTORE THE AC
3136 6244      RMF /RESTORE MEMORY FIELDS

```

```

3137 6001      ION
3140 5744      JMP I INTRET /TURN THE INTERRUPT ON /RETURN TO PROGRAM

3141 8000      INTAC, 0
3142 8000      INTLNK, 0
3143 8000      ADDRS0, 0
3144 8000      INTRET, 0
3145 6201      KKCDF, CDF    00

/
3146 #3146
/
3146 4747      KOHNG, JMS I KRENG
/
3147 8202      KRENG, CHANGE
3150 1143      XCNFLG, CNVFLG
3151 1522      XLIMIT, LIMITS
3152 1401      OPRAND, RANDY
3153 3232      SLUXMT, XHTSLU
3154 3234      PIDXMT, XHTPIO
3155 3226      FLGXMT, XMTFLG
3156 3227      FLGREC, RECFLG
3157 3230      FLGPID, PIOFLG
3160 3231      FLGRTO, RTCFLG
3161 3337      DVINAC, INACDV
3162 3401      CONSET, SETCON
3163 3254      PIORDY, RDYPIO
3164 3304      POWERF, POWFAL
3165 3201      SERXMT, XMTSER
3166 3204      SERREC, RECSER
3167 3237      SERPID, PIOSER
3170 3255      SERRTC, RTCSER
3171 1145      CONTLM, HGWLIM
3172 0247      FLDCCNT, CNTN3
3173 0200      ABGN, BGN
3174 0527      ZFILL, FILALL
3175 3576      ENDOFF, PRGEND
3176 3436      XADD01, ADDONE
3177 8000      0

/
3200 #3200
/
3200 5367      JMP LCHNG
/
/SERIAL LINE UNIT TRANSMIT SERVICE ROUTINE
3201 3226      XMTSER, DCA XMTFLG /SET SLU XMIT FLAG ACTIVE
3202 6042      TCF /CLEAR TRANSMIT FLAG
3203 5313      JMP CHKACT /CHECK ALL DEVICES TO BE ACTIVE

/SERIAL LINE UNIT RECEIVER SERVICE ROUTINE
3204 3227      REGSER, DCA RECPFLG /SET SLU RECEIVER FLAG ACTIVE

```

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3205 6835 KR3 /CLEAR RECEIVE FLAG AND READ THE BUFFER  
3206 3233 DCA RECSLU /SAVE THE WORD  
3207 1232 TAD XMTSLU /COMPARE WORD TRANSMITTED WITH WORD READ  
3210 7841 CIA  
3211 1233 TAD RECSLU  
3212 7640 SZA CLA /ARE THEY EQUAL?  
3213 7402 HLT/JMP I PSRERR//DATA ERROR SLU  
3214 1773 TAD I KILLIT  
3215 7640 SZA CLA  
3216 5271 JMP OUT /KILL INTERRUPTS UNTIL RELOCATION OCCURS  
3217 4341 JMS RANDOM /GO GENERATE A NEW WORD  
3220 0236 AND CO377 /MASK BITS 4+11  
3221 3232 DCA XNTSLU /SAVE WORD TO BE TRANSMITTED  
3222 1232 TAD XNTSLU /GET THE WORD  
3223 6046 TLS CLL /TRANSMIT IT  
3224 7300 CLA CHKACT /CHECK ALL DEVICES TO BE ACTIVE  
3225 5313 JMP  
  
3226 0000 KMTFLG, 0  
3227 0000 RECFLG, 0  
3228 0000 PIOFLG, 0  
3229 0000 RTCFLG, 0  
3230 0000 XMTSLU, 0  
3231 0000 RECSLU, 0  
3234 0000 KHTPIO, 0  
3235 0000 RECPIO, 0  
3236 0377 CO377, 377  
  
/PARALLEL I/O INTERRUPT SERVICE ROUTINE  
3237 3230 PIOSER, DCA PIOFLG /SET PARALLEL I/O ACTIVE FLAG  
3240 6572 DBRD /READ THE PARALLEL I/O BUFFER  
3241 6573 DBCF /CLEAR DATA READY FLAG SET DATA ACCEPTED  
3242 3235 DCA RECPIO /SAVE THE WORD READ  
3243 6570 OBST /SKIP AND CLEAR DATA ACCEPTED AND DATA AVAILABLE  
3244 7402 HLT/JMP I PSRERR/DBCF FAILED TO SET DATA ACCEPTED  
3245 1234 TAD XHTPIO /COMPARE THE WORD TRANSMITTED WITH THE WORD READ  
3246 7041 CIA  
3247 1235 TAD RECPIO  
3250 7640 SZA CLA /ARE THEY EQUAL?  
3251 7402 HLT/JMP I PSRERR/NO, DATA ERROR PARALLEL I/O  
3252 3254 DCA RDYPIO /SET A PROGRAM FLAG TO SIGNIFY TO RTE, THAT  
3253 5313 JMP CHKACT /A NEW WORD IS NEEDED FOR PARALLEL I/O  
3254 0000 RDYPIO, 0  
  
/REAL TIME CLOCK INTERRUPT SERVICE ROUTINE  
3255 3231 RTCSER, DCA RTCFLG /SET REAL TIME CLOCK FLAG TO ACTIVE  
3256 6136 CLCL /CLEAR CLOCK FLAG  
3257 7000 NOR/JMS I ACTLINV/ THIS LOCATION USED IF ACT LINE AND OPTION 1 SELECTED  
3260 1254 TAD RDYPIO /GET PARALLEL I/O STATUS  
3261 7640 SZA CLA /TRANSMIT ANOTHER WORD?  
3262 5313 JMP CHKACT /NO, MUST BE INACTIVE GO CHECK ALL DEVICES

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3263 2254 ISZ RDYPIO /SET PROGRAM FLAG TO INACTIVE  
3264 1773 TAD I KILLIT /GET THE KILL FLAG  
3265 7650 SNA CLA /WAS IT SET  
3266 5275 JMP OUT+4 /AND CONTINUE RUNNING  
3267 6135 CLL E /YES = CLEAR RTC AND P I/O INT ENA  
3270 6576 DBCE  
  
3271 2303 OUT, ISZ CNTEND  
3272 5776 JMP I PRGRET /WAIT FOR NEXT INTERRUPT  
3273 6002 IOF /TURN THE INTERRUPT OFF  
3274 5774 JMP I RELGO /RETURN TO PROGRAM FOR RELOCATION OR RUN  
3275 4341 JMS RANDOM /GENERATE A NEW WORD FOR PARALLEL I/O  
3276 3234 DCA XHTPIO /SAVE IT  
3277 1234 TAD XHTPIO /GET THE NEW WORD  
3280 6574 DBTD CLL /TRANSMIT IT  
3281 7300 CLA CHKACT /CHECK OTHER DEVICES TO BE ACTIVE  
3282 5313 JMP  
  
3283 0000 CNTEND, 0

/POWER FAIL INTERRUPT SERVICE ROUTINE

3304 6103 POWFAL, CAL /CLEAR AC LOW F/F  
3305 6102 SPL /SKIP ON AC LOW AS A LEVEL  
3306 7418 SKP  
3307 6101 SBE  
3310 7418 SKP  
3311 7402 HLT /BATTERY EMPTY - ITS ALL OVER  
3312 5776 JMP I PRGRET /RETURN TO THE PROGRAM  
  
3313 1226 CHKAOT, TAD XMTFLG /CHECK ALL DEVICES TO BE INTERRUPTING  
3314 1227 TAD RECFLG  
3315 1231 TAD RTCFLG  
3316 1230 TAD PIOFLG  
3317 7630 SNA CLA /ARE THEY ?  
3320 5324 JMP RESET /YES, RESET ALL FLAGS TO INACTIVE  
3321 2337 ISZ INACDV /BUMP INACTIVE COUNTER  
3322 5776 JMP I PRGRET /RETURN TO THE PROGRAM  
3323 7402 HLT/JMP I PSRERR/ONE OR MORE DEVICES ARE INACTIVE  
3324 7348 RESET, CLA CLL CMA /SET ALL DEVICES TO INACTIVE  
3325 3226 DGA XMTFLG  
3326 7240 CLA CMA  
3327 3227 DCA RECFLG  
3330 7240 CLA CMA  
3331 3230 DCA PIOFLG  
3332 7240 CLA CMA  
3333 3231 DCA RTCFLG  
3334 1348 TAD MMH55  
3335 3337 DCA INACDV /RESET INACTIVE COUNTER  
3336 5776 JMP I PRGRET /RETURN TO THE PROGRAM  
  
3339 0000 INACDV, 0

3340 7723 MMBB, #55

3341 0000 RANDOM: 0  
 3342 7361 CLA CLL IAC  
 3343 1395 TAD RANN1  
 3344 1396 TAD RANN2  
 3345 7106 CLL RTL  
 3346 3395 DCA RANN1  
 3347 1396 TAD RANN2  
 3348 7012 RTR  
 3351 1395 TAD RANN1  
 3352 3396 DCA RANN2  
 3353 1396 TAD RANN2  
 3354 5741 JMP I RANDOM  
  
 3355 1234 RANN1, 1234  
 3356 5670 RANN2, 5670  
  
 3359 0000 WAITEN: 0  
 3360 1775 TAD I OPI // WAS OPTION 1 SELECTED  
 3361 7004 RAL  
 3362 7700 SMA CLA  
 3363 5757 JMP I WAITEN //NO, RETURN TO RELOCATION  
 3364 2773 ISZ I KILLIT  
 3365 5365 JMP , //WAIT FOR DEVICES TO BECOME IN ACTIVE  
  
 3367 #3367  
/  
 3369 4770 LCHNG, JMS I LRERNG  
/  
 3370 9262 LRERNG, CHANGE  
 3371 1333 PSRERR, ERRPSR  
 3372 1320 ACTLIN, ERROR+5  
 3373 3101 KILLIT, KILL  
 3374 0274 RELGO, XCNT+3  
 3375 3034 OPI, SE\_OPI  
 3376 3133 PRORET, RETPRG  
 3377 0000 0  
  
 3400 #3400  
/  
 3400 5275 JMP DCNNG  
/  
 3401 1271 SETCON, TAD H7 //ROUTINE TO SET UP CONSTRAINT WORDS  
 3402 3347 DCA UPDN  
 3403 1325 TAD TABLE  
 3404 3373 DCA MOVHDX  
 3405 1773 CONRAN, TAD I MOVHDX  
 3406 3374 DCA MVWDPG  
 3407 4710 JMS I XRNAN  
 3410 7040 CMA  
 3411 3774 DCA I MVWDPG  
 3412 2373 ISZ MOVHDX  
  
 3424 4785 STARTP, JMS I OPISET  
 3425 7344 CLA CLL CMA RAL  
 3426 3706 DCA I ENDCNT  
 3427 4707 JMS I INITF0  
 3430 6081 ION //TURN THE INTERRUPT ON  
 3431 9240 JMP FILL  
 3432 7380 CLA CLL  
 3433 1272 TAD [INSCNT] //NUMBER OF INSTRUCTIONS BEFORE RELOCATION  
 3434 3722 DCA I XCNTR3  
 3435 5724 RESONT, JMP I XGNFLD //GO,BABY GO !!!  
/  
 3436 1274 ADDONE, TAD C10  
 3437 1722 TAD I XCNTR3  
 3438 3722 FILL, DCA I XCNTR3 //START WITH FIELD 0  
 3441 1722 TAD I XCNTR3 //IS THIS FIELD = TO LAST FIELD OR OVER  
 3442 7041 CIA  
 3443 1711 TAD I ZFLDLM  
 3444 7510 SPA  
 3445 5232 JMP RESONT-3  
 3446 7650 SNA CLA  
 3447 1712 TAD I ZUPLIM //YES,SET UPPER LIMITS(<1777- -7777)  
 3450 7041 CIA //IF NOT LAST FIELD UPPER LIMITS=0  
 3451 3713 DCA I ZHIGH //READ THE INSTRUCTION FIELD  
 3452 6224 RIP //NEGATE THE FIELD  
 3453 7041 CIA  
 3454 1722 TAD I XCNTR3 //GET THE FIELD TO FILL  
 3455 7650 SNA CLA //IS IT EQUAL TO THE PROGRAM FIELD  
 3456 5715 JMP I XFLRND //YES FILL AROUND THE PROGRAM  
 3457 1722 TAD I XCNTR3 //GET THE FIELD TO FILL  
 3460 7650 SNA CLA //IS IT EQUAL TO FIELD ZERO  
 3461 7387 CLA CLL IAC RTL //YES ADD FOUR TO FILL COUNTER  
 3462 1713 TAD I ZHIGH //GET COUNTER  
 3463 3713 DCA I ZHIGH //RESTORE IT  
 3464 1722 TAD I XCNTR3 //GET THE FIELD TO FILL  
 3465 7650 SNA CLA  
 3466 7387 CLA CLL IAC RTL //START FILLING AT ADDRESS 4 FOR FIELD 0  
 3467 4714 JMS I AFILL //FILL THE WHOLE FIELD  
 3470 5236 JMP ADDONE  
/  
 3471 7771 H7, #7  
 3472 0000 INSCNT, 0  
 3473 0200 K200, 200  
 3474 0010 C10, 10  
/  
 3475 #3475

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/
3475 1720 DCHNG, TAD I XDRFLG
3476 7640 SZA CLA
3477 1317 TAD XSWAP2
3500 7498 SNA
3501 1316 TAD XSWAP1
3502 3347 DCA UPDNW
3503 4704 JMS I DRERNG
3504 0202 DRERNG, CHANGE
3505 3035 OP1SET, SETOP1
3506 3303 ENDCHT, CNTEND
3507 0337 INITF0, F0INIT
3510 2127 XBRANCH, RAVCON
3511 1144 ZFLDLIM, FLDLIM
3512 1550 ZUPLIM, UPRLIM
3513 1145 ZHIGH, HGHLLIM
3514 0527 XTILL, FILALL
3515 3081 XPLRND, FILRND
3516 0216 XSWAP1, SWAP1
3517 3550 XSWAP2, SWAP2
3520 0213 XDRFLG, DIRFLG
3521 0500 XFLDFG, FLDFLG
3522 0247 XCNTR3, CNTR3
3523 0200 PRGBGN, BGV3
3524 1601 XONFLD, GENFLD
3525 3526 TABLEI, XFDSK
3526 1127 XFDSK, FLDSK
3527 1130 XONFLD, CONFLO
3530 1131 XADRNS, ADRNSK
3531 1132 XONADH, COVADH
3532 1642 XINSHS, INSHSK
3533 1643 XONINS, COVINS
3534 1133 XINOMS, INOMSK
3535 1134 XONIND, COVIND
3536 1135 XNDTHS, MDTHSK
3537 1136 XONMOT, CONMOT
3540 1137 XACOMS, ACOMSK
3541 1148 XONACD, COVACD
3542 1141 XHOMS, HQOMSK
3543 1142 XONMOC, COVMOC
3544 3424 XRSCTN, STARTP
3545 0000 J
3546 5747 JMP I ,+1
3547 7402 UPDNW, HLT /ADDRESS OF SWAP ROUTINE
3550 1375 SWAP2, TAD SIZPRO
3551 3347 DCA UPDNW
3552 1323 TAD PRGBGN
3553 3573 DCA MOVWDX
3554 1323 TAD PRGBGN
3555 1273 TAD K200
3556 3374 DCA MMWDPG
3557 1774 MOVDOWN, TAD I MMWDPG
3560 3773 DCA I MOVWDX
3561 1774 TAD I MMWDPG /COMPARE THE WORDS BEING RELOCATED

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3562 7041 CIA
3563 1773 TAD I MOVWDX
3564 7640 SZA CLA
3565 7402 HLT /COMPARE ERROR DURING RELOCATION
3566 2373 ISZ MOVWDX
3567 2374 ISZ MMWDPG
3570 2347 ISZ UPDNW
3571 5357 JMP MOVDOWN
3572 5744 JMP I XRSCTN
3573 2803 MOVWDX, 0
3574 0000 MMWDPG, 0
3575 4401 STEPORG, BG4=PRGEND-1
3576 3576 PRGEND, .

2200 *200
0200 4823 JMS PATCH
0201 4823 JMS PATCH

3600 *3600

/
3600 1821 PATCH2, TAD OP1SEL /GET THE HARDWARE CONFIGURATION
3601 7004 RA, /PUT OPTION1 BIT IN BIT 0
3602 7700 SMA CLA /IS OPTION 1 SELECTED?
3603 5233 JMP SLOWRN /NO, OVERLAY SECTIONS OF RANDY AND ERROR1
3604 1276 TAD K4772 /YES, SET UP A TALK LOOP TO PROM
3605 3677 DCA I OP1HND /PUT JMS I ACTLIN IN RTCSER+2
3606 1300 TAD OP1OVR
3607 3010 DCA AUTO10
3610 1301 TAD ERRORS
3611 3011 DCA AUTO11
3612 1250 TAD MM20
3613 3145 DCA PATHMOV
3614 1410 TAD I AUTO10
3615 3411 DCA I AUTO11
3616 2145 ISZ PATHMOV
3617 5214 JMP ,#3
3620 1275 TAD R5771
3621 3651 DCA I CVR1
3622 1275 TAD R5771
3623 3652 DCA I CVR2
3624 1275 TAD R5774
3625 3653 DCA I CVR3
3626 1275 TAD R5771
3627 3653 DCA I CVR3
3630 1275 TAD R5771
3631 3654 DCA I CVR4
3632 5423 JMP I PATCH /THIS SECTION OF CODE WILL OVERLAY LAST 2 LOCATIONS OF RANDY ALL OF ERROR1 EXCEPT LAST 2 LOC.

3633 1246 SLOWRN, TAD ACTOVR
3634 3810 DCA AUTO10
3635 1247 TAD STRRND

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3636 3911      DCA    AUTO11
3637 1290      TAD    MM28
3640 3145      DCA    PATMOV
3641 1419      TAD I AUTO16
3642 3411      DCA I AUTO11
3643 2145      ISE    PATHOV
3644 5241      JMP   .+3
3645 5423      JMP I PATCH

3646 3654      ACTOVR, ACCDOV=1
3647 1412      STRRND, RANDY1=1
3650 7760      MM28, =23
3651 3213      OVR1, RESET#7
3652 3244      OVR2, PISSER#5
3653 3251      OVR3, PISSER#12
3654 3323      OVR4, RESET#1
/
3655 2230      ACCDOV, 2230
3656 5226      5226
3657 2231      2231
3658 5226      5226
3661 6002      10F
3662 7248      CLA    DMA
3663 3231      3231
3664 3238      3238
3665 6272      CIF    70
3666 4632      4632
3667 6001      104
3670 1344      CONTRD, 1344
3671 5601      5601
3672 0000      0
3673 7777      +1
3674 6500      6500

3675 5771      R5771, 5771
3676 4772      K4772, 4772
3677 3287      DP1HND, RTSSER#2
3700 3701      DP1OVR, OVRP1=1
3701 1317      ERROR5, ERROR#4
/
3702 0000      OVRP1, 0
3703 2331      2331
3704 5720      5720
3705 1332      1332
3706 3331      3331
3707 6272      CIF    70
3710 4730      4730
3711 5720      5720
3712 6500      6500
3713 7634      =144
3714 7634      =144
3715 6002      10F
3716 1333      1333
3717 6272      CIF    70

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3720 5737      5737
3721 6520      6520
0208      0208
5

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4200  
4100  
  
4200  
4300  
  
4400  
4500  
  
4500  
4700

5000  
5100  
5200  
5300  
5400  
5500  
5600  
5700

6200  
6100  
6200  
6300  
6400  
6500

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	PAL10	V142	21-DEC-74	14:34	PAGE 1-48
A1	1653	BCHNG	1347	CONACD	1148
A14	1651	BDATHR	1376	CONADR	1132
A1CHG	1150	BDATHH	1375	CONFLO	1130
A1RRNG	1151	SEGIN	0567	CONFGLG	1143
A200	1647	SGN	0260	CONING	1134
A400	1124	SGNCON	3026	CONINS	1643
A5201	2144	SGOTST	0524	CONHOI	1136
A7	0743	SINSTR	0540	CONHOI	1142
AACHNG	0551	SLPCNT	1365	CONRAN	3495
AADDRS	1166	SNDCON	1551	CONSET	3162
AASAVA	1760	SNDOOK1	1445	CONST1	1105
AASAVB	1761	SNDOOK2	1457	CONST2	1112
ABON	3173	SNDRY1	1435	CONTLY	3171
ABNRY1	1164	SP6	1554	CONTRO	3670
ACDATA	1031	BRANDY	1795	COPRST	2773
ACDMSK	1137	BREFAD	1374	CREFAD	1574
ACGDDV	3655	CRERNG	1350	CRERNG	1563
ACHG	0768	CSAVA	2145	CSAVB	1553
AICHNG	0355	CSINAC	2575	CSAVC	1554
ACL	7701	CSMLNK	2576	CSAVD	1555
ACTL1N	3372	C18	3474	CSIHAG	2778
ACTOVR	3646	C1777	1561	CSIMM9	2772
ACUTST	2623	C280	1312	CSMLNK	2771
ADATAH	1172	C408	1547	CSWPUP	0553
ADATAT	1171	C5	1556	D4800	2625
ADD11	1124	C6201	1123	D5281	2070
ADDONE	3436	C7	1557	DATADN	2758
ADDRS	0745	C78	1560	DATANR	0752
ADDRS0	3143	CADDRS	1575	DATATR	0751
ADDRS5	1612	CAF	6067	DATEN	1372
ADMMSK	1131	CAL	6103	DBCE	6576
AEROV1	0113	CC377	3236	DBCF	6573
AEROV2	0128	CCHNG	1562	DBRD	6572
AINST	1167	CDATAT	1567	DBSE	6575
ALOP1D	1176	CDHLT1	0540	DBSK	6571
AMHOAT	1173	CDHLT2	0544	DBSS	6577
ANDTST	1281	CHANGE	2020	DBST	6570
ANTIND	1175	CHK1KF	1154	DBTD	6574
ARANDY	1163	CHKACT	3313	DOATST	1255
AREFAD	1170	CIFFD0	0345	DOCHNG	3475
ARERNG	0767	CINSTR	2162	DECSWP	1116
ARRANG	0305	CLASHP	2571	DINSTR	2345
ASAME1	1161	CLCL	6136	DIRFLG	0213
ASAME2	1162	CLLE	6135	DONEMD	1362
ASAVA	1146	CLSK	6137	DRERNG	3504
ASAVB	1147	CNT	2425	DVINAC	3161
AUTO10	0018	ENTEYD	3303	EINSTR	1794
AUTO11	0211	CNTR2	0246	ENDCMA	2422
B400	1650	CNTR3	0247	ENDCTN	3508
B6201	0526	CO4CHK	2227	ENDOFP	3175
B7	0543	COMPAR	1206	ERROPR	2304
B400RS	1366	COV37	0134	ERROR	1313
				HCHNG	2343

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	PAL10	V142	21-DEC-74	14:34	PAGE 1-49
HGHLM	1145	K5771	0136	M1N37	0135
HIGHLM	0374	K5772	0137	M20	3650
HLT	7402	K6201	0756	MH4	3023
HLTFIL	0466	K6202	0757	MHS	3077
HLTOP1	1741	K78	1125	MM6	0142
HMOC1P	0744	K7800	1644	MM7	1545
HRERNG	2344	K7610	0132	MHHS	3340
ICHNG	2567	K7700	2517	MHDWN	3557
ILLHQ	1703	K7721	1655	MHOVVR	2163
ILLDPS	1664	K7773	0760	MHOVUP	0225
ILLDPS2	1672	K7777	2624	MHOVWD	3573
INACDV	3337	KCHNG	3146	MHQAT	1363
INDAD	0750	KIE	6035	MHQATA	0753
INDMSK	1133	KILL	3101	MQDMSK	1141
INITFD	3507	KILLIT	3373	MQDNE	2753
INSCNT	3472	KJMS	0784	MQL	7421
INSGEN	1601	KK377	3182	MRIERR	2161
INSHSH	1642	KKCDF	3145	MRIORV	0126
INSOK	1520	KKK4	3024	MHQAT	1363
INSTR	0746	KLAS	0147	MVMDPG	3574
INT	0803	KRERNG	3147	NEG10	2072
INTAC	3141	LAS	7684	NEG14	1652
INTERS	3102	LASCNT	0143	NEG20	2304
INTLNK	3142	LASTBL	0144	NEG6	2460
INTMD	1757	LCHNG	3367	NEWDFA	0475
INTDPR	2175	LIMITS	1522	NEWDTF	0457
INTRET	3144	LINKDN	2791	NOTAUT	0611
INTSET	2137	LINKRT	1301	NOTIND	0625
IRERNG	2578	LINKSY	1360	NOTJJ	0658
IS2ST	1234	LNSKAV	1174	O201	1733
JCHNC	2764	LOC200	0140	OADDRS	1773
JMPJMS	0565	LOC221	0141	OP1SET	3505
JMPITST	1304	LOOPID	0681	OPERR1	2767
JMSLOC	0762	LOOPSW	1342	OPFIELD	2176
JMSLGD	0763	LCBLIM	0214	OP1	3375
JMSRET	0776	LPCNT	0252	OP1HND	3677
JMSTST	1267	LPSW0	2737	OP1OVR	3708
JRERNG	2765	LRERNG	3370	OP1SEL	0021
K1	2861	M10	0503	OP1SET	3505
K10	0501	M13	2536	OP2SEL	0022
K100	2067	M14	2424	OPERR1	1734
K14	2864	M1777	2565	OPRANR	3152
K177	1646	M20	0502	OPRBGN	1656
K2	2862	M280	0251	OPRCOM	2706
K20	2065	M480	0215	OPRER1	2376
K200	3473	M5	0133	OPRERR	2744
K37	1546	M60	2365	OPRETF	2752
K4	2863	M6002	1645	OPRHLT	2364
K46	2866	M7	3471	OPRINT	0637
K480	0258	MAXFLD	0573	OPROVH	0130
K4800	2434	MOTMSK	1135	OPRPN1	1752
K4772	3676	MEMDAT	1029	OPRRET	2675
				OPRSET	2094
				RESET	3324

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RESETT	0256	SIMOP1	2001	WAIT	0360	XMQDMS	3542
RETFLD	0225	SIMOP2	2201	WAITEN	3357	XMTFLG	3226
RETHR	0204	SIMRAL	2461	XACDM8	3540	XMTPIO	3234
RETPNT	0224	SIMRAR	2442	XACSIM	2352	XMTSER	3201
RETPRG	3533	SIMRTL	2537	XADDI	3176	XMTSLU	3232
RETTDF	2705	SIMRTR	2520	XADRM8	3530	XNTIND	0363
RETURN	1354	SIMSNL	2626	XASAVA	0354	XPCSAV	2155
RMF	0244	SIMSWA	2611	XASAVB	0345	XRNANCN	3518
ROLBAK	0401	SIMSWP	2692	XBCON	0372	XREFAD	0366
ROLFLG	2574	SIMSEA	2681	XBCRAN	0371	XRETF1L	1373
ROLLUP	0320	SIMTC	2042	XBNDCN	1160	XRETIWR	1165
RSGNT	0376	SIEPRG	3575	XBSAVA	1356	XRETPC	2156
RSGNTX	0370	SKPFLG	2251	XCHNGE	0552	XRNFLD	1364
RTGFLG	3231	SLDRN	3633	XCLSHP	2362	XROLBK	0361
RTGSER	3255	SLJXMT	3153	XCNACD	3541	XRSCTN	3544
RTPLGF	0572	SMACK	2202	XCNACR	3531	XRTDFF	1774
RTPLGF	0574	SNLCHK	2220	XCNFLD	3527	XSIHAC	1763
RTPLGR	0571	SPL	6102	XCNFLG	3150	XSIHM2	1766
RTLINK	0557	SRSAV	2766	XCNIND	3535	XSIZE	0575
RTMOD	0556	START	1756	XCNINS	3533	XSMAL	2361
SAME1	1483	STARTP	3424	XCNMDT	3537	XSMBSW	2170
SAME2	1473	STRFLD	0205	XCNMDQ	3543	XSMCAM	2360
SAVESW	1564	STRRND	3647	XCNNT	0271	XSMCLA	2357
SAVLNK	0755	SUBADD	0212	XCNTRS	3522	XSMCMA	2163
SAVSWR	0336	SWAP1	0216	XOATAH	1762	XSMCML	2164
SBE	6101	SWAP2	3550	XOFGLG	3520	XSMIAC	2165
SEL0PI	3834	SNAPON	0422	XENDPR	0375	XSMLNK	1765
SERPID	0167	SNAPUP	0415	XERR2	1572	XSMHQA	2355
SERRREC	3166	SWITCH	0200	XERROR	1353	XSMHOI	1772
SERRTC	3170	SWPFLD	0463	XEXPRT	1767	XSMQD	2354
SERXMT	3165	SWPUP	0452	XFDMSK	3526	XSMOP1	1770
SETCON	3401	SWRSAY	1351	XFTELD	2775	XSMOP2	1771
SETFLG	0327	SZACHK	2211	XFTILL	3514	XSMRAL	2167
SETIN	0357	SZPG	0245	XFLD	0386	XSMRAR	2166
SETDPI	0335	TABLAS	0150	XFLDFG	3521	XSMRTL	2172
SETRET	0710	TABLE	3525	XFLDLH	1573	XSMRTT	2171
SETSKP	2226	TAJTST	1223	XFLRND	3515	XSMSSA	2346
SFLDFG	0433	TEMP	3025	XGETNTI	1155	XSMSNL	2350
SIMAC	2755	TINT	2143	XGETFLD	1357	XSMSWP	2356
SIMACL	2666	TSAME	1583	XGNFLD	3524	XSMSEA	2347
SIMBSX	2477	TSFLDF	2572	XGO	0576	XSTFLD	0554
SIMCAH	2663	TSTI41	0562	XHALT	1565	XSVLNK	1764
SIMCLA	2661	TSTI42	0563	XINDM8	3534	XSWAP1	3516
SIMCLR	6160	TSTI43	0564	XINSGN	2774	XSWAP2	3517
SIMCM8	2401	TSTI44	0565	XINSMS	3532	XUPERL	0373
SIMCML	2426	TSTI45	0566	XINSTR	0362	XUPLIM	1157
SIMIAC	2435	TSTI4S	0561	XINT	2151	XWDNSY	2153
SIMLNK	2756	TSTPC	1355	XJMSLQ	2154	XXCNT	2776
SIMHO	2757	TSAPDN	2573	XLIIMIT	3151	YHALT	0770
SIMMGA	2644	UPDNW	3547	XLOOP	0367	Z100	2243
SIMMOL	2252	UPPERL	2571	XLWLIM	0570	Z220	2245
SIMMOL	2640	UPRLIM	1550	XHDHTMS	3536	Z220	2246

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Z320	2247
Z40	2244
ZASAVA	1370
ZASAVB	1371
ZBSAVA	1570
ZCNFLG	2152
ZCNT	0775
ZEXPRT	2351
ZFILL	3174
ZFIND	2774
ZFLDLH	3511
ZGETHD	0773
ZHIGH	3513
ZINADD	1571
ZINSTR	1566
ZJMSTR	1793
ZLIMIT	0555
ZSETOP	2353
ZUPLIM	3912

ERRORS DETECTED: 0

LINKS GENERATED: 0

RUN-TIME: 14 SECONDS

3K CORE USED