File: s100_B0386_SRAM_V11.sch
Sheet: /
Title: noname.sch
Size: D | Date: 18 Aug 2016 | Rev:
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable | Id: 1/1

Card Ejector Mounting Holes

Pololu 5V,1A Reg (Optional)

Pololu 5V,2.5 Amp Regulator

Spare for further needs

Spare for further needs

Note pulse is short I will only work up when a good logic probe

Note It's important this is a 74S04 (74S04 or 74LS04-s) will not work well.

To RAM Chips

To CPU

BE3-BANK

BE2-BANK

BE1-BANK

BE0-BANK

BANK3*

BANK2*

BANK1*

BANK0*

V_RAM

ADDR_VALID*

DB_PHANTOM*

BOARD_SELECT*

DB_ADS*

BOARD_SELECT*

D_LED*

C_LED*

B_LED*

A_LED*

VCC

GND