

PORT MAP

Revision A

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PORT ASSIGNMENTS

<u>Address (Hex)</u>	<u>Boards</u>	<u>Descriptions</u>
00	Flashwriter I	Console or Keyboard Status
00	Bitstreamer I	Parallel Port A: Bidirectional
00	Flashwriter II	Console or Keyboard Status
00	Analog Interface	Input: Look at Comparators
01	Flashwriter I	Keyboard Data
01	Bitstreamer I	Parallel Port A: Bidirectional
01	Flashwriter II	Keyboard Data
01	Analog Interface	Output: Reset
02	Bitstreamer I	Serial Port A: Data
02	Bitstreamer II	Serial Port A: Data
03	Bitstreamer I	Serial Port A: Status
03	Bitstreamer II	Serial Port A: Status
04	Bitstreamer II	Serial Port B: Data
04	ZCB	Serial Port A: Data
05	Bitstreamer II	Serial Port B: Status
05	ZCB	Serial Port A: Status
06	Bitstreamer II	Serial Port C: Data
06	ZCB	Serial Port A1: Data
07	Bitstreamer II	Serial Port C: Status
07	ZCB	Serial Port A1: Status
08	Bitstreamer II	Parallel Port: Bidirectional
08	ZCB	Parallel Port: A (programmable)
09	Bitstreamer II	Parallel Port: Bidirectional
09	ZCB	Parallel Port: B (programmable)
0A	ZCB	Parallel Port: C (programmable)
0B	ZCB	Parallel Port: Control
10	Precision Analog	Output: MSB Channel A Input: Comparator Status
11	Precision Analog	Lower 4 Bits: MSB of Channel A
12	Precision Analog	Output: MSB of Channel B
13	Precision Analog	Output: LSB of Channel B
14	Precision Analog	Control
40	64K Dynamic RAM	Memory Bank Select
40	16K Static RAM	Memory Bank Select
7E	Video Digitizer	Output: T Count High Input: Status

<u>Address (Hex)</u>	<u>Boards</u>	<u>Descriptions</u>
7F	Video Digitizer	Output: T Count Low Input: A Data
80	8 Port Parallel I/O	Parallel Port: Bidirectional
81	8 Port Parallel I/O	Parallel Port: Bidirectional
82	8 Port Parallel I/O	Parallel Port: Bidirectional
82	Clock/Calendar	Serial: Data
83	8 Port Parallel I/O	Parallel Port: Bidirectional
83	Clock/Calendar	Serial: Status
84	8 Port Parallel I/O	Parallel Port: Bidirectional
84	Clock/Calendar	Output: Write Data to Clock Input: Read Data from Clock
85	8 Port Parallel I/O	Parallel Port: Bidirectional
85	Clock/Calendar	Output: Incrementing Clock
86	8 Port Parallel I/O	Parallel Port: Bidirectional
86	Clock/Calendar	Output: Initializes/Sets Clock
87	8 Port Parallel I/O	Parallel Port: Bidirectional
87	Clock/Calendar	Output: Control
C0	Dualmode Disk Controller	Output: Control Ports Input: Status
C1	Dualmode Disk Controller	Output: Control Ports Input: Status
C2	Dualmode Disk Controller	Bidirectional Data Port
C3	Dualmode Disk Controller	Output: Start Input: Reset
D0	Network Controller	Input: Control Hardware Address
D1	Network Controller	Output: Load Controller Word Input: Read Controller Status Word
D2	Network Controller	Output: Load Buffer RAM Input: Read Buffer RAM
D3	Network Controller	Output: Output Data Don't Care Input: Reset
E0	Floppy Disk Controller	Output: Communication Input: Status
E1	Floppy Disk Controller	Output: Track Input: Track
E2	Floppy Disk Controller	Output: Sector Input: Sector

<u>Address (Hex)</u>	<u>Boards</u>	<u>Descriptions</u>
E3	Floppy Disk Controller	Output: Data Input: Data
E4	Floppy Disk Controller	Output: Drive Select Input: Data Request and Interrupt Request
E5	Floppy Disk Controller	Input: Data Request Wait
E6	Floppy Disk Controller	Serial Port A: Data
E7	Floppy Disk Controller	Serial Port A: Status

NOTE: The version 3 multiuser time share system occupies parallel ports 08-0F on the Flashwriter board (five user system).