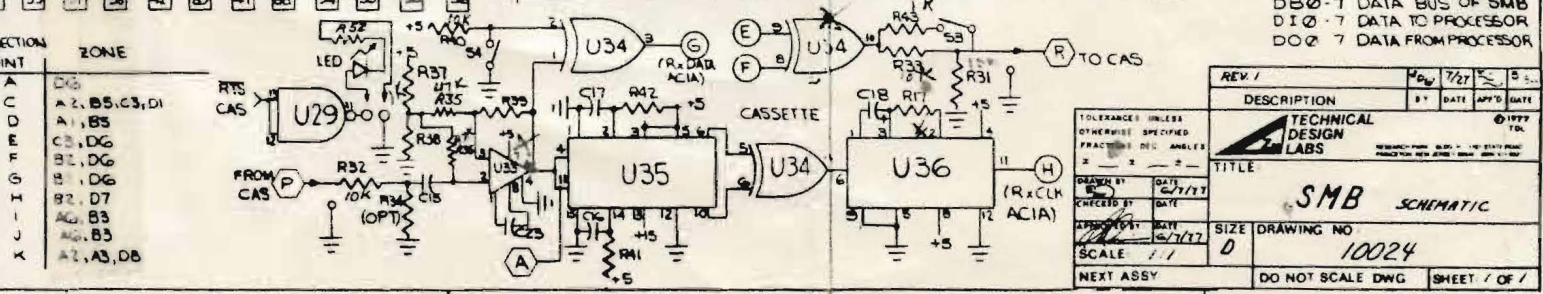
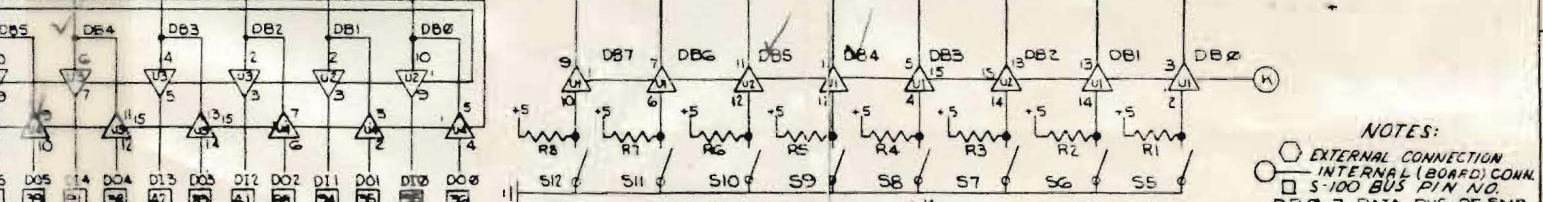
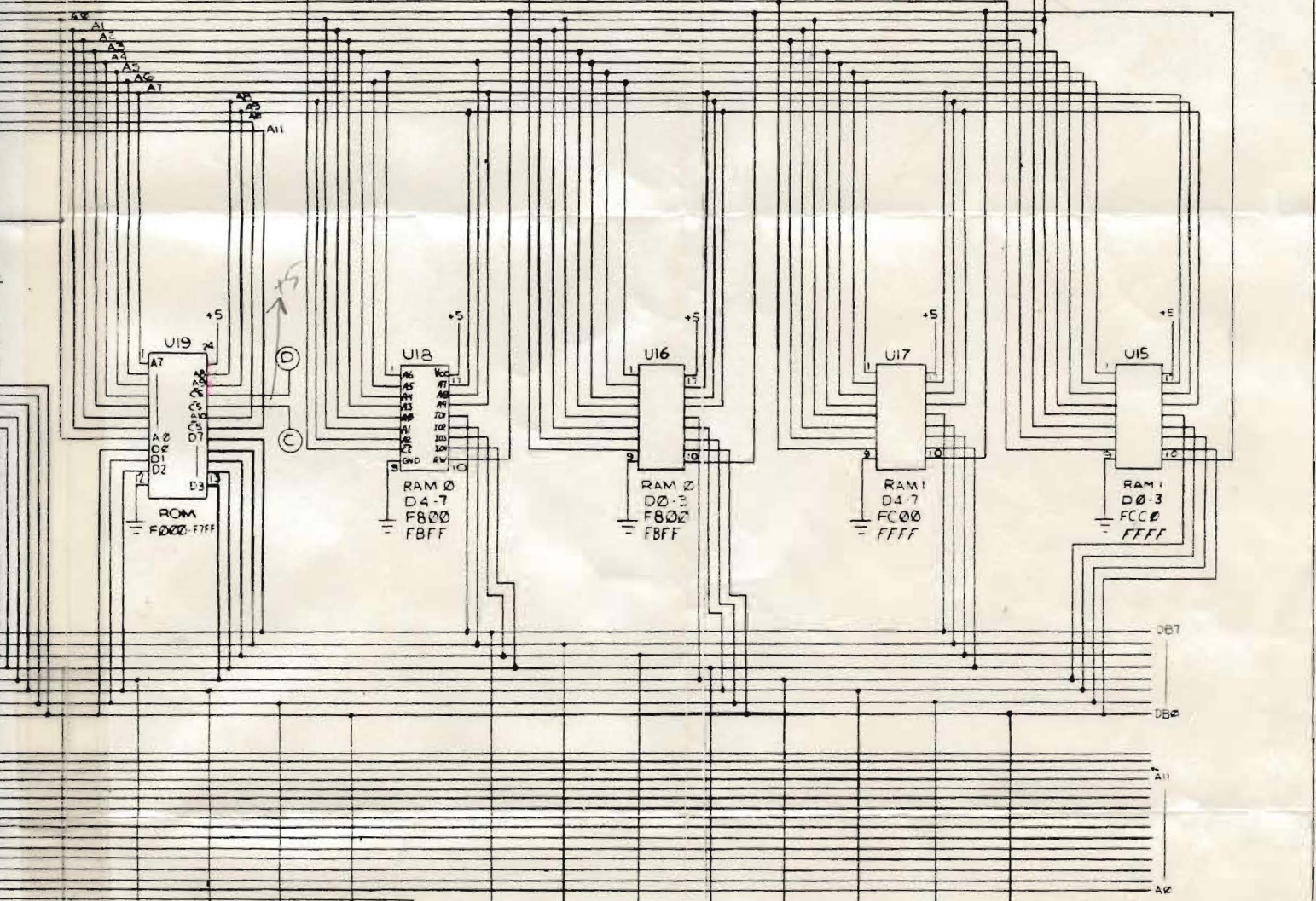
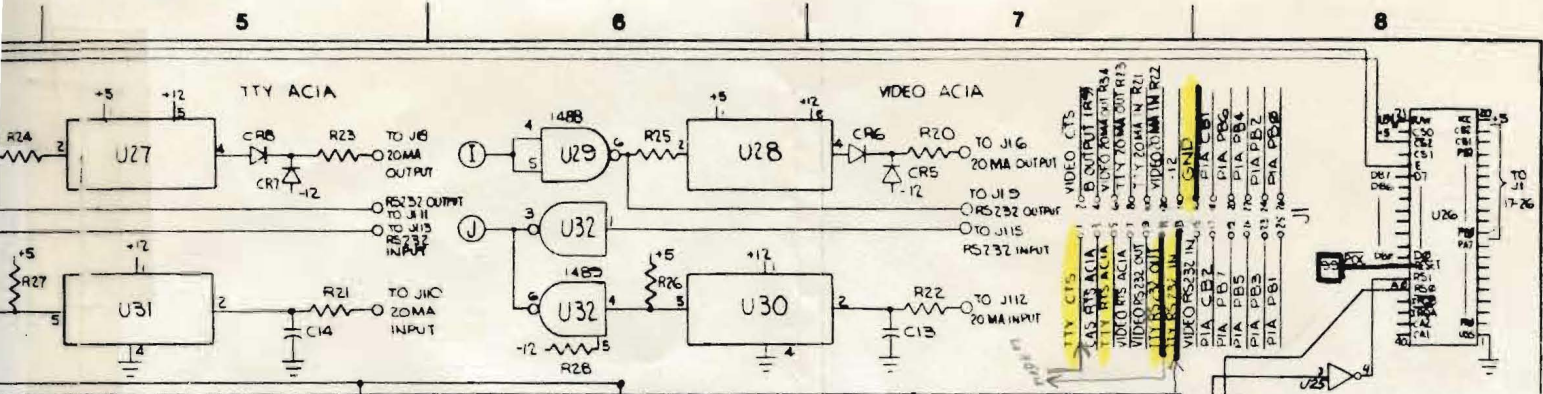


TOLERANCES UNLESS OTHERWISE SPECIFIED	
FRACWISE SPECIFIED	
RESISTORS: 1% - 1% - 1%	
CAPACITORS: 1% - 1% - 1%	
DRAWN BY: DATE: 10/17/77	
CHECKED BY: DATE:	
APPROVED BY: DATE:	
SCALE: 1:1	
NEXT ASSY:	
REV: 1	DESIGN:
TITLE:	
SIZE: D	DATE:



NOTES:
 ○ EXTERNAL CONNECTION
 — INTERNAL (BOARD) CONN.
 S-100 BUS PIN NO.
 DB0-7 DATA BUS OF SMB
 D10-7 DATA TO PROCESSOR
 D02-7 DATA FROM PROCESSOR

CONNECTION POINT	ZONE
A	D6
C	A2, B5, C3, D1
D	A1, B5
E	C3, D6
F	B1, D6
G	B1, D6
H	B1, D6
I	B2, D7
J	AG, B3
K	AG, B3
L	A2, A3, D8

REV. /	DATE	BY	DATE	APP'D.	DATE
1	2/7/77				

DESCRIPTION: **TECHNICAL DESIGN LABS**

TITLE: **SMB SCHEMATIC**

SIZE: **D** DRAWING NO: **10024**

SCALE: **1/1**

DO NOT SCALE DWG. SHEET / OF /