

88-SYS CLG

8800B-T systems that have the Turnkey - Module PCBA installed cannot utilize the total 64K of RAM memory (4 16MCD PCBA's or 4 16MCS PCBA's). Modification of the Turnkey - Module PCBA to disable the 1K RAM and to allow PROM and RAM to address the same locations will allow the total 64K of memory to be installed. This modified Turnkey Module is called the 88-SYS CLG. The modifications required are shown below.

TURNKEY BOARD MODIFICATIONS

Component side of board:

- 1) cut land going to pin 11 of IC "D"
- 2) cut land between pins 11 & 12 of IC "D"
- 3) cut land going to pin 12 of IC "D"

Back side of board:

- 1) cut land going to pin 11 of IC "B"
- 2) cut land going to pin 7 of IC "Q"
- 3) cut land going to pin 13 of IC "M1"
- 4) jumper from collector of "Q2" to pin 47 of buss
- 5) jumper from pin 13 of IC "M1" to pin 47 of buss
- 6) jumper from pin 11 of IC "D" to land from pin 5 of IC "E"
- 7) jumper from pin 12 of IC "D" to pin 8 of IC "T"
- 8) jumper from pin 11 of IC "B" to ground
- 9) jumper from pin 7 of IC "T" to pin 14 of IC "U" (+5v)
- 10) jumper from pin 10 to pin 11 of IC "T"
- 11) jumper from pin 2 to 6 of IC "T"
- 12) jumper from pin 5 of IC "T" to pin 4 of IC "W"
- 13) jumper from pin 2 of IC "W" to pin 15 of IC "M1"
- 14) jumper from pin 7 of IC "Q" to "IRQ" land