

Cromemco Z-1

MICROCOMPUTER SYSTEM

PROGRAMMED OUTPUT

[Empty bar for programmed output]

6 5 4 3 2 1 0

INP MI INTA 4MHZ WO INTA

[Empty bar for status byte]

STATUS BYTE

7 6 5 4 3 2 1 0

[Empty bar for data bus]

DATA BUS

8 4 2 1 8 4 2 1 2 1 4 4 2 2 1 1

8 4 2 1 8 4 2 1 2 1

HEXADECIMAL
OCTAL

[Empty bar for address bus]

ADDRESS BUS

15 7 14 6 13 3 12 2 11 1 10 9 8 1 0

7 6 5 4 3 2 1 0

ADDRESS BUS

ADDRESS PROGRAMMED INPUT

INTERRUPTS ENABLED

[Empty bar]

RUN

[Empty bar]

WAIT

[Empty bar]

HOLD

[Empty bar]

EXAMINE DEPOSIT

[Empty bar]

EXAMINE NEXT DEPOSIT NEXT

[Empty bar]

RESET

[Empty bar]

RUN

[Empty bar]

SINGLE STEP

[Empty bar]

STOP

[Empty bar]

SINGLE STEP

[Empty bar]

PWR ON

[Empty bar]

EXT. CLR.

[Empty bar]

SINGLE STEP

[Empty bar]

PWR OFF

[Empty bar]

ADDRESS-DATA