

CLOCK GENERATOR

SEQUENCER & LOGIC

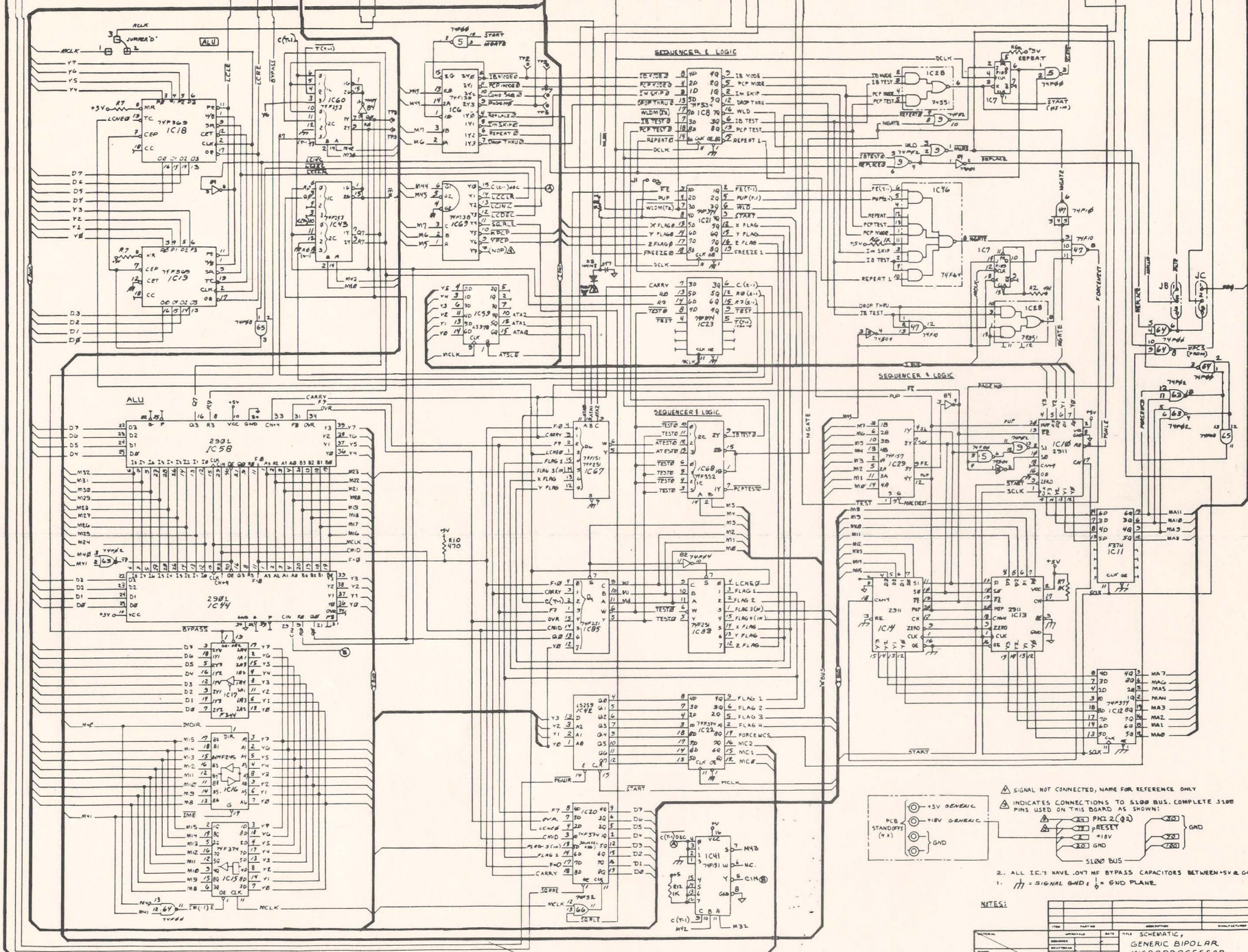
SEQUENCER & LOGIC

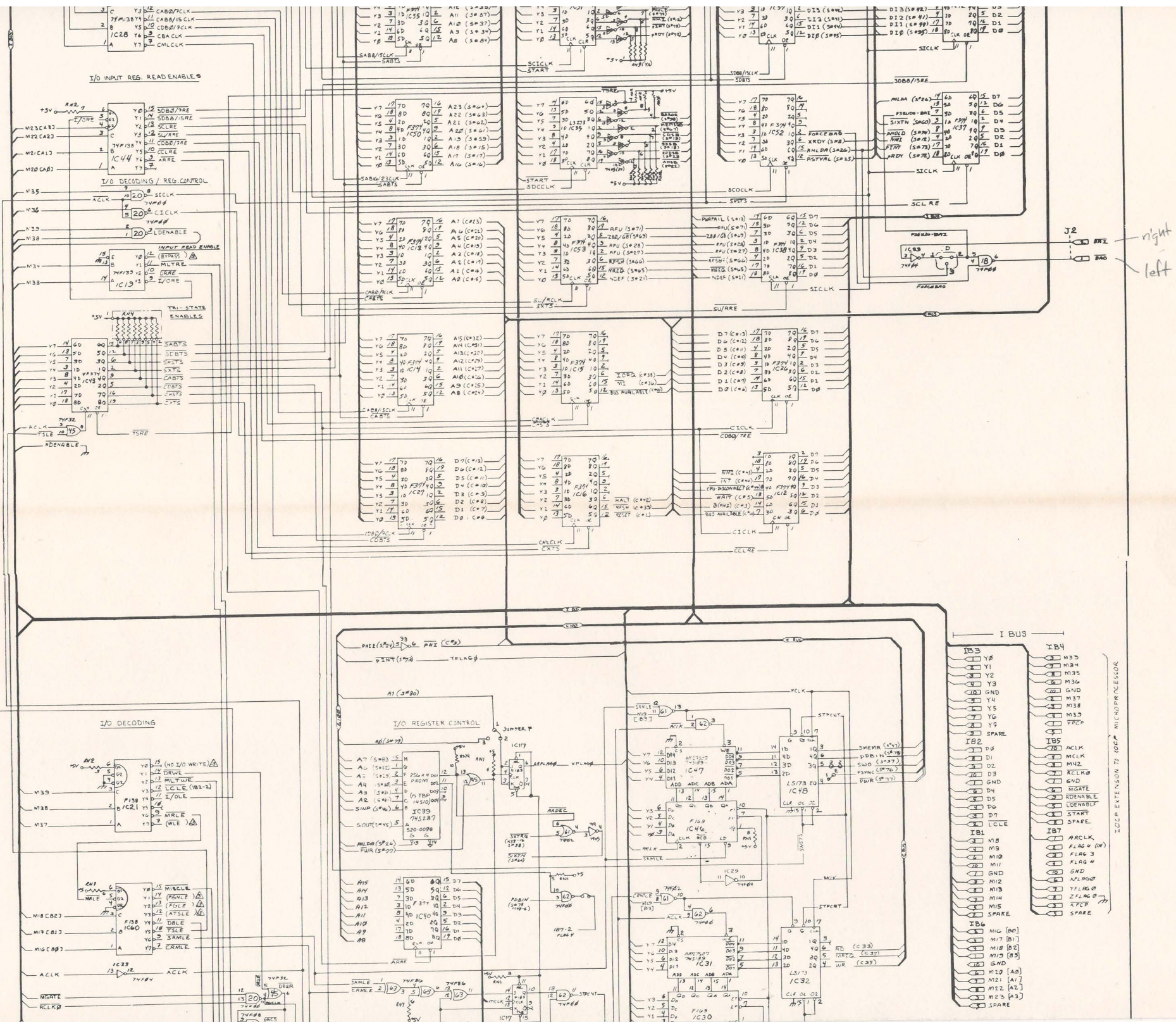
SEQUENCER & LOGIC

ALU

ALU

- IB1 1 GND
- IB1 2 SPARE
- IB1 3 M8
- IB1 4 M9
- IB1 5 M10
- IB1 6 M11
- IB1 7 M12
- IB1 8 M13
- IB1 9 M14
- IB1 10 M15
- IB2 1 GND
- IB2 2 YPCP
- IB2 3 LCLK
- IB2 4 D7
- IB2 5 M33
- IB2 6 M34
- IB2 7 M35
- IB2 8 M36
- IB2 9 M37
- IB2 10 D9
- IB3 1 Y8
- IB3 2 Y1
- IB3 3 Y2
- IB3 4 Y3
- IB3 5 Y4
- IB3 6 Y5
- IB3 7 Y6
- IB3 8 Y7
- IB3 9 SPARE
- IB3 10 GND
- IB4 1 YPCP
- IB4 2 LCLK
- IB4 3 M33
- IB4 4 M34
- IB4 5 M35
- IB4 6 M36
- IB4 7 M37
- IB4 8 M38
- IB4 9 M39
- IB4 10 D9
- IB5 1 GND
- IB5 2 START
- IB5 3 M42
- IB5 4 NCLK
- IB5 5 DEENABLE
- IB5 6 MDATE
- IB5 7 RCLK
- IB5 8 SPARE
- IB5 9 DEENABLE
- IB5 10 ACLK
- IB6 1 M15 (B3)
- IB6 2 M16 (B4)
- IB6 3 M17 (B1)
- IB6 4 M18 (B2)
- IB6 5 SPARE
- IB6 6 M20 (A3)
- IB6 7 M21 (A1)
- IB6 8 M22 (A2)
- IB6 9 M23 (A3)
- IB6 10 GND
- IB7 1 FLAG 3 (A)
- IB7 2 FLAG 3 (A)
- IB7 3 FLAG 3 (A)
- IB7 4 FLAG 4
- IB7 5 SPARE
- IB7 6 FLAG 6
- IB7 7 FLAG 6
- IB7 8 FLAG 6
- IB7 9 XPCP
- IB7 10 GND



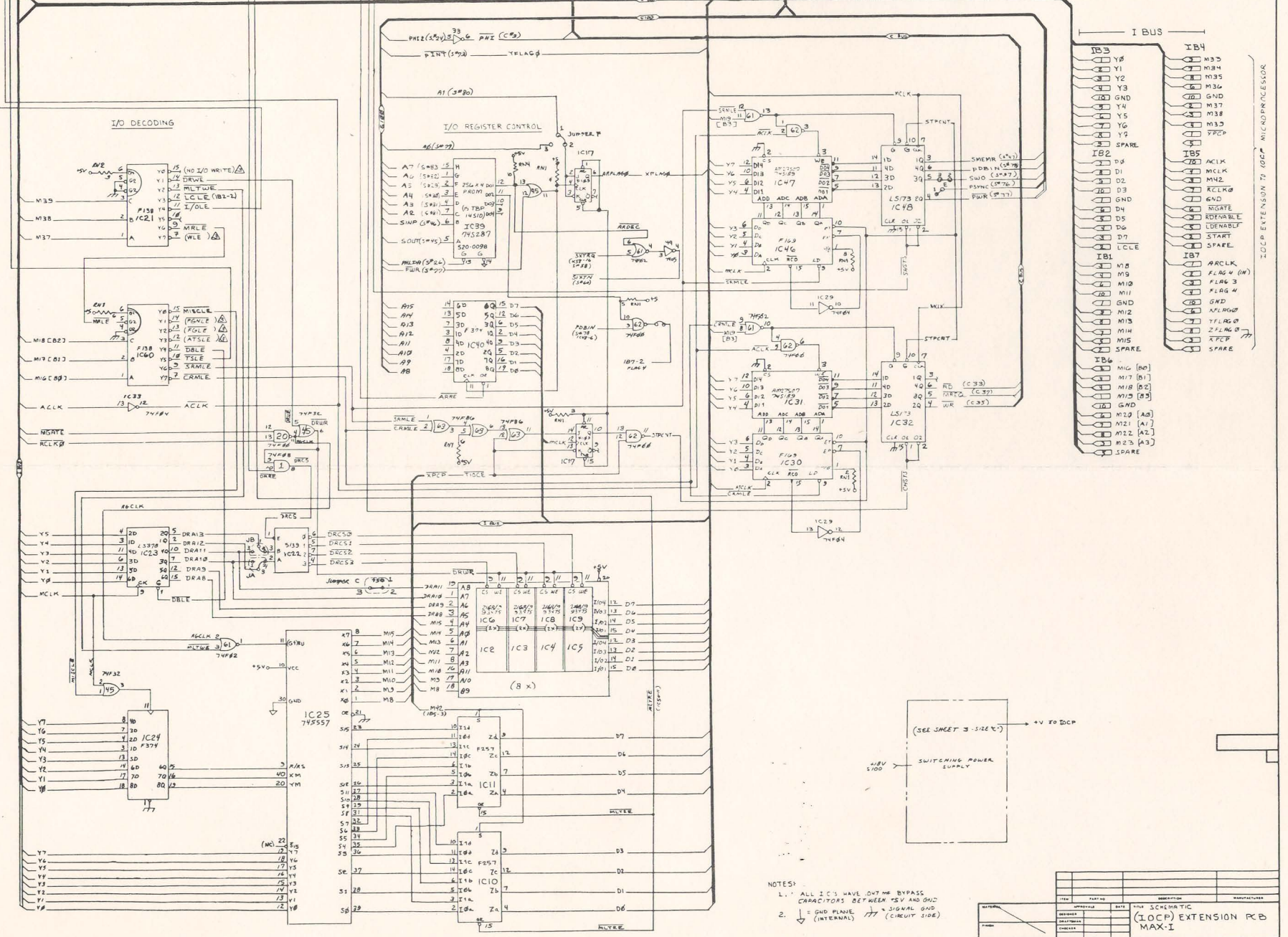


right
left

I BUS
IB3
IB4
IB5
IB6
IB7
IB8
IB9
IB10
IB11
IB12
IB13
IB14
IB15
IB16
IB17
IB18
IB19
IB20
IB21
IB22
IB23
IB24
IB25
IB26
IB27
IB28
IB29
IB30
IB31
IB32
IB33
IB34
IB35
IB36
IB37
IB38
IB39
IB40
IB41
IB42
IB43
IB44
IB45
IB46
IB47
IB48
IB49
IB50
IB51
IB52
IB53
IB54
IB55
IB56
IB57
IB58
IB59
IB60
IB61
IB62
IB63
IB64
IB65
IB66
IB67
IB68
IB69
IB70
IB71
IB72
IB73
IB74
IB75
IB76
IB77
IB78
IB79
IB80
IB81
IB82
IB83
IB84
IB85
IB86
IB87
IB88
IB89
IB90
IB91
IB92
IB93
IB94
IB95
IB96
IB97
IB98
IB99
IB100

M30
M31
M32
M33
M34
M35
M36
M37
M38
M39
M40
M41
M42
M43
M44
M45
M46
M47
M48
M49
M50
M51
M52
M53
M54
M55
M56
M57
M58
M59
M60
M61
M62
M63
M64
M65
M66
M67
M68
M69
M70
M71
M72
M73
M74
M75
M76
M77
M78
M79
M80
M81
M82
M83
M84
M85
M86
M87
M88
M89
M90
M91
M92
M93
M94
M95
M96
M97
M98
M99
M100

ICLCP EXTENSION TO I/O CPU MICROPROCESSOR



- NOTES:
- ALL IC'S HAVE OUT NO BYPASS CAPACITORS BETWEEN +5V AND GND
 - ↓ = GND PLANE ↗ = SIGNAL GND (INTERNAL) ↘ = CIRCUIT SIDE
- ▲ SIGNAL NOT CONNECTED, NAME FOR REFERENCE ONLY.

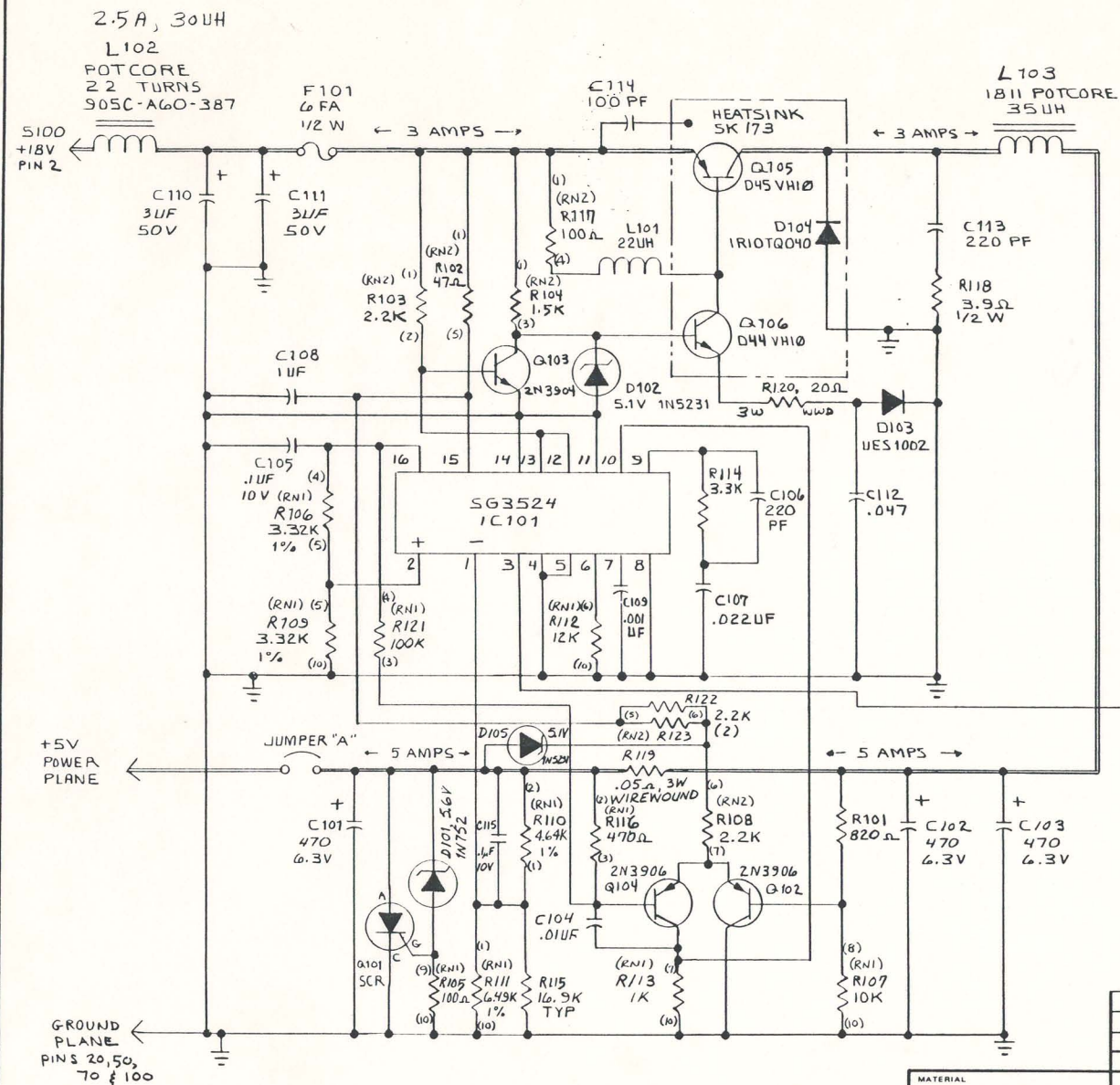
REV	DATE	DESCRIPTION	MANUFACTURER
1		SCHMATIC	
2		(10CP) EXTENSION PCB	
3		MAX-I	

REV	DATE	DESCRIPTION	MANUFACTURER
1		SCHMATIC	
2		(10CP) EXTENSION PCB	
3		MAX-I	



MAX-I - 040 - 0100

REVISIONS			
LTR	DESCRIPTION	DRAFTSMAN	DATE



INTERNAL SIGNAL
REG. SYNC
ON MAX-C-020-0101 ONLY

BOARD REV. B

ITEM	PART NO	DESCRIPTION	MANUFACTURER

MATERIAL	APPROVALS	DATE	TITLE
	DESIGNER		25W 5100 BOARD
	DRAFTSMAN		SWITCHING POWER SUPPLY
FINISH	CHECKER		
	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE FRACTIONS DECIMALS ANGLES	SIZE	PRODUCT
	I XX I I XXX I	C	MAX-C-040 - 0101
SCALE		SHEET	OF
		3	3