

CP/M 2.2 for Tarbell SD Controller

In the CPM22-48K-SSSD.DSK disk image, CP/M is sized for 48K of RAM and uses an 88-2SIO at 10h/11h as the console port and the 2nd 2SIO port at 12h/13h as the listing port.

MOVCPM must be run with parameters to properly load into low memory for saving or SYSGEN (e.g., MOVCPM * * or MOVCPM 48 *). MOVCPM is patched to prevent serialization errors.

The image created by MOVCPM **must be patched** with BOOT.HEX and BIOS.HEX before it is written to disk using SYSGEN. The following steps are specific to this version of CP/M only:

- Put the actual memory size in the equate statement in BOOT.ASM and BIOS.ASM. E.g., for 48K of RAM, put 48 in the MSIZE equate statement.
- Run MOVCPM with the actual memory size. E.g., for 48K of RAM, run "MOVCPM 48 *"
- Follow these steps to patch the saved MOVCPM image in DDT with BOOT.HEX and BIOS.HEX:

```
A>DDT CPMxxx.COM
```

```
-IBOOT.HEX
```

```
-R900
```

```
-IBIOS.HEX
```

```
-Rxxxx where xxxx = 0980h - CCP (equate found in BIOS.PRN)
```

```
-G0 (G zero, not "oh", or type ctrl-c)
```

```
A>SYSGEN (hit return at source prompt to use image in memory, then follow remaining prompts)
```