

CPM141-24K System.dsk

This is a CP/M 1.41 System Release disk from Lifeboat Associates. This disk served as the starting point for the end user to create their own CP/M disks by using MOVCPM and SYSGEN to modify the RAM size of CP/M and then write CP/M to a boot disk.

This version of CP/M uses Lifeboat's CONFIG feature to allow "patching" of serial I/O routines without having to actually patch the BIOS. Location 0120h in CONFIG.COM contains a computer configuration code that has some common pre-defined I/O options. A code of 0FFh means no configuration is to be done and the BIOS is used as-is. In this disk image, location 0120h of CONFIG.COM has been set to zero which defines the Altair 2SIO configuration (see below). This configuration routes CON, RDR, PUN and LPT through 2SIO port 010h.

The CONFIG.COM file must be copied to any new CP/M disk being SYSGEN'd before an attempt is made to boot that disk. The first time a newly SYSGEN'd disk is booted, CONFIG will alert the user that the system has been patched in RAM and to run SAVEUSER to permanently apply the patch to the CP/M image on the disk.

Note: The "user.asm" file on the disk is provided as a reference and is not patched into the BIOS.

List of standard configuration provided by CONFIG in byte 0120h:

- 0 - ALTAIR 88-2SIO, I/O ports 10h,11h
- 1 - IMSAI SIO2, I/O ports 3,2
- 2 - ALTAIR SIO (REV NON-ZERO), I/O ports 0,1
- 3 - ALTAIR SIO (REV ZERO), I/O ports 0,1
- 4 - PROCESSOR TECHNOLOGY 3P+S, I/O ports 0,1
- 5 - IMSAI MIO, I/O ports 43h,42h
- 6 - ALTAIR 88-4PIO, I/O ports 10h,11h
- 80 - COMPAL 80
- 81 - PROCESSOR TECHNOLOGY SOL
- 82 - XITAN 5MB (ZAPPLE IDNITOR)
- 83 - NORTH STAR HORIZON, I/O ports 3,2
- 84 - VECTOR GRAPHIC MZ Computer

CPM141-48K.dsk

This is also a CP/M 1.41 disk. It has several useful utilities in addition to the default CP/M applications. This version of CP/M has been manually patched after running MOVCPM using the USER.ASM file. The USER.ASM file contains instructions for applying the patch. The patch puts the CON, PUN and RDR on 2SIO at 010h and LPT on 2SIO at 012h.