

## **Original Altair 8800 CPU Board, Rev 0 versus Rev 1**

CPU board Rev 1 added an additional pair of component holes in parallel for C4 and C5. These capacitors, in turn are part of the one-shot time constant for the phi-1 and phi-2 system clocks. I assume the additional component holes were to make it easier to adjust capacitance and thereby modify the phi-1 and phi-2 time periods. However, I find it easier to modify the values of R41 and/or R42 to adjust the CPU clock.

Rev 1 also provided a wire jumper option to ground bus pin 20. In a front panel Altair, bus pin 20 is used for the memory unprotect function, however, other manufacturers began using pin 20 as a ground pin and that later became official in the IEEE-696 (S-100) spec. However, pin 20 as ground was not an option with the later 8800b CPU board, so I'm not sure how this feature would be used.