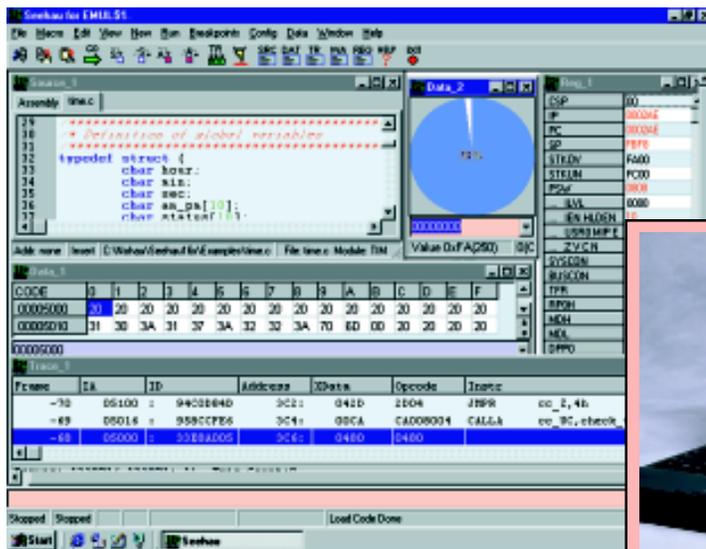


# EMUL68-PC

## In-Circuit Emulator



### Key Benefits

- ◆ Nearly all 68HC11s are supported.
- ◆ Full feature ICE. To 24 MHz. (6 MHz E clock)
- ◆ New SeeHau User Interface: Windows 95, 98, NT and 2000.
- ◆ Emulation and trace ISA boards install in your PC or in the Nohau HSP box (shown) and via the PC LPTx port.
- ◆ Optional trace board can be installed or upgraded later.
- ◆ Trace and Triggers configured and accessible in real-time.
- ◆ Programs OTP memory. (DOS software only)
- ◆ High Level Language (HLL) support for C compilers.
- ◆ Unlimited Breakpoints, Bank Switching, Program Performance Analysis, and Code Coverage.
- ◆ Supports all HC11 chip RESET modes.

### Product Overview

EMUL68-PC supports nearly the entire 68HC11 family of microcontrollers. Nohau has delivered over 18,000 emulators since 1986. The emulator consists of an ISA emulation board, an ISA trace board and a pod. The trace can be added later. Only the pod is different for various flavors of the HC11. The pod contains your exact production chip and is easily changed. SeeHau, the new Windows user interface from Nohau, is standard. Nohau products are Made in the USA. They are sold and supported everywhere.

### Trace Memory and Triggers

Trace memory and the triggers are configurable and viewable without stealing CPU cycles. The trace contents can be saved to an ASCII file. Source code will appear in the trace as well as assembly cycles.

Triggers permit you to specify what to trigger on and then what to do when this event occurs. Triggers can qualify on address and data ranges and control the trace memory or break emulation.

### Shadow RAM

Shadow RAM allows data accesses in real-time to be displayed in the Data window. Shadow RAM is continuously updated in real-time. The data can be displayed in many numerical and graphical formats.

### Emulation Memory

The EMUL68-PC has 64K with the standard emulator and 256K or 512K with a bankswitched model. This memory is 0 waitstates for intrusion free operation. This memory is mapped to the target in 4K blocks or 64 bytes in the case of bankswitching. Individual blocks can be write protected.

### Port Replacement Unit (PRU)

Nohau uses the HC24 Port Replacement Unit or a Xilinx LCA to reconstruct ports used for addressing. The address and data bus is always presented allowing advanced emulator features such as no skid hardware breakpoints. Both single chip and external bus emulation is supported. Low voltage is supported for external mode.

See our web site for more information and call today for the name of your local Nohau representative.

[www.nohau.com](http://www.nohau.com)



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