

Memorandum M-2878

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Division 6 - Lincoln Laboratory
Massachusetts Institute of Technology
Cambridge 38, Massachusetts

SUBJECT: GROUP 62 SECTION LEADERS MEETING, JUNE 21, 1954
To: Division 6 Group Leaders, Group 62 Staff Members
From: A. P. Kromer
Date: June 22, 1954

Abstract: A conference reached agreement on the display specifications. They are presently being written up for formal concurrence action. SDV demodulator circuits are being reviewed. Initial memory planes for XD-1 System, ade at IBM, are now being tested at MIT. Discussions regarding the duplex central planning (IBM Report IM-91) are continuing.

1. Display

At a meeting last week, IBM and MIT jointly reached agreement on display specifications for the consoles and the display system for XD-1. These are presently being written up and will be concurred upon at a very early date.

Pluggable unit design is continuing for the central display system. The initial group of etched cards for lab units has been received from the Division 7 shops.

Because the work of this group at its present stage will be severely hindered if it is not transferred to Lexington at a very early date, priority treatment has been requested to make the laboratory space in Room B-132 available by Monday, June 28. All related services, such as drafting, shops, production control, etc., will also be in Lexington at that time.

2. SDV Demodulator

The basic circuits group are continuing their work of revision of the circuits for this unit to add marginal checking features. Certain other circuit changes are being made simultaneously. IBM is working on the revised layout of the new circuits and packaging into nine-tube pluggable units.

A meeting is planned for Tuesday, June 29 at Lexington to discuss the subject of data transmission over phone lines for XD-1. This will include both SDV and digital data transmission. This meeting will be a joint meeting with IBM and MIT (Division 2 and Division 6). The subject

matter will be made known to the ADES organization so that they may have a representative present if they so desire. Such attendance would provide certain background information for them. However, at the moment the Bell System does not have any responsibility regarding the development or provision of this equipment.

3. MRI

The number of telephone lines to be used to transmit MRI data and the make-up of the message for this data has been determined. Gus O'Brien, who will follow this subject in detail, is visiting IBM to discuss the question further, so that IBM may proceed with design work. Amendment 3 of AFCRC Exhibit 1, covering inclusion of MRI in AN/FSQ-7, has been released to the Air Force.

4. Magnetic Core Memory

The first two memory core planes for XD-1 have been received from IBM and are being tested at MIT. Preliminary indications show that these planes are satisfactory. Additional planes will continue to be received, a few each week, for testing at MIT. The array tester, formerly at High Street, is being converted into the memory unit for XD-2. This has been equipped with planes and is in operation and being tested and debugged by the High Street organization. It appears to be temperature sensitive. However, it will be moved to Plant 2 this week where more completely controlled air cooling will be available to it.

5. System Block Schematic Diagram

Ron Mayer plans to visit IBM on July 1 and make a presentation and explanation of his series of drawings to a relatively large group of the Project High personnel. He will supplement this discussion by a series of additional meetings for IBM people as required to cover more detail. Taylor requested that a similar presentation be arranged for MIT personnel at a relatively early date.

The classification for these drawings is to be reviewed so as to place them in the lowest security level consistent with the information contained thereon.

6. Meetings this Week

On Tuesday, June 22, there will be a meeting at Lexington to discuss the mechanical clock. At IBM, Poughkeepsie, there will be a discussion of basic circuit, status, and program for duplex centrals.

Wednesday, June 23 a meeting will take place at IBM Plant 2, Classroom C, to discuss Report IM-91 with particular regard to intercommunication between computers, input, and display.

Thursday, June 24 discussion will continue on IM-91 with particular attention to power and order code. Also on Thursday there will be a

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discussion regarding concurrence procedure memorandum for duplex centrals.

The following two items have no particular date assigned: meeting with IBM to review status of XD-1 construction program and future schedule for completion of construction and testing of this system; a meeting with Air Force Inspector to review procedure for inspection of prototype systems, this to be attended by Taylor, Lynch, and Olson.

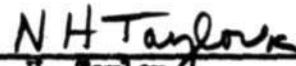
7. Concurrence and Approval

Design specifications for the tape recorders have been concurred upon by MIT and IBM. Provision of facilities for the use of outside air as an emergency cooling facility for XD-1 System was agreed upon and approved in a Lincoln letter dated June 10, 1954.

Signed


R. P. Kroder

Approved


N. H. Taylor

APK/jmm

cc: J. W. Forrester
R. R. Everett
R. P. Crago
C. Lynch