

Recent IBM Patents

• The following patents were recently issued by the countries in which the inventions were made.

France

7,211,396 Apparatus for Detecting the Fundamental Frequency of a Speech Sound ... A. Nemeth

Germany

1,544,205 Method of Producing Semiconductor Components W. Von Muench and W. Hoffmeister
2,118,101 Linear DC Motor S. Padmanabhan

United Kingdom

1,319,227 Movement Control System R. D. Commander and J. D. Dixon
1,322,657 Adders P. L. Gardner
1,323,048 Communications Control Unit M. L. Kingdom Hockings,
M. N. Lovelace, B. D. Key,
H. C. Rankin and J. B. Stredwick

United States

3,769,543 Low Voltage Gas Discharge Display W. B. Pennebaker
3,769,794 Manipulation Apparatus R. P. Cachon
3,770,395 Ferromagnetic Material E. Sawatzky and G. B. Street
3,770,516 Monolithic Integrated Circuits M. C. Duffy, P. A. Schumann, Jr.
and T.-H. Yeh
3,770,519 Isolation Diffusion Method for Making Reduced Beta Transistor or Diodes S. K. Wiedmann
3,770,521 Method for Diffusing B or P into S: Substrates H. M. Demsky, W. H. Dexter,
R. F. Lever and P. A. Melzer
3,770,529 Method of Fabricating Multilayer Circuits L. C. Anderson
3,770,892 Connected Word Recognition System G. L. Clapper
3,770,900 Audio Multifrequency Signal Receiver E. Morlec
3,770,940 Optical Bar Coding Scanning Apparatus J. D. Harr
3,770,942 Optical Bar Coding Scanning Device D. H. McMurtry
3,770,967 Field Effect Transistor Detector Amplifier Cell and Circuit Providing a
Digital Output and/or Independent of Background R. W. Hanna and J. J. Hession
3,770,968 Field Effect Transistor Detector Amplifier Cell and Circuit for Low Level
Light Signals J. J. Hession and H. Klepp
3,771,095 Monolithic Integrated Circuit Resistor Design for Optimum Resistor
Tracking H. C. Calhoun and C. L. Kaufman
3,771,107 Conductive Metal Powder Connector K. R. Forster and W. J. Wheeler
3,771,136 Control Unit M. J. Heneghan and M. A. Hughes
3,771,137 Memory Control in a Multipurpose System Utilizing a Broadcast R. P. Barner, J. A. Devere and
J. G. Oblonsky
3,771,138 Apparatus and Method for Serializing Instructions from Two Independent
Instruction Streams J. O. Celtruda, W. R. Crosthwait,
J. G. Earle, J. W. Fennel, Jr.
and R. F. Henderson
3,771,142 Digital Data Storage System J. F. Minshull and A. S. Murphy
3,771,144 Clock for Computer Performance Measurements L. A. Belady, W. R. De Orazio and
R. W. O'Neill
3,771,218 Process for Fabricating Passivated Transistors J. L. Langdon
3,771,635 Type Element Support G. T. Miles and S. A. Okcuoglu
3,771,850 Scan Angle Multiplier of Constant Aperture D. H. Casler
3,771,939 Ultrasonic Magnetic Surface Finisher H. V. Barth
3,772,016 Method of Producing Multicolor Planographic Printing Surface H. R. Anderson, Jr., and E. A. Bartkus
3,772,101 Landless Plated-through Hole Photoresist Making Process L. G. Chumbres and G. J. Rudy
3,772,607 FET Interface Circuit G. C. Luckett and G. Sonoda
3,772,654 Method and Apparatus for Data Form Modification J. R. Evans, N. N. Krewson and
J. W. Roossien
3,772,655 Method of Obtaining Correspondence between Memory and Output R. G. Bluethman and R. L. James
3,773,162 Document Card Transport Mechanism R. C. Lentz and R. K. Turley
3,773,571 Preparation of Semiconductor Ternary Compounds of Controlled
Composition by Predetermined Cooling Rates H. S. Rupprecht and J. M. Woodall
3,773,670 Novel Etchant for Etching Thin Metal Films L. A. Colom and H. A. Levine
3,773,706 Method for Indicating Stage of Cure of Crosslinked Resins and
Compositions Resulting Therefrom H. F. Dunn, Jr.
3,773,981 Parallel Tone Multiplexer-receiver G. R. Stilwell and A. Weiss
3,774,066 Interactive-display Storage Cathode Ray Tube B. Kazan
3,774,079 Monolithically Fabricated Transistor Circuit with Multilayer Conductive
Patterns J. H. Zechman
3,774,088 Integrated Circuit Test Transistor Structure and Method of Fabricating the
Same I. E. Magdo and S. Magdo

3,774,123	Broad Band Microstrip N-Pole M-Throw Pin Diode Switch Having Predetermined Spacing between Pole and Throw Conductors	<i>R. M. Hume</i>
3,774,154	Error Control Circuits and Methods	<i>E. W. Devore and P. H. Hall</i>
3,774,157	Method of and Arrangement for the Distribution of Timing Pulses in an Electronic Data Processor	<i>F. Tsui</i>
3,774,531	Print Chain Assembly for High Speed Printer	<i>R. L. Hansen and C. C. Roshon</i>
3,774,677	Cooling System Providing Spray Type Condensation	<i>V. W. Antonetti, O. R. Gupta and K. P. Moran</i>
3,775,105	Disazo Pigment Sensitized Photoconductor	<i>W. J. Kukla</i>
3,775,201	Method for Polishing Semiconductor Gallium Phosphide Planar Surfaces ...	<i>J. S. Basi</i>
3,775,579	Method and Apparatus for Repairing Printed Circuits	<i>A. V. Burghart, D. E. Houser, J. E. Kieffer and G. A. Wallace</i>
3,775,598	Fault Simulation System for Determining the Testability of a Non-linear Integrated Circuit by an Electrical Signal Test Pattern	<i>C. C. Chao, L. K. Lange, E. Lindbloom and A. D. Savkar</i>
3,775,746	Method and Apparatus for Detecting Odd Numbers of Errors and Burst Errors of Less than a Predetermined Length in Scrambled Digital Sequences	<i>P. E. Boudreau, R. T. Chien and C. C. Peck</i>
3,775,759	Magnetic Recording and Readback Systems with Raised Cosine Equalization	<i>J. D. Armitage, Jr., and M. R. Cannon</i>
3,775,999	Method of Sealing Electrodes to Glass with a Glass Frit	<i>B. J. Foster and R. R. Tummala</i>
3,776,633	Method of Exposure for Ghost Line Suppression	<i>A. Frosch and A. Schmackpfeffer</i>
3,776,729	Photosensitive Dielectric Composition and Process of Using the Same	<i>M. F. Levy and G. P. Schmitt</i>
3,776,789	Method for Protecting GaAs Wafer Surfaces	<i>J. S. Basi and E. M. Hull</i>
3,777,059	Multiple Display Device	<i>F. Wilkey, Jr.</i>
3,777,072	Editing Procedures for Dictation and Typing Systems	<i>R. A. Kolpek, R. A. Rahenkamp and W. R. Stewart, Jr.</i>
3,777,130	Digital Filter for PCM Encoded Signals	<i>A. Croisler, D. J. Esteban, M. E. Levilion and V. Riso</i>
3,777,186	Charge Transfer Logic Device	<i>W. H. Chang</i>
3,777,220	Circuit Panel and Method of Construction	<i>P. A. Tatusko and R. A. Williams</i>
3,777,221	Multi-layer Circuit Package	<i>P. A. Tatusko and R. A. Williams</i>
3,777,222	Modular Touch Sensitive Indicating Panels with Touch and Light Transmissive Overlay Cover Membrane Containing Visible Printed Indicia	<i>T. J. Harris</i>
3,777,260	Grid for Making Electrical Contact	<i>P. J. Davies, A. S. Gasparri and D. J. McAtee</i>
3,777,280	Laser Generator of TE Wave Guide Modes	<i>D. Pohl</i>
3,777,633	Structure for Making Phase Filters	<i>J. P. Kirk</i>
3,778,126	Gas Display Panel without Exhaust Tube Structure	<i>D. M. Wilson</i>
3,778,127	Sealing Technique for Gas Panel	<i>P. R. Langston, Jr., R. R. Tummala and D. M. Wilson</i>
3,778,262	Improved Electrophotographic Process	<i>C. A. Queener, W. G. Ralston, T. C. Smith and J. P. Welsh</i>
3,778,639	Transistor Switch Using a Current Sharing Pulse Transformer	<i>H. A. Higuchi and L. P. Trubell</i>
3,778,640	Signal Voltage Level Translating Circuit	<i>S. Platt, J. N. Pomeranz and D. K. Tewarson</i>
3,778,687	Shallow Junction Semiconductor Devices	<i>J. J. F. Chang and M. B. Vora</i>
3,778,779	Logic and Storage Circuit for Terminal Device	<i>V. E. Hake, A. W. McDowell, D. R. Mersel, L. G. Mosher and S. O. Stilwell</i>
3,778,780	Operation Request Block Usage	<i>B. B. Moore</i>
3,778,785	Method for Writing Information at Nanosecond Speeds and a Memory System Therefor	<i>R. J. von Gutfeld</i>
3,778,787	Readback Systems for Digital Recorders	<i>M. R. Cannon</i>
3,778,819	Keyboard with Four Character Set Shift	<i>V. S. Bhagawan, R. A. Jones, A. W. McDowell, G. D. Mine, L. J. Remsbuger, B. A. Rupp and R. R. Whittaker</i>
3,778,893	Method of Fabricating a Coherent Superconducting Oscillator	<i>W. A. Thompson</i>
3,778,900	Method for Forming Interconnections between Circuit Layers of a Multi-layer Package	<i>F. W. Haining and L. J. Puterbaugh</i>
3,778,901	Method of Protecting Electrical Conductor Terminations during Gas Panel Fabrication	<i>P. R. Wagner and D. M. Wilson</i>
3,779,359	System for Performing Carrier Return Operation	<i>D. J. Morrison and R. Raesz</i>
3,779,440	Form Run-out and Jam Detector for Printer	<i>W. R. Casale, F. Saltz, G. R. Schreck and E. V. Tomassini</i>
3,779,542	Gate Mechanism for Card Reader	<i>J. H. Barker, N. C. Dribelbis and K. B. Stevens, Sr.</i>
3,779,803	Infrared Sensitive Semiconductor Device and Method of Manufacture	<i>R. E. Lee, P. S. McDermott and E. S. Pan</i>
3,779,806	Electron Beam Sensitive Polymer T-Butyl Methacrylate Resist	<i>E. Gipstein, W. A. Hewett and H. A. Levine</i>

3,780,274	Measurement Method and System	<i>L. J. Thompson, C. J. Kugler, Jr. and G. H. Latta</i>
3,780,276	Hybrid Redundancy Interface	<i>W. C. Carter and A. B. Wadia</i>
3,780,312	Threshold Logic Using Magnetic Bubble Domains	<i>Y. S. Lin and Y. L. Yao</i>
3,780,320	Schottky Barrier Diode Read-only Memory	<i>J. A. Dorler, J. L. Forneris and D. J. Swietek</i>
3,780,337	Field-effect Storage Tube with Gas-discharge Output	<i>B. Kazan</i>
3,780,359	Bipolar Transistor with a Heterojunction Emitter and a Method Fabricating the Same	<i>W. P. Dumke, V. L. Rideout and J. M. Woodall</i>
3,780,568	Quantitative Test Method for Measuring Solder-flux Surface Tension Inter- action	<i>L. S. Goldmann, D. A. Jeannotte and B. Krall</i>
3,780,650	Print Hammer with Moving Coil	<i>J. H. Meier</i>
3,780,846	Automatic Erasing Typewriter System	<i>R. A. Kolpek and R. A. Rahenkamp</i>
3,781,016	Cassette Player	<i>K. E. Benton and V. L. Jacques</i>
3,781,017	Cassette Player	<i>D. S. Baldwin, K. E. Benton and V. L. Jacques</i>
3,781,490	Web Tension and Speed Control in a Reel-to-Reel Web Transport	<i>W. B. Phillips</i>
3,781,609	A Semiconductor Integrated Circuit Chip Structure Protected against Impact Damage from Other Chips during Chip Handling	<i>T. H. Baker, M. Ghafghaichi and P. A. Totta</i>
3,781,670	AC Performance Test for Large Scale Integrated Circuit Chips	<i>M. T. McMahon, Jr.</i>
3,781,681	Test Probe Apparatus	<i>J. W. Wagner and P. M. Young</i>
3,781,683	Test Circuit Configuration for Integrated Semiconductor Circuits and a Test System Containing Said Configuration	<i>L. E. Freed</i>
3,781,793	Monolithic Array Error Detection System	<i>R. A. Henle, I. T. Ho, T.-S. Jen and G. A. Maley</i>
3,781,799	Control System Employing Microprogram Discrete Logic Control Routines ..	<i>T. S. Robinson</i>
3,781,808	Virtual Memory System	<i>T. P. Ahearn, R. S. Capowski, N. T. Christensen, P. M. Gannon, A. E. Lee and J. S. Liptay</i>
3,781,813	Machine Log System	<i>F. T. May</i>
3,781,815	Terminal Interface for a Serial Loop Communications System having Store and Forward Capability	<i>P. E. Boudreau, R. C. Dixon, R. A. Donnan and E. A. Moss</i>
3,781,819	Shift Unit for Variable Data Widths	<i>H. R. Geng, J. Hajdu and P. Skuin</i>
3,781,821	Selective Shift Register	<i>R. I. Roth</i>
3,781,826	Monolithic Memory Utilizing Defective Storage Cells	<i>W. F. Beausoleil</i>
3,781,828	Three-dimensionally Addressed Memory	<i>S. Platt and J. N. Pomeranz</i>
3,781,829	Test Pattern Generator	<i>S. Singh</i>
3,781,832	Magnetoresistive Sensing of Magnetic Bubble Domains Using Expansion ..	<i>G. S. Almasi, R. J. Hendel and G. E. Keefe</i>
3,781,845	Centralized Security System Employing a Magnetic Checking Device	<i>J. M. Ellul</i>
3,781,851	Pulse Analyzing Tester	<i>A. D. Alexander</i>
3,781,873	Digital Data Transmission System Using Multilevel Encoding with Variable Dipulse Spacing	<i>H. J. Nussbaumer</i>