

VOLUME 042 MACHINE 3705- -0080232 MODEL M81 SYSTEM 0002904 MODE

BOX SHIP 81/12/11

LOGIC TYPE -0- SYSTEMS DIAGRAMS

PAGE NUM	SH	TITLE	PART NUM	EC NUM	FEATURE B/M OR B/MS
AAA01		BINDER TAB VOL 42	0008496494	344270	.W. 0001862344
AA001		CENTRAL CONTROL	0008550059	344270	.W. 0001862344
AA002		CENTRAL CONTROL	0008550060	344828	.W. 0001862344
AA003		CENTRAL CONTROL	0008550061	344828	.W. 0001862344
AA004		CENTRAL CONTROL	0008550062	344828	.W. 0001862344
AA005		CENTRAL CONTROL	0008550063	344270	.W. 0001862344
AB001		CENTRAL CONTROL	0001645348	344828	.W. 0001862344
AB002		CENTRAL CONTROL	0001645349	344828	.W. 0001862344
AB003		CENTRAL CONTROL	0001645350	344828	.W. 0001862344
AB004		CENTRAL CONTROL	0001645351	344828	.W. 0001862344
AB005		CENTRAL CONTROL	0004499472	344828	.W. 0001862344 .W. 0001862344
AB006		CENTRAL CONTROL	0001768291	344828	.W. 0001862344
AB007		CENTRAL CONTROL	0001768292	344828	.W. 0001862344
AB008		CENTRAL CONTROL	0001768293	344828	.W. 0001862344
AB009		CENTRAL CONTROL	0001768294	344828	.W. 0001862344
AB010		CENTRAL CONTROL	0001768295	344828	.W. 0001862344
AB011		CENTRAL CONTROL	0004499509	344828	.W. 0001862344
AJ001		CENTRAL CONTROL	0008550064	344828	.W. 0001862344
AJ001A		CENTRAL CONTROL	0001768296	344828	.W. 0001862344
AJ002		CENTRAL CONTROL	0008550065	344270	.W. 0001862344
AM001		CENTRAL CONTROL	0008550066	344270	.W. 0001862344
AM002		CENTRAL CONTROL	0008550067	344270	.W. 0001862344
AM003		CENTRAL CONTROL	0008550068	344270	.W. 0001862344
AP001		CENTRAL CONTROL	0008550069	344270	.W. 0001862344
AP002		CENTRAL CONTROL	0008550070	344828	.W. 0001862344
AP003		CENTRAL CONTROL	0008550071	344270	.W. 0001862344
AP004		CENTRAL CONTROL	0008550072	344270	.W. 0001862344
AP005		CENTRAL CONTROL	0008550073	344828	.W. 0001862344
AP006		CENTRAL CONTROL	0008550074	344270	.W. 0001862344
AP007		CENTRAL CONTROL	0008550075	344828	.W. 0001862344
AP008		CENTRAL CONTROL	0008550076	344270	.W. 0001862344
AP009		CENTRAL CONTROL	0008550077	344828	.W. 0001862344
AP010		CENTRAL CONTROL	0008550078	344270	.W. 0001862344
AP011		CENTRAL CONTROL	0008550079	344270	.W. 0001862344
AP012		CENTRAL CONTROL	0008550080	344270	.W. 0001862344
AP013		CENTRAL CONTROL	0008550081	344270	.W. 0001862344
AP014		CENTRAL CONTROL	0008550082	344270	.W. 0001862344
AP015		CENTRAL CONTROL	0008550083	344270	.W. 0001862344
AU001		CENTRAL CONTROL	0008550084	344270	.W. 0001862344
CA001		CENTRAL CONTROL	0008550085	344828	.W. 0001862344
CA002		CENTRAL CONTROL	0008550086	344828	.W. 0001862344
CA003		CENTRAL CONTROL	0001850830	344270	.W. 0001862344

VOLUME 042 MACHINE 3705- -0080232 MODEL M81 SYSTEM 0002904 MODE

BOX SHIP 81/12/11

LOGIC TYPE -0- SYSTEMS DIAGRAMS

PAGE NUM	SH	TITLE	PART NUM	EC NUM	FEATURE B/M OR B/MS
CA004		CENTRAL CONTROL	0001986965	344270	.W. 0001862344
CC001		CENTRAL CONTROL	0001850831	344270	.W. 0001862344
CC002		CENTRAL CONTROL	0001850832	344270	.W. 0001862344
CC003		CENTRAL CONTROL	0001850833	344270	.W. 0001862344
CC004		CENTRAL CONTROL	0001850834	344270	.W. 0001862344
CC005		CENTRAL CONTROL	0001850835	344270	.W. 0001862344
CC006		CENTRAL CONTROL	0001850836	344270	.W. 0001862344
CC007		CENTRAL CONTROL	0001850837	344270	.W. 0001862344
CC008		CENTRAL CONTROL	0001850838	344828	.W. 0001862344
CD001		CENTRAL CONTROL	0001850839	344270	.W. 0001862344
CD002		CENTRAL CONTROL	0001850840	344270	.W. 0001862344
CD003		CENTRAL CONTROL	0001850841	344270	.W. 0001862344
CD004		CENTRAL CONTROL	0001850842	344270	.W. 0001862344
CF001		CENTRAL CONTROL	0001850843	344270	.W. 0001862344
CF002		CENTRAL CONTROL	0001850844	344270	.W. 0001862344
CF003		CENTRAL CONTROL	0001850845	344270	.W. 0001862344
CF004		CENTRAL CONTROL	0001850846	344270	.W. 0001862344
CG001		CENTRAL CONTROL	0001850847	344828	.W. 0001862344
CK001		CENTRAL CONTROL	0001850848	344270	.W. 0001862344
CK002		CENTRAL CONTROL	0001850849	344270	.W. 0001862344
CK003		CENTRAL CONTROL	0001850850	344270	.W. 0001862344
CK004		CENTRAL CONTROL	0001850851	344270	.W. 0001862344
CK005		CENTRAL CONTROL	0001850852	344828	.W. 0001862344
CK006		CENTRAL CONTROL	0001850853	344270	.W. 0001862344
CK007		CENTRAL CONTROL	0001850854	344828	.W. 0001862344
CL001		CENTRAL CONTROL	0001850855	344270	.W. 0001862344
CL002		CENTRAL CONTROL	0001850856	344270	.W. 0001862344
CL003		CENTRAL CONTROL	0001850857	344270	.W. 0001862344
CL004		CENTRAL CONTROL	0001850858	344270	.W. 0001862344
CL005		CENTRAL CONTROL	0001850859	344270	.W. 0001862344
CM001		CENTRAL CONTROL	0001850860	344270	.W. 0001862344
CM002		CENTRAL CONTROL	0001850861	344270	.W. 0001862344
CM003		CENTRAL CONTROL	0001850862	344270	.W. 0001862344
CP001		CENTRAL CONTROL	0001850863	344828	.W. 0001862344
CP002		CENTRAL CONTROL	0001850864	344270	.W. 0001862344
CP003		CENTRAL CONTROL	0001769189	344270	.W. 0001862344
CP004		CENTRAL CONTROL	0001769190	344270	.W. 0001862344
CP005		CENTRAL CONTROL	0001769191	344270	.W. 0001862344
CP006		CENTRAL CONTROL	0001769192	344270	.W. 0001862344
CP007		CENTRAL CONTROL	0001769193	344270	.W. 0001862344
CQ001		CENTRAL CONTROL	0001769194	344828	.W. 0001862344
CQ002		CENTRAL CONTROL	0001769195	344828	.W. 0001862344

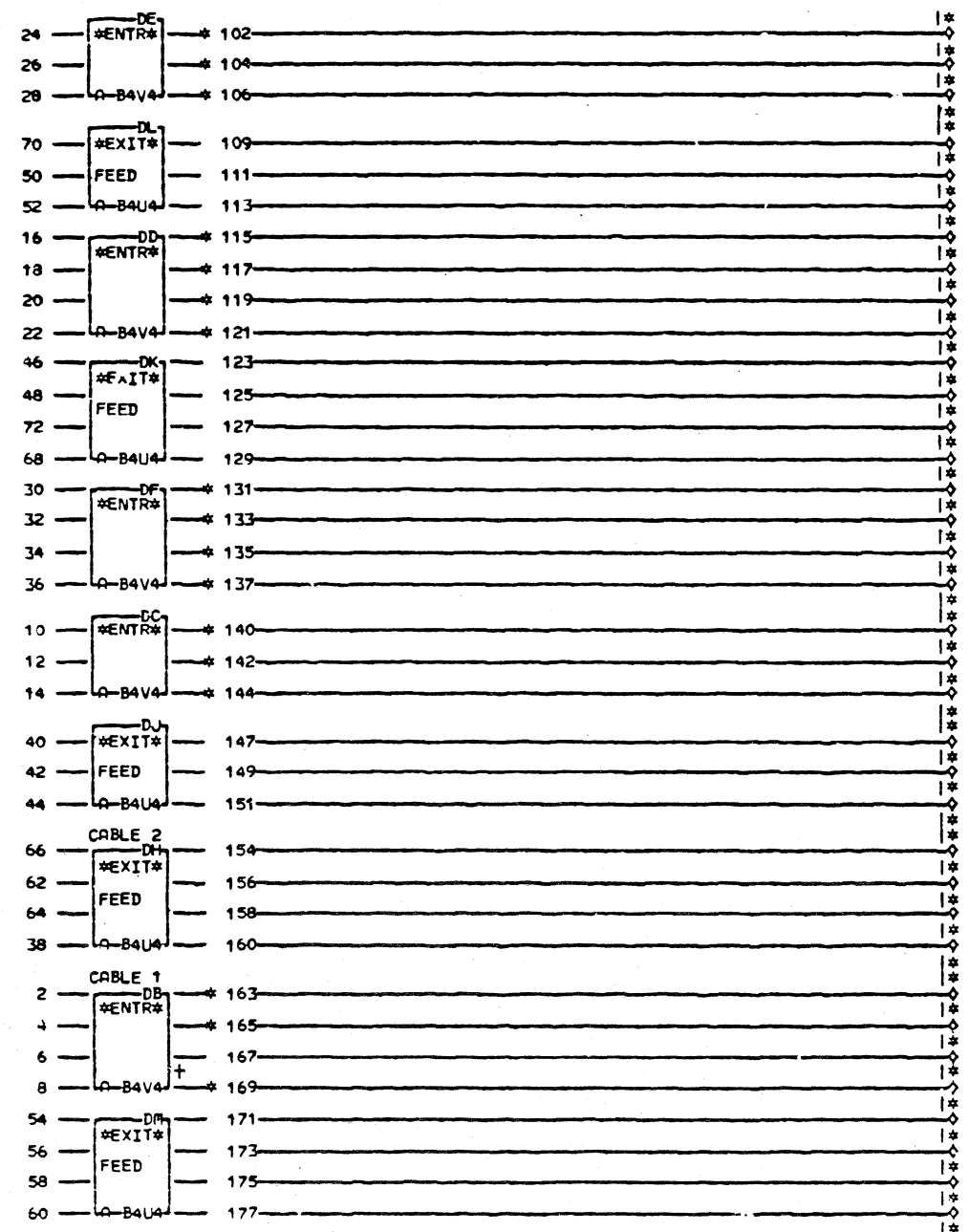
VOLUME 042 MACHINE 3705- -0080232 MODEL M81 SYSTEM 0002904 MODE BOX SHIP 81/12/11

LOGIC TYPE -0- SYSTEMS DIAGRAMS

PAGE NUM	SH	TITLE	PART NUM	EC NUM	FEATURE B/M OR B/MS
CQ004		CENTRAL CONTROL	0001769196	344270	.W. 0001862344
CQ005		CENTRAL CONTROL	0001769197	344270	.W. 0001862344
CR001		CENTRAL CONTROL	0001769198	344270	.W. 0001862344
CR002		CENTRAL CONTROL	0001769199	344270	.W. 0001862344
CR003		CENTRAL CONTROL	0001769200	344828	.W. 0001862344
CR004		CENTRAL CONTROL	0001769201	344270	.W. 0001862344
CR005		CENTRAL CONTROL	0001769202	344270	.W. 0001862344
CR006		CENTRAL CONTROL	0001769203	344270	.W. 0001862344
CR007		CENTRAL CONTROL	0001769204	344828	.W. 0001862344
CR008		CENTRAL CONTROL	0001769205	344828	.W. 0001862344
CS001		CENTRAL CONTROL	0001769206	344270	.W. 0001862344
CS002		CENTRAL CONTROL	0001769207	344270	.W. 0001862344
CS003		CENTRAL CONTROL	0001769208	344270	.W. 0001862344
CS004		CENTRAL CONTROL	0001769209	344828	.W. 0001862344
CS005		CENTRAL CONTROL	0001769210	344828	.W. 0001862344
CS006		CENTRAL CONTROL	0001769211	344828	.W. 0001862344
CS007		CENTRAL CONTROL	0001769212	344270	.W. 0001862344
CU001		CENTRAL CONTROL	0001769213	344828	.W. 0001862344
CU003		CENTRAL CONTROL	0001769214	344828	.W. 0001862344
CU004		CENTRAL CONTROL	0001769215	344270	.W. 0001862344
CU005		CENTRAL CONTROL	0001769216	344270	.W. 0001862344
CU006		CENTRAL CONTROL	0001769217	344270	.W. 0001862344
CU007		CENTRAL CONTROL	0001769218	344270	.W. 0001862344
CU009		CENTRAL CONTROL	0001769219	344828	.W. 0001862344
CU010		CENTRAL CONTROL	0001769220	344828	.W. 0001862344
CU011		CENTRAL CONTROL	0001769221	344270	.W. 0001862344
CU012		CENTRAL CONTROL	0001769222	344828	.W. 0001862344
CU013		CENTRAL CONTROL	0001769223	344828	.W. 0001862344
CU014		CENTRAL CONTROL	0001769224	344828	.W. 0001862344
CU015		CENTRAL CONTROL	0001769225	344828	.W. 0001862344
CU016		CENTRAL CONTROL	0001769226	344828	.W. 0001862344
CU017		CENTRAL CONTROL	0001769227	344828	.W. 0001862344
CV001		CENTRAL CONTROL	0001769228	344270	.W. 0001862344
CV011		CENTRAL CONTROL	0001769229	344828	.W. 0001862344
CV021		CENTRAL CONTROL	0001769230	344270	.W. 0001862344
CV031		CENTRAL CONTROL	0001769231	344270	.W. 0001862344
CV041		CENTRAL CONTROL	0001769232	344270	.W. 0001862344
CV051		CENTRAL CONTROL	0001769233	344828	.W. 0001862344
CV061		CENTRAL CONTROL	0001769234	344270	.W. 0001862344

TOTAL PART NUMBERS THIS VOLUME 123

+ INBUS BYTE 0 BIT 0 — AB001AB1— 2
 + INBUS BYTE 0 BIT 1 — AB001AB2— 4
 + INBUS BYTE 0 BIT 2 — AB001AB3— 6
 + INBUS BYTE 0 BIT 3 — AB001AB4— 8
 + INBUS BYTE 0 BIT 4 — AB001AB5— 10
 + INBUS BYTE 0 BIT 5 — AB001AB6— 12
 + INBUS BYTE 0 BIT 6 — AB001AB7— 14
 + INBUS BYTE 0 BIT 7 — AB001AB8— 16
 + INBUS BYTE 1 BIT 0 — AB001AC0— 20
 + INBUS BYTE 1 BIT 1 — AB001AC1— 22
 + INBUS BYTE 1 BIT 2 — AB001AC2— 24
 + INBUS BYTE 1 BIT 3 — AB001AC3— 26
 + INBUS BYTE 1 BIT 4 — AB001AC4— 28
 + INBUS BYTE 1 BIT 5 — AB001AC5— 30
 + INBUS BYTE 1 BIT 6 — AB001AC6— 32
 + INBUS BYTE 1 BIT 7 — AB001AC7— 34
 + INBUS BYTE 1 BIT 8 — AB001AC8— 36
 + OUTBUS BIT 0.2 — DHO14GC2— 38
 + OUTBUS BIT 0.3 — DHO14GL2— 40
 + OUTBUS BIT 0.4 — DJO14GC2— 42
 + OUTBUS BIT 0.5 — DJO14GL2— 44
 + OUTBUS BIT 0.6 — DLO04GC2— 46
 + OUTBUS BIT 0.7 — DLO04GL2— 48
 + OUTBUS BIT 1.2 — DMO04GC2— 50
 + OUTBUS BIT 1.3 — DMO04GL2— 52
 + OUTBUS BIT 1.4 — DMO04GC2— 54
 + OUTBUS BIT 1.5 — DMO04GL2— 56
 + OUTBUS BIT 1.6 — DMO04GC2— 58
 + OUTBUS BIT 1.7 — DMO04GL2— 60
 + OUTBUS BIT 0.0 — DNO01BK2— 62
 + OUTBUS BIT 0.1 — DP992AE2— 64
 + OUTBUS BIT 0.P — DQ001BK2— 66
 + OUTBUS BIT 1.0 — DR992AE2— 68
 + OUTBUS BIT 1.1 — DR992AE2— 70
 + OUTBUS BIT 1.P — DR992AE2— 72



000 AR001
 163 + INBUS BYTE 0 BIT 0 — DG971—DB1
 165 + INBUS BYTE 0 BIT 1 — DG971—DB3
 167 + INBUS BYTE 0 BIT 2 — DG971—DB5
 169 + INBUS BYTE 0 BIT 3 — DHO11—DB7
 140 + INBUS BYTE 0 BIT 4 — DHO11—DC2
 142 + INBUS BYTE 0 BIT 5 — DHO11—DC4
 144 + INBUS BYTE 0 BIT 6 — DJO11—DC5
 115 + INBUS BYTE 0 BIT 7 — DJO11—DD1
 117 + INBUS BYTE 1 BIT 0 — DK971—DD5
 119 + INBUS BYTE 1 BIT 1 — DK971—DD7
 102 + INBUS BYTE 1 BIT 2 — DLO01—DE4
 104 + INBUS BYTE 1 BIT 3 — DLO01—DE6
 106 + INBUS BYTE 1 BIT 4 — DLO01—DF1
 131 + INBUS BYTE 1 BIT 5 — DMO01—DF3
 133 + INBUS BYTE 1 BIT 6 — DMO01—DF5
 135 + INBUS BYTE 1 BIT 7 — DMO01—DF7
 154 + OUTBUS BIT 0.P — AB002—DH1
 156 + OUTBUS BIT 0.0 — AB002—DH3
 158 + OUTBUS BIT 0.1 — AB002—DH5
 160 + OUTBUS BIT 0.2 — AB002—DH7
 147 + OUTBUS BIT 0.3 — AB002—DJ2
 149 + OUTBUS BIT 0.4 — AB002—DJ4
 151 + OUTBUS BIT 0.5 — AB002—DJ6
 123 + OUTBUS BIT 0.6 — AB002—DK1
 125 + OUTBUS BIT 0.7 — AB002—DK3
 127 + OUTBUS BIT 1.P — AB002—DK5
 129 + OUTBUS BIT 1.0 — AB002—DK7
 109 + OUTBUS BIT 1.1 — AB002—DL2
 111 + OUTBUS BIT 1.2 — AB002—DL4
 113 + OUTBUS BIT 1.3 — AB002—DL6
 171 + OUTBUS BIT 1.4 — AB002—DM1
 173 + OUTBUS BIT 1.5 — AB002—DM3
 175 + OUTBUS BIT 1.6 — AB002—DM5
 177 + OUTBUS BIT 1.7 — AB002—DM7

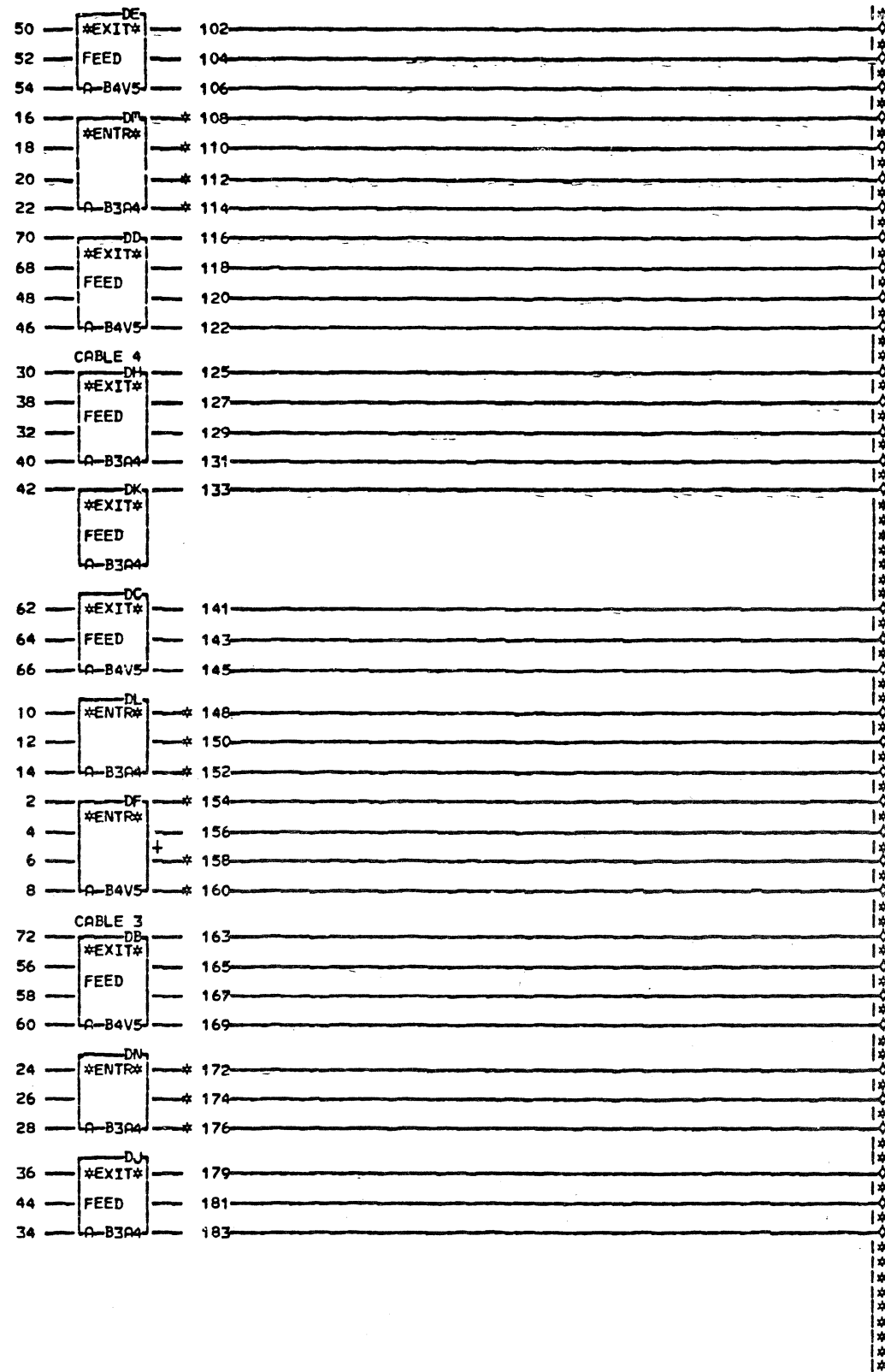
EDGE CONN. 135 A-B4V4D11
 102 P-B4V4D05 137 P-B4V4D13
 104 P-B4V4D06 140 P-B4V4B08
 106 P-B4V4D07 142 P-B4V4B09
 115 P-B4V4B12 144 P-B4V4B10
 117 P-B4V4B13 163 P-B4V4B02
 119 P-B4V4D02 165 P-B4V4B04
 121 P-B4V4D03 169 P-B4V4B06
 131 P-B4V4D09
 133 P-B4V4D10

AR001
 000

LOC. TYPE

ADAPTER INTERFACE	
E.C.—HISTORY	E. MACH. 3705
DATE LAST EC	FRAME 01
10-14-80 344270	IBM CORP. SCD P.N. 8550059
	AR001 000

+ BID PROGRAM LEV 1 AB003AB1- 2
 + BID PROGRAM LEV 2 AB003AB2- 4
 + BID PROGRAM LEV 3 AB003AB3- 6
 + ADAPTER I-O ADDRESS DECODED AB003AB4- 8
 + BID CHANNEL 1 AB004AB0- 10
 + BID CHANNEL 2 AB004AB1- 12
 + CHANNEL 1 INTF A ENABLED AB004AB2- 14
 + CHANNEL 2 INTF A ENABLED AB004AB3- 16
 + CHANNEL 1 INTF B ENABLED AB004AB4- 18
 ALWAYS -4V THREE AB004AB5- 20
 ALWAYS -4V FOUR AB004AB6- 22
 + ADBUS BIT X.0 AB004AB8- 24
 + ADBUS BIT X.6 AB004AB9- 26
 + ADBUS BIT X.7 AB004AC1- 28
 + CHANNEL 1 ENABLE INTF A POS AP007EG4- 30
 + CHANNEL 1 DISABLE INTF A POS AP007EH4- 32
 + CHANNEL 1 DISABLE INTF B POS AP007EJ4- 34
 + CHANNEL 1 ENABLE INTF B POS AP007EK4- 36
 + CHANNEL 2 ENABLE INTF A POS AP007EL4- 38
 + CHANNEL 2 DISABLE INTF A POS AP007EM4- 40
 ALWAYS -4V TWO AP007FM4- 42
 ALWAYS -4V ONE AP007FN4- 44
 + GATE INPUT DATA ON INBUS CQ001CD2- 46
 + SAMPLE OUTPUT DATA ON OUTBUS CQ001EA2- 48
 - GATE INPUT 76 CQ005CJ6- 50
 - GATE INPUT 77 CQ005CL6- 52
 + RESET CU010GM6- 54
 + I-O REG ADDR BIT 1 DN001GB2- 56
 + I-O REG ADDR BIT 2 DN001GC2- 58
 + I-O REG ADDR BIT 3 DN002GB2- 60
 + I-O REG ADDR BIT 4 DG001GA2- 62
 + I-O REG ADDR BIT 5 DG001GB2- 64
 + I-O REG ADDR BIT 6 DG001GC2- 66
 + I-O REG ADDR BIT P DG002FK6- 68
 + I-O REG ADDR BIT 7 DG002GB2- 70
 + I-O REG ADDR BIT 0 DQ002GD2- 72



000 AA002

163 + I-O REG ADDR BIT 0 AB003-DB1
 165 + I-O REG ADDR BIT 1 AB003-DB3
 167 + I-O REG ADDR BIT 2 AB003-DB5
 169 + I-O REG ADDR BIT 3 AB003-DB7
 141 + I-O REG ADDR BIT 4 AB003-DC2
 143 + I-O REG ADDR BIT 5 AB003-DC4
 145 + I-O REG ADDR BIT 6 AB003-DC6
 116 + I-O REG ADDR BIT 7 AB003-DD1
 118 + I-O REG ADDR BIT P AB003-DD3
 120 + SAMPLE OUTPUT DATA ON OUTBUS DD5 LAB003
 122 + GATE INPUT DATA ON INBUS DD7 LAB003
 102 - GATE INPUT 76 AB003-DE2
 104 - GATE INPUT 77 AB003-DE4
 106 + RESET AB003-DE6
 154 + BID PROGRAM LEV 1 CP005-DF1
 156 + BID PROGRAM LEV 2 CP005-DF3
 158 + BID PROGRAM LEV 3 CP005-DF5
 160 + ADAPTER I-O ADDRESS DECODED DF7 LC001
 125 + CHANNEL 1 ENABLE INTF A POS DH1 LAB004
 127 + CHANNEL 2 ENABLE INTF A POS DH3 LAB004
 129 + CHAN 1 DISABLE INTF A POS DHS LAB004
 131 + CHANNEL 2 DISABLE INTF A POS DH7 LAB004
 179 + CHANNEL 1 ENABLE INTF B POS DJ2 LAB004
 181 ALWAYS -4V ONE AB004-DJ4
 183 + CHANNEL 1 DISABLE INTF B POS DJ6 LAB004
 133 ALWAYS -4V TWO AB004-DK1
 148 + BID CHANNEL 1 CP001-DL2
 150 + BID CHANNEL 2 CP001-DL4
 152 + CHANNEL 1 INTF A ENABLED DL6 LC001
 108 + CHANNEL 2 INTF A ENABLED DM1 LC001
 110 + CHANNEL 1 INTF B ENABLED DM3 LC001
 112 ALWAYS -4V THREE CU001-DM5
 114 ALWAYS -4V FOUR CP001-DM7
 172 + ADBUS BIT X.0 DF971-DN2
 174 + ADBUS BIT X.6 DF971-DN4
 176 + ADBUS BIT X.7 DF971-DN6

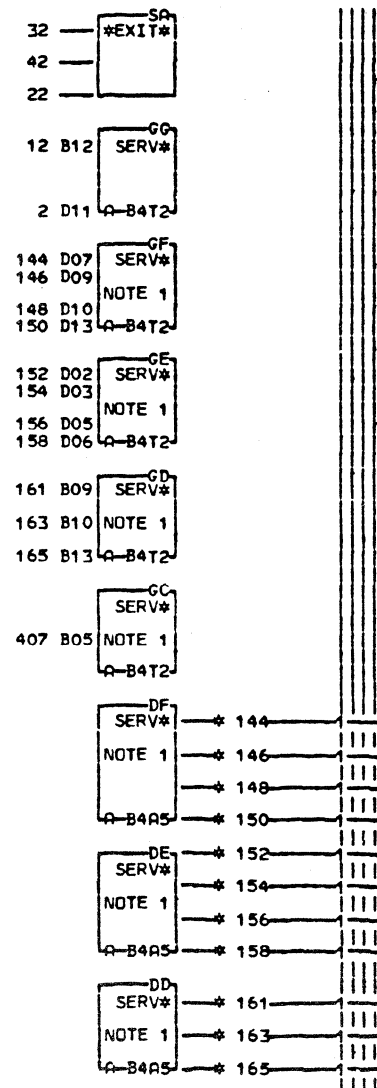
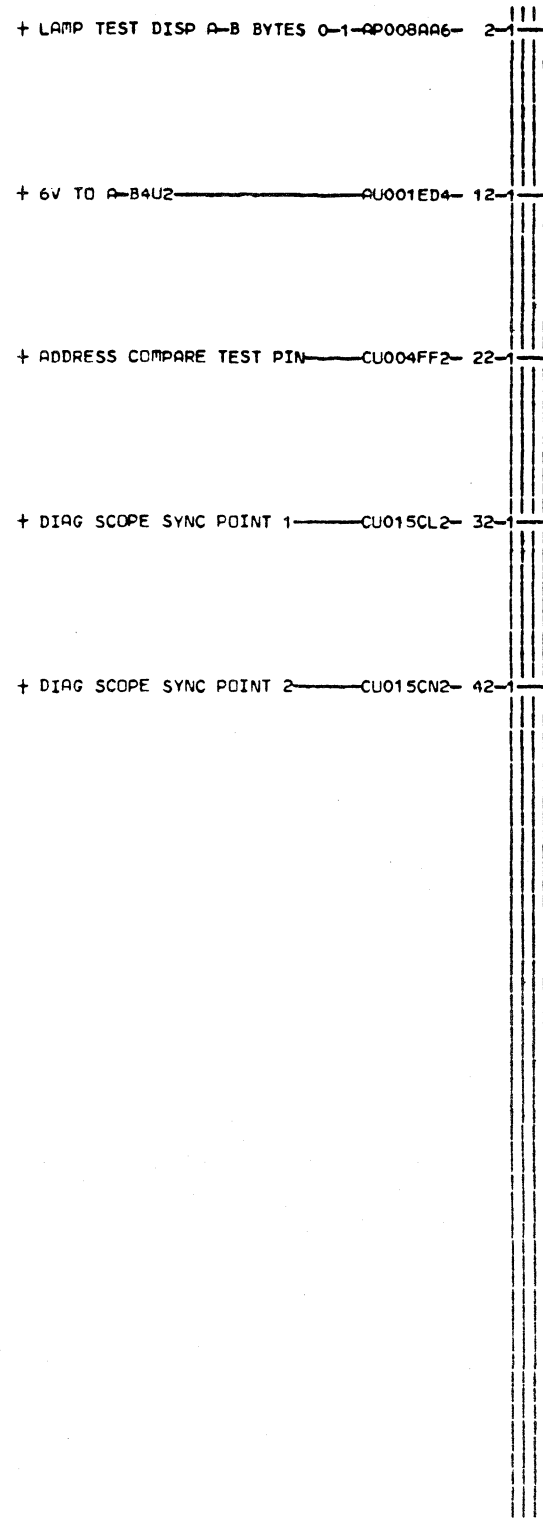
EDGE CONN.

108 A-B3A4D02	01A-B3B1B13	174 A-B3A4D11
110 A-B3A4D03	01A-B4V5D13	01A-B3C1B11
112 A-B3A4D05	01A-B4B6C04	01A-B4C6B02
114 A-B3A4D09	01A-B3B1C13	176 A-B3A4D13
148 A-B3A4D06	01A-B4V5D09	01A-B3C1B13
150 A-B3A4D07	01A-B4B6D02	01A-B4C6E04
152 A-B3A4B13	01A-B3B1D11	
154 A-B4V5D10	172 A-B3A4D10	
01A-B4B6B04	01A-B3C1A13	
	01A-B4C6A04	

AA002
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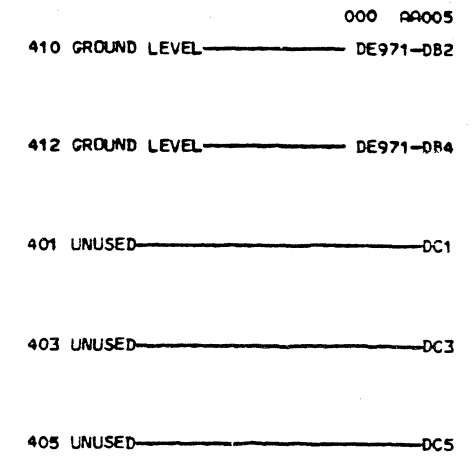
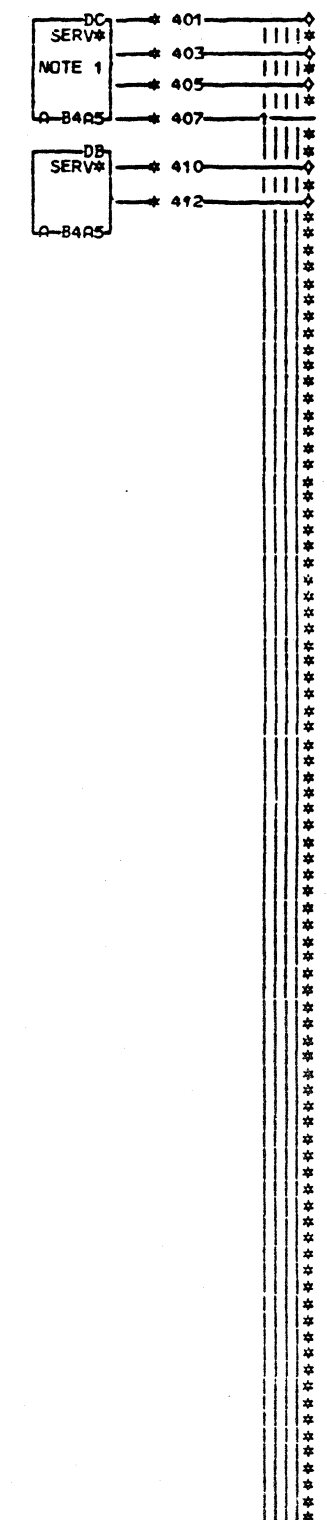
LOC. TYPE

ADAPTER INTERFACE	
E.C. HISTORY 344270	E1 MACH.3705
DATE LAST EC 06-02-81 344828	FRAME 01 IBM CORP. SC D P.No. 8550060
	AA002 000



-BLANK COLUMN-

-BLANK COLUMN-



NOTE 1. THESE SERV BLOCK PINS ARE GROUND LEVEL.

EDGE CONN.		
163	A-B4A5B10	
144	A-B4A5D07	165 A-B4A5B13
146	A-B4A5D09	401 A-B4A5B06
148	A-B4A5D10	403 A-B4A5B08
150	A-B4A5D13	405 A-B4A5B12
152	A-B4A5D02	407 A-B4A5B05
154	A-B4A5D03	410 A-B4A5B02
156	A-B4A5D05	412 A-B4A5B04
158	A-B4A5D06	
161	A-B4A5B09	

LOC. TYPE

AA005
000

SCOPING TEST POINTS AND SERV WIRING	
E.C. HISTORY	MACH. 3705
FRAME	01
DATE	LAST EC
10-14-80	344270
IBM CORP. SCD	AA005
P.N. 8550063	000

CABLE 8

1768293 B

23

3705-80
CCU
LINE NAME

OIA-B3 SOCKET	SOURCE/SINK	LINE NAME	PIN	SOCKET	CS-2 OIA-A3 BOARD	SOCKET
EXIT	AA004DH1	- IDENTIFY CSB	B02	A5		A3
	AA004DH3	BOARD GROUND ONE	B04			
	AA004DH5	BOARD GROUND TWO	B05			
	AA004DH7	BOARD GROUND THREE	B06			
	AA004DN2	- PRI REG 03 AVAILABLE	D11			
	AA004DN4	- PRI REG AVAILABLE PARITY	D13			
ENTRY	CX003	+ CSB WANTS A PRI REG	B08	A5		A3
	CX003	- FLOAT ONE	B09			
	CX003	- FLOAT TWO	B10			
	CX003	- FLOAT THREE	B12			
	CX004	+ CSB TO COMMON BIT A	B13			
	CX004	- FLOAT FOUR	D02			
	CX004	- FLOAT FIVE	D03			
	CX004	- FLOAT SIX	D05			
	CX004	+ CSB TO COMMON BIT B	D06			
	CX004	- FLOAT SEVEN	D07			
	CX004	- FLOAT EIGHT	D09			
	CX004	- FLOAT NINE	D10			

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CABLE NUMBER 8

DATE
AUG 80
CHANGE NO
344270

DATE
JUN 81
CHANGE NO
344828

DATE
CHANGE NO

DESIGN DJR JUL 80 SHT OF
DETAIL RTS AUG 80

CLASSIFICATION
MUST CONFORM TO ENG SPEC

DEVELOPMENT NO

LOGIC PG NO

APPRO

AB008

B

1768293

NOTES

- 1 TYPE 2 CS INTERFACE PAGES TAO4I (ENTRY) TAO6I (EXIT)
- 2 SEE ABO10 FOR THE PHYSICAL PATH OF THE CABLING FOR CABLES 1 THRU 9
- 3 SEE ABO11 FOR CABLE GROUP P/N'S

PART NO
1768293
LOGIC PG NO
AB008

CABLES 1 THRU 9 14

1768295 B

-3705-80-

	CCU	CCU	REMOTE OR CA-4	CS2	CA-1 OR CA-4
	OIA-B4 BD	OIA-B3 BD	OIA-B1 BD	OIA-A3 BD	OIA-A4 BD
CABLE NO.1	EXITS AT V4		ENTERS AT A2 EXITS AT A4	ENTERS AT V3 EXITS AT Z4	ENTERS AT A2 ENDS AT A4
CABLE NO.2	EXITS AT U4		ENTERS AT B2 EXITS AT B4	ENTERS AT V4 EXITS AT Z5	ENTERS AT B2 TERMINATES AT B4 2
CABLE NO.3	EXITS AT V5		ENTERS AT C2 EXITS AT C4	ENTERS AT V5 EXITS AT Z6	ENTERS AT C2 TERMINATES AT C4 3
CABLE NO.4		EXITS AT A4	ENTERS AT C3 EXITS AT C5		ENTERS AT C3 ENDS AT C5
CABLE NO.5	EXITS AT U5		ENTERS AT A3 EXITS AT A5		ENTERS AT A3 ENDS AT A5
CABLE NO.6		EXITS AT A5	ENTERS AT B3 EXITS AT B5		ENTERS AT B3 TERMINATES AT B5 3
CABLE NO.7		EXITS AT A2		ENTERS AT A4 TERMINATES AT A2 3	
CABLE NO.8		EXITS AT A3		ENTERS AT A5 ENDS AT A3	
CABLE NO.9	EXITS AT A5 5		ENTERS AT Y3 ENDS AT Y2 5		

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IBM

NAME			DATE	CHANGE NO	DATE	CHANGE NO
DESIGN	DJR	JUL80	AUG80	344270		
DETAIL	RTS	JUN81	JUN81	344828		
CHECK						
APPRO						
CLASSIFICATION		MUST CONFORM TO ENG SPEC		DEVELOPMENT NO		LOGIC PG NO
DJR	JUL80					AB010
						B

NOTES

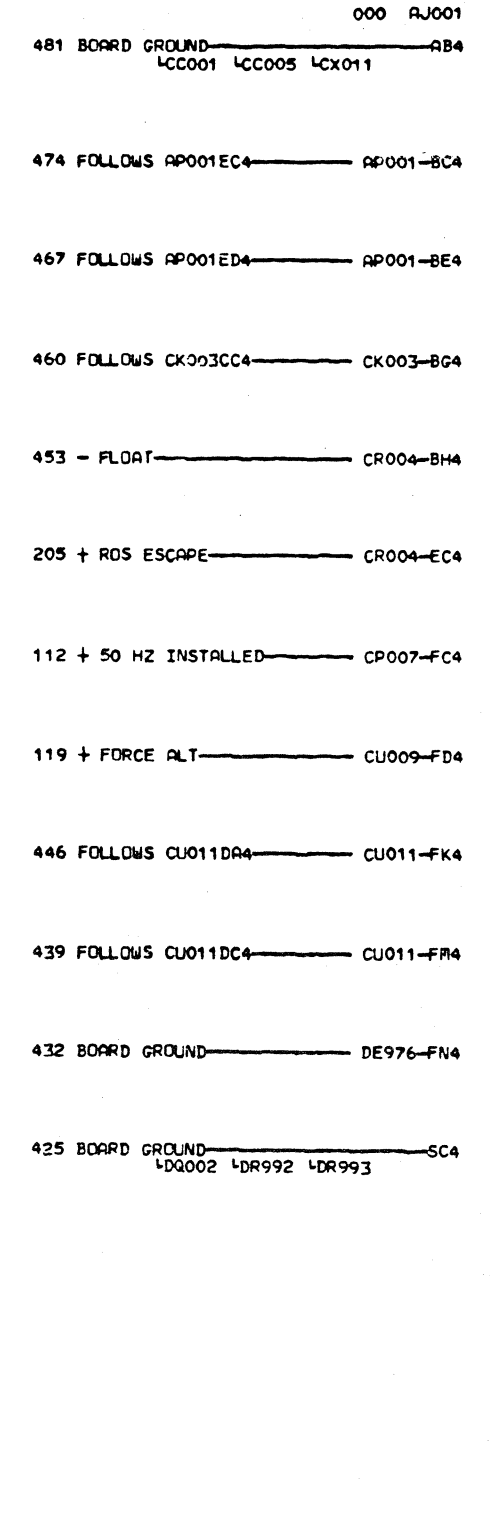
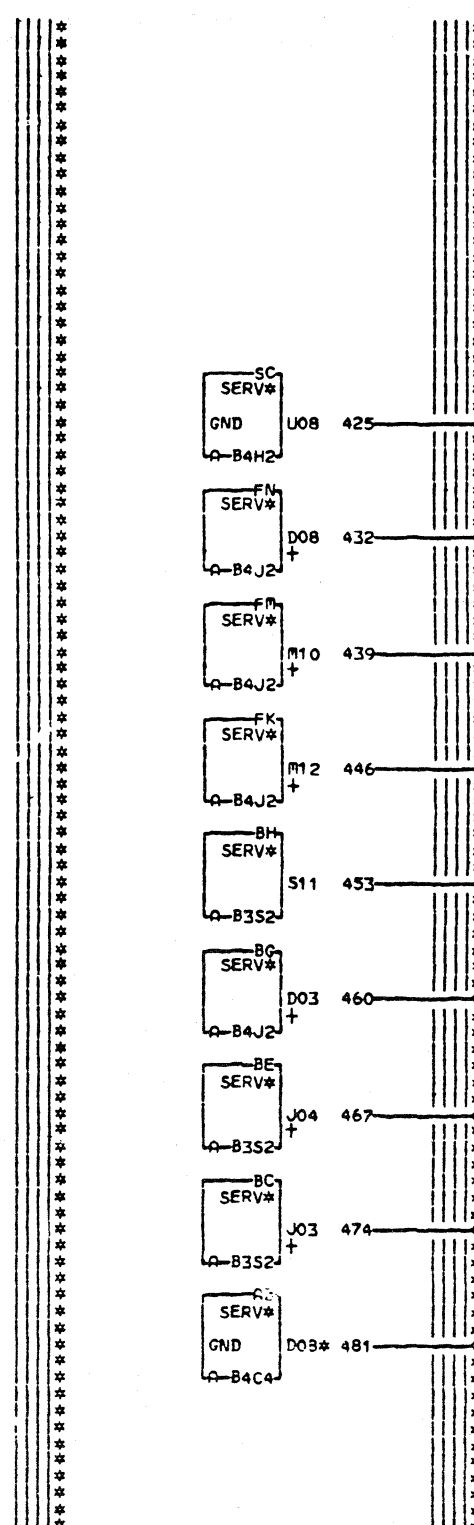
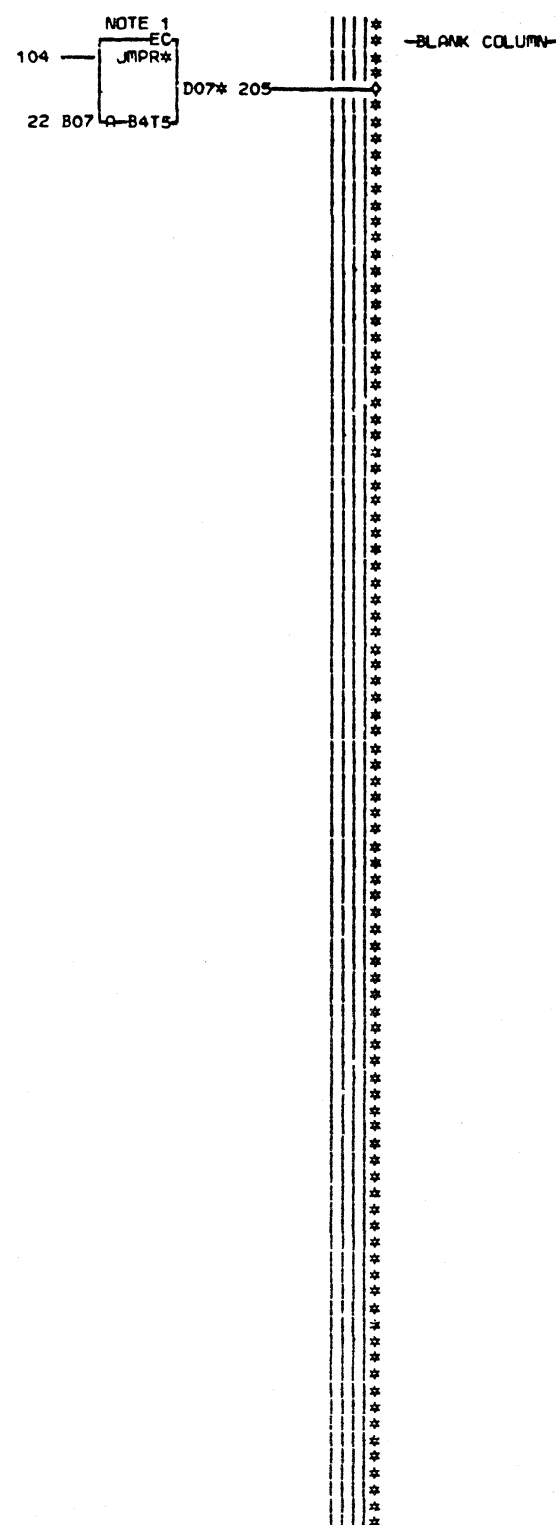
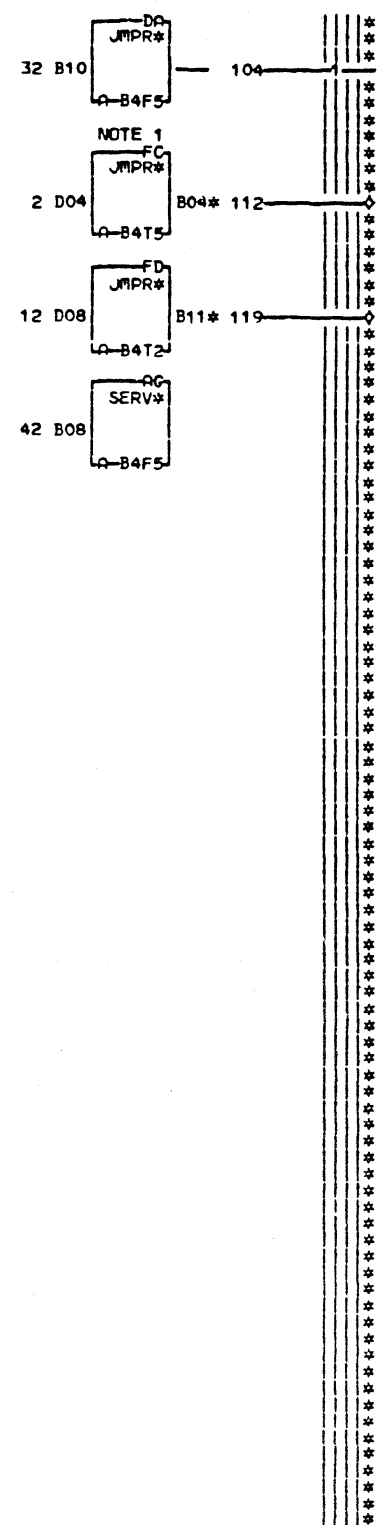
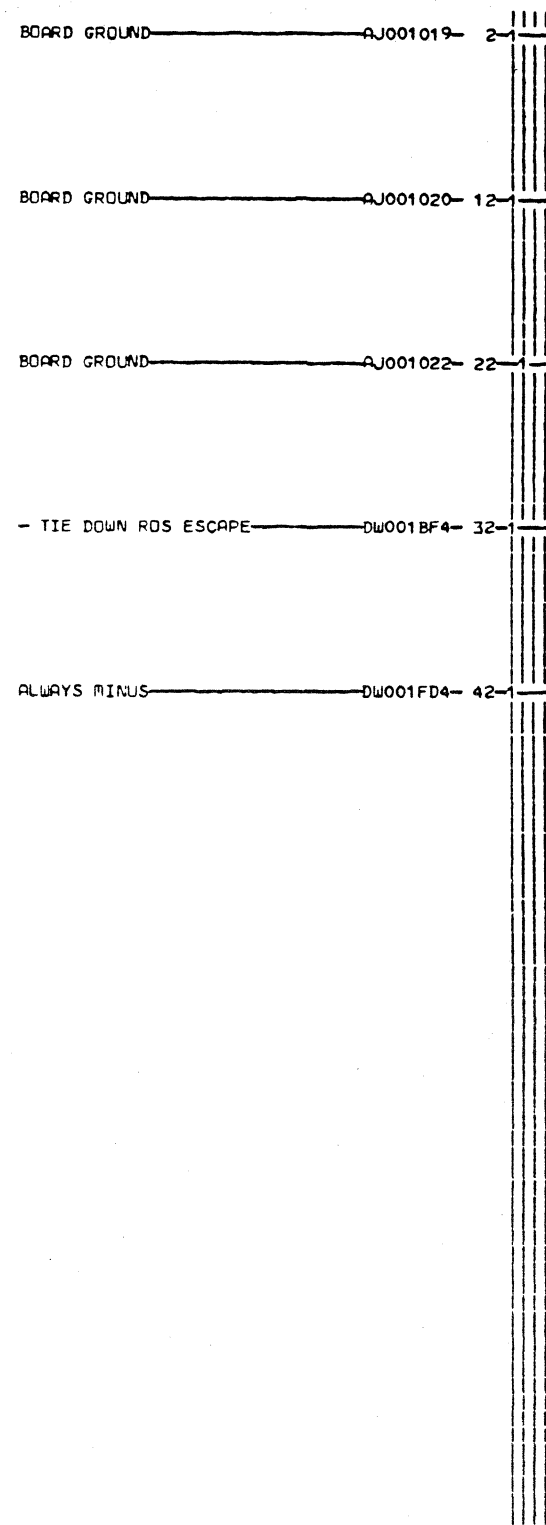
1 THE CABLES PHYSICALLY START AT THE CCU (OIA-B4 OR OIA-B3 BOARD) AND GO TO THE FIRST ADAPTER INSTALLED. E.G. CABLE NO.1 STARTS AT OIA-B4V4. IF A REMOTE OR CA-4 IS NOT INSTALLED IN THE OIA-B1 BOARD, CABLE NO.1 WILL GO FROM OIA-B4V4 TO OIA-A3V3. FROM THERE IT WILL EXIT TO THE OIA-A4 BOARD. BLANKS INDICATE THAT THE SPECIFIC CABLE NEVER GOES TO THAT ADAPTER.

FOR THE VARIOUS LOGIC PAGES AND BOARD SIGNAL CROSSINGS SEE APPROPRIATE AB PAGE:

- | | |
|------------------|------------------|
| CABLE NO.1 AB001 | CABLE NO.6 AB006 |
| CABLE NO.2 AB002 | CABLE NO.7 AB007 |
| CABLE NO.3 AB003 | CABLE NO.8 AB008 |
| CABLE NO.4 AB004 | CABLE NO.9 AB009 |
| CABLE NO.5 AB005 | |

- 2 TERMINATOR CARD P/N 5862885
- 3 TERMINATOR CARD P/N 5862884
- 4 SEE AB011 FOR CABLE GROUP P/N'S
- 5 PRESENT ONLY IF REMOTE IS INSTALLED

PART NO
1768295
LOGIC PG NO
AB010



NOTE 1. D04 AND B07 ARE WIRED TO GROUND THRU DY001EC3 AND 4. REF AJ001A FOR SUMMARY OF ALL CCU JUMPING REQUIRED.

AJ001
000

EDGE CONN. 481 A-B4E6A02
112 A-B4T5B04 01A-B3E1A11
01A-B4E6B04
01A-B3E1B13
119 A-B4E6C02
01A-B3E1C11
01A-B4T2B11
205 A-B4T5D07
01A-B4E6C04
01A-B3E1C13

LOC. TYPE

BOARD JUMPERS

E.C. HISTORY E.MACH.3705

FRAME 01

DATE LAST EC 10-14-80 344270

IBM CORP. SCD P.N. 8550064

AJ001
000

1768296 B

PART NO
1768296

LOGIC PG NO
AJ001A

IF REPLACING A DEFECTIVE OIA-B4 CCU BOARD THE FOLLOWING JUMPERS MUST BE ADDED TO THE BOARD:

- 1 JUMPER OIA-B4T5D07 TO OIA-B4F5B10 (ROS ESCAPE TIE DOWN)
REF AJ001EC4
- 2 JUMPER OIA-B4T5D04 TO OIA-B4T5B04 (ONLY REQUIRED IF POWER SOURCE IS 50 HZ). DO NOT INSTALL THIS JUMPER IF 60HZ POWER SOURCE. REF AJ001FC4
- 3 JUMPER OIA-B4E2D13 TO OIA-B4E2B13 (256K OF MEMORY INSTALLED).
REF AJ002DC4
- 4 JUMPER OIA-B4T2B11 TO OIA-B4T2D08 IF A REMOTE PROGRAM LOADER IS INSTALLED AND NO CHANNEL ADAPTERS ARE INSTALLED (STANDALONE REMOTE)

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IBM				DATE	CHANGE NO	DATE	CHANGE NO
NAME				AUG80	344270		
CYCLIC UTILIZ. CTR CARD				JUN81	344828		
DESIGN	DJR	AUG80	SHT OF				
DETAIL	RTS	AUG80					
CHECK			CLASSIFICATION	MUST CONFORM TO ENG SPEC	DEVELOPMENT NO	LOGIC PG NO	
APPRO						AJ001A	

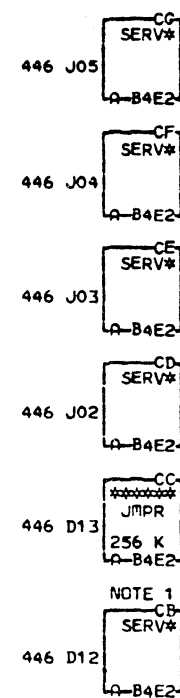
1768296 B

VERTICAL ELECTRICAL FORMAT

MRO# 780522203

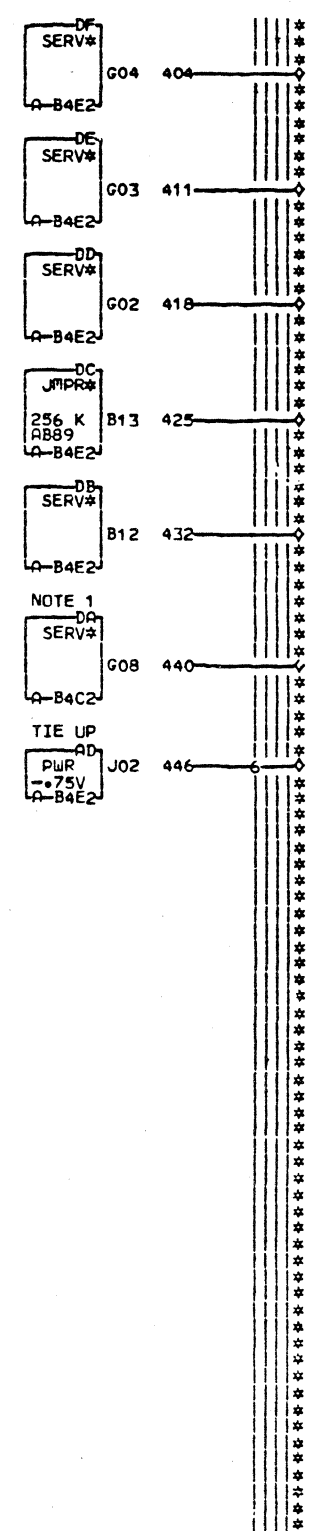
620-0133-1

ASTROCLON N50T



-BLANK COLUMN-

-BLANK COLUMN-



- 000 AJ002
- 446 + TIE UP ————— C0002-ADA
 - 440 - FLOAT ————— C0002-DA4
 - 432 - FLOAT ————— C0002-DB4
 - 425 + 256K OF MEM INSTALLED — C0002-DC4
 - 418 - FLOAT ————— C0002-DD4
 - 411 - FLOAT ————— C0002-DE4
 - 404 - FLOAT ————— C0002-DF4

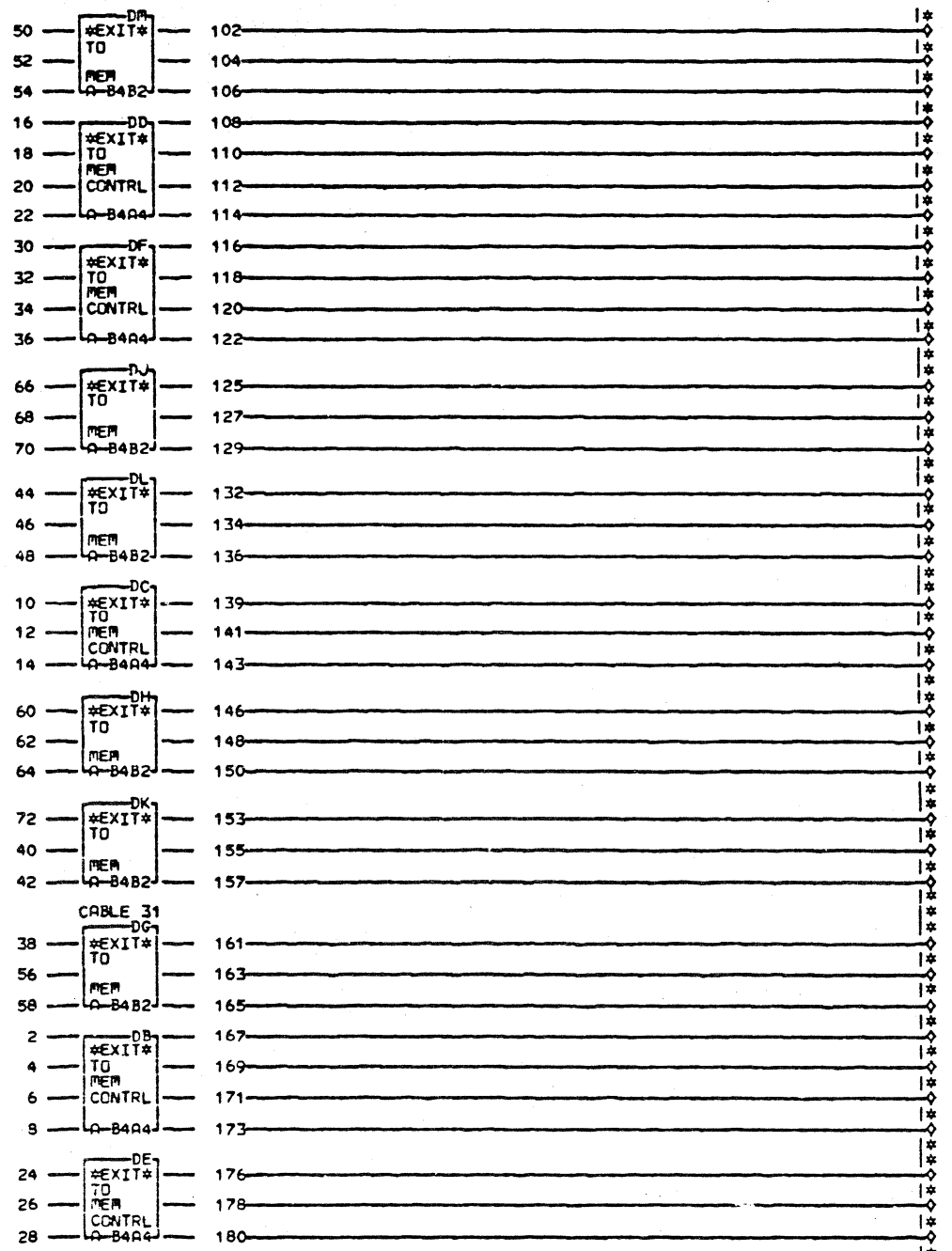
NOTE 1. ALWAYS JUMPER
B4E2D13 TO B4E2B13.
REF AJ001A FOR
CCU JUMPING
REQUIRED.

LCC. TYPE
A-B4E2 AB89

AJ002
000

MEMORY SIZE JUMPER	
01A-B4E2 SOCKET	
E-C-HISTORY	E-MACH.3705
FRAME	01
DATE	LHST EC
10-14-80	344270
P.N.	8550065
	000

+ MEM RESET DB101FD1- 2
 + STORE NEW DB101FD2- 4
 + RESET DB101FD4- 6
 - ALLOW SET MEMORY DIAG REG DB101FD5- 8
 + RESET MEMORY DIAG REG DB101FD6- 10
 + BAD ADDRESS DB101FD8- 12
 + CABLE PLUGGED IN DB101FH1- 14
 GROUND LEVEL DB101FH2- 16
 GROUND LEVEL DB101FH4- 18
 GROUND LEVEL DB101FH5- 20
 GROUND LEVEL DB101FH7- 22
 GROUND LEVEL DB101FH8- 24
 GROUND LEVEL DB101FM1- 26
 GROUND LEVEL DB101FM3- 28
 GROUND LEVEL DB101FM4- 30
 GROUND LEVEL DB101FM5- 32
 GROUND LEVEL DB101FM7- 34
 GROUND LEVEL DB101FM8- 36
 + SAR BIT X.6 TO MEM DS001FD1- 38
 + SAR BIT 0.7 TO MEM DS001FD2- 40
 + SAR BIT 1.0 TO MEM DS001FD4- 42
 + SAR BIT 1.1 TO MEM DS001FD5- 44
 + SAR BIT 1.2 TO MEM DS001FD6- 46
 + SAR BIT 1.3 TO MEM DS001FD8- 48
 + SAR BIT 1.4 TO MEM DS001FH1- 50
 + SAR BIT 1.5 TO MEM DS001FH2- 52
 + SAR BIT 1.6 TO MEM DS001FH4- 54
 GROUND LEVEL DS001FH5- 56
 + SAR BIT X.7 TO MEM DS001FH7- 58
 + SAR BIT 0.0 TO MEM DS001FH8- 60
 + SAR BIT 0.1 TO MEM DS001FM1- 62
 + SAR BIT 0.2 TO MEM DS001FM3- 64
 + SAR BIT 0.3 TO MEM DS001FM4- 66
 + SAR BIT 0.4 TO MEM DS001FM5- 68
 + SAR BIT 0.5 TO MEM DS001FM7- 70
 + SAR BIT 0.6 TO MEM DS001FM8- 72



000 AM001

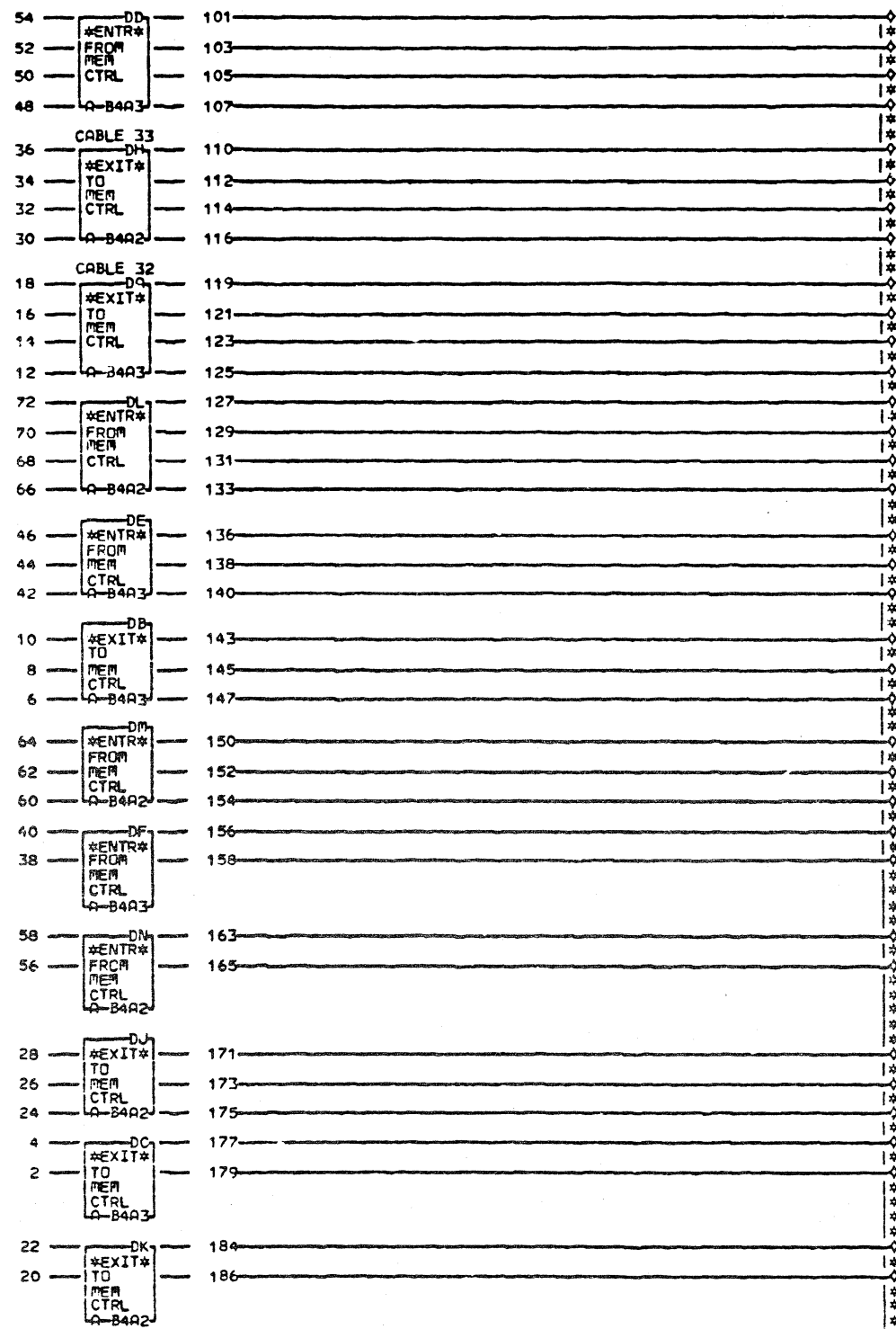
167 + MEM RESET UNUSED MM151-DB1
 169 + STORE NEW MM151-DB3
 171 + RESET MM151-DB5
 173 - ALLOW SET MEMORY DIAG REG MM151-DB7
 139 + RESET MEMORY DIAG REG MM151-DC2
 141 + BAD ADDRESS UNUSED MM151-DC4
 143 + CABLE PLUGGED IN UNUSED MM151-DC6
 108 UNUSED MM151-DD1
 110 UNUSED MM151-DD3
 112 UNUSED MM151-DD5
 114 UNUSED MM151-DD7
 176 UNUSED MM151-DE2
 178 UNUSED MM151-DE4
 180 UNUSED MM151-DE6
 116 UNUSED MM151-DF1
 118 UNUSED MM151-DF3
 120 UNUSED MM151-DF5
 122 UNUSED MM151-DF7
 161 + SAR BIT X.6 TO MEM MM451-DG2
 163 UNUSED MM451-DG4
 165 + SAR BIT X.7 TO MEM MM451-DG6
 146 + SAR BIT 0.0 TO MEM MM451-DH2
 148 + SAR BIT 0.1 TO MEM MM451-DH4
 150 + SAR BIT 0.2 TO MEM MM451-DH6
 125 + SAR BIT 0.3 TO MEM MM451-DJ2
 127 + SAR BIT 0.4 TO MEM MM451-DJ4
 129 + SAR BIT 0.5 TO MEM MM451-DJ6
 153 + SAR BIT 0.6 TO MEM MM451-DK2
 155 + SAR BIT 0.7 TO MEM MM451-DK4
 157 + SAR BIT 1.0 TO MEM MM451-DK6
 132 + SAR BIT 1.1 TO MEM MM451-DL2
 134 + SAR BIT 1.2 TO MEM MM451-DL4
 136 + SAR BIT 1.3 TO MEM MM451-DL6
 102 + SAR BIT 1.4 TO MEM MM451-DM2
 104 + SAR BIT 1.5 TO MEM MM451-DM4
 106 + SAR BIT 1.6 TO MEM MM451-DM6

LDC. TYPE

AM001
000

MEMORY INTERFACE	
E.C. HISTORY	E. PACH. 3705
DATE	FRAME 01
LAST EC	IBM CORP. SCD AM001
10-14-80 344270	P. No. 8550066 000

+ STORE BIT 0.0 — DS003BC1 — 2
 - STORE BIT 0.7 — DS003BC2 — 4
 - STORE BIT 0.6 — DS003BC4 — 6
 - STORE BIT 0.5 — DS003BC5 — 8
 - STORE BIT 0.4 — DS003BC6 — 10
 - STORE BIT 0.3 — DS003BC8 — 12
 - STORE BIT 0.2 — DS003BG1 — 14
 - STORE BIT 0.1 — DS003BG2 — 16
 - STORE BIT 0.0 — DS003BG4 — 18
 + STORE BIT 1.0 — DS005BC1 — 20
 - STORE BIT 1.7 — DS005BC2 — 22
 - STORE BIT 1.6 — DS005BC4 — 24
 - STORE BIT 1.5 — DS005BC5 — 26
 - STORE BIT 1.4 — DS005BC6 — 28
 - STORE BIT 1.3 — DS005BC8 — 30
 - STORE BIT 1.2 — DS005BG1 — 32
 - STORE BIT 1.1 — DS005BG2 — 34
 - STORE BIT 1.0 — DS005BG4 — 36
 + SENSE BIT 0.0 TO CCU — MM351BE5 — 38
 - SENSE BIT 0.7 TO CCU — MM351BE7 — 40
 - SENSE BIT 0.6 TO CCU — MM351BE8 — 42
 - SENSE BIT 0.5 TO CCU — MM351BH1 — 44
 - SENSE BIT 0.4 TO CCU — MM351BH3 — 46
 - SENSE BIT 0.3 TO CCU — MM351BH4 — 48
 - SENSE BIT 0.2 TO CCU — MM351BH5 — 50
 - SENSE BIT 0.1 TO CCU — MM351BH7 — 52
 - SENSE BIT 0.0 TO CCU — MM351BH8 — 54
 + SENSE BIT 1.0 TO CCU — MM371BE5 — 56
 - SENSE BIT 1.7 TO CCU — MM371BE7 — 58
 - SENSE BIT 1.6 TO CCU — MM371BE8 — 60
 - SENSE BIT 1.5 TO CCU — MM371BH1 — 62
 - SENSE BIT 1.4 TO CCU — MM371BH3 — 64
 - SENSE BIT 1.3 TO CCU — MM371BH4 — 66
 - SENSE BIT 1.2 TO CCU — MM371BH5 — 68
 - SENSE BIT 1.1 TO CCU — MM371BH7 — 70
 - SENSE BIT 1.0 TO CCU — MM371BH8 — 72



000 AM002

119 - STORE BIT 0.0 — MM351-DA1
 121 - STORE BIT 0.1 — MM351-DA3
 123 - STORE BIT 0.2 — MM351-DA5
 125 - STORE BIT 0.3 — MM351-DA7
 143 - STORE BIT 0.4 — MM351-DB2
 145 - STORE BIT 0.5 — MM351-DB4
 147 - STORE BIT 0.6 — MM351-DB6
 177 - STORE BIT 0.7 — MM351-DC1
 179 + STORE BIT 0.0 — MM351-DC3
 101 - SENSE BIT 0.0 — DS003-DD1
 103 - SENSE BIT 0.1 — DS003-DD3
 105 - SENSE BIT 0.2 — DS003-DD5
 107 - SENSE BIT 0.3 — DS003-DD7
 136 - SENSE BIT 0.4 — DS003-DE2
 138 - SENSE BIT 0.5 — DS003-DE4
 140 - SENSE BIT 0.6 — DS003-DE6
 156 - SENSE BIT 0.7 — DS003-DF1
 158 + SENSE BIT 0.0 — DS003-DF3
 110 - STORE BIT 1.0 — MM371-DH1
 112 - STORE BIT 1.1 — MM371-DH3
 114 - STORE BIT 1.2 — MM371-DH5
 116 - STORE BIT 1.3 — MM371-DH7
 171 - STORE BIT 1.4 — MM371-DJ2
 173 - STORE BIT 1.5 — MM371-DJ4
 175 - STORE BIT 1.6 — MM371-DJ6
 184 - STORE BIT 1.7 — MM371-DK1
 186 + STORE BIT 1.0 — MM371-DK3
 127 - SENSE BIT 1.0 — DS005-DL1
 129 - SENSE BIT 1.1 — DS005-DL3
 131 - SENSE BIT 1.2 — DS005-DL5
 133 - SENSE BIT 1.3 — DS005-DL7
 150 - SENSE BIT 1.4 — DS005-DM2
 152 - SENSE BIT 1.5 — DS005-DM4
 154 - SENSE BIT 1.6 — DS005-DM6
 163 - SENSE BIT 1.7 — DS005-DN1
 165 + SENSE BIT 1.0 — DS005-DN3

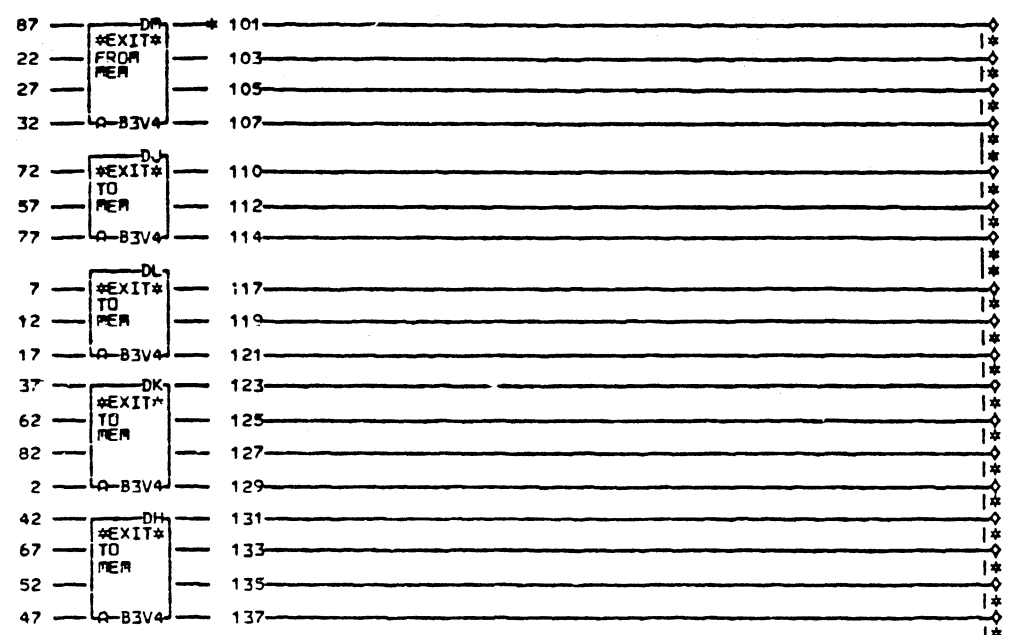
LDC. TYPE

AM002
000

MEMORY INTERFACE
 E-C-HISTORY — E-MACH 3705
 FRAME 01
 DATE LAST EC 10-14-80 344270
 IBA CORP. SCD P.N. 8550067 AM002
 000

UNUSED AM003003 2
 UNUSED AM003004 7
 UNUSED AM003005 12
 UNUSED AM003006 17
 UNUSED AM003007 22
 UNUSED AM003008 27
 UNUSED AM003009 32
 + RESET MACHINE CHECK CU006CB6 37
 + REFRESH DOUBLETIME CU016GL6 42
 + OSC OUTPUT TO MEM CU017EA6 47
 - MEM STORE NEW TIME TO MEM CU017FA6 52
 + LAMP TEST TO MEM CU017FC6 57
 + RD CALL WR CALL PULSE TO MEM CU017FG2 62
 - C TIME TO MEM CU017GA6 67
 + ACTIVE CLOCK STEP GATED CU017GC6 72
 - LATCHED STORE TO MEM CU017GE6 77
 - GATE DOUBLE BIT ERR LATCH CU017GJ2 82
 - REFRESH PENDING IF CLK STEP MM151DM1 87

EDGE CONN.
 101 A-B3V4B12

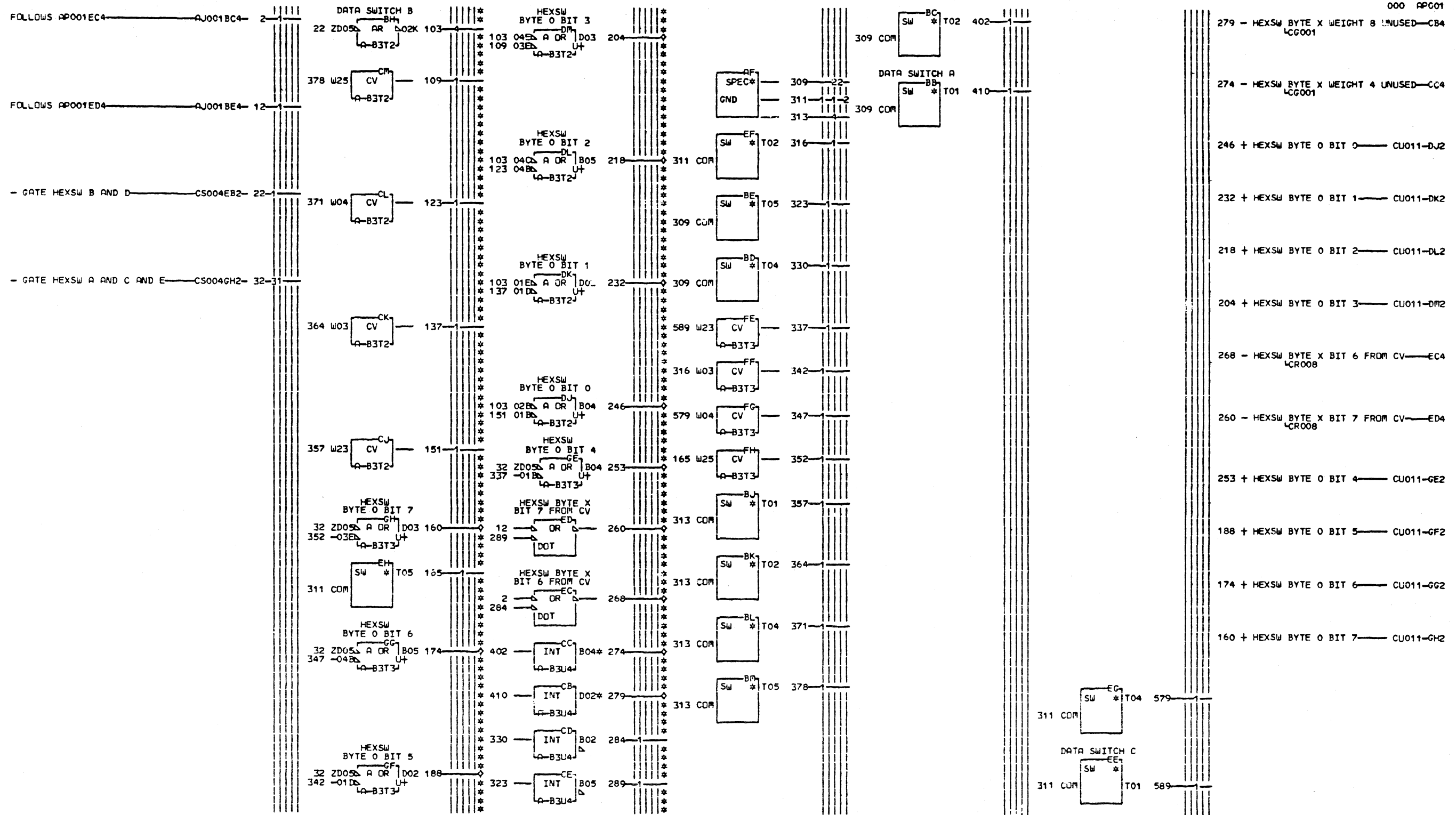


LOC. TYPE

000 AM003
 131 + REFRESH DOUBLETIME MM151-DH1
 133 - C TIME TO MEM MM151-DH3
 135 - MEM STORE NEW TIME TO MEM MM151-DH5
 137 + OSC OUTPUT TO MEM MM151-DH7
 110 + ACTIVE CLOCK STEP GATED MM151-DJ2
 112 + LAMP TEST TO MEM MM151-DJ4
 114 - LATCHED STORE TO MEM MM151-DJ6
 123 + RESET MACHINE CHECK MM151-DK1
 125 + RD CALL WR CALL PULSE TO MEM MM151-DK3
 127 - GATE DOUBLE BIT ERR LATCH MM151-DK5
 129 UNUSED MM151-DK7
 117 UNUSED MM151-DL2
 119 UNUSED MM151-DL4
 121 UNUSED MM151-DL6
 101 - REFRESH PEND IF CLK STEP MM151-DM1
 103 UNUSED MM151-DM3
 105 UNUSED MM151-DM5
 107 UNUSED MM151-DM7

AM003
 000

MEMORY INTERFACE			
E.C.-HISTORY	E. PACH. 3705	FRAME	01
DATE	LAST EC	IBM CORP. SCD	AF003
10-14-80	344270	P.N. 8550068	000



000 AP001

- 279 - HEXSW BYTE X WEIGHT 8 UNUSED - CBA LCG001
- 274 - HEXSW BYTE X WEIGHT 4 UNUSED - CC4 LCG001
- 246 + HEXSW BYTE 0 BIT 0 - CU011-DJ2
- 232 + HEXSW BYTE 0 BIT 1 - CU011-DK2
- 218 + HEXSW BYTE 0 BIT 2 - CU011-DL2
- 204 + HEXSW BYTE 0 BIT 3 - CU011-DM2
- 268 - HEXSW BYTE X BIT 6 FROM CV - ECA LCG008
- 260 - HEXSW BYTE X BIT 7 FROM CV - EDA LCG008
- 253 + HEXSW BYTE 0 BIT 4 - CU011-GE2
- 188 + HEXSW BYTE 0 BIT 5 - CU011-GF2
- 174 + HEXSW BYTE 0 BIT 6 - CU011-GG2
- 160 + HEXSW BYTE 0 BIT 7 - CU011-GH2

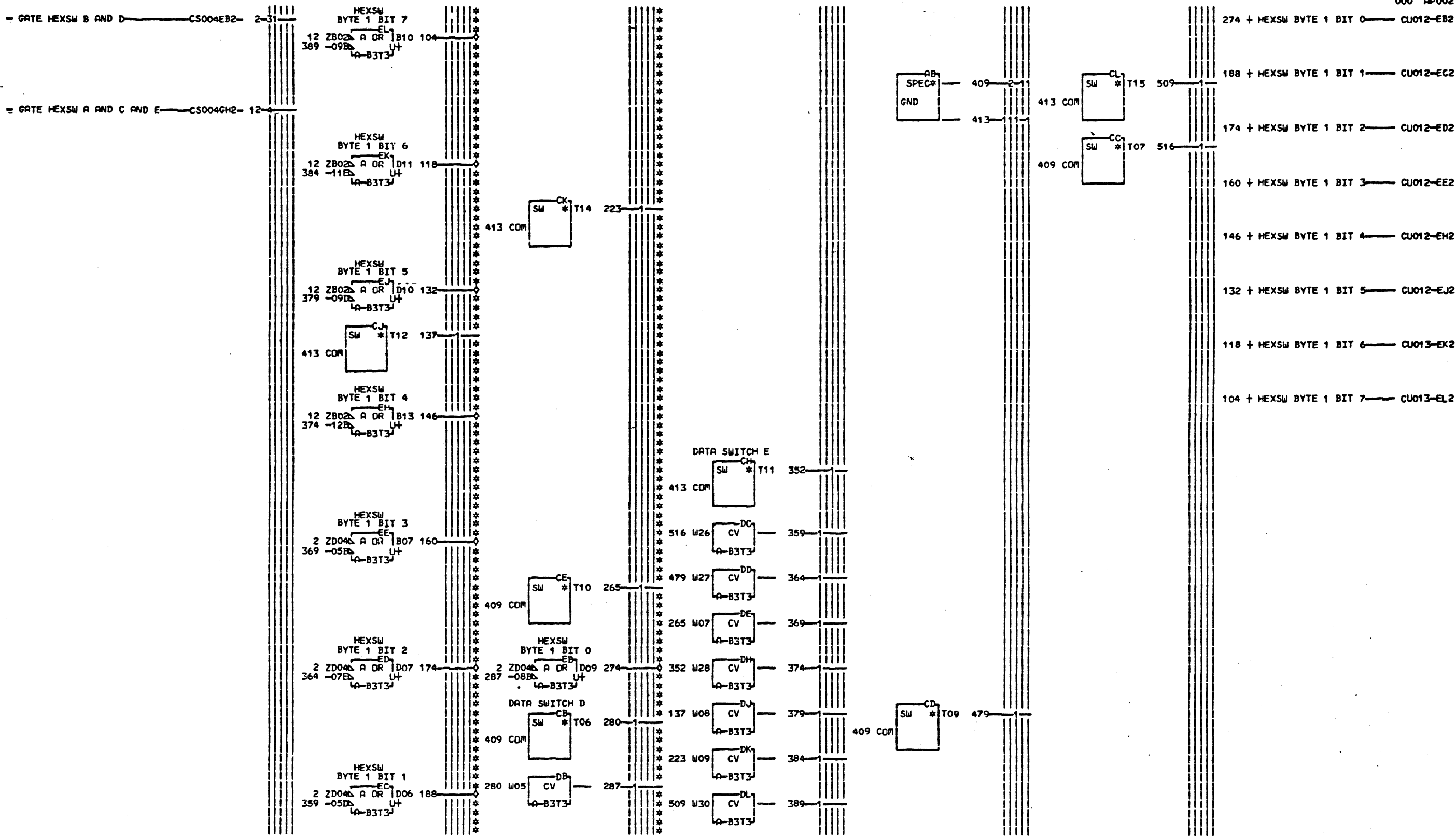
EDGE CONN.
 274 A-B3F1C11
 01A-B4F6C02
 279 A-B3F1B13
 01A-B4F6B04

LOC. TYPE
 A-B3T2 6804
 A-B3T3 6804
 A-B3U4 2589

PANEL ROTOR SWITCHES
 -E.C.-HISTORY-E RACH#3705
 FRAME 01
 DATE LAST EC 10-14-80 344270
 IBM CORP.SCD P.N. 8550069

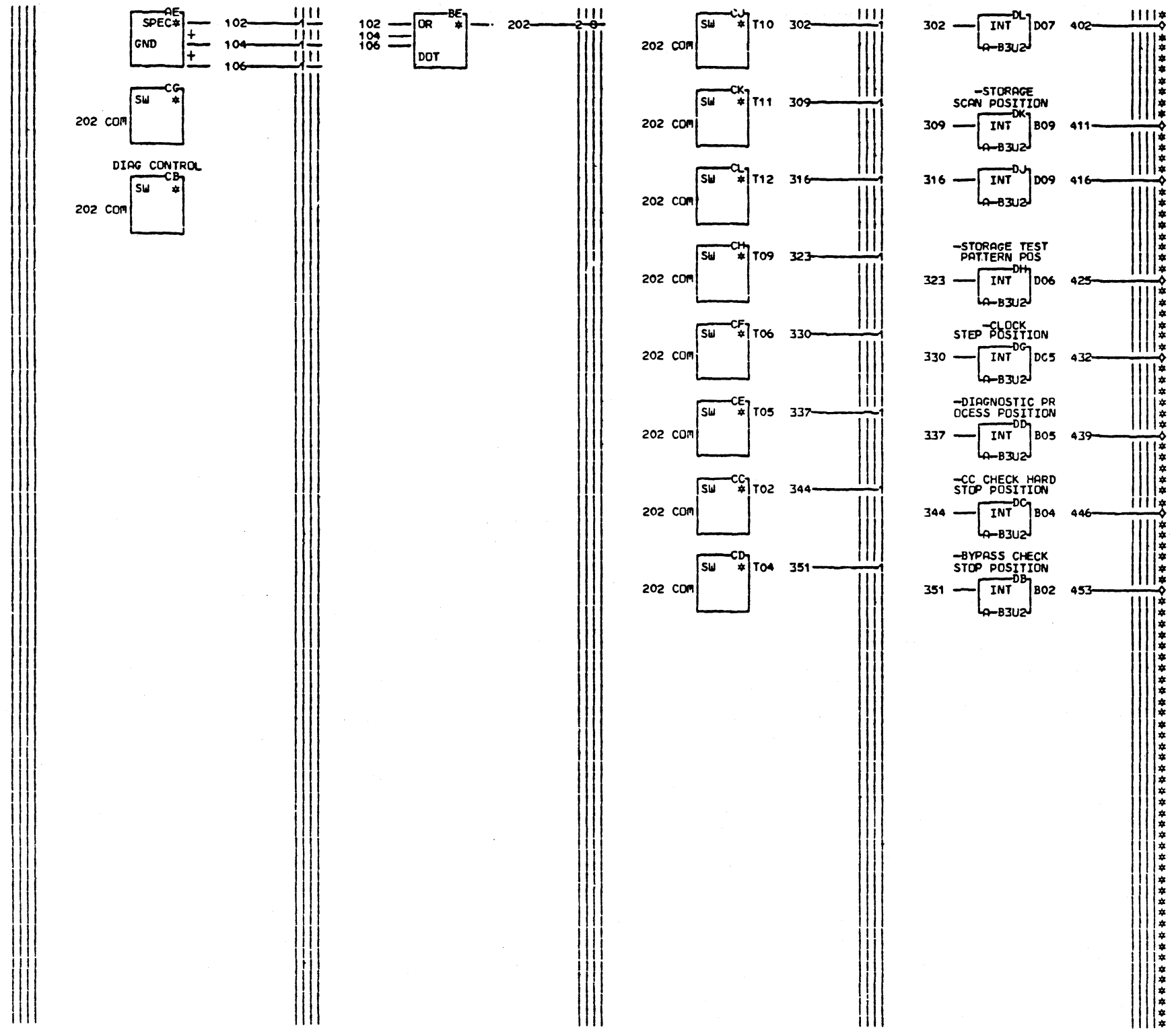
AP001
 000

AP001
 000



LOC. TYPE
A-B3T3 6804

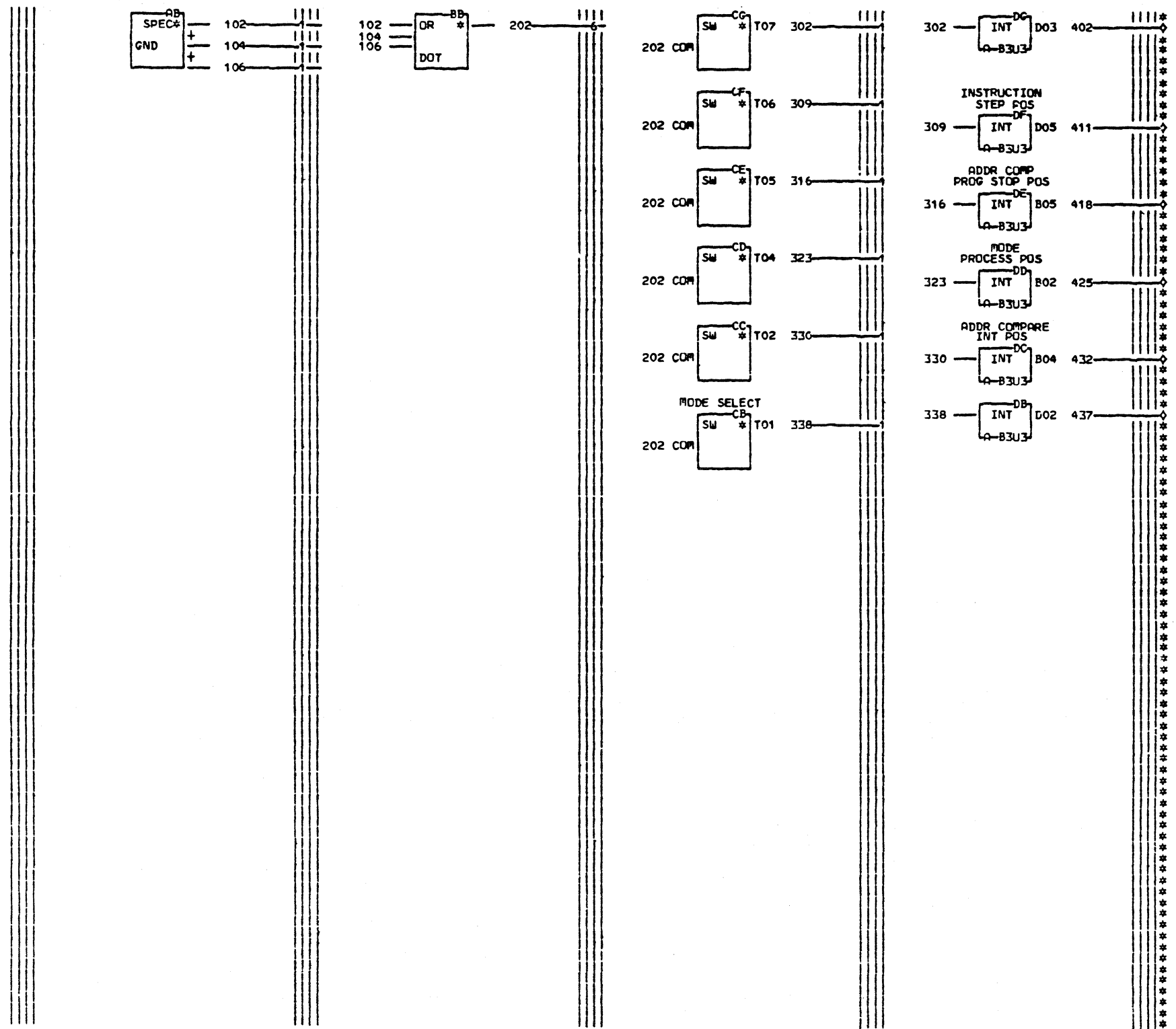
PANEL ROTOR SWITCHES			
E.C. HISTORY		E. MACH. 3705	
344270		FRAME 01	
DATE LAST EC		IBM CORP. SCD AP002	
06-02-81 344828		P.N. 8550070 000	



- 453 - BYPASS CHECK STOP POSITION—DB4
LCU006
- 446 - CC CHECK HARD STOP POSITION—DC4
LCU006
- 439 - DIAGNOSTIC PROCESS POSITION—DD4
LCU001
- 432 - CLOCK STEP POSITION—CU007-DG4
- 425 - STORAGE TEST PATTERN POS—DH4
LCU007
- 416 - SINGLE ADDRESS SCAN POSITION—DJ4
LCU007
- 411 - STORAGE SCAN POSITION—CU007-DK4
- 402 - SINGLE ADDR TEST PATTERN POS—DL4
LCU007

LDC. TYPE
A-B3U2 2589

MODE SELECT	E-MACH.3705
ROTOR SWITCH	FRAME 01
E.C.-HISTORY	IBM CORP.SCD AP003
DATE LAST EC	P.N. 8550071 000
10-14-80 344270	



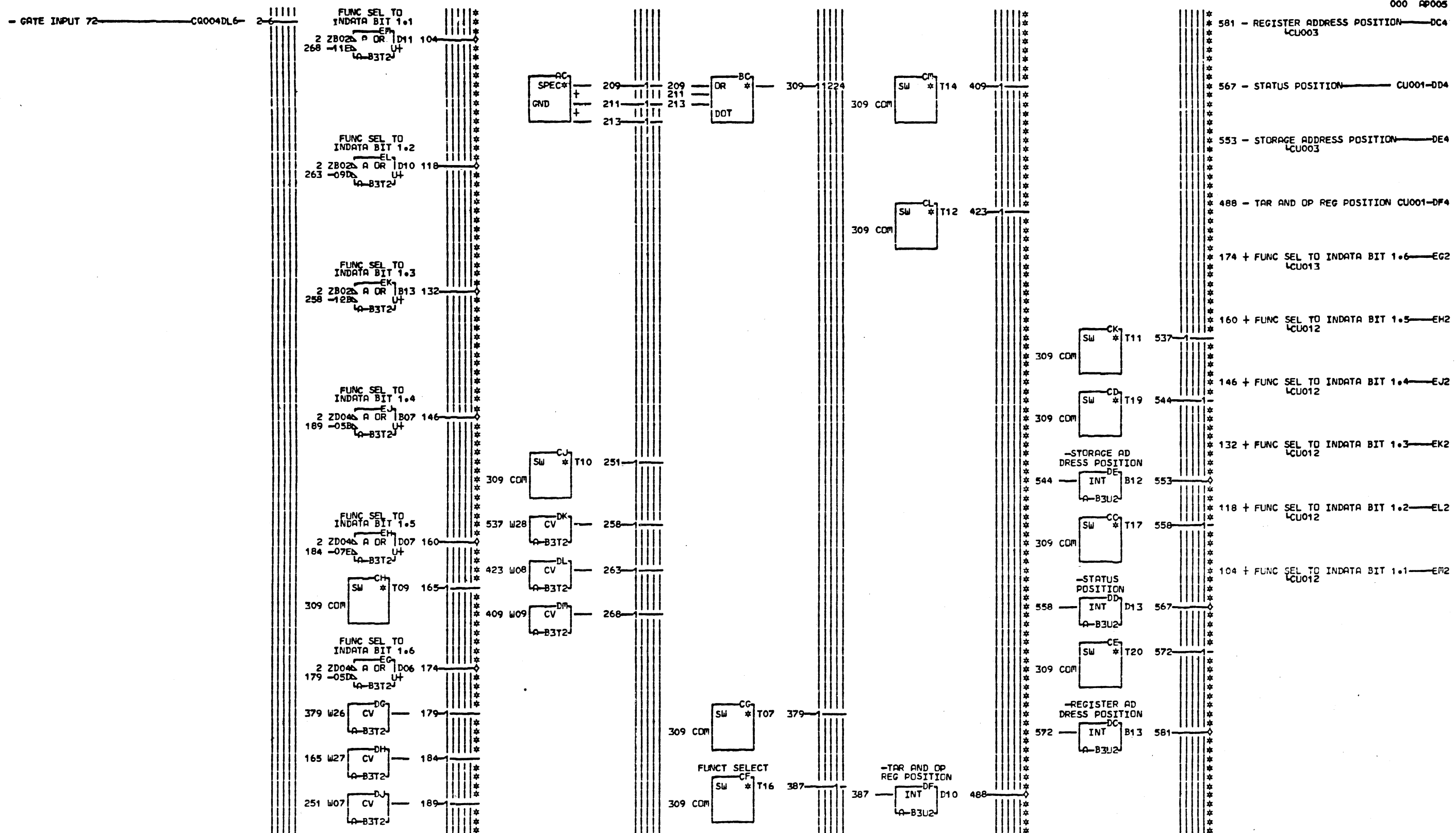
LOC. TYPE
A-83U3 2589

000 AP004

437 + SPARE POS — DBA
 432 + ADDR COMPARE INT POS — CU004-DC4
 425 + MODE PROCESS POS — CU001-DD4
 418 + ADDR COMP PROG STOP POS CU004-DE4
 411 + INSTRUCTION STEP POS — CU006-DF4
 402 + SPARE POS — DG4

AP004
000

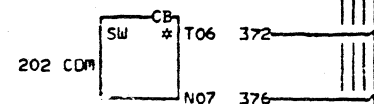
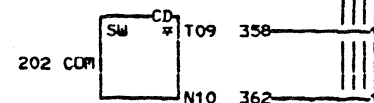
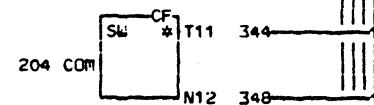
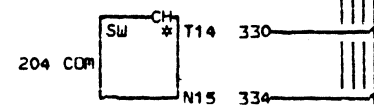
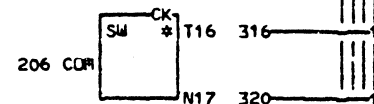
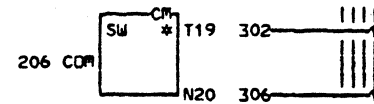
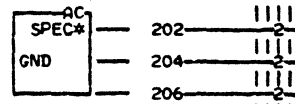
ADDRESS STOP-INTERRUPT SELECT ROTOR SWITCH		E.C.—HISTORY—E.MACH#3705	
DATE	LAST EC	FRAME	01
10-14-80	344270	IBM CORP.SCD	AP004
		P.N. 8550072	000



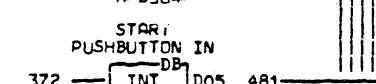
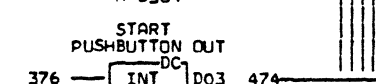
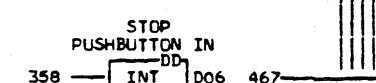
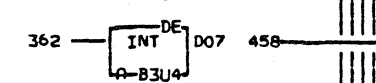
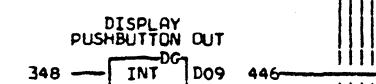
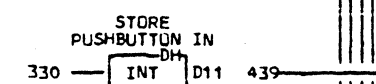
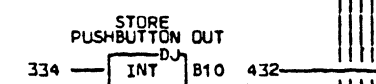
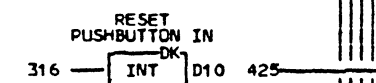
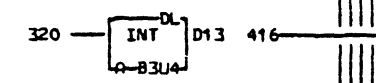
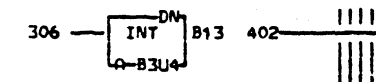
LDC TYPE
 A-B3T2 6804
 A-B3U2 2589

FUNCTION SELECT ROTOR SWITCH		E. MACH. 3705	
E.C. HISTORY 344270		FRAME	01
DATE	LAST EC	IBM CORP. SCD	AP005
06-02-81	344828	P.N. 8550073	000

-BLANK COLUMN-



LOC. TYPE
A-B3U4 2589



000 AP006

481 + START PUSHBUTTON IN CU017-DB4

474 + START PUSHBUTTON OUT DC4
LC0007 LC0003

467 + STOP PUSHBUTTON IN CU004-DD4

458 + STOP PUSHBUTTON OUT UNUSED DE4

453 + DISPLAY PUSHBUTTON IN CU003-DF4

446 + DISPLAY PUSHBUTTON OUT CU003-DG4

439 + STORE PUSHBUTTON IN CU003-DH4

432 + STORE PUSHBUTTON OUT CU003-DJ4

425 + RESET PUSHBUTTON IN CU010-DK4

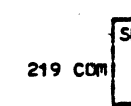
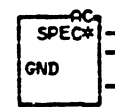
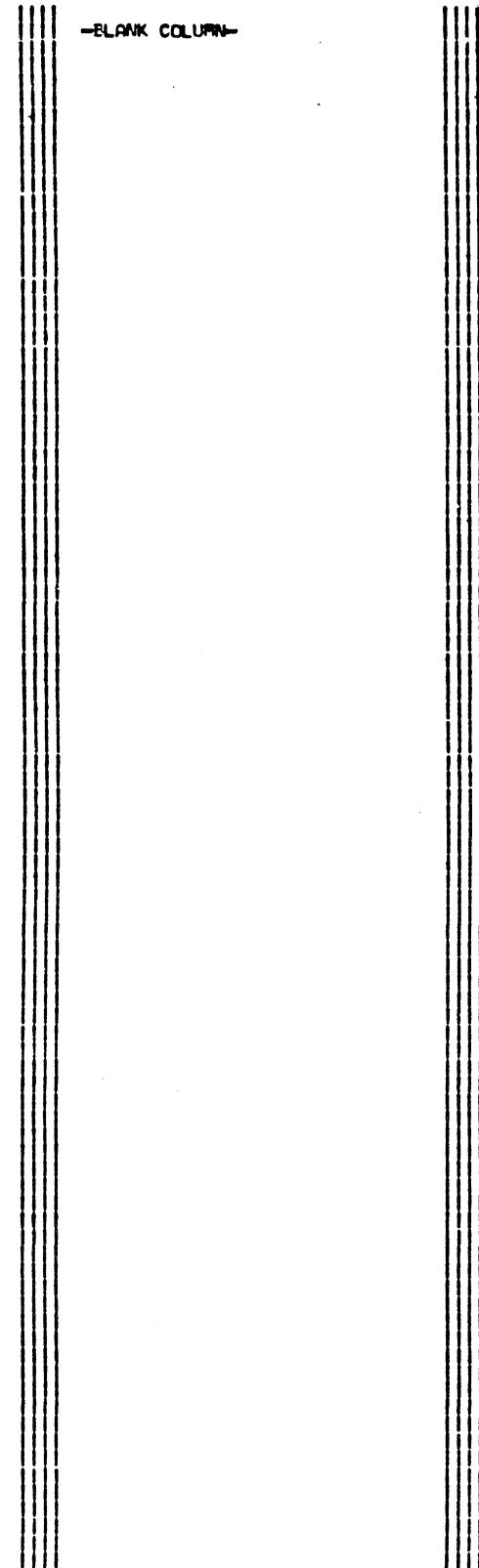
416 + RESET PUSHBUTTON OUT UNUSED DL4

411 + CHECK RESET PUSHBUTTON IN DM4
LCU006

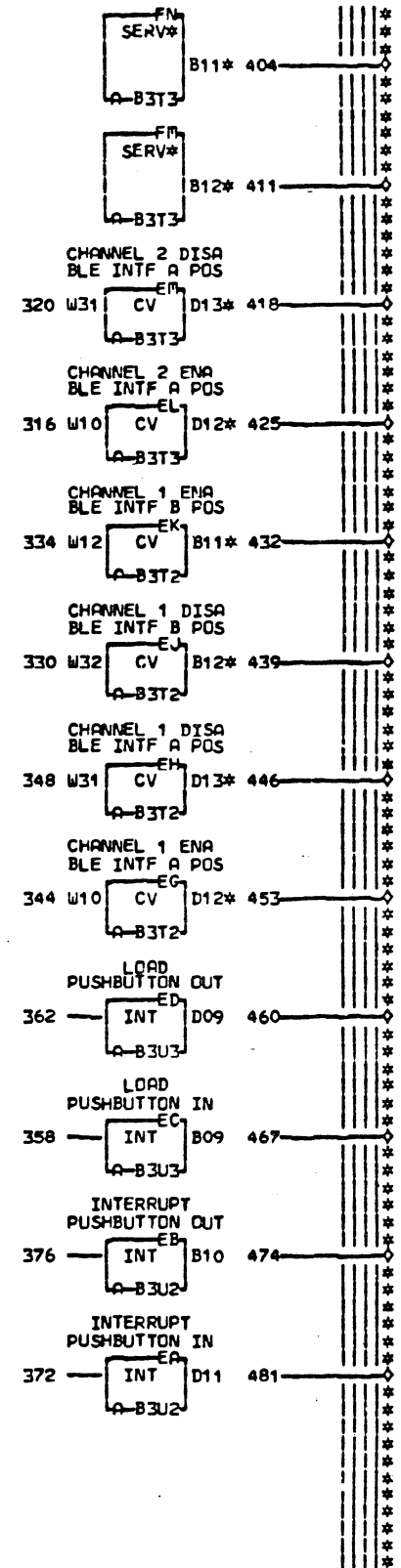
402 + CHECK RESET PB OUT UNUSED DN4

PUSH BUTTONS			
E.C.	HISTORY	E. MACH.	3705
		FRAME	01
		IBM CORP.	SCD AP006
DATE	LAST EC	P.N.	8550074 000
10-14-80	344270		

-BLANK COLUMN-



LOC. TYPE
A-B3T2 6804
A-B3T3 6804
A-B3U2 2589
A-B3U3 2589



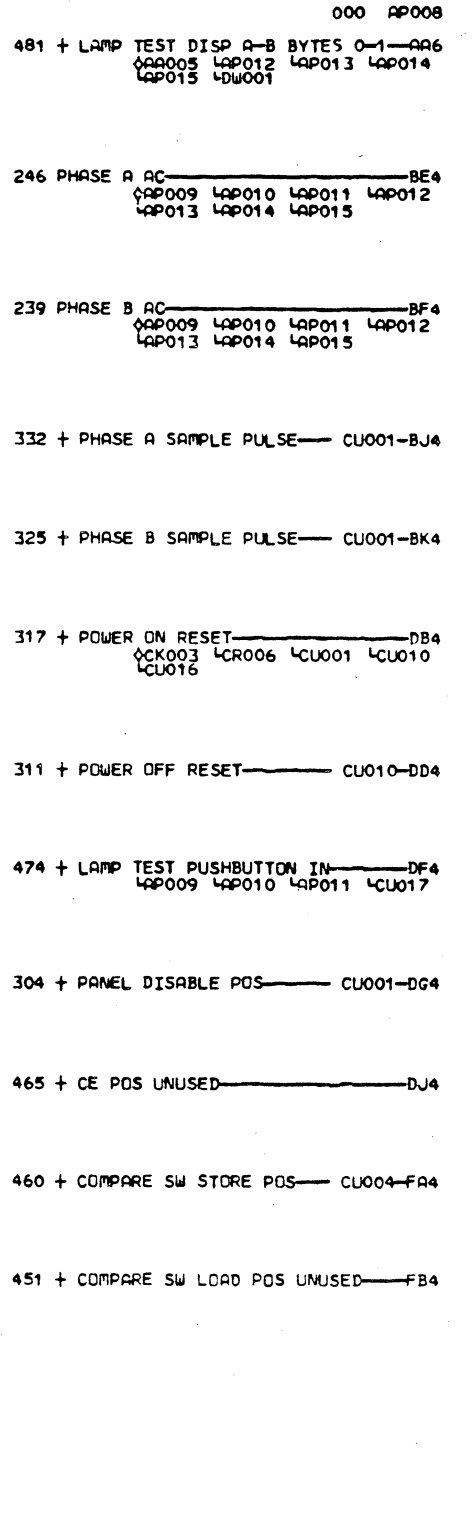
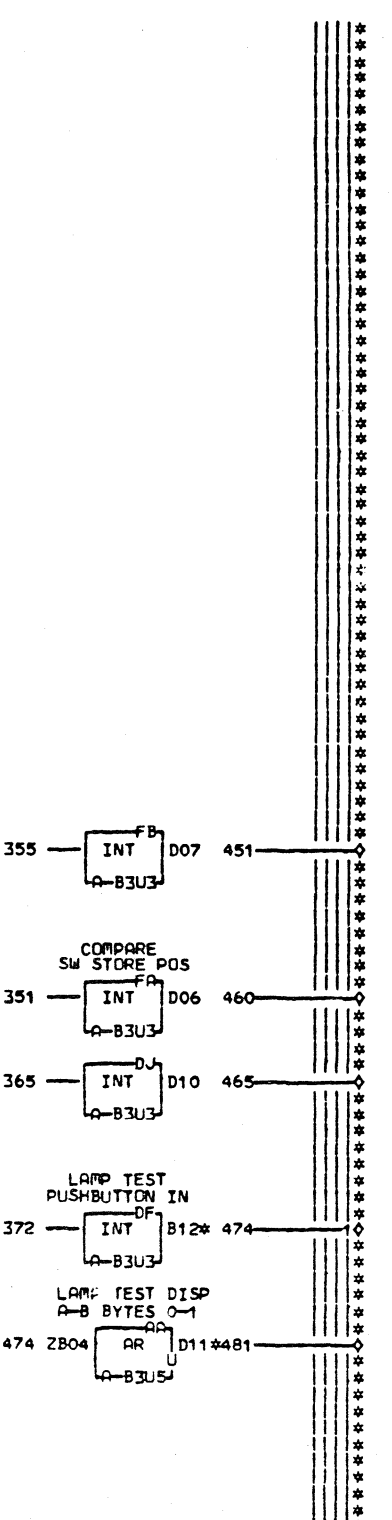
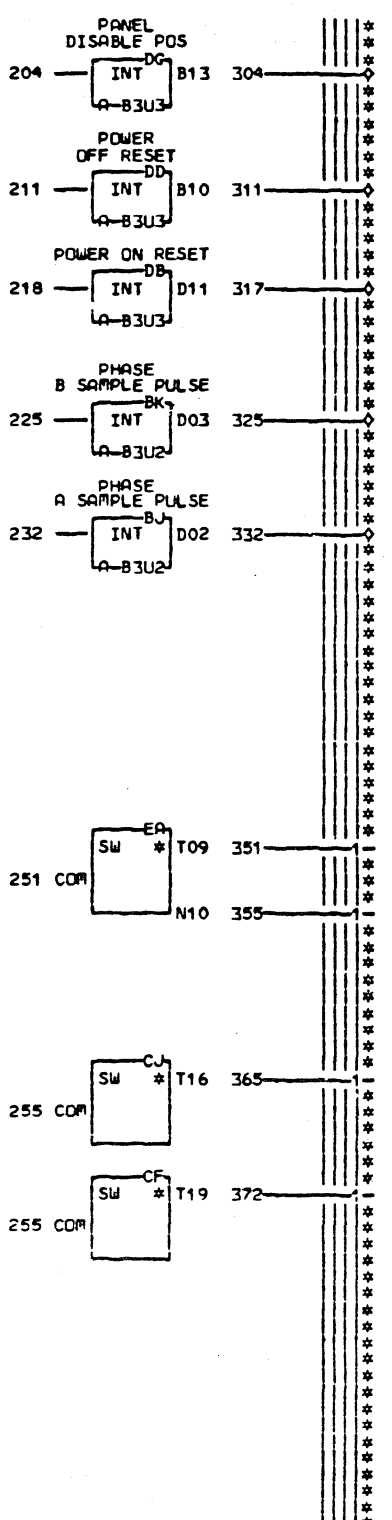
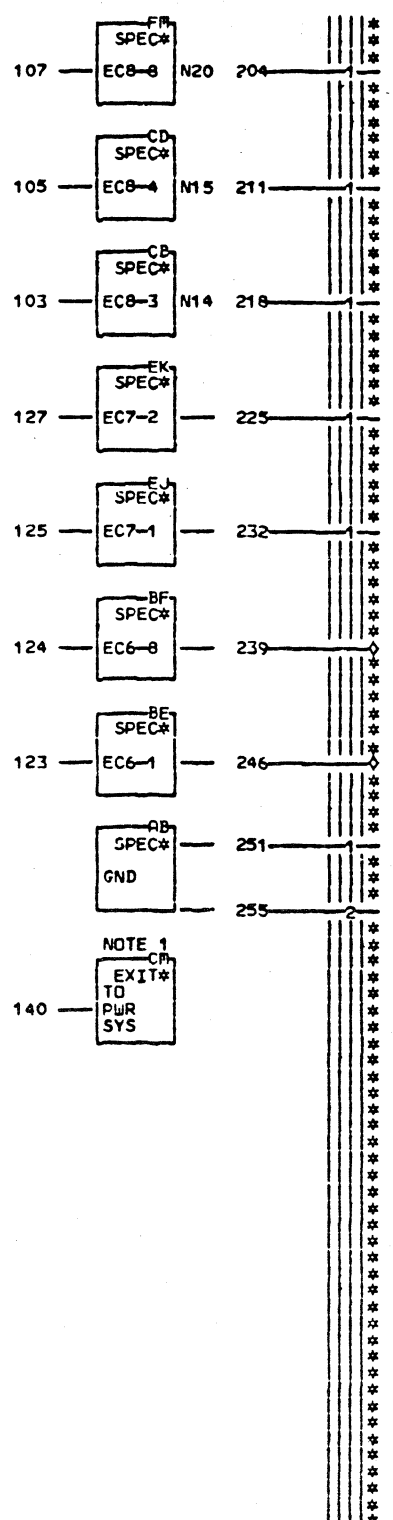
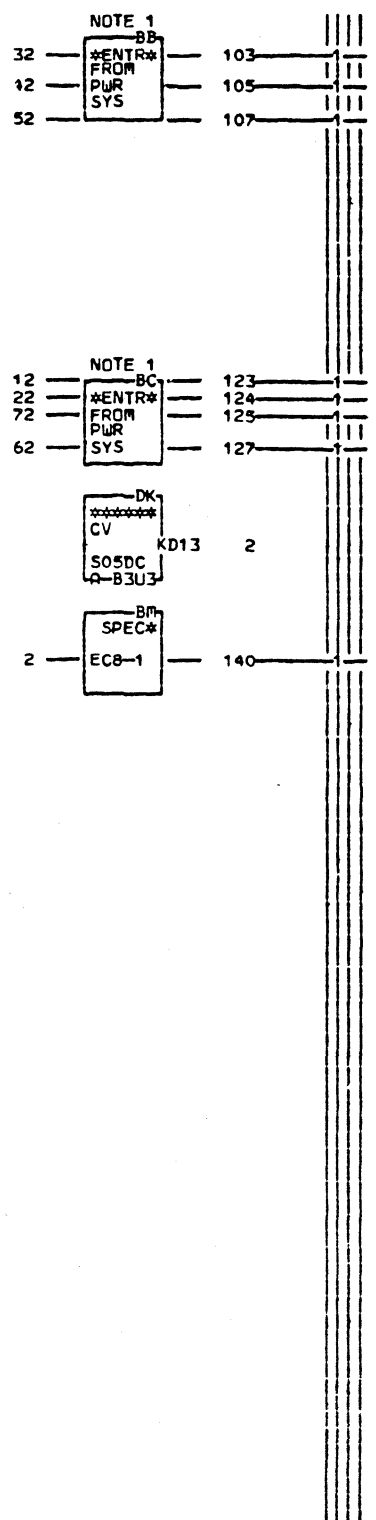
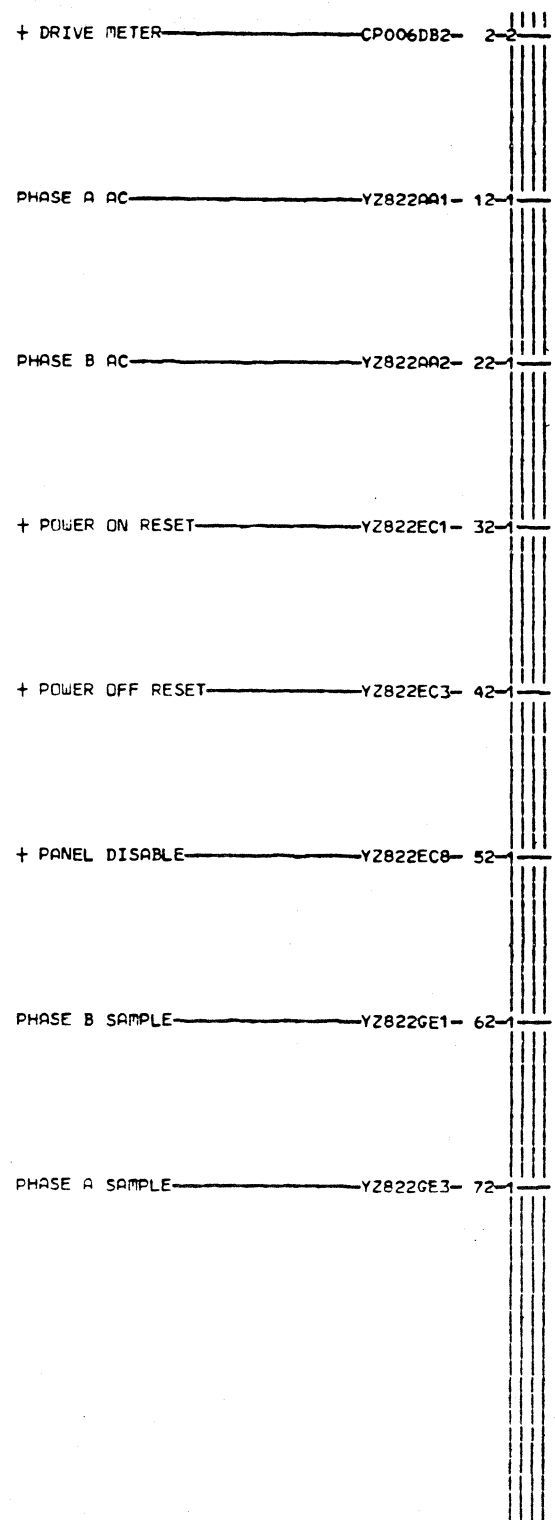
- 481 + INTERRUPT PUSHBUTTON IN CU014-ER4
- 474 + INTERRUPT PUSHBUTTON OUT EBA CU014
- 467 + LOAD PUSHBUTTON IN CU010-EC4
- 460 + LOAD PUSHBUTTON OUT CU010-ED4
- 453 + CHANNEL 1 ENABLE INTF A POS EGA LA002
- 446 + CHANNEL 1 DISABLE INTF A POS EHA LA002
- 439 + CHANNEL 1 DISABLE INTF B POS EJ4 LA002
- 432 + CHANNEL 1 ENABLE INTF B POS EK4 LA002
- 425 + CHANNEL 2 ENABLE INTF A POS EL4 LA002
- 418 + CHANNEL 2 DISABLE INTF A POS EMA LA002
- 411 ALWAYS -4V TWO RA002-FR4
- 404 ALWAYS -4V ONE RA002-FN4

EDGE COM.
404 P-B3A4B09
411 P-B3A4B12
418 P-B3A4B06
425 P-B3A4B04
432 P-B3A4B08
439 P-B3A4B10
446 P-B3A4B05
453 P-B3A4B02

PUSH BUTTONS AND TOGGLES

E.C. HISTORY E MACH. 3705
344270 FRAME 01

DATE LAST EC IBM CORP. SCD AP007
06-02-81 344828 P.N. 8550075 000



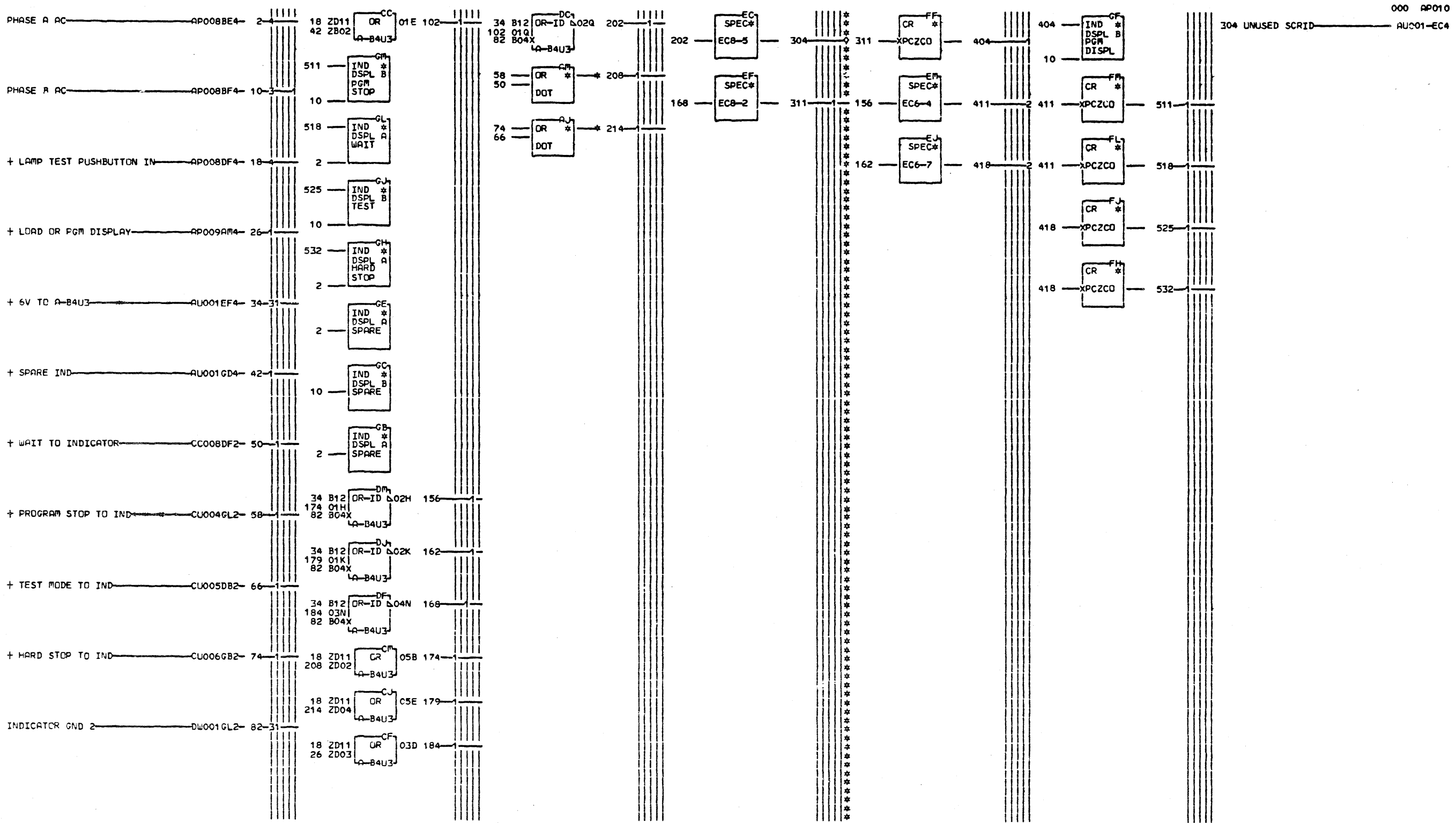
NOTE 1.
MATCH ECXX
CONNECTORS
PAGE Y2822.
YZ SOURCES ON
THIS SHEET ARE
PSEUDO SOURCES.

EDGE CONN.
474 A-B3V1B13
01A-B4V6B04
481 A-B3A1D11
01A-B4A6D02

LOC. TYPE
A-B3U2 2589
A-B3U3 2589
A-B3U5 885

AP008
000

POWER ON RESET RELAY POWER OFF SEQ RELAY CE CUST POWER ENABLE E.C.-HISTORY E-ARCH-3705		FRAME 01
DATE 10-14-80	LAST EC 344270	IBM CORP.SCD AP008 P.N. 8550076 000



EDGE CONN.
 208 A-B3V3B02
 01A-B4V3B02
 214 A-B3V2D05
 01A-B4V2D05

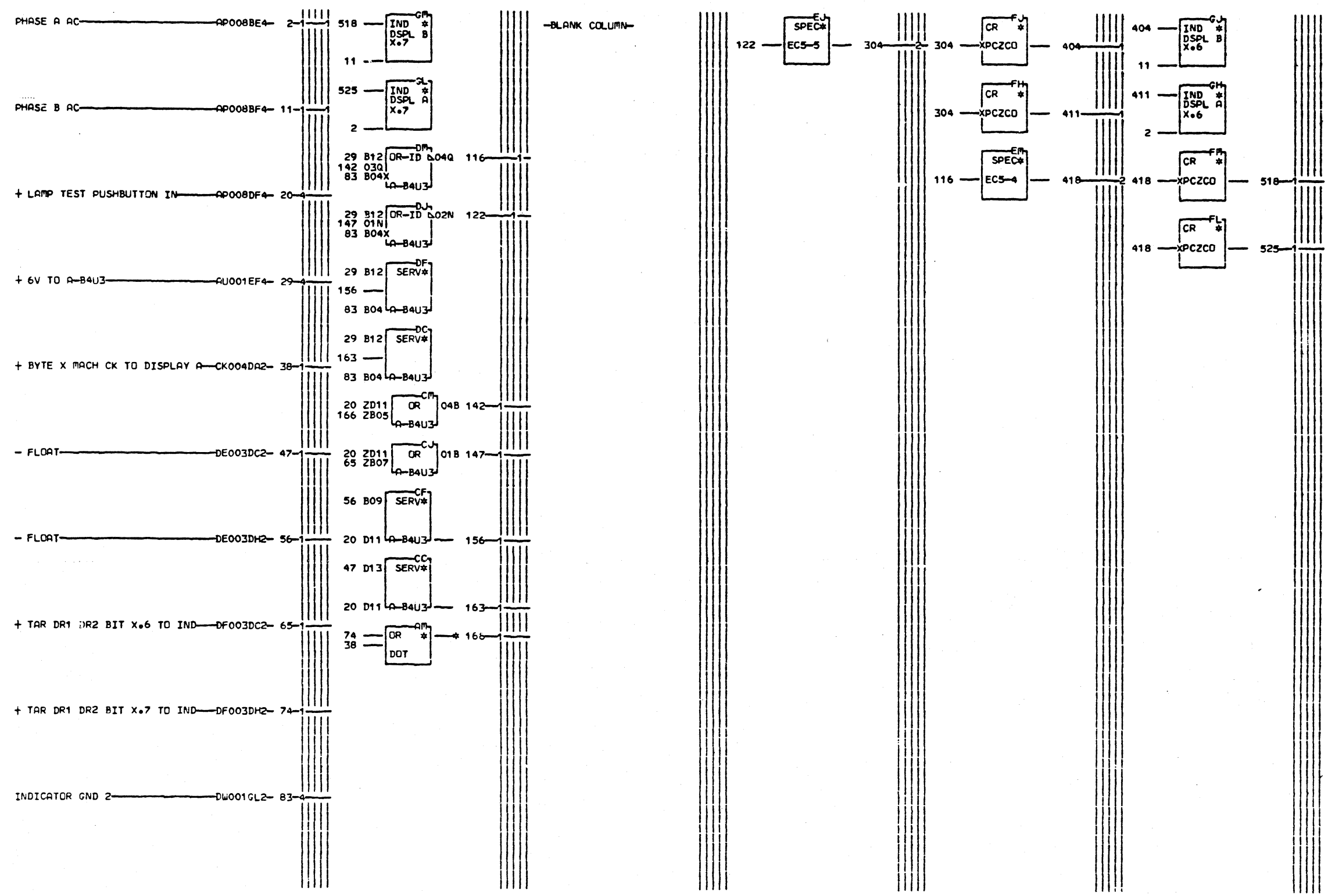
LOC. TYPE
 A-B4U3 6845

INDICATOR CIRCUITS FOR
 DISPLAY A AND B OTHER IND
 -E.C.-HISTORY- E-RACH.3705

DATE	LAST EC	FRAME	01
10-14-80	344270	IBM CORP.SCD	AP010
		P.No. 8550078	000

AP010
 000

000 AP010
 304 UNUSED SCRID AUC01-EC4

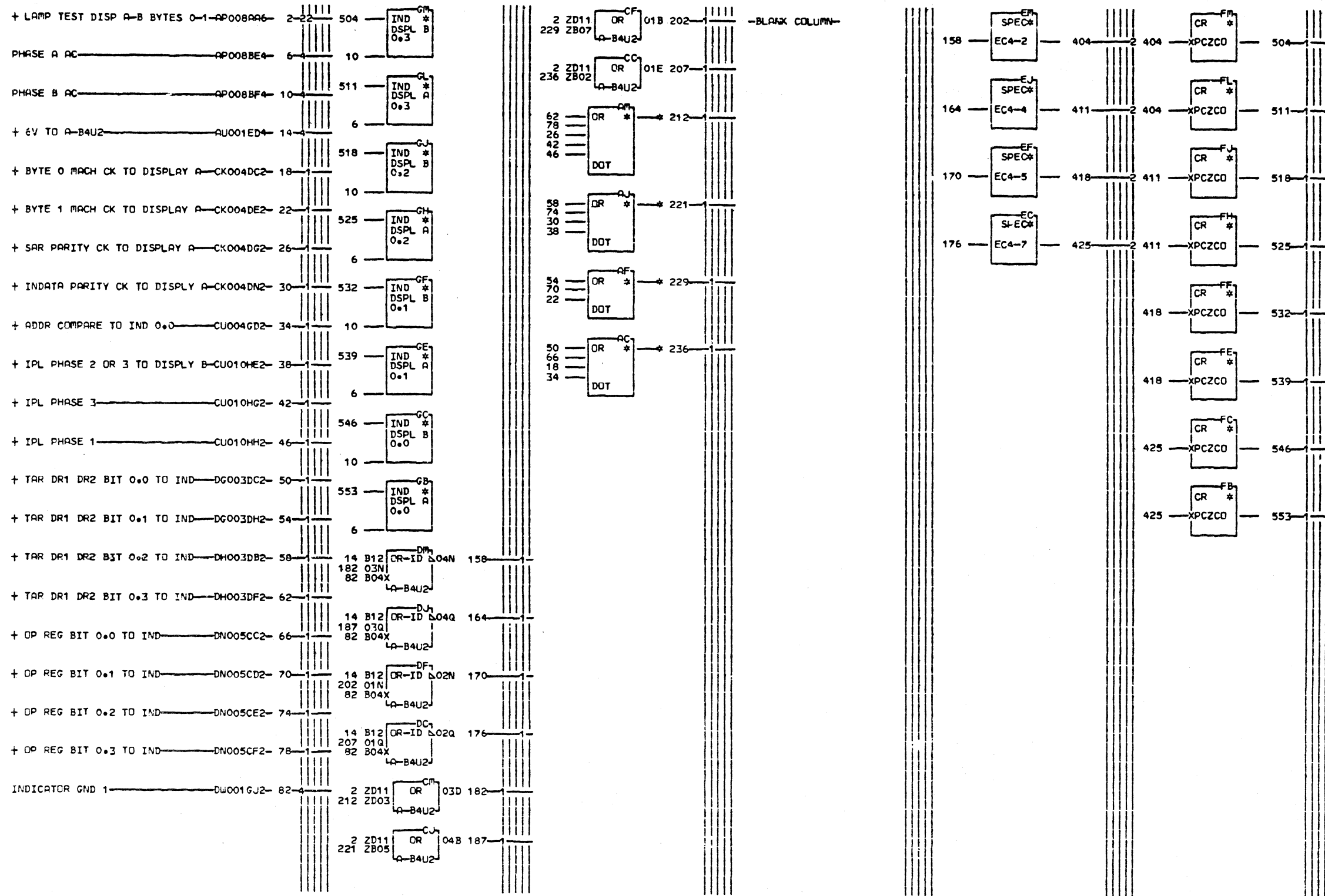


EDGE CONN.
166 A-B3V2B04
01A-B4V2B04

LOC. TYPE
A-B4U3 6845

INDICATOR CIRCUITS FOR
DSPL A AND B AND BYTE X
-E.C.-HISTORY-E-MACH.3705

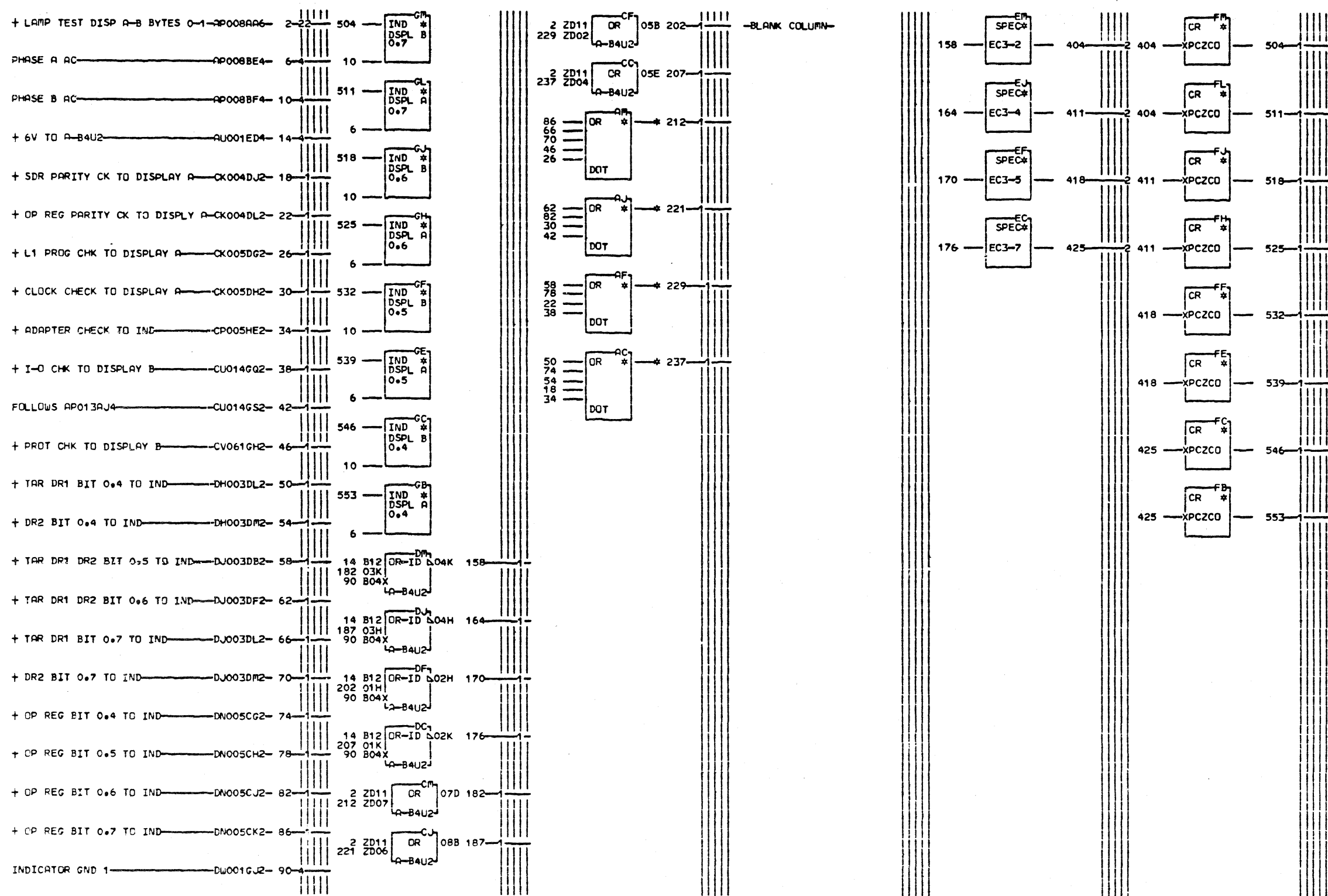
DATE	LAST EC	FRAME	01
10-14-80	344270	IBM CORP.SCD	AP011
		P.N. 8550079	000



EDGE CONN.
 212 A-B3V2B09
 01A-B4V2B09
 221 A-B3V2B08
 01A-B4V2B08
 229 A-B3V2B06
 01A-B4V2B06
 236 A-B3V2B05
 01A-B4V2B05

LDC TYPE
 A-B4U2 6845

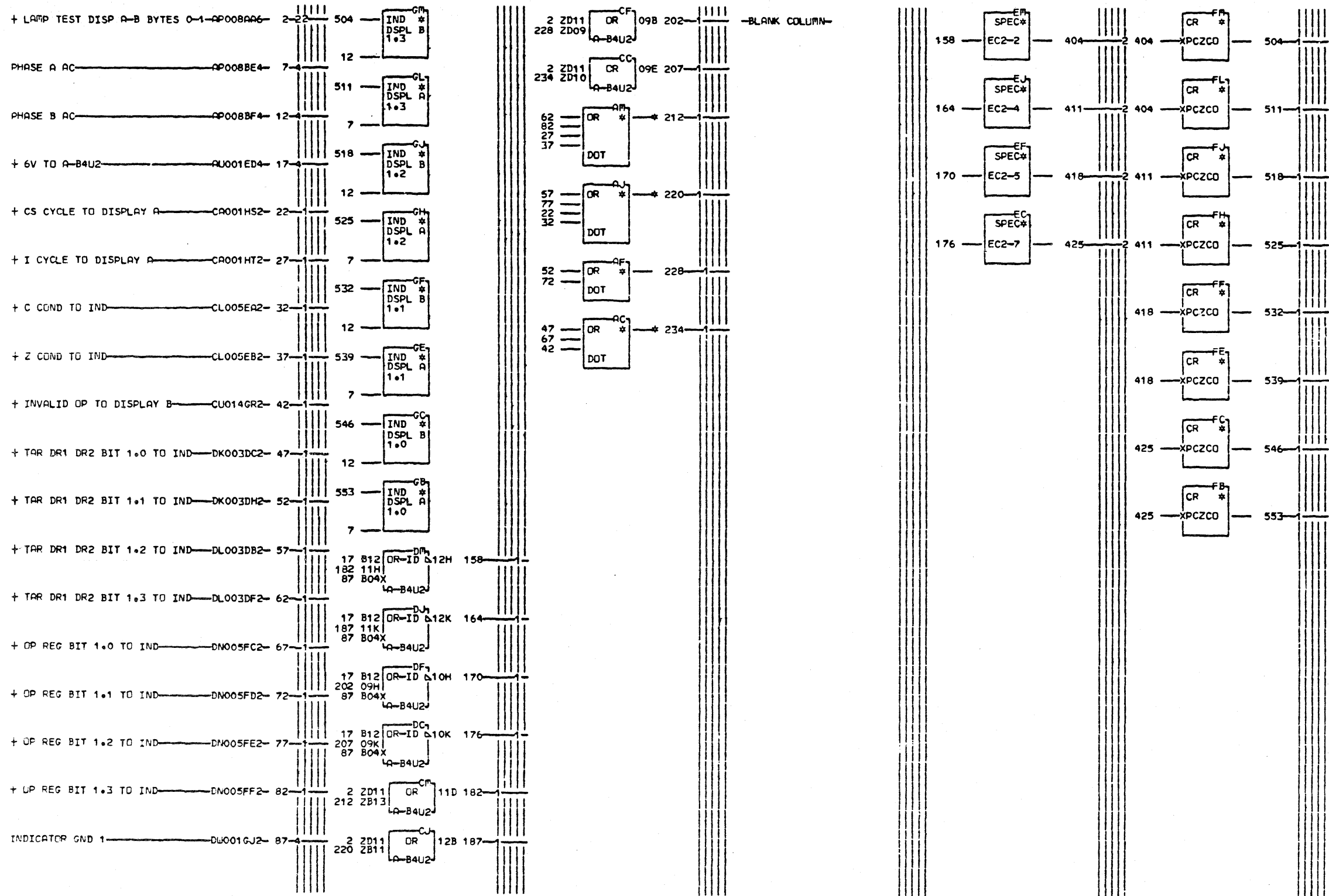
INDICATOR CIRCUITS FOR
 DISPLAY A AND B BYTE 0
 E-C-HISTORY E-MACH.3705
 FRAME 01
 IBR CCRP.SCD AP012
 DATE LAST EC 10-14-80 344270 P.N. 8550080 000



EDGE CONN.
 212 A-B3V2D02
 01A-B4V2D02
 221 A-B3V2B13
 01A-B4V2B13
 229 A-B3V2B12
 01A-B4V2B12
 237 A-B3V2B10
 01A-B4V2B10

LOC. TYPE
 A-B4U2 6845

INDICATOR CIRCUITS FOR
 DISPLAY A AND B BYTE 0
 -E.C.-HISTORY- E1 MACH. 3705
 FRAME 01
 IBM CORP. SCD AP013
 DATE LAST EC 10-14-80 344270 P.N. 8550081 000

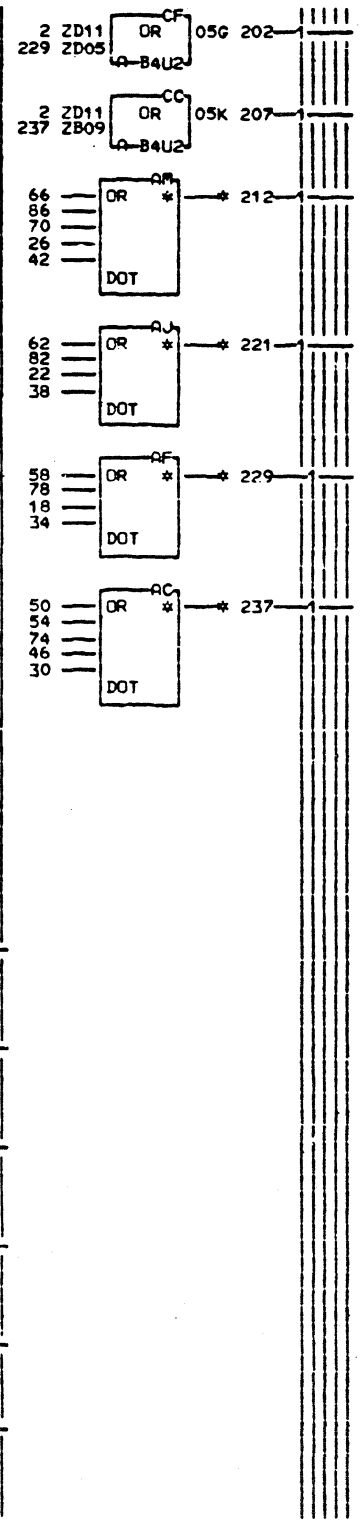
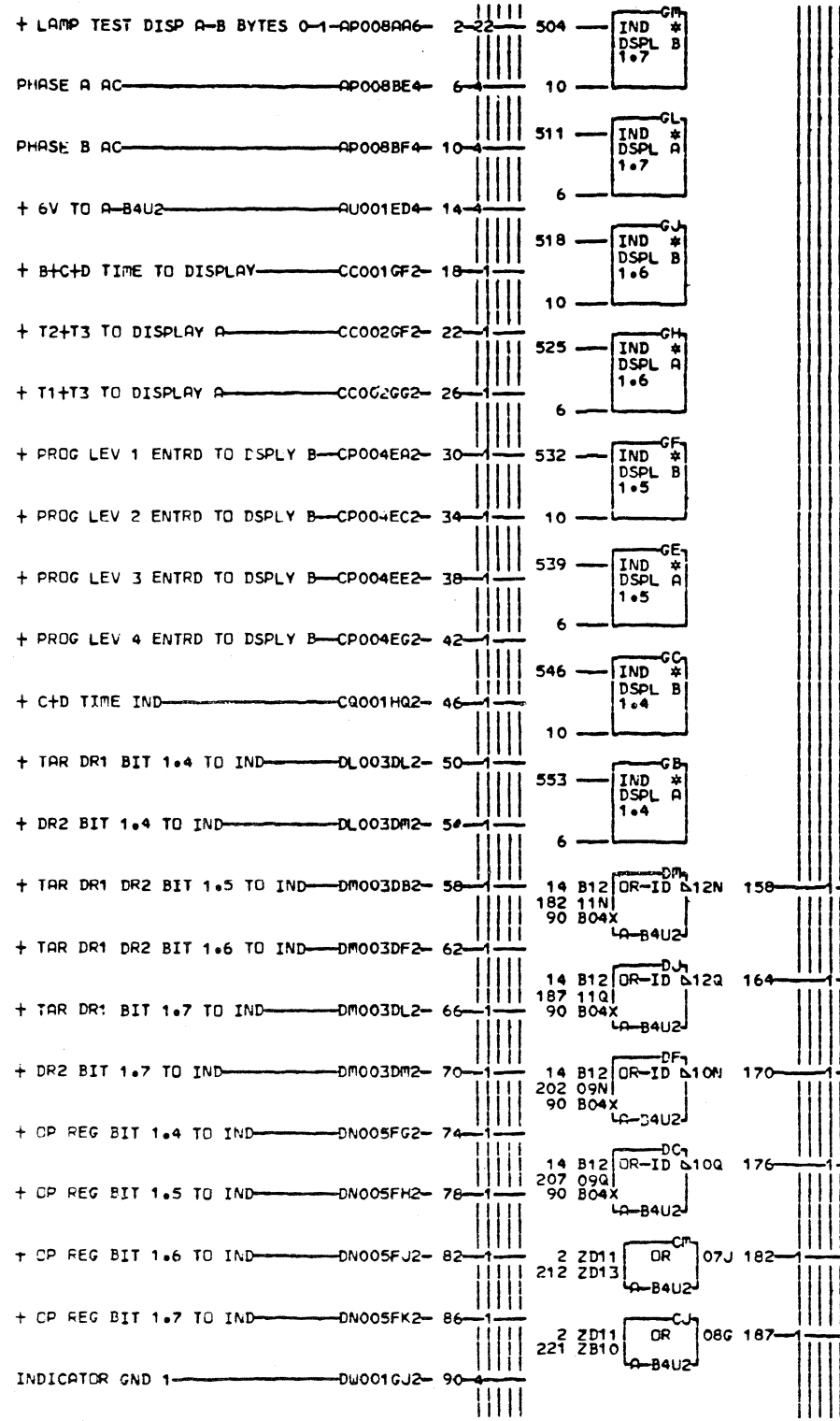


EDGE CONN.
 212 A-B3V2D07
 01A-B4V2D07
 220 A-B3V2D06
 01A-B4V2D06
 234 A-B3V2D03
 01A-B4V2D03

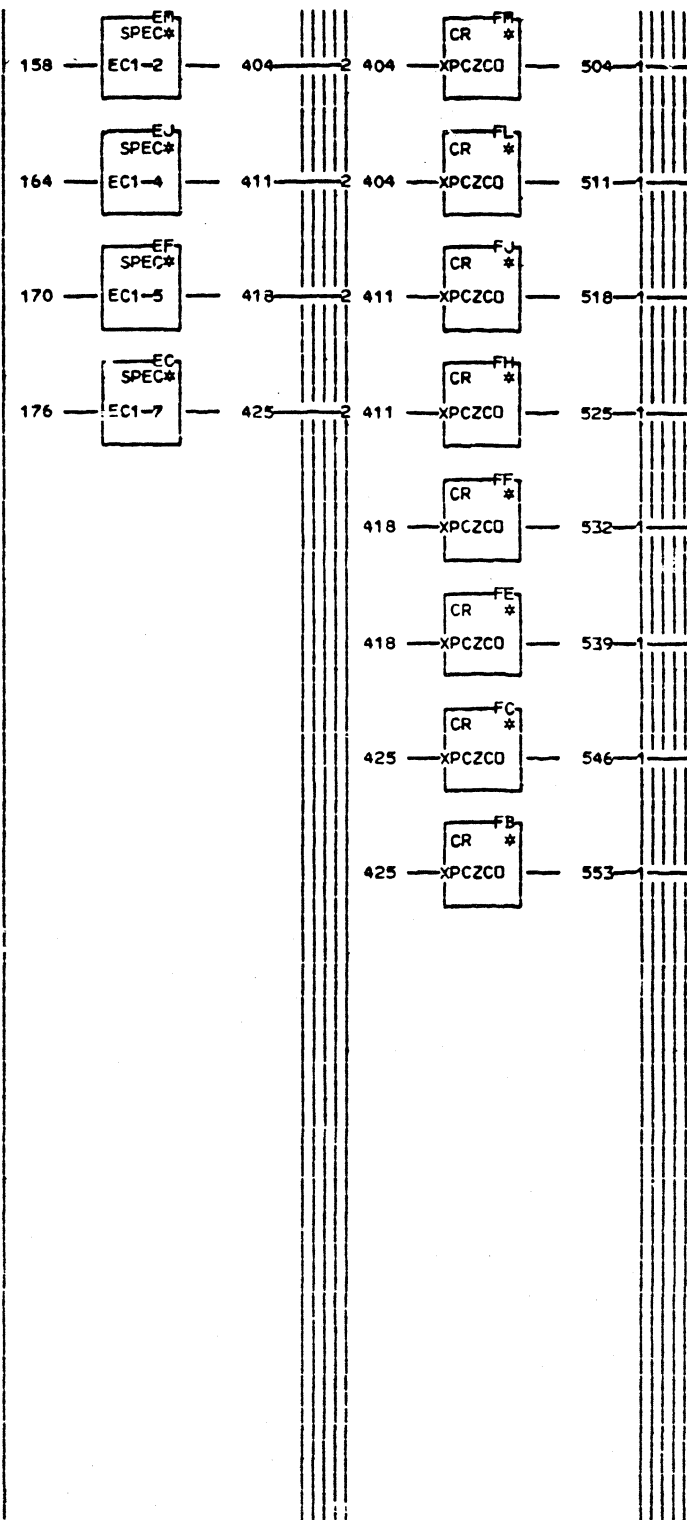
LOC. TYPE
 A-B4U2 6845

INDICATOR CIRCUITS FOR
 DISPLAY A AND B BYTE 1
 -E.C.-HISTORY-E.MACH.3705

FRAME	01
DATE	10-14-80
LAST EC	344270
P.No.	8550082
AP014	000



-BLANK COLUMN-



EDGE CONN.
 212 A-B3V2D13
 01A-B4V2D13
 221 A-B3V2D11
 01A-B4V2D11
 229 A-B3V2D10
 01A-B4V2D10
 237 A-B3V2D09
 01A-B4V2D09

LOC. TYPE
 A-B4U2 6845

INDICATOR CIRCUITS FOR
 DISPLAY A AND B BYTE 1
 E.C.-HISTORY E1 MACH.3705
 FRAME 01
 IBR CORP.SCD AP015
 DATE LNST EC 10-14-80 344270 P.N. 8550083 000

UNUSED SCRID — AP010EC4 — 2 —

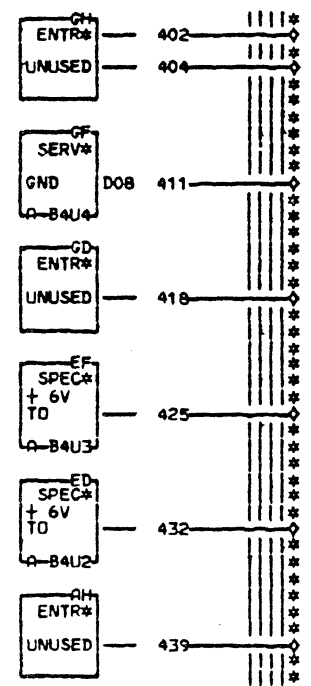
— INT SCOPING POINT UNLOADED — CQ001BF6 — 12 —

NOTE
 EXIT*
 SCOPE

NOTE
 EXIT*
 UNUSED

—BLANK COLUMN—

—BLANK COLUMN—



000 AU001

439 - FLOAT — CC007-AH4

432 + 6V TO A-B4U2 — EDA
 CA005 LAP012 LAP013 LAP014
 LAP015

425 + 6V TO A-B4U3 — EF4
 LAP009 LAP010 LAP011

418 + SPARE IND — AP010-GD4

411 + TIE UP — GF4
 DE971 LDE977 LDF975 LDF977
 DG975 LDM002 LDP992 LDP993
 LDP994

402 - FLOAT — GH2
 CA003 LCD001 LDF002 LDG002
 LDK002

404 - SPARE GO FOR PROTECTION KEY — GH4
 LCV011

NOTE
 WHEN NOT PROGRAM
 LEVEL 5 + ADPT I-O
 CKO - CCU I-O CK

AU001
 000

LOC. TYPE

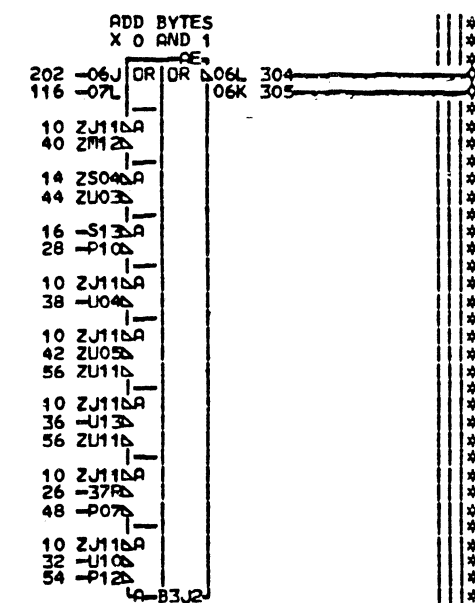
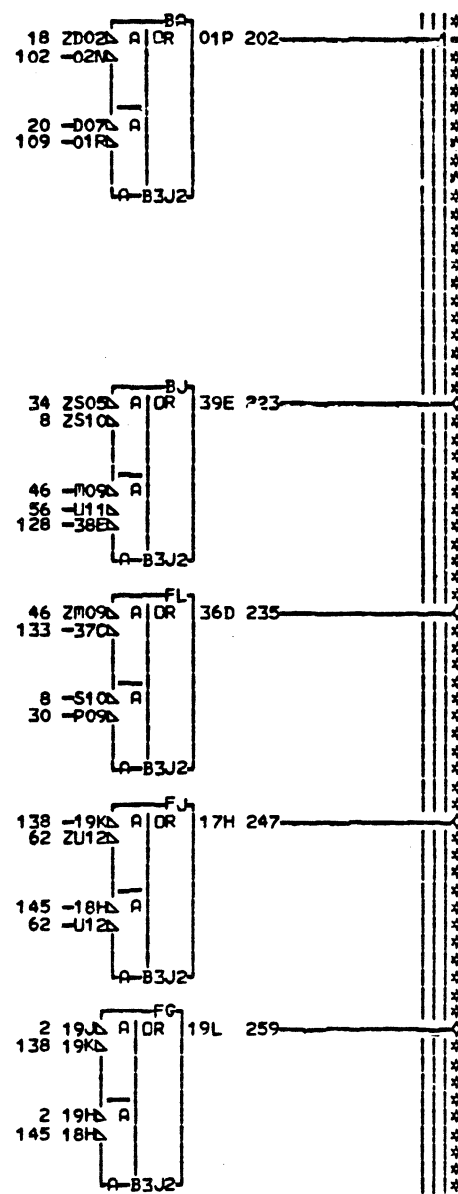
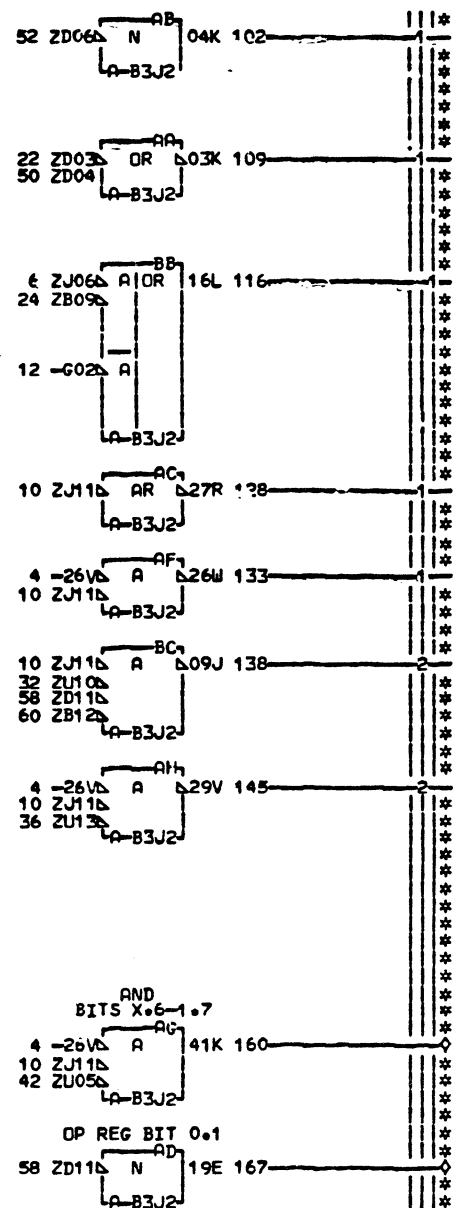
UNUSED SIGNALS SPARES FLOATS
 TIE UPS AND SCOPE POINTS
 E-C-HISTORY E-PACH-3705

FRAME 01

IBM CORP. SCD AU001

DATE LAST EC 10-14-80 344270 P.N. 8550084 000

+ DP REG BIT 0.7 CA001HH2 2-2
 = DP XXXX XXXX X110 XXXX CA003BB9 4-3
 = AB TIME CC001BE6 6-1
 - I1 BC TIME CC003BH6 8-2
 - I1 CD TIME CC003BJ6 10-5
 - I1 XA TIME CC003BL6 12-1
 - I2 CD TIME CC003CJ6 14-1
 - I2 XA TIME CC003CL6 16-1
 - CS2 XA TIME CC003EB6 18-1
 - CS1 XA TIME CC003EL6 20-1
 - CYCLE STEAL AB CC008AE6 22-1
 - ANY I TIME CC008AM2 24-1
 + DP X000 XXXX XXXX XXXX CD001CG2 26-1
 - BAL+LA INST CD001DK6 28-1
 - BB INST CD002CE6 30-1
 - RI INST TYPE CD002CF6 32-1
 - BCT INST CD002CG6 34-1
 - RR INST TYPE-BYTE CD002CH6 36-1
 - B+BB+BCL+BZL+BCT INST CD002EF2 38-1
 - ICT OR STCT INST CD003AA6 40-1
 - RR INST TYPE-ADDRESS CD003BN6 42-1
 - L+ST INST CD003CG2 44-1
 - RR INST TYPE-HALFWORD CD003CM6 46-2
 - IC+STC+LH+STH+L+ST INST CD003DF2 48-1
 + STST+BSM+SS AB CS003BN6 50-1
 + ACTIVE STORAGE TEST CU007FF4 52-1
 - RI ADD+SUBTRACT+COMPARE CZ002CC6 54-1
 - RR ADD+SUBTRACT+COMPARE CZ003BB6 56-1
 - DP REG BIT 0.1 DN004GD2 58-2
 - DP REG BIT 0.2 DN004GF2 60-1
 - DP REG BIT 0.7 DP992GA6 62-2



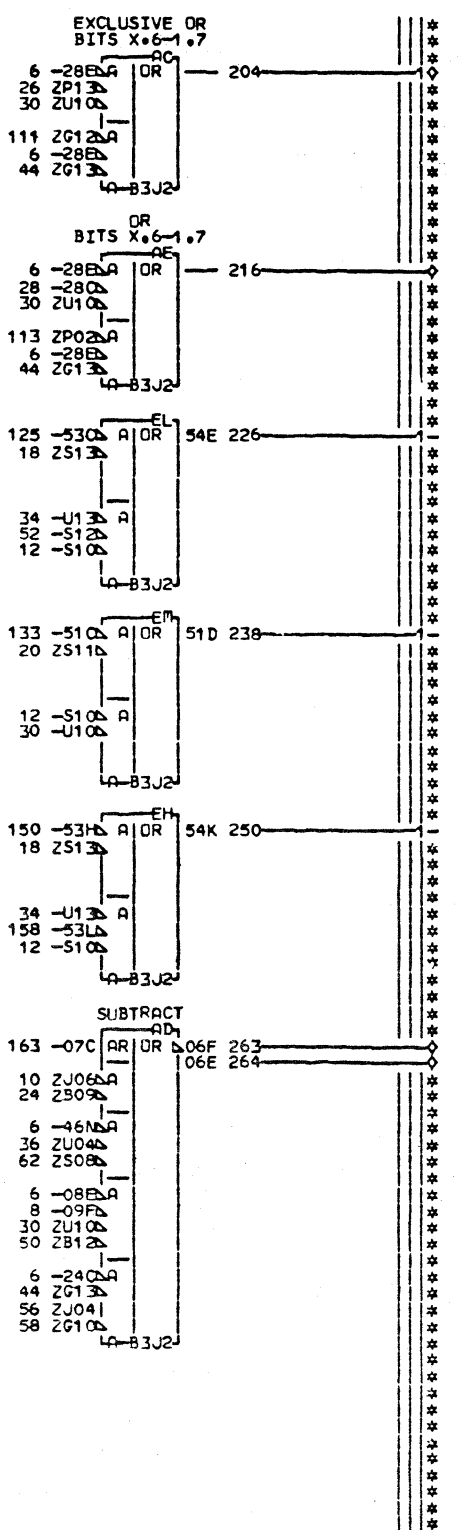
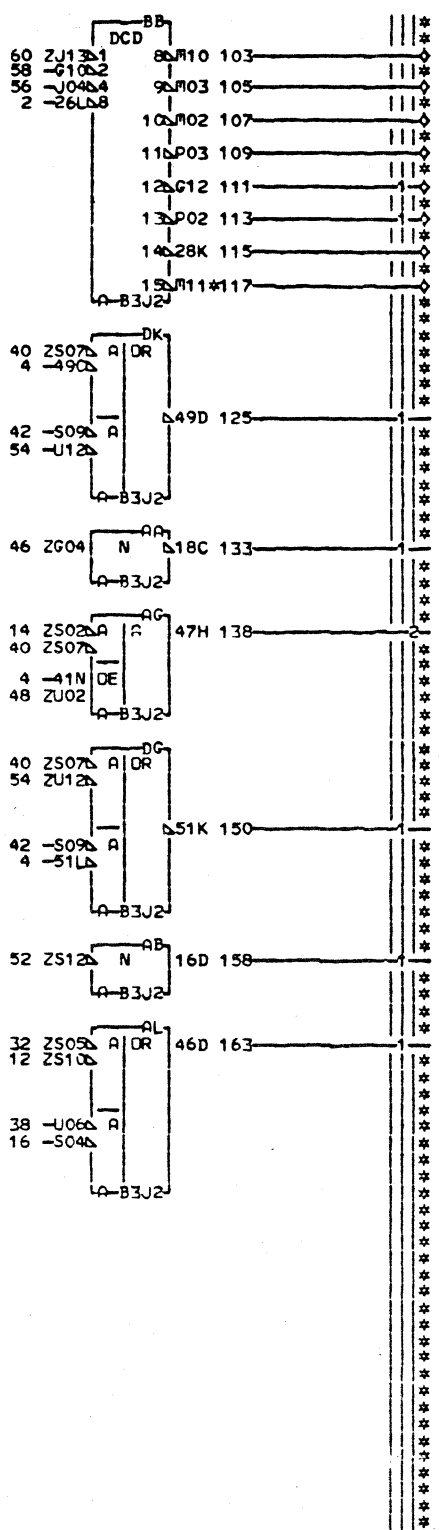
000 CA002
 223 + ADD BYTES 0 AND 1 CA004-BJ2
 167 + DP REG BIT 0.1 CA003-BA2
 304 - ADD BYTES X 0 AND 1 CA004-CC2
 305 + ADD BYTES X 0 AND 1 CA004-CC6
 160 + AND BITS X.6-1.7 CA004-DE2
 259 + AND BITS 0.0-0.7 CA004-FG2
 247 + AND BITS 1.0-1.7 CA004-FJ2
 235 + AND BITS 0.0-1.7 CA004-FL2

LOC. TYPE
 A-B3J2 6815

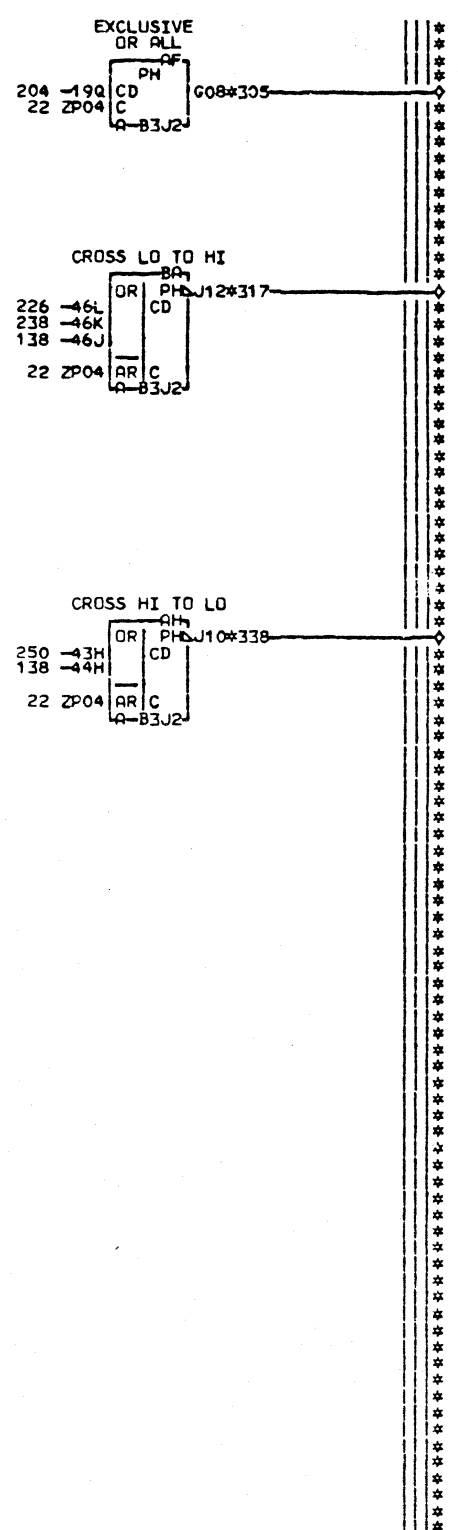
CA002
 000

ALU CONTROLS	
E.C. HISTORY 344270	E. MACH. 3705
DATE 06-02-81	LAST EC 344828
FRAME 01	IBM CORP. SCD
P.No. 8550086	CA002 000

- FLOAT AU001GH2 2-1
 + OP REG BIT 0.7 CA001HM2 4-3
 - I1 CD TIME CA001HQ6 6-7
 + OP REG BIT 0.1 CA002BN2 9-1
 - AB TIME CC001BE6 10-1
 - I1 BC TIME CC003BH6 12-3
 - I2 BC TIME CC003CH6 14-1
 - I2 CD TIME CC003CJ6 16-1
 - I2 XA TIME CC003CL6 18-2
 - CS1 AB TIME CC003EC6 20-1
 + TO TIME CC006AF2 22-3
 - ANY I TIME CC008BM2 24-1
 - OP X100 XXXX XXXX XXXX CD001BH7 26-1
 - OP X101 XXXX XXXX XXXX CD001BH8 28-1
 - RI INST TYPE CD002CF6 30-1
 - BCT INST CD002CG6 32-1
 - RR INST TYPE-BYTE CD002CH6 34-2
 - B+BB+BCL+BZL+BCT INST CD002EF2 36-1
 - ST INST CD003AJ6 38-1
 - IC+CT INST CD003CC2 40-3
 - STCT+STC INST CD003CE2 42-2
 - RR INST-BYTE+HW+ADDR CD004AB2 44-3
 + BOOTSTRAP MODE GATED CU016GE2 46-1
 + SAR BIT 1.7 DM001EK2 48-1
 - OP REG BIT 0.2 DN004GF2 50-1
 - OP REG BIT 0.3 DN004GH2 52-1
 - OP REG BIT 0.7 DP992GA6 54-2
 - OP REG BIT 1.1 DQ004GD2 56-1
 - OP REG BIT 1.2 DQ004GF2 58-1
 - OP REG BIT 1.3 DQ004GH2 60-1
 - OP REG BIT 1.7 DR992GA6 62-1



LOC. TYPE
A-B3J2 6815



000 CA003

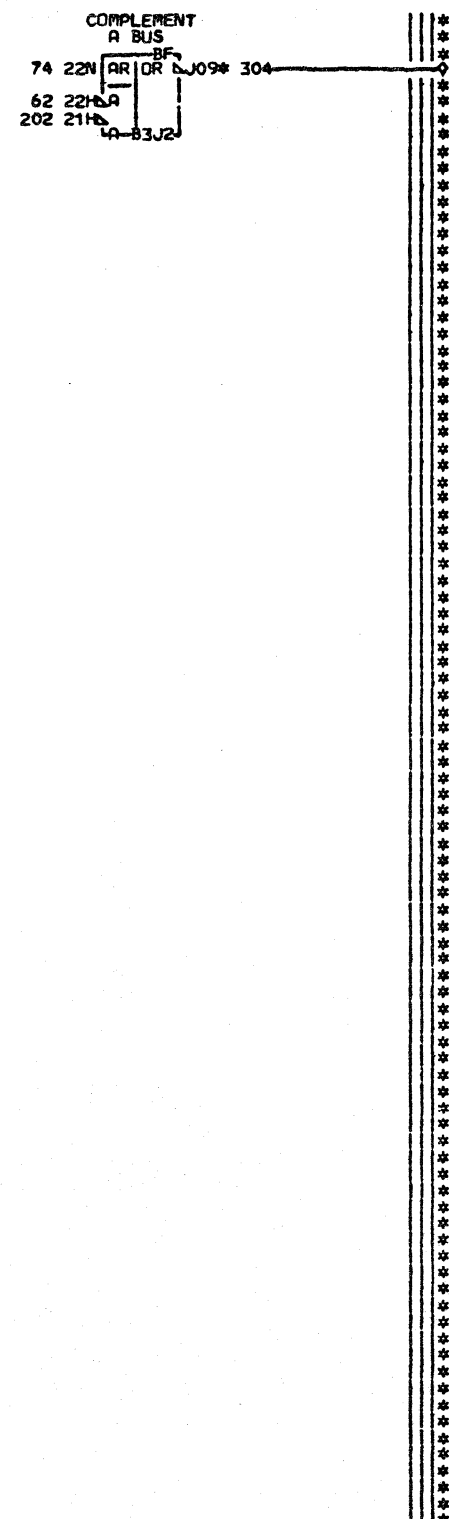
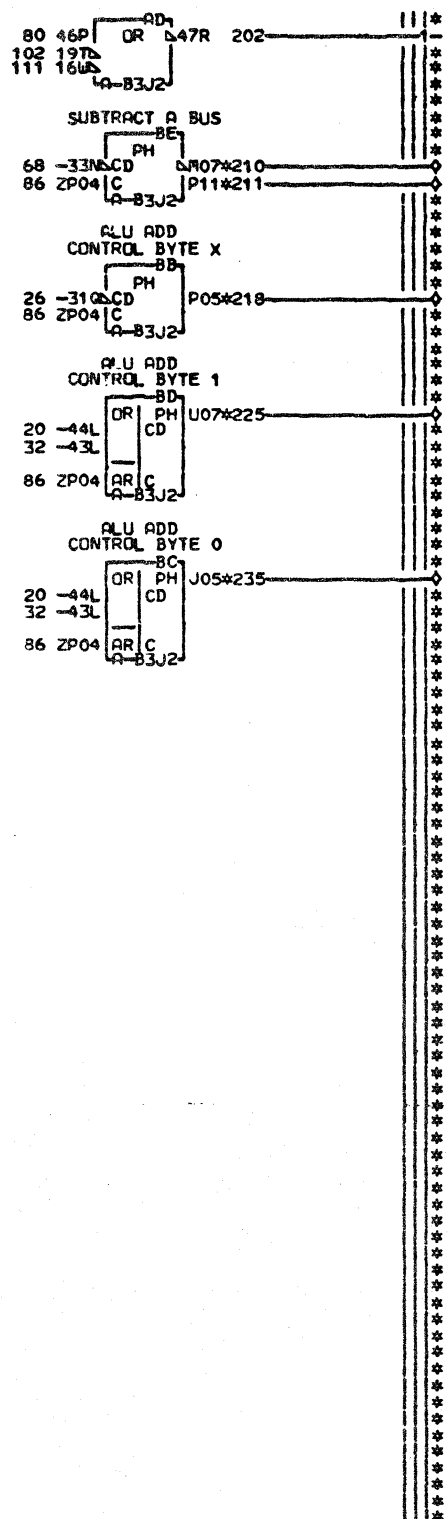
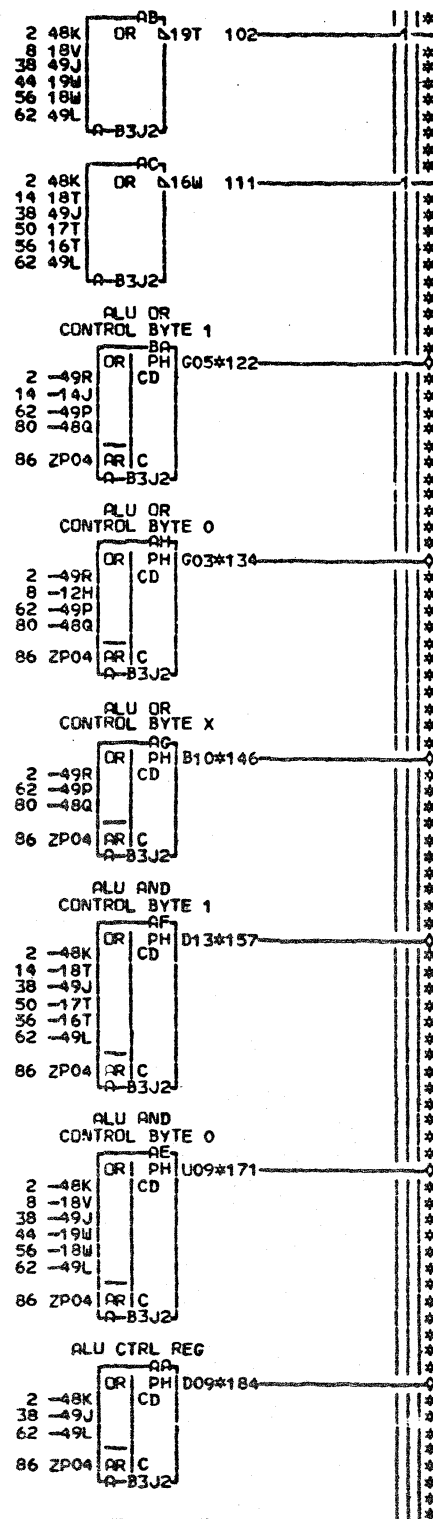
117 - OP XXXX XXXX X111 XXXX BBO
 LCA001 LCF004
 103 - OP XXXX XXXX X000 XXXX BB3
 LCA001 LCD001 LCS003 LCU006
 105 - OP XXXX XXXX X001 XXXX BB4
 LCS007 LCX001
 107 - OP XXXX XXXX X010 XXXX CS007-BB5
 109 - OP XXXX XXXX X011 XXXX BB6
 LCL003 LCS003
 111 - OP XXXX XXXX X100 XXXX CU015-BB7
 113 - OP XXXX XXXX X101 XXXX CU015-BB8
 115 - OP XXXX XXXX X110 XXXX CA002-BB9
 204 + EXCLUSIVE OR BITS X.6-1.7 BD4
 LCA004
 263 - SUBTRACT CA004-BJ2
 264 + SUBTRACT CA004-BJ6
 216 + OR BITS X.6-1.7 CA004-CA4
 305 + EXCLUSIVE OR ALL HE6
 LDE976 LDF976 LDG976 LDK976
 338 - CROSS HI TO LD HM2
 LDK976 LDL006 LDM006
 317 - CROSS LD TO HI HL2
 LDG976 LDH016 LDJ016

ALU CONTROLS
 -E.C.-HISTORY-E MACH.3705
 FRAME 01
 DATE LAST EC 10-14-80 344270
 IBM CORP.SCD P.N. 1850830
 CA003 000

EDGE CONN.
 117 A-B3P1E11
 01A-B4P6E02
 305 A-B3H6A02
 01A-B4H1A11
 317 A-B3H6B02
 01A-B4H1B11
 338 A-B3H6A04
 01A-B4H1A13

CA003
000

+ A DIRECT BITS X.6-1.7 CA001DB6 2-9
 + A DIRECT BITS 0.0-0.7 CA001DK6 8-3
 + A DIRECT BITS 1.0-1.7 CA001DL6 14-3
 + ADD BYTES 0 AND 1 CA002BJ2 20-2
 - ADD BYTES X 0 AND 1 CA002CC2 26-1
 + ADD BYTES X 0 AND 1 CA002CC6 32-2
 + AND BITS X.6-1.7 CA002DE2 38-5
 + AND BITS 0.0-0.7 CA002FG2 44-2
 + AND BITS 1.0-1.7 CA002FJ2 50-2
 + AND BITS 0.0-1.7 CA002FL2 56-4
 + EXCLUSIVE OR BITS X.6-1.7 CA003BD4 62-8-1
 - SUBTRACT CA003BJ2 68-
 + SUBTRACT CA003BJ6 74-1
 + OR BITS X.6-1.7 CA003CA4 80-3-1
 + TO TIME CC006AF2 86-6-2



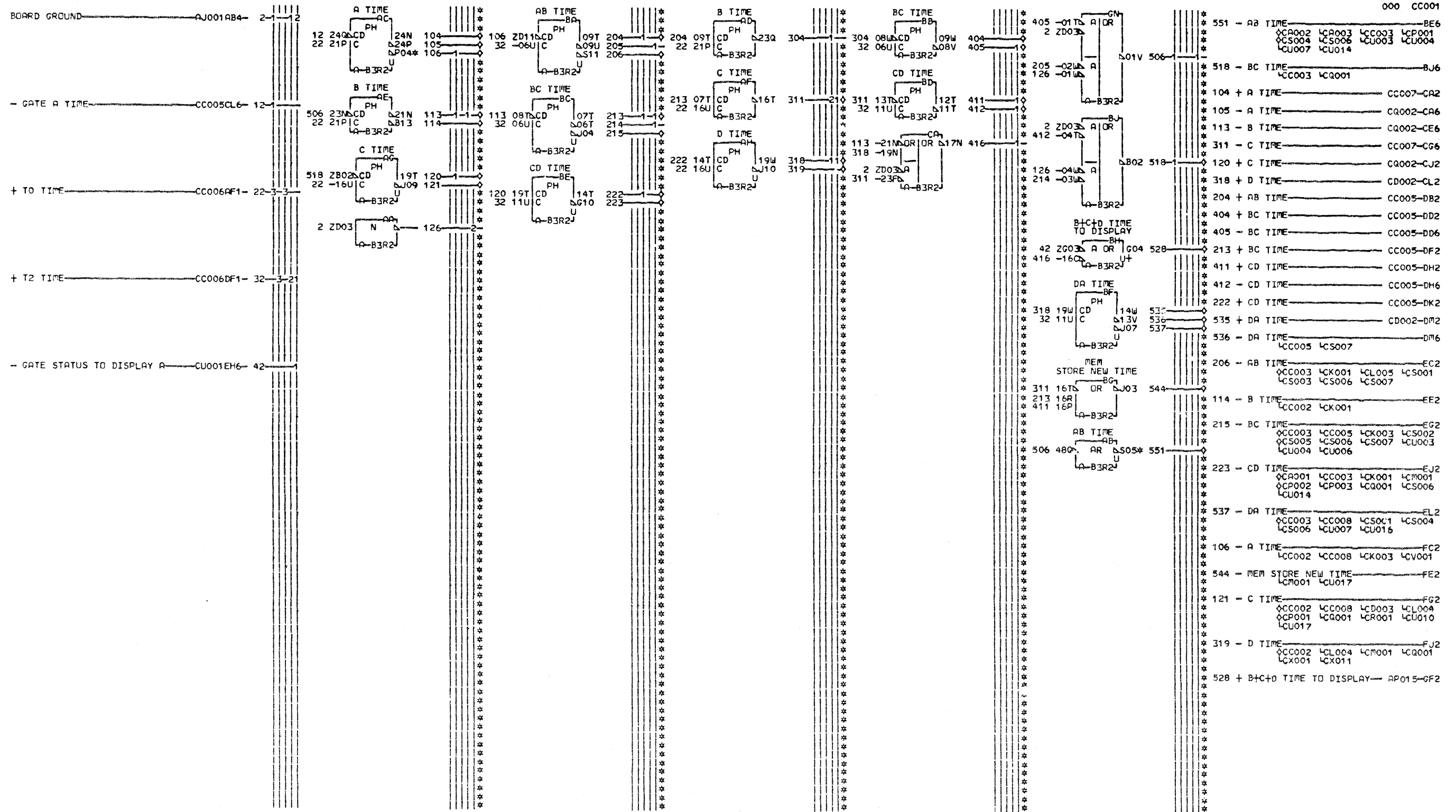
000 CA004
 184 + ALU AND CONTROL BYTE X BA2
 LDE008 LDF008 LDF009
 304 - COMPLEMENT A BUS DD2
 QDE975 LDF975 LDF975 LDH015
 LDJ015 LDK975 LDL005 LDM005
 171 + ALU AND CONTROL BYTE 0 EB6
 QDG008 LDG009 LDH008 LDH009
 LDH010 LDJ008 LDJ009 LDJ010
 157 + ALU AND CONTROL BYTE 1 EC6
 QDK008 LDK009 LDL008 LDL009
 LDL010 LDM008 LDM009 LDM010
 146 + ALU OR CONTROL BYTE X EE6
 LDE008 LDF008 LDF009
 134 + ALU OR CONTROL BYTE 0 EF6
 QDG008 LDG009 LDH008 LDH009
 LDH010 LDJ008 LDJ009 LDJ010
 122 + ALU OR CONTROL BYTE 1 EG6
 QDK008 LDK009 LDL008 LDL009
 LDL010 LDM008 LDM009 LDM010
 218 + ALU ADD CONTROL BYTE X EH6
 LDE008 LDF008 LDF009 LDF974
 235 + ALU ADD CONTROL BYTE 0 EJ6
 QDG008 LDG009 LDK974 LDH008
 LDH009 LDH010 LDH014 LDJ008
 LDJ009 LDJ010 LDJ014
 225 + ALU ADD CONTROL BYTE 1 EK6
 QDK008 LDK009 LDK974 LDL004
 LDL008 LDL009 LDL010 LDM004
 LDM008 LDM009 LDM010
 210 - SUBTRACT A BUS EM2
 LDM006 LDM007
 211 + SUBTRACT A BUS DMO6-EM6

EDGE CONN. 01A-B4H1D11 01A-B4J1C13
 122 A-B3J6A02 184 A-B3H6C04 235 A-B3J6C02
 01A-B4J1A11 01A-B4H1C13 01A-B4J1C11
 134 A-B3H6E04 210 A-B3J6D02 304 A-B3J6E02
 01A-B4H1E13 01A-B4J1D11 01A-B4J1E11
 146 A-B3H6E02 211 A-B3J6D04
 01A-B4H1E11 01A-B4J1D13
 157 A-B3H6D04 218 A-B3J6B04
 01A-B4H1D13 01A-B4J1B13
 171 A-B3H6D02 225 A-B3J6C04

LOC. TYPE
 A-B3J2 6815

CA004
 000

ALU CONTROLS
 -E.C.-HISTORY-E FACH.3705
 FRAME 01
 IBM CORP.SCD CA004
 DATE LAST EC 10-14-80 344270 P.No. 1986965 000



EDGE CONN.
 106 A-B3N6A04
 01A-B4N1A13
 551 RESISTOR
 A-B3J2J06

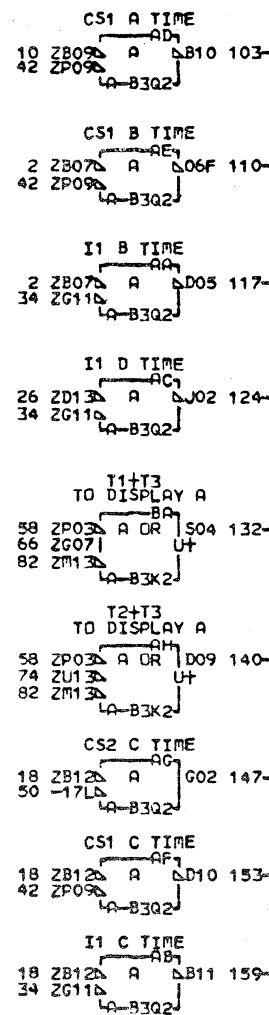
LCC. TYPE
 A-B3J2 6815
 A-B3R2 AB92

ABCD COUNTER	
E.C. HISTORY	E FRACH. 3705
	FRAME 01
DATE LAST EC	IBM CORP. SCD
10-14-80 344270	P.N. 1850831

CC001
000

CC001
000

- B TIME ----- CC001EE2- 2-2
 - A TIME ----- CC001FC2- 10-1
 - C TIME ----- CC001FG2- 18-3
 - D TIME ----- CC001FJ2- 26-1
 - I1 TIME ----- CC004EA6- 34-3
 - CS1 TIME ----- CC004EL6- 42-3
 - CS2 TIME ----- CC005EC6- 50-1
 + TO TIME ----- CC006AF2- 58-2
 - T2 TIME ----- CC006DF8- 66-1
 + T1 TIME ----- CC006ED2- 74-1
 - GATE STATUS TO DISPLAY A ----- CU001EH6- 82-2



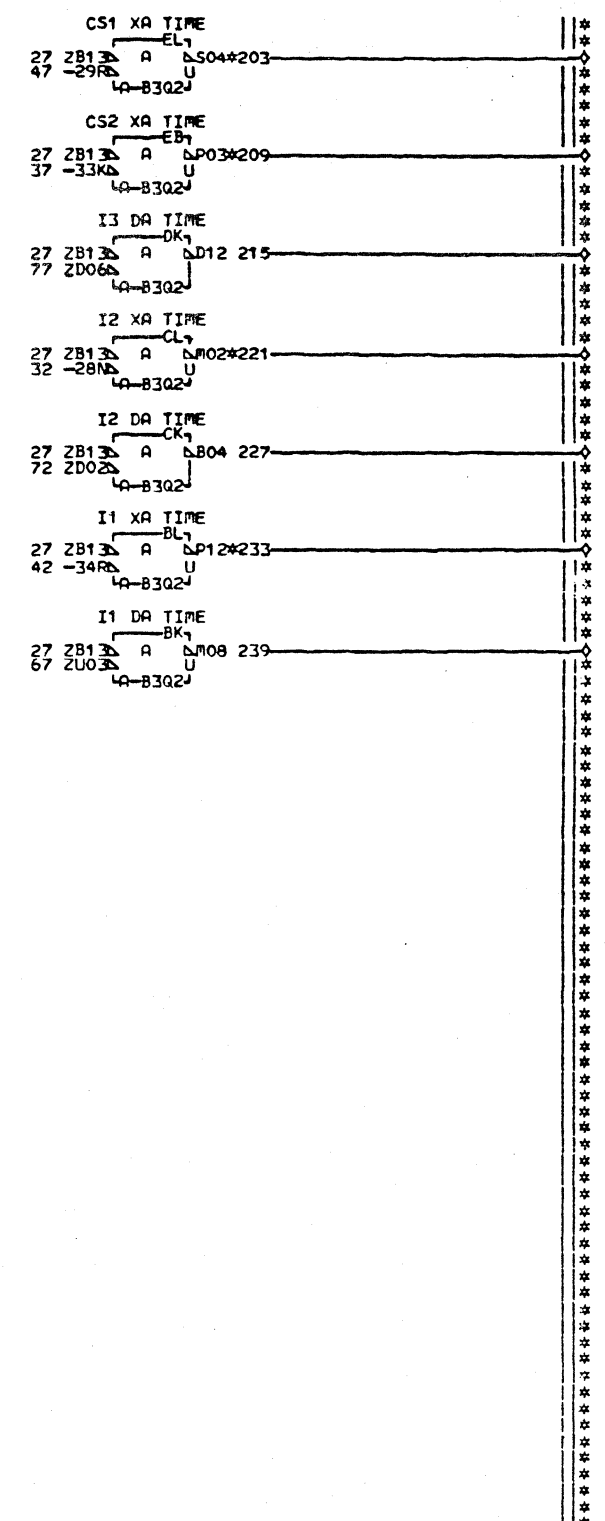
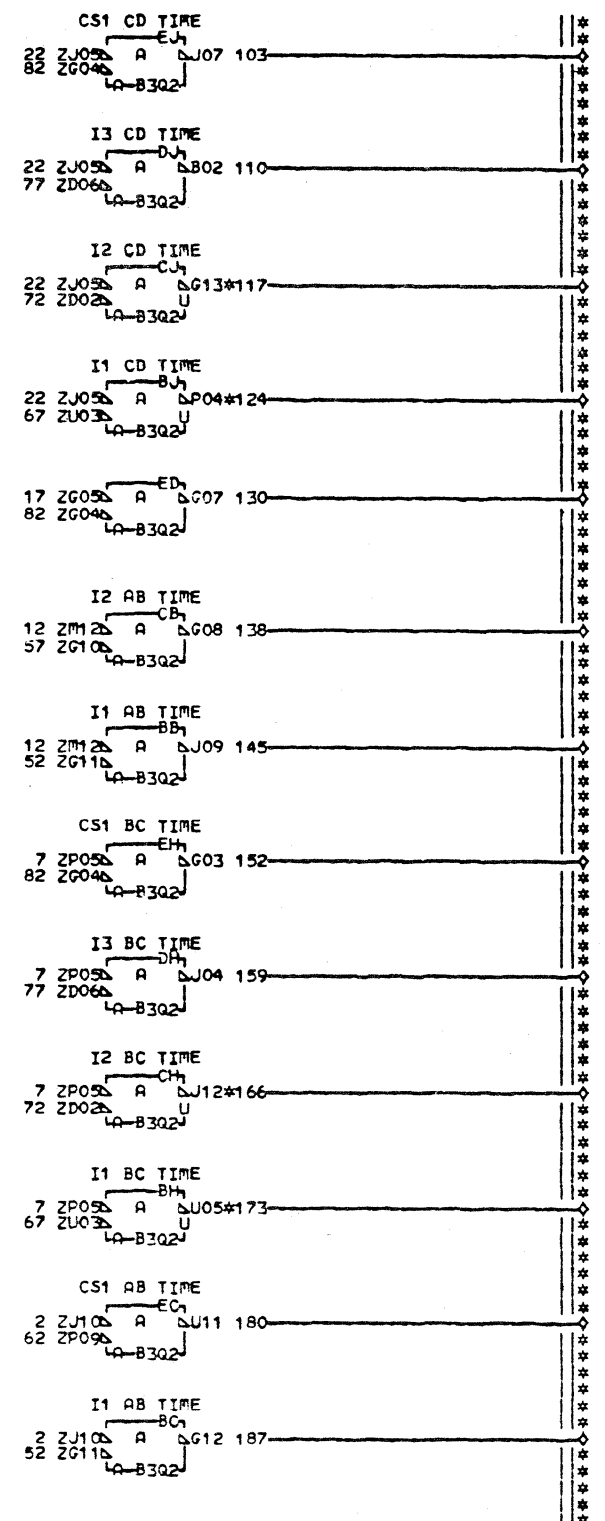
000 CC002
 117 - I1 B TIME ----- BC6
 LCP003 LCS007
 159 - I1 C TIME ----- CP006-BD6
 124 - I1 D TIME ----- BE6
 LCK001 LCP003 LCU001
 103 - CS1 A TIME ----- CQ002-BK6
 110 - CS1 B TIME ----- CQ002-BL6
 153 - CS1 C TIME ----- BM6
 LCP006 LCU004 LCU007
 147 + CS2 C TIME ----- CU007-DM2
 140 + T2+T3 TO DISPLAY A ----- AP015-GF2
 132 + T1+T3 TO DISPLAY A ----- AP015-GG2

LOC. TYPE
 A-B3K2 6816
 A-B3Q2 6821

CC002
 000

INST AND CS TIMES			
-E.C.-HISTORY-		E1 MACH.3705	
	FRAME	01	
DATE	LAST EC	IBM CORP.SCD	CC002
10-14-80	344270	P.No. 1850832	000

- AB TIME ----- CC001BE6 2-2
 - BC TIME ----- CC001BJ6 7-4
 - AB TIME ----- CC001EC2 12-2
 - BC TIME ----- CC001EG2 17-1
 - CD TIME ----- CC001EJ2 22-4
 - DA TIME ----- CC001EL2 27-7
 - GD SECOND INST CYCLE ----- CC004CE6 32-1
 - GD SECOND CS CYCLE ----- CC004CN6 37-1
 - GD FIRST INSTR CYCLE ----- CC004DA6 42-1
 - GD FIRST CS CYCLE ----- CC004DL6 47-1
 - I1 TIME ----- CC004EA6 52-2
 - I2 TIME ----- CC004EE6 57-1
 - CS1 TIME ----- CC004EL6 62-1
 - I1 TIME + 125 NS ----- CC004FB6 67-2
 - I2 TIME + 125 NS ----- CC004FF6 72-2
 - I3 TIME + 125 NS ----- CC004FK6 77-2
 - CS1 TIME + 125 NS ----- CC004FM6 82-3



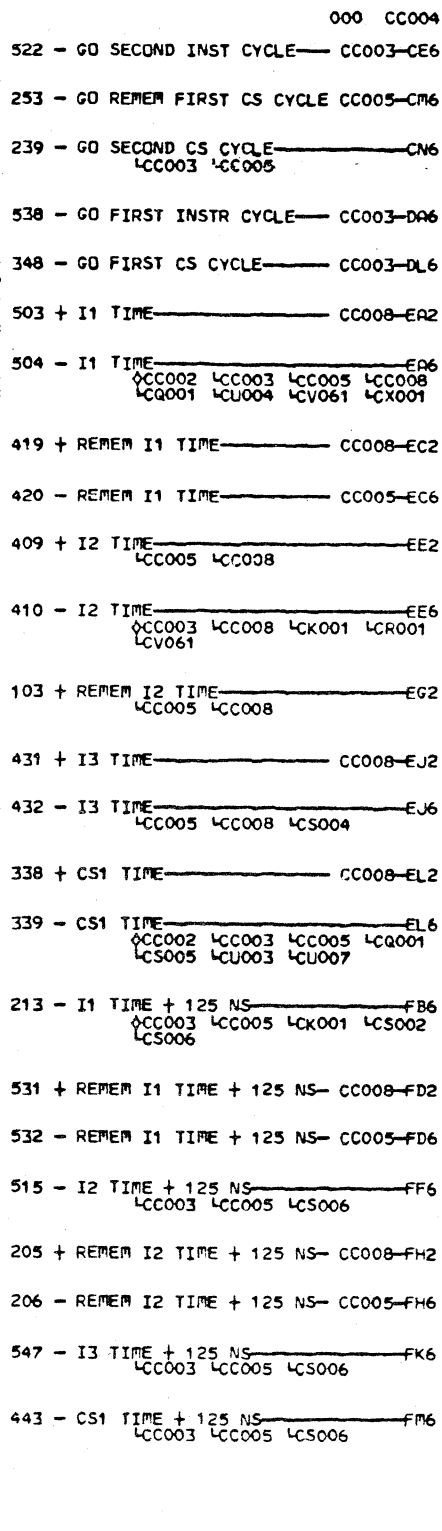
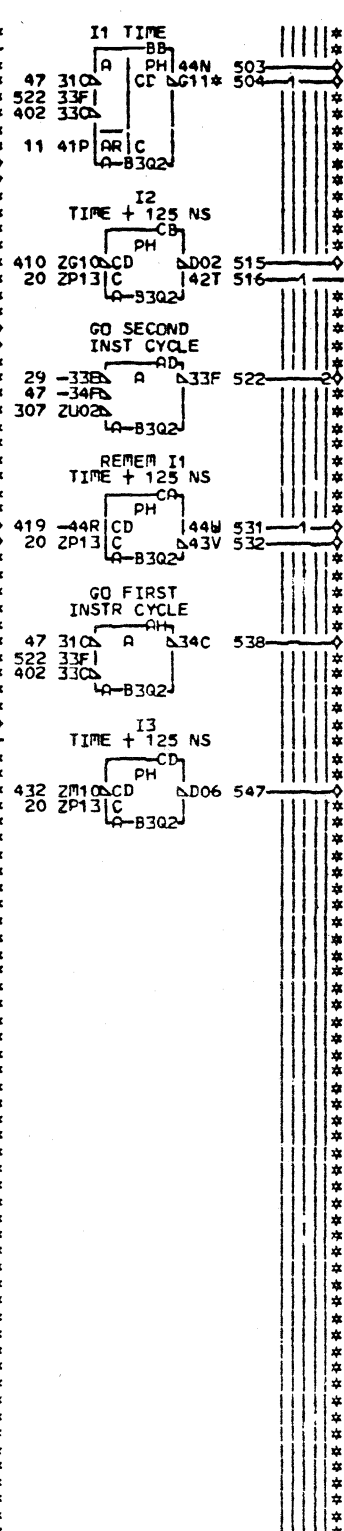
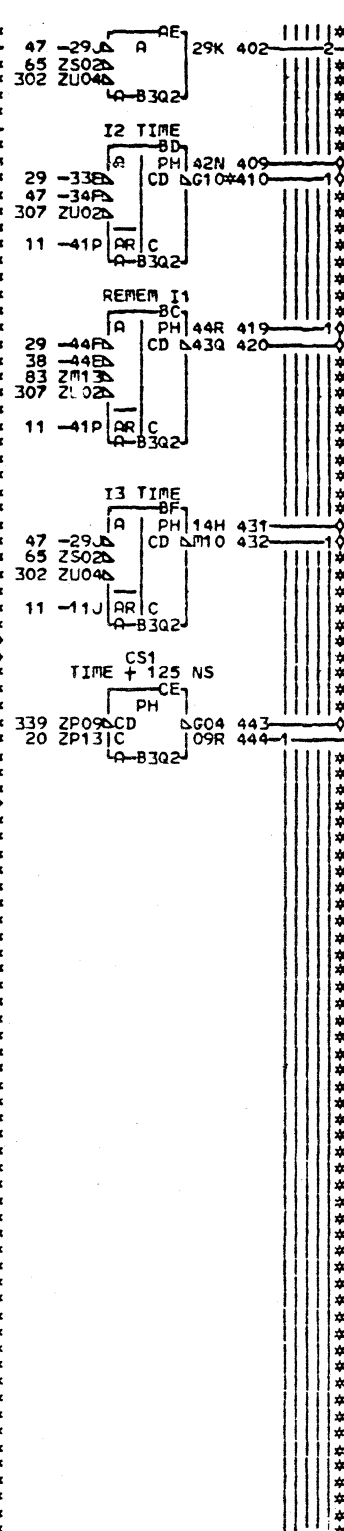
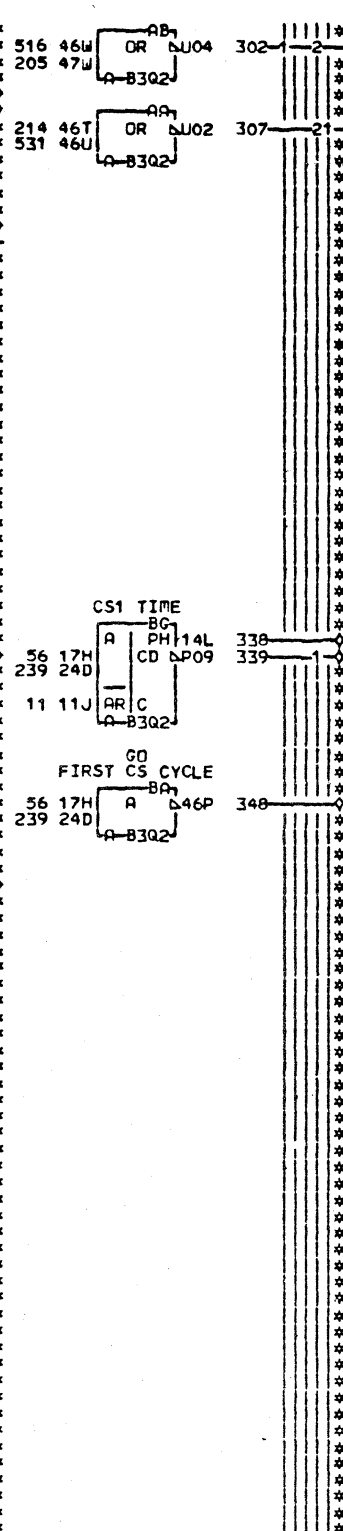
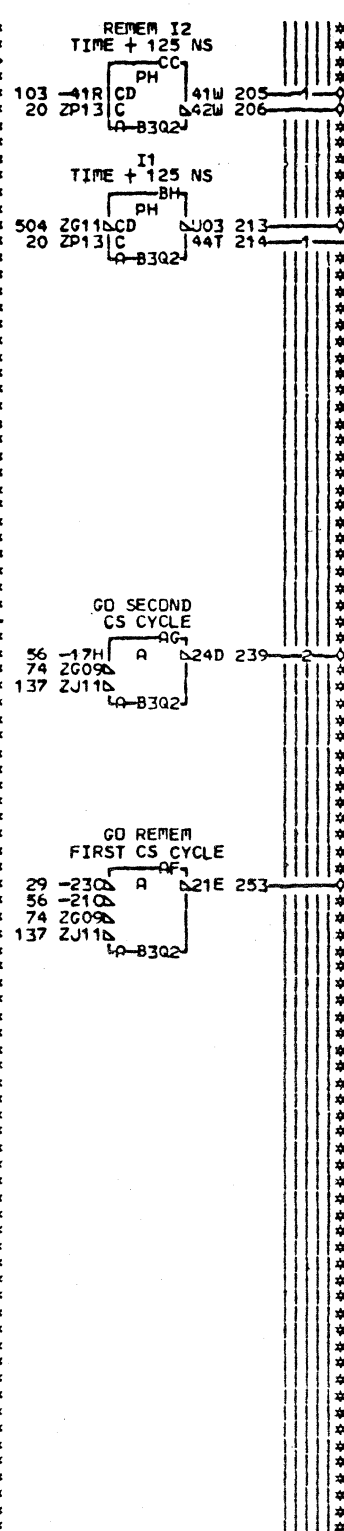
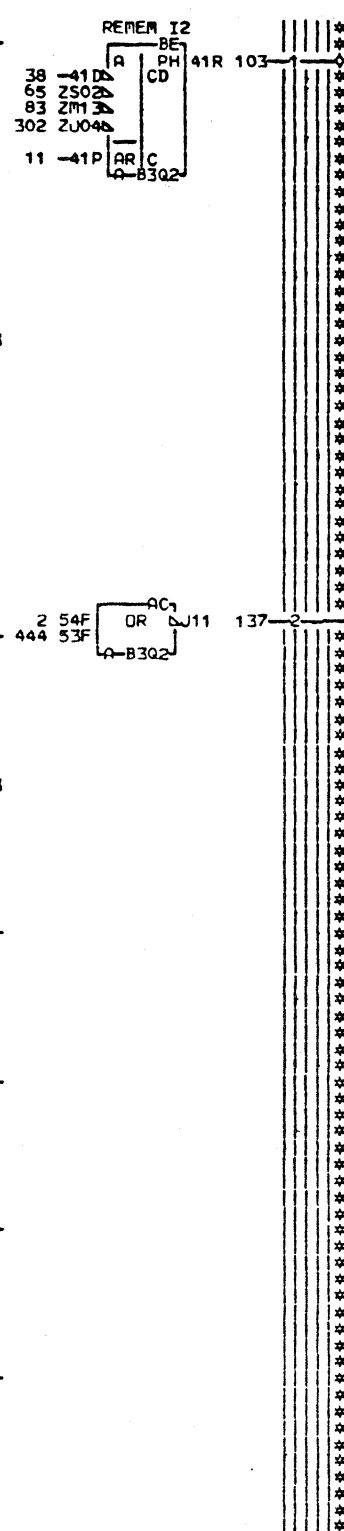
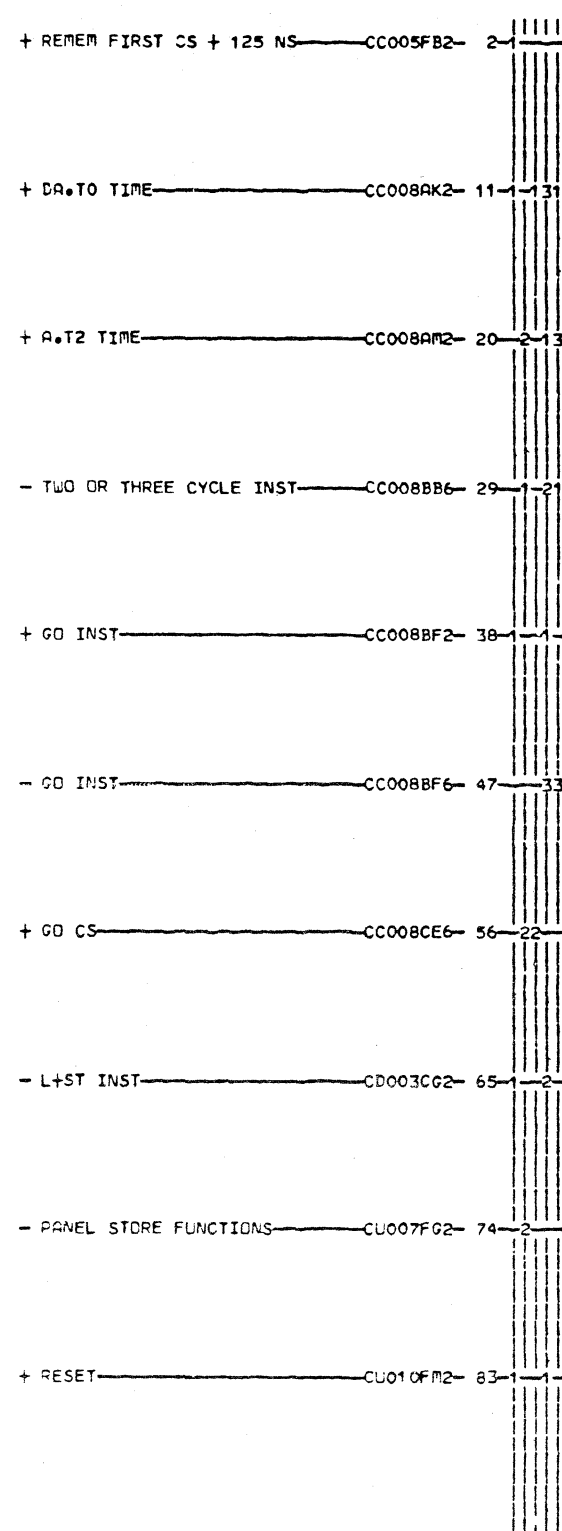
000 CC003
 145 - I1 AB TIME ----- CL005-BB6
 187 - I1 AB TIME ----- CL004-BC6
 173 - I1 BC TIME ----- BH6
 LCA001 LCA002 LCA003 LCF002
 LCL001 LCL002 LCL003 LCP003
 124 - I1 CD TIME ----- BJ6
 LCA001 LCA002 LCA003 LCL001
 LCL002 LCL003 LCL005 LCR001
 LCS001
 239 - I1 DA TIME ----- BK6
 LCK001 LCL005 LCZ002
 233 - I1 XA TIME ----- BL6
 LCA002 LCF002 LCP002 LCP004
 LCS005
 138 - I2 AB TIME ----- CL005-CB6
 166 - I2 BC TIME ----- CH6
 LCA001 LCA003 LCF003 LCL001
 LCL002 LCS004
 117 - I2 CD TIME ----- CJ6
 LCA001 LCA002 LCA003 LCF002
 LCF003 LCL001 LCL002 LCL005
 LCZ002
 227 - I2 DA TIME ----- CL005-CK6
 221 - I2 XA TIME ----- CL6
 LCA002 LCA003 LCF002 LCL001
 LCL002 LCL003 LCS004
 159 - I3 BC TIME ----- DH6
 LCA001 LCL001 LCL002
 110 - I3 CD TIME ----- DJ6
 LCL005 LCZ002
 215 - I3 DA TIME ----- CL005-DK6
 209 - CS2 XA TIME ----- EB6
 LCA002 LCF002
 180 - CS1 AB TIME ----- EC6
 LCA003 LCA002 LCS004
 130 - CS1 BC TIME UNUSED ----- ED6
 152 - CS1 BC TIME ----- EH6
 LCA001 LCS004
 103 - CS1 CD TIME ----- EJ6
 LCA001 LCA002
 203 - CS1 XA TIME ----- EL6
 LCA002 LCF002 LCS004

EDGE CONN. A-B4R2J13 A-B4R2G09
 117 A-B3V3B09 01A-B3V3B13 01A-B3V3B06
 01A-B4V3B09 01A-B4V3B13 01A-B4V3B06
 124 A-B3V3B05 209 RESISTOR
 01A-B4V3B05 A-B4R2G04
 166 A-B3V3B08 01A-B3V3B12
 01A-B4V3B08 01A-B4V3B12
 173 A-B3V3B04 221 A-B3V3B10
 01A-B4V3B04 01A-B4V3B10
 203 RESISTOR 233 RESISTOR

LOC. TYPE
 A-B3Q2 6821
 A-B4R2 6807

INST AND CS TIMES
 -E.C.-HISTORY-E MACH.3705
 FRAME 01
 IBM CORP.SCD CC003
 DATE LAST EC 10-14-80 344270 P.No. 1850833 000

CC003
000



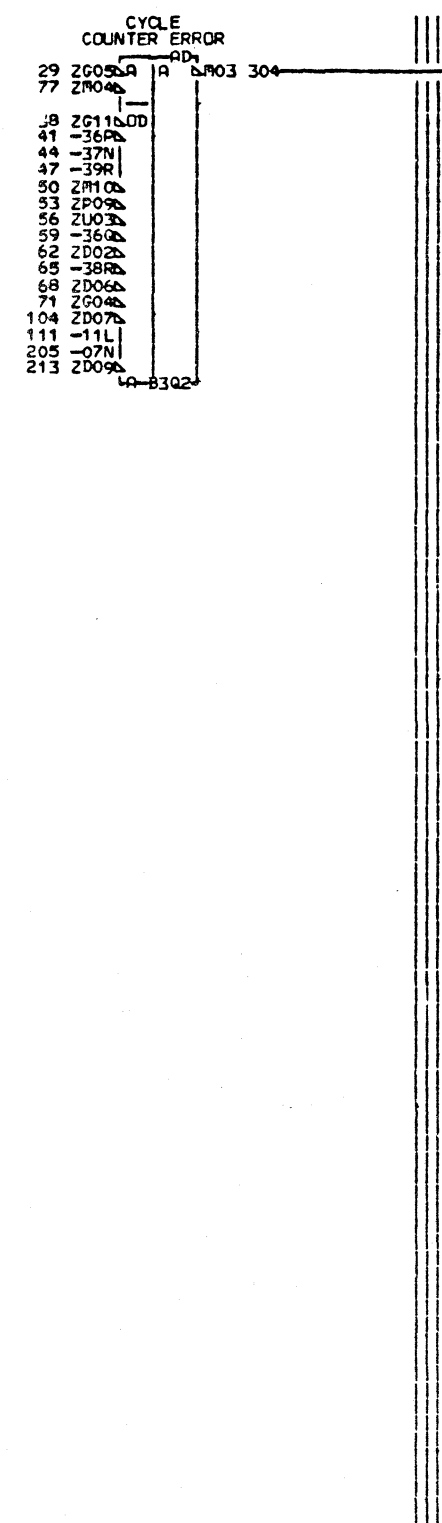
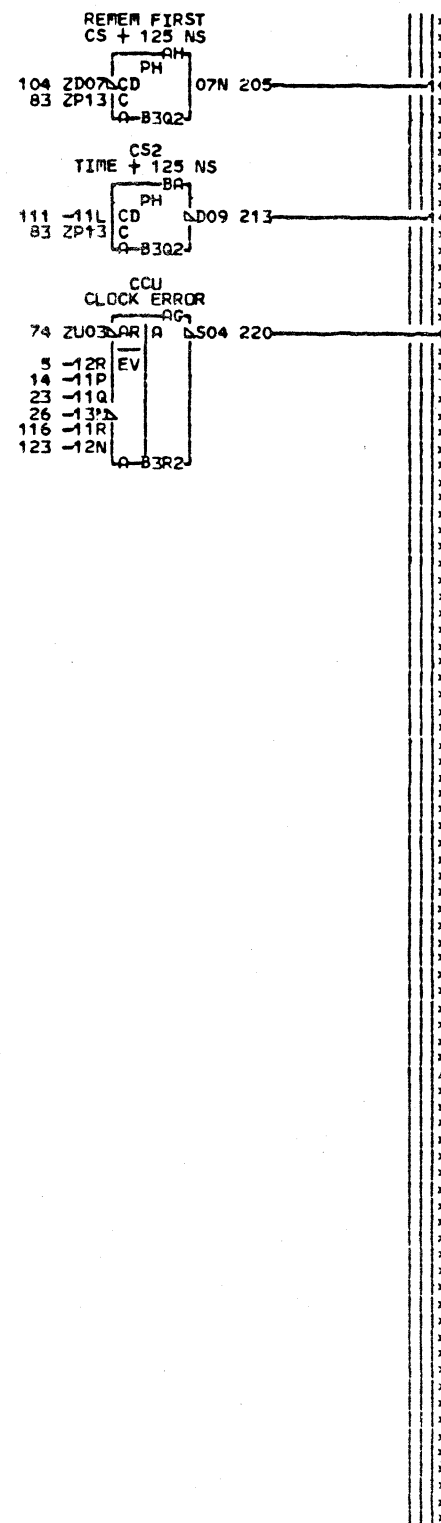
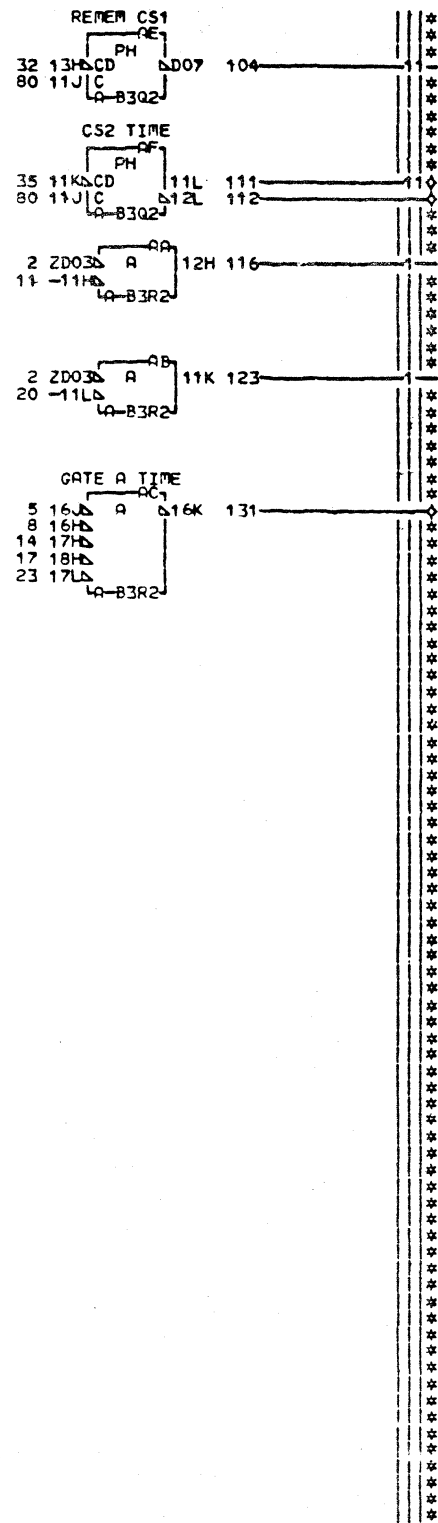
EDGE CONN.
 410 A-B3V3D03
 01A-B4V3D03
 504 A-B3V3D02
 01A-B4V3D02

LOC. TYPE
 A-B3Q2 6821

INST AND CS CYCLE COUNTER
 E.C. HISTORY — E-MACH. 3705
 FRAME 01
 IBM CORP. SCD CC004
 DATE LAST EC 10-14-80 344270 P.N. 1850834 000

CC004
 000

BOARD GROUND AJ001AB4 2-2
 + AB TIME CC001DB2 5-1
 + BC TIME CC001DD2 8-1
 - BC TIME CC001DD6 11-1
 + BC TIME CC001DF2 14-1
 + CD TIME CC001DH2 17-1
 - CD TIME CC001DH6 20-1
 + CD TIME CC001DK2 23-1
 - DA TIME CC001DM6 26-1
 - BC TIME CC001EG2 29-1
 - GO REPEM FIRST CS CYCLE CC004CM6 32-1
 - GO SECOND CS CYCLE CC004CN6 35-1
 - I1 TIME CC004EA6 38-1
 - REPEM I1 TIME CC004EC6 41-1
 + I2 TIME CC004EE2 44-1
 + REPEM I2 TIME CC004EG2 47-1
 - I3 TIME CC004EJ6 50-1
 - CS1 TIME CC004EL6 53-1
 - I1 TIME + 125 NS CC004FB6 56-1
 - REPEM I1 TIME + 125 NS CC004FD6 59-1
 - I2 TIME + 125 NS CC004FF6 62-1
 - REPEM I2 TIME + 125 NS CC004FH6 65-1
 - I3 TIME + 125 NS CC004FK6 68-1
 - CS1 TIME + 125 NS CC004FM6 71-1
 - T0 TIME CC006AF0 74-1
 - T3 TIME CC006HB6 77-1
 + DA TO TIME CC008AK2 80-2
 + A.T2 TIME CC008AM2 83-2

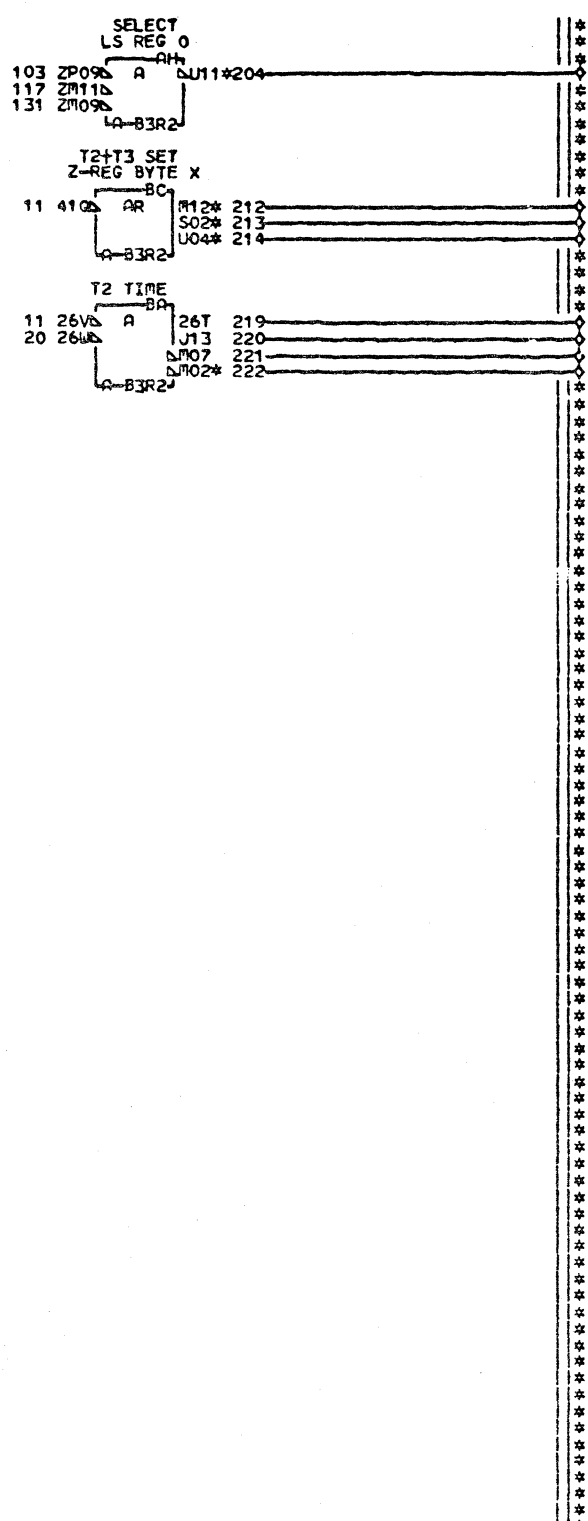
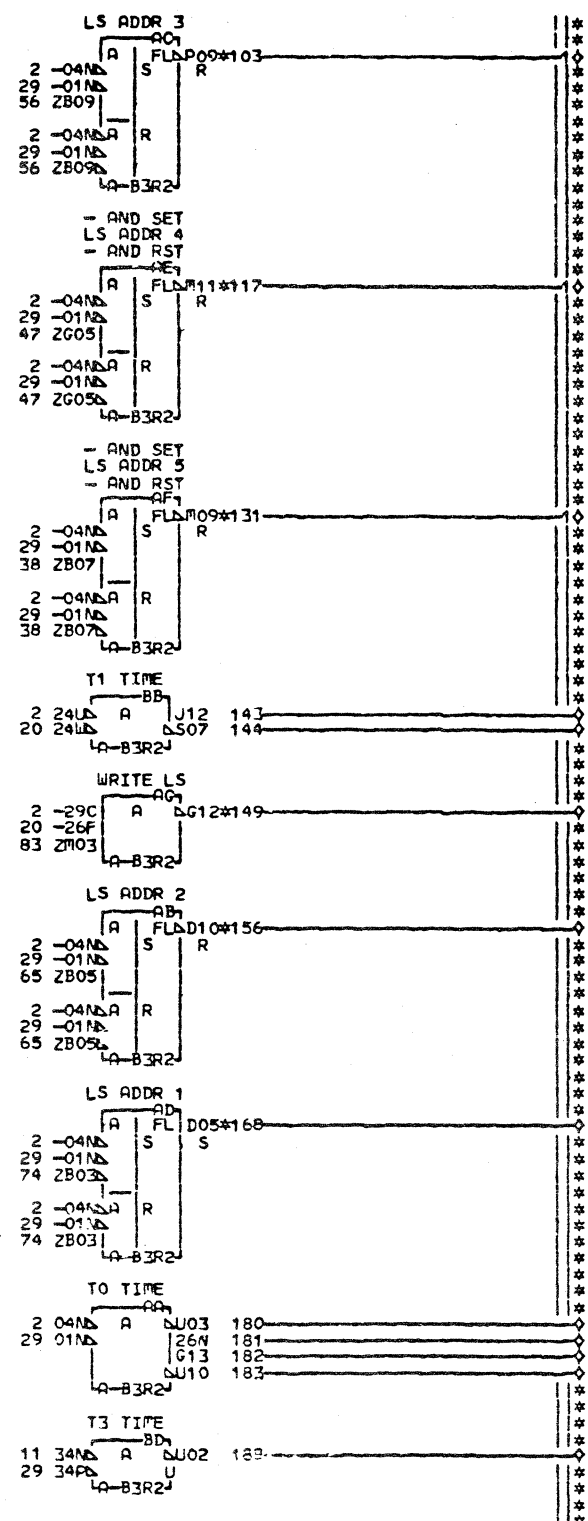
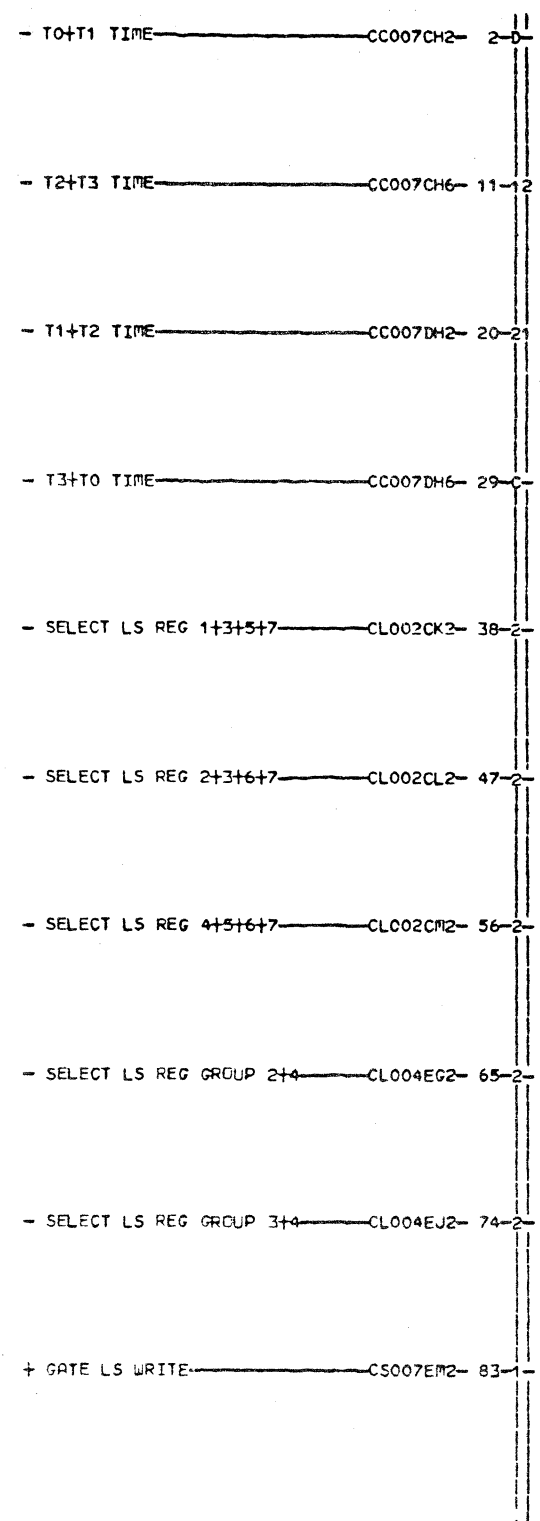


000 CC005
 131 - GATE A TIME CC001-CL6
 304 - CYCLE COUNTER ERROR DG6
 LCK006 LCK007
 111 + CS2 TIME CC008-EC2
 112 - CS2 TIME CC002-EC6
 220 - CCU CLOCK ERROR EM6
 LCK006 LCK007
 205 + REPEM FIRST CS + 125 NS CC004-FB2
 213 - CS2 TIME + 125 NS FD6
 LCK003 LCK006

LOC. TYPE
 P-B3Q2 6821
 P-B3R2 AB92

CC005
 000

INST AND CS CYCLE COUNTER
 ERROR DETECTION
 E.C. HISTORY E MACH. 3705
 FRAME 01
 IBM CORP. SCD CC005
 DATE LAST EC P.N. 1850835 000
 10-14-80 344270



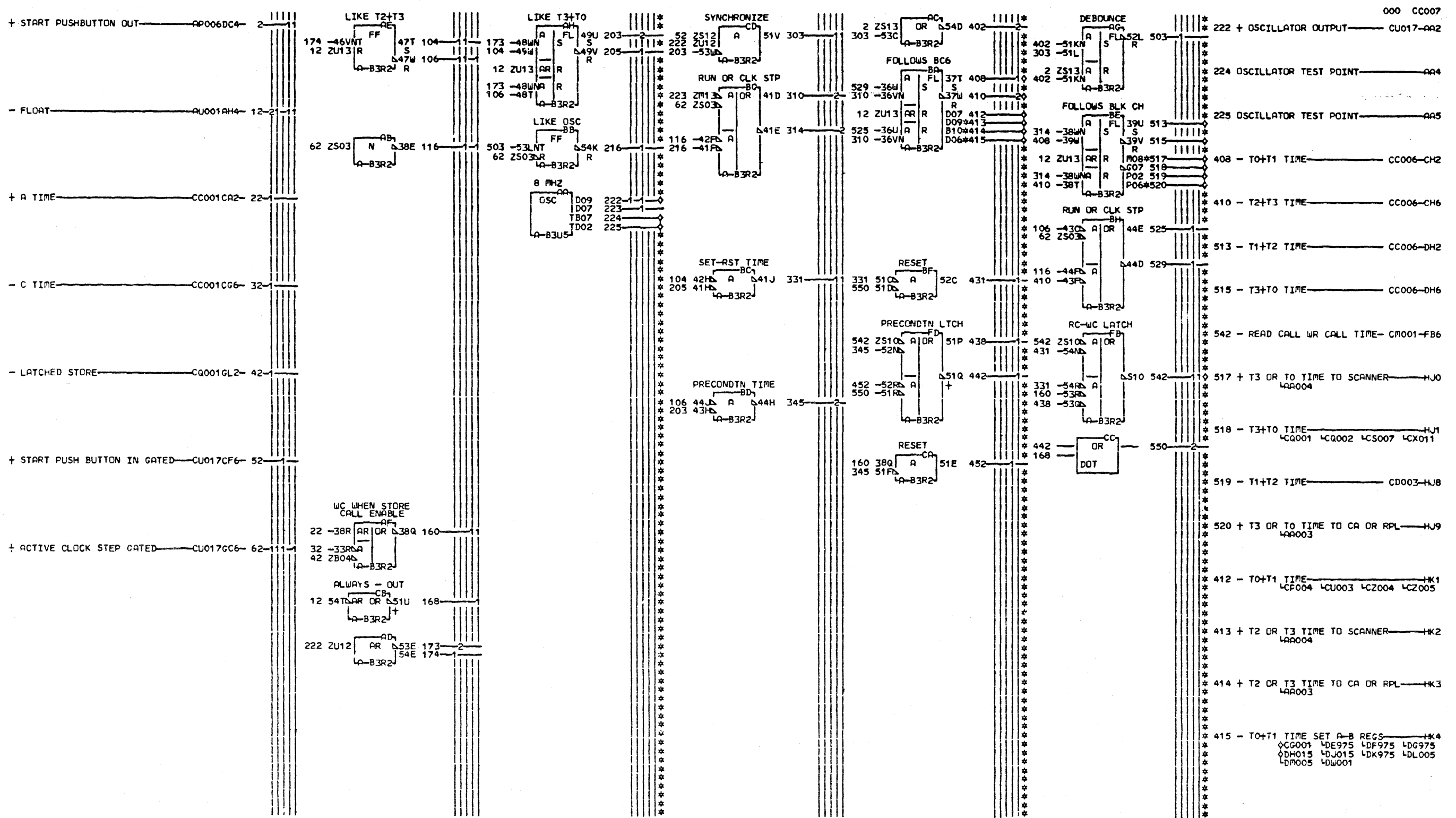
- 000 CC006
- 180 - TO TIME ----- AF0
 QCC005 LCC008 LCU004 LCU006
 LCU007 LCX002 LCX003
 - 181 + TO TIME ----- CC001-AF1
 - 182 + TO TIME ----- AF2
 QCA001 LCA003 LCA004 LCC002
 LCX011
 - 183 - TO TIME ----- AF8
 QCK003 LCR001 LCP001 LCP002
 QCP003 LCP007 LCG001 LCG005
 LCU010
 - 168 - SELECT LS REG GROUP 1+2 ----- AU4
 QDE002 LDF002 LDG002 LDH002
 QDJ002 LDK002 LDL002 LDM002
 LDW001
 - 156 - SELECT LS REG GROUP 1+3 ----- AV4
 QDE002 LDF002 LDG002 LDH002
 QDJ002 LDK002 LDL002 LDM002
 LDW001
 - 103 - SELECT LS REG BIT 0+1+2+3 ----- AW4
 QDE002 LDF002 LDG002 LDH002
 QDJ002 LDK002 LDL002 LDM002
 LDW001
 - 149 - WRITE LS ----- BJ4
 QDE002 LDF002 LDG002 LDH002
 QDJ002 LDK002 LDL002 LDM002
 LDW001
 - 117 - SELECT LS REG 0+1+4+5 ----- BK4
 QDE002 LDF002 LDG002 LDH002
 QDJ002 LDK002 LDL002 LDM002
 LDW001
 - 131 - SELECT LS REG BIT 0+2+4+6 ----- BL4
 QDE002 LDF002 LDG002 LDH002
 QDJ002 LDK002 LDL002 LDM002
 LDW001
 - 204 - SELECT LS REG 0 ----- CF004-BM6
 - 219 + T2 TIME ----- CC001-DF1
 - 220 + T2 TIME ----- CX011-DF2
 - 221 - T2 TIME ----- DF8
 QCC002 LCC008 LCL004 LCM001
 QCP001 LCP007 LCR001 LCQ002
 LCU010
 - 222 - T2 TIME ----- DF9
 LCK003 LCV061 LCX001 LCX006
 - 143 + T1 TIME ----- ED2
 LCC002 LCL005
 - 144 - T1 TIME ----- ED6
 LCK003 LCQ002 LCU010 LCU013
 - 212 + T2+T3 SET Z-REG BYTE X ----- FG1
 LDE974 LDF974 LDF977
 - 213 + T2+T3 SET Z-REG BYTE 0 ----- FG2
 LDG974 LDG977 LDH014 LDJ014
 - 214 + T2+T3 SET Z-REG BYTE 1 ----- FG3
 LDK974 LDK977 LDL004 LDM004
 - 188 - T3 TIME ----- HB6
 LCC005 LCK003 LCS001 LCS007

EDGE CCM#	01A-B4R1B11	01A-B4N1E13
103 A-B3R6B04	168 A-B3R6A04	222 A-B3N6B02
01A-B4R1B13	01A-B4R1A13	01A-B4N1B11
117 A-B3R6C04	204 A-B3N6C04	
01A-B4R1C13	01A-B4N1B13	
131 A-B3R6D02	212 A-B3N6D04	
01A-B4R1D11	01A-B4N1D13	
149 A-B3R6E02	213 A-B3N6E02	
01A-B4R1E11	01A-B4N1E11	
156 A-B3R6B02	214 A-B3N6E04	

LOC. TYPE
A-B3R2 AB92

CC006
000

LS ADDR REG AND T-TIMES	
-E.C.-HISTORY-E: PACH,3705	
FRAME 01	
DATE LAST EC	IBM CORP,SCD CC006
10-14-80 344270	P.No. 1850836 000



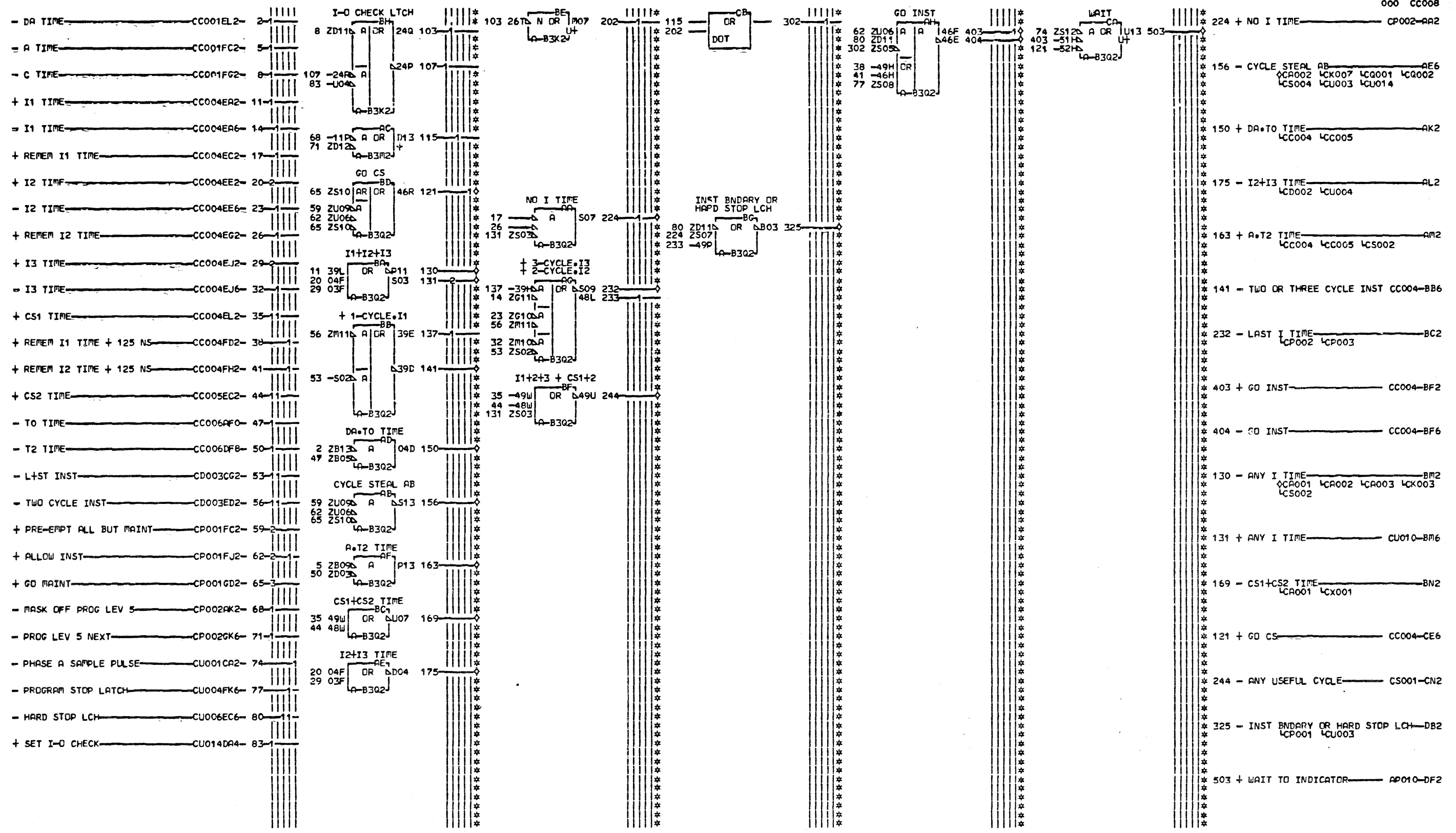
NOTE. AA2 IS + 8 MHZ OSC
AA3 IS - 8 MHZ OSC

EDGE CONN.
413 A-B3A2D07
414 A-B3A5B08
415 A-B3N6C02
01A-B4N1C11
517 A-B3A2D09
520 A-B3A5B09

LOC. TYPE
A-B3R2 AB92
A-B3U5 AB85

CC007
000

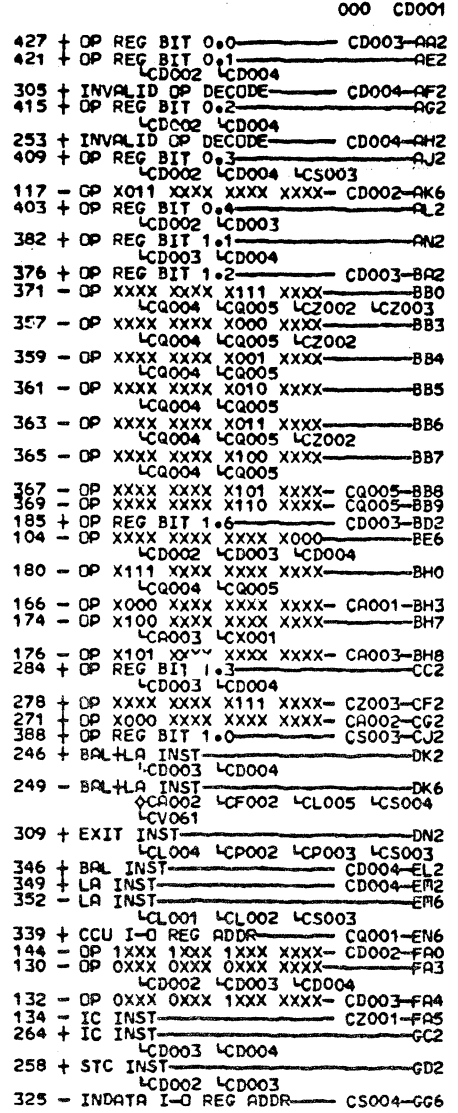
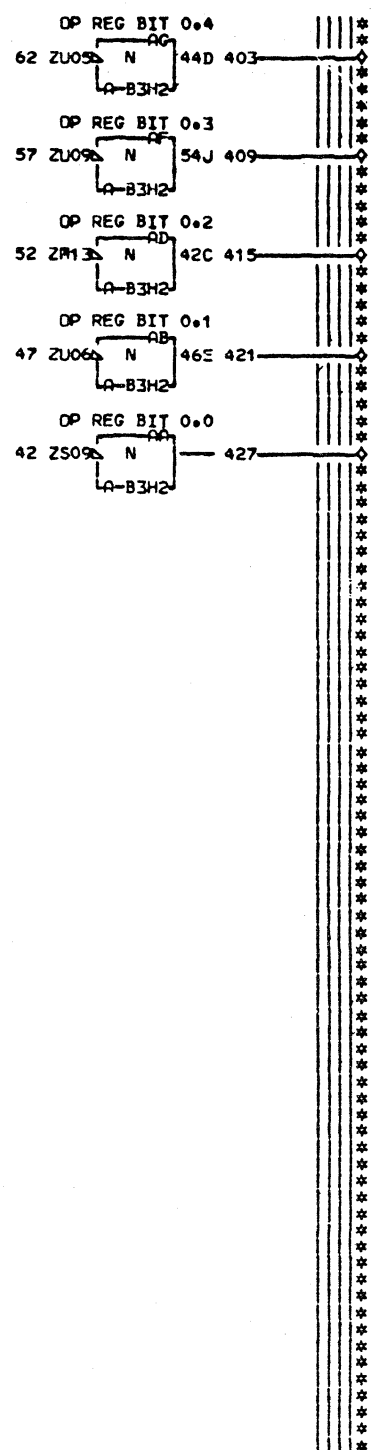
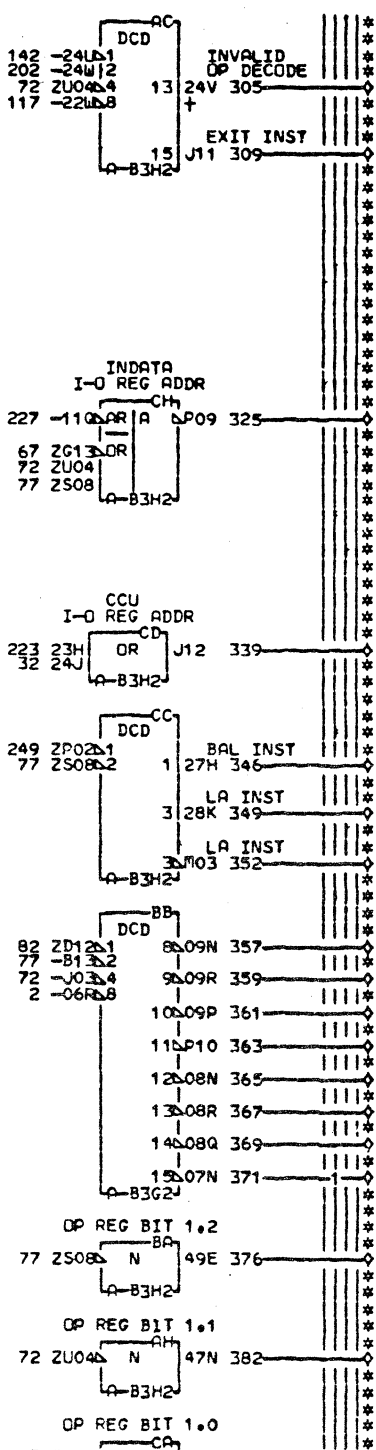
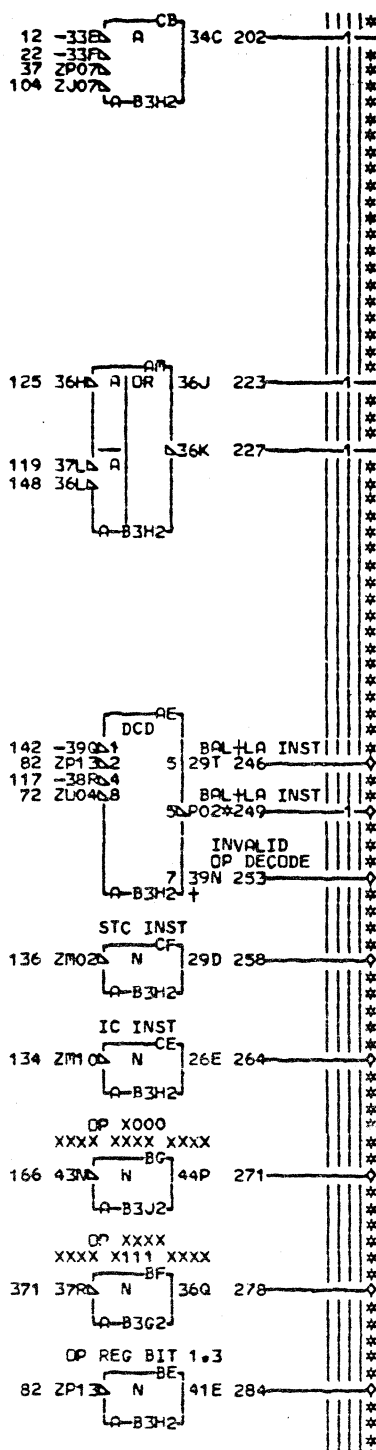
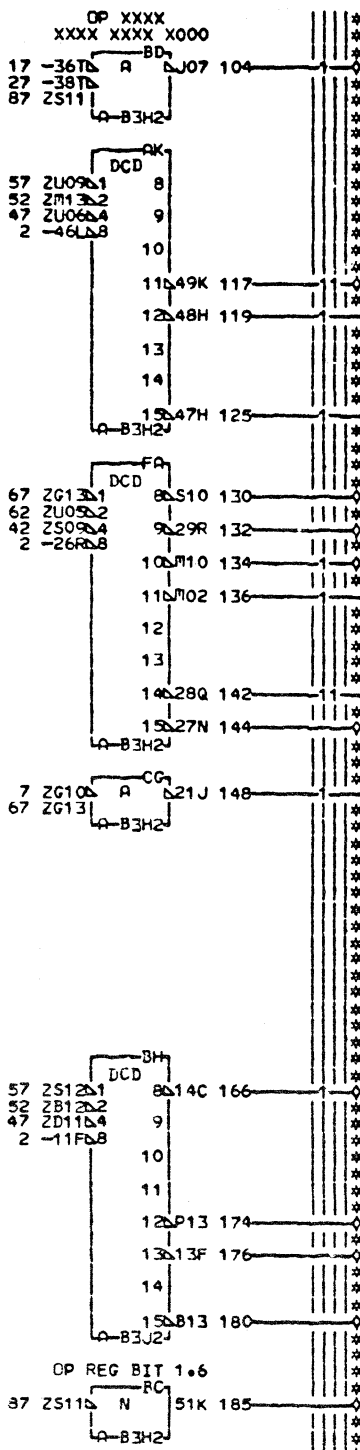
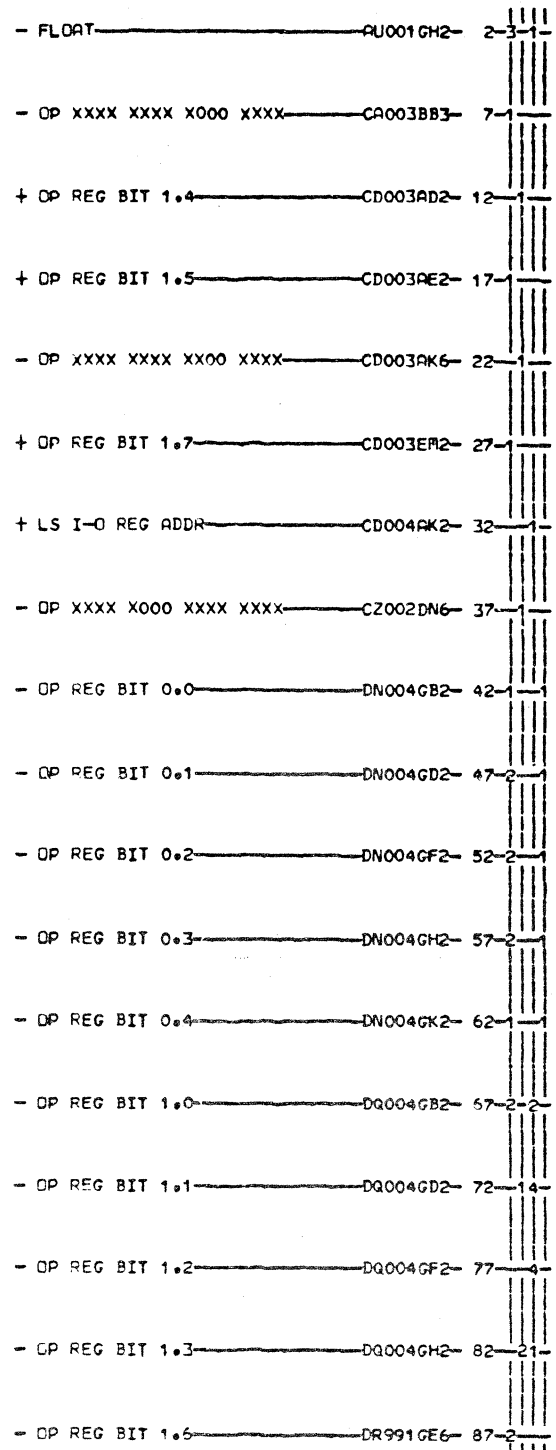
CLOCK	
E.C.-HISTORY	E-MACH-3705
DATE LAST EC	FRAME 01
10-14-80 344270	IBM CORP.SCD CC007
	P.No. 1850837 000



CC008
000

LOC. TYPE
P-B3K2 6816
P-B3M2 6818
P-B3Q2 6821

INSTRUCTION STARTS CYCLES STOP			
E.C. HISTORY		E. MACH. 3705	
344270			
DATE		FRAME	01
LAST EC		IBM CORP. SCD CC008	
06-02-81 344828		P.N.	1850838 000



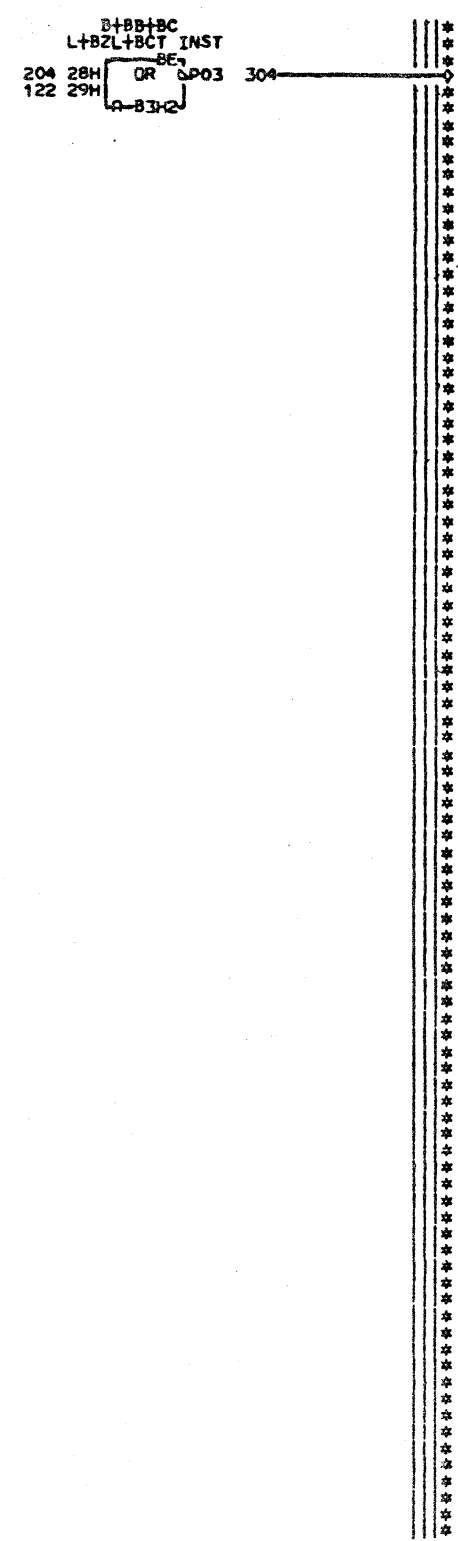
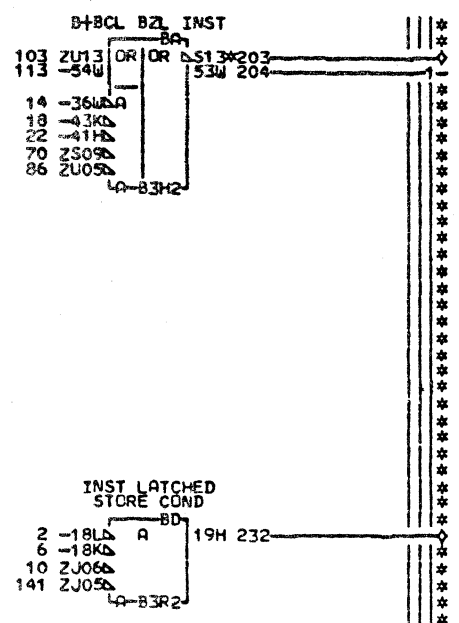
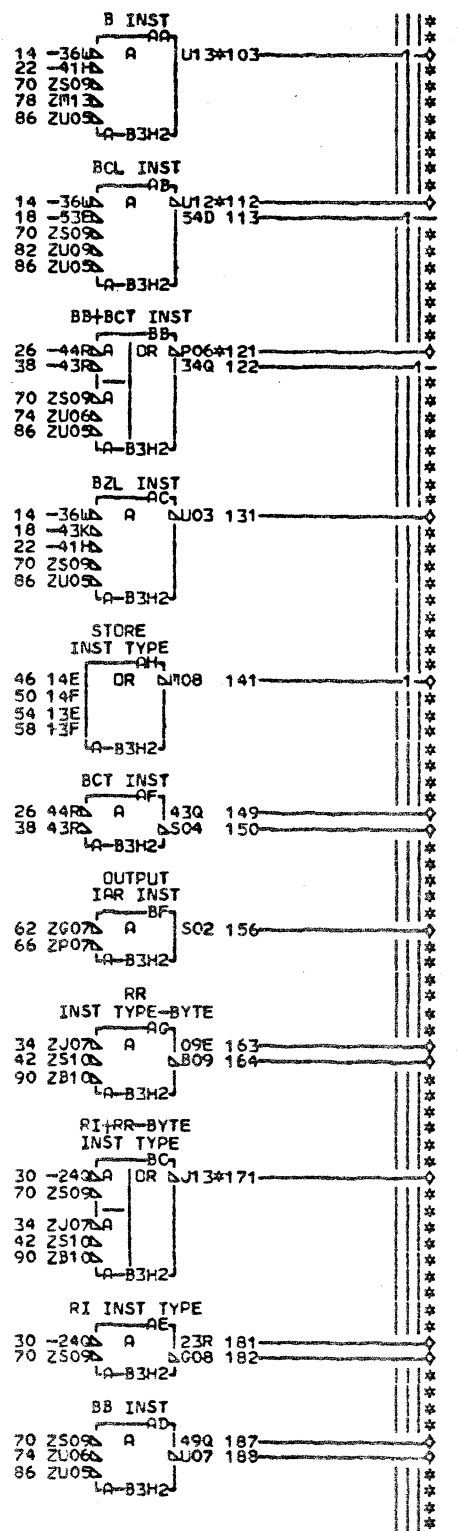
EDGE CONN.
249 A-B3P1E13
01A-B4P6E04

LOC. TYPE
A-B3G2 7702
A-B3H2 AB91
A-B3J2 6815

INSTRUCTION DECODE	
E.C. HISTORY	E. MACH. 3705
FRAME	01
DATE	10-14-80 344270
LAST EC	
IBM CORP. SCD	CD001
P.N. 1850839	000

CD001
000

+ D TIME-----CC001CL2- 2-1-1
 + DR TIME-----CC001DM2- 6-1-1
 - I2+I3 TIME-----CC008AL2- 10-1-1
 + OP REG BIT 0.1-----CD001AE2- 14-3-1
 + OP REG BIT 0.2-----CD001AG2- 18-2-1
 + OP REG BIT 0.3-----CD001AJ2- 22-2-1
 - OP X011 XXXX XXXX XXXX-----CD001AK6- 26-2-2
 + OP REG BIT 0.4-----CD001AL2- 30-2-2
 - OP XXXX XXXX XXXX X000-----CD001BE6- 34-2-2
 - OP 1XXX 1XXX 1XXX XXXX-----CD001FA0- 38-2-2
 - OP OXXX OXXX OXXX XXXX-----CD001FA3- 42-2-2
 + STC INST-----CD001GD2- 46-1-1
 + STH INST-----CD003AG2- 50-1-1
 + ST INST-----CD003AJ2- 54-1-1
 + STCT INST-----CD003ED2- 58-1-1
 - OUTPUT INST-----CD003DJ6- 62-1-1
 - OP XXXX X000 XXXX XXXX-----C2002DN6- 66-1-1
 - OP REG BIT 0.0-----DN004GB2- 70-7-1
 - OP REG BIT 0.1-----DN004GD2- 74-2-1
 - OP REG BIT 0.2-----DN004GF2- 78-2-1
 - OP REG BIT 0.3-----DN004GH2- 82-2-1
 - OP REG BIT 0.4-----DN004GK2- 86-3-1
 - OP REG BIT 1.4-----DQ004GK2- 90-2-1



000 CD002
 103 + B INST-----CF003 LCL005-----CB2
 112 - BCL INST-----CF003 LCL005-----CC6
 131 - BZL INST-----CL005-CD6
 187 + BB INST-----CD004-CE2
 188 - BB INST-----CA002-CE6
 181 + RI INST TYPE-----CD004-CF2
 182 - RI INST TYPE-----CF6
 CA001 LCA002 LCA003 LCL003
 LCA002 LCA003 LCL004 LCL005
 LCA001 LCA002 LCA003 LCL003
 LCA002 LCA003 LCL004 LCL005
 149 + BCT INST-----CD004-CG2
 150 - BCT INST-----CG6
 CA002 LCA003 LCL004 LCL005
 LCA001 LCA002 LCA003 LCL003
 163 + RR INST TYPE-BYTE-----CD004-CH2
 164 - RR INST TYPE-BYTE-----CH6
 CA001 LCA002 LCA003 LCL001
 LCA002 LCA003 LCL003
 141 - STORE INST TYPE-----CN2
 LCA003 LCU004
 203 - B+BCL BZL INST-----CF003-DA2
 121 - BB+BCT INST-----DF2
 LCF002 LCL005
 171 - RI+RR-BYTE INST TYPE-----DH2
 LCF003 LCA002 LCA003
 232 + INST LATCHED STORE COND CQ001-DR2
 304 - B+BB+BCL+BZL+BCT INST-----EF2
 LCA002 LCA003
 156 + OUTPUT IAR INST-----CA001-EK2

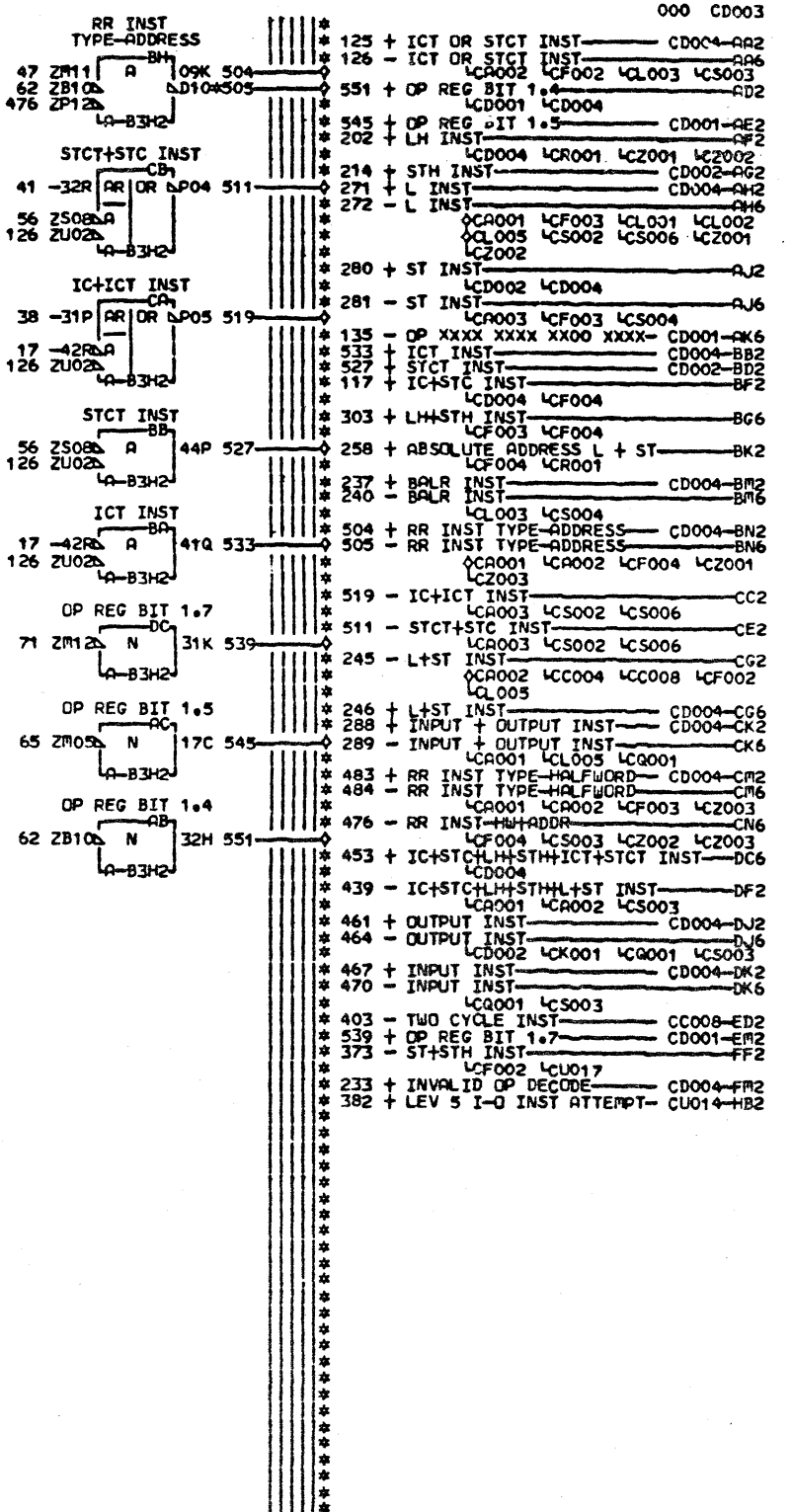
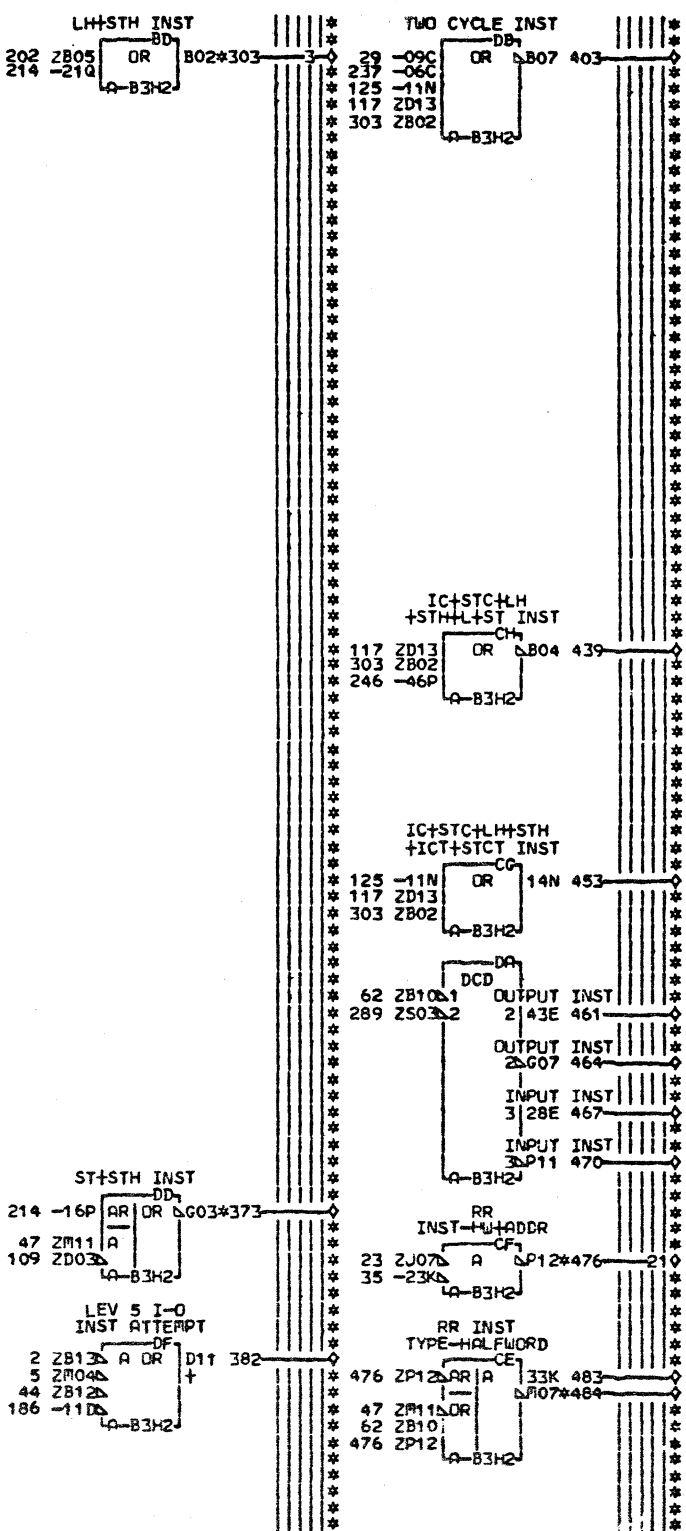
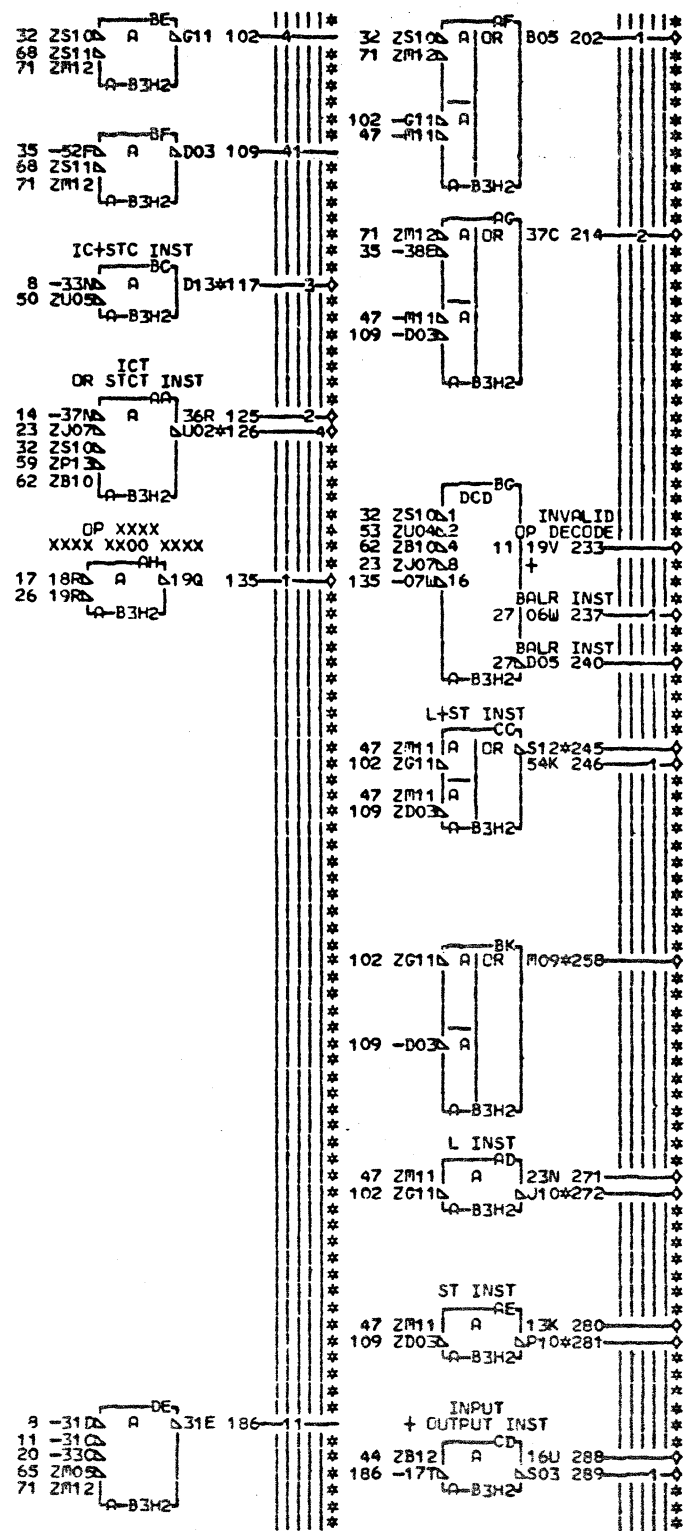
EDGE CONN.
 103 A-B3Q1A11
 01A-B4Q6A02
 112 A-B3Q1B13
 01A-B4Q6B04
 121 A-B3Q1C13
 01A-B4Q6C04
 171 A-B3Q1D11
 01A-B4Q6D02
 203 A-B3Q1E11

CD002
 000

01A-B4Q6C02
 LOC. TYPE
 A-B3H2 AB91
 A-B3R2 AB92

INSTRUCTION DECODE			
-E.C.-HISTORY-		E1 MACH.3705	
DATE LAST EC		FRAME	01
10-14-80 344270		IBM CORP.SCD	CD002
		P.N. 1850840	000

- C TIME CC001FG2* 2
 - T1+T2 TIME CC007HJ8 5
 + OP REG BIT 0.0 CD001AA2 8-2
 + OP REG BIT 0.4 CD001AL2 11-1
 + OP REG BIT 1.1 CD001AN2 14-1
 + OP REG BIT 1.2 CD001BA2 17-1
 + OP REG BIT 1.6 CD001BD2 20-1
 - OP XXXX XXXX XXXX X000 CD001BE6 23-1
 + OP REG BIT 1.3 CD001CC2 26-1
 + BAL+LA INST CD001DK2 29-1
 - OP OXXX OXXX OXXX XXXX CD001FA3 32-22
 - OP OXXX OXXX 1XXX XXXX CD001FA4 35-1
 + IC INST CD001GC2 38-1
 + IC INST CD001GD2 41-1
 - PROG LEV 5 CURRENT CP003EM6 44-1
 + BYTE X TIE UP DF002GF4 47-6111
 - OP REG BIT 0.4 DN004GK2 50-1
 - OP REG BIT 1.1 DQ004GD2 53-1
 - OP REG BIT 1.2 DQ004GF2 56-2
 - OP REG BIT 1.3 DQ004GH2 59-1
 - OP REG BIT 1.4 DQ004GK2 62-1-22
 - OP REG BIT 1.5 DR991GB6 65-1
 - OP REG BIT 1.6 DR991GE6 68-2
 - OP REG BIT 1.7 DR992GA6 71-32-1



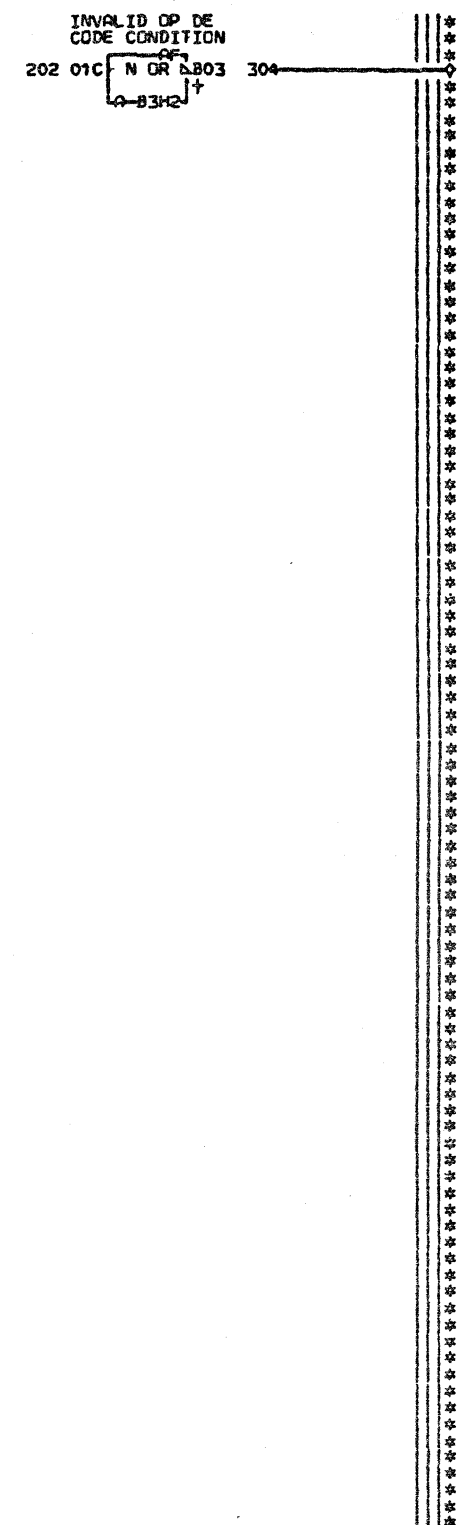
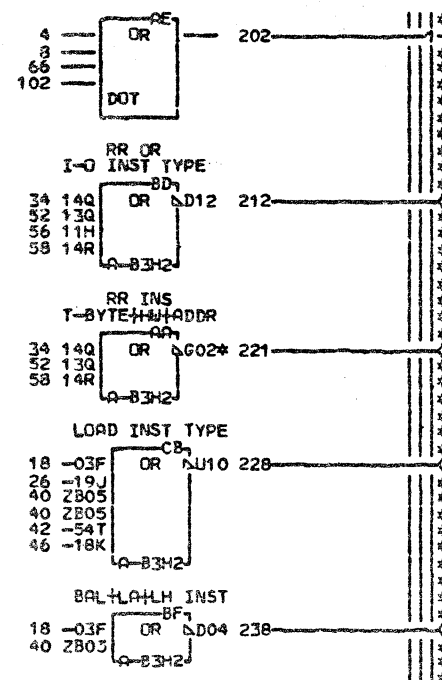
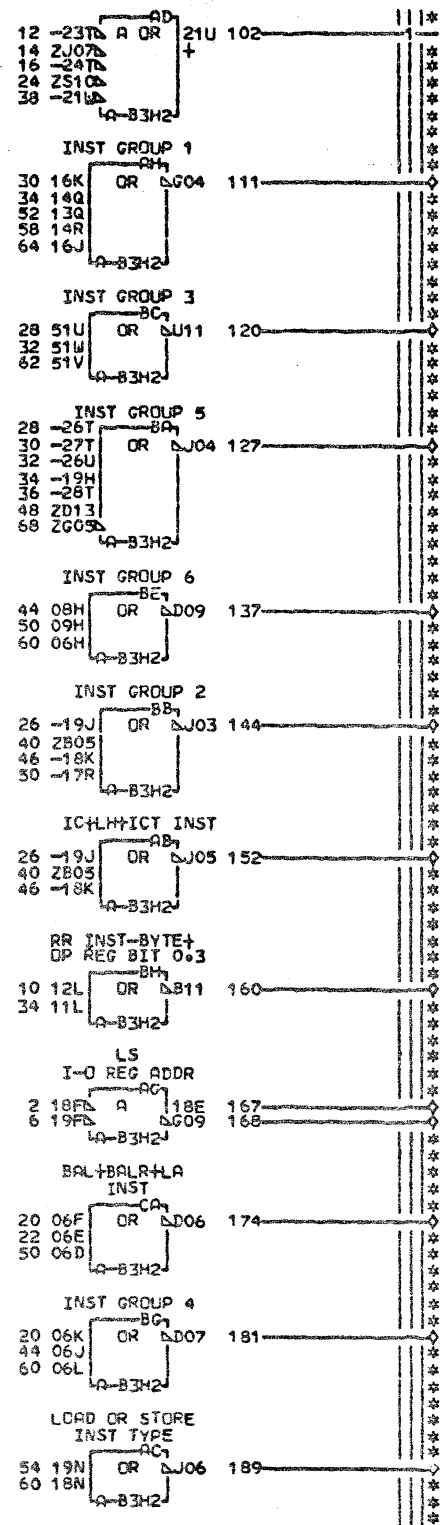
CD003
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EDGE CONN.	01A-B4R6C02	01A-B4S6A02
2 RESISTOR	272 A-B3Q1D13	484 A-B3R1E13
117 A-B3H2B13	01A-B4Q6D04	01A-B4R6E04
01A-B3R1A13	281 A-B3Q1E11	505 A-B3R1C13
01A-B4R6A04	01A-B4Q6E02	01A-B4R6C04
126 A-B3R1B13	303 A-B3R1B11	
01A-B4R6B04	01A-B4R6B02	
245 A-B3R1D11	373 A-B3S1A13	
01A-B4R6D02	01A-B4S6A04	
258 A-B3R1C11	476 A-B3S1A11	

LOC. TYPE
A-B3H2 AB91

CYCLE AND INSTRUCTION DECODE
 -E-C-HISTORY- E-RPACH-3705
 FRAME 01
 DATE LAST EC IBA CORP-SCD CD003
 10-14-80 344270 P.N. 1850841 000

+ OP REG BIT 0.1 CD001AE2- 2-111
 + INVALID OP DECODE CD001AF2- 4-111
 + OP REG BIT 0.2 CD001AG2- 6-111
 + INVALID OP DECODE CD001AH2- 8-111
 + OP REG BIT 0.3 CD001AJ2- 10-111
 + OP REG BIT 1.1 CD001AK2- 12-111
 - OP XXXX XXXX XXXX X000 CD001BE6- 14-111
 + OP REG BIT 1.3 CD001CC2- 16-111
 + BAL+LA INST CD001DK2- 18-111
 + BAL INST CD001EL2- 20-211
 + LA INST CD001EM2- 22-111
 - OP OXXX OXXX OXXX XXXX CD001FA3- 24-111
 + IC INST CD001GC2- 25-211
 + BB INST CD002CE2- 28-111
 + RI INST TYPE CD002CF2- 30-111
 + BCT INST CD002CG2- 32-211
 + RR INST TYPE-BYTE CD002CH2- 34-32
 + ICT OR STCT INST CD003AA2- 36-111
 + OP REG BIT 1.4 CD003AD2- 38-111
 + LH INST CD003AF2- 40-23
 + L IN I CD003AH2- 42-111
 + ST INST CD003AJ2- 44-211
 + ICT INST CD003BB2- 46-211
 + IC+STC INST CD003BF2- 48-111
 + BALR INST CD003BM2- 50-311
 + RR INST TYPE-ADDRESS CD003BN2- 52-211
 + L+ST INST CD003CG6- 54-111
 + INPUT + OUTPUT INST CD003CK2- 56-111
 + RR INST TYPE+HALFWORD CD003CM2- 58-211
 + IC+STC+LH+STH+ICT+STCT INST CD003DC6- 60-311
 + OUTPUT INST CD003DJ2- 62-111
 + INPUT INST CD003DK2- 64-111
 + INVALID OP DECODE CD003FM2- 66-111
 - OP REG BIT 0.7 DP992GA6- 68-111



000 CD004

221 - RR INST-BYTE+H+ADDR AB2
 4CA003 4CF004 4CL003
 152 - ICT+STC INST CA001-AD2
 89 - LOAD OR STORE INST TYPE CL003-AE2
 304 - INVALID OP DECODE CONDITION AH2
 4CK007
 167 + LS I-O REG ADDR CD001-AK2
 168 - LS I-O REG ADDR AK6
 4CA001 4CL004 4CL005 4CS003
 111 - INST GROUP 1 BB2
 4CL001 4CL002
 127 - INST GROUP 5 CL002-BC2
 144 - INST GROUP 2 BD2
 4CL001 4CL002 4CL005
 120 - INST GROUP 3 BF2
 4CL001 4CL002
 212 - RR OR I-O INST TYPE CS002-BG2
 137 - INST GROUP 6 CS002-BH2
 238 - BAL+LA+LH INST CS002-BK2
 181 - INST GROUP 4 BL2
 4CL001 4CL002
 160 - RR INST-BYTE+OP REG BIT 0.3 CK2
 4CL003
 174 - BAL+BALR+LA INST CA001-DM2
 228 - LOAD INST TYPE CU004-DN2

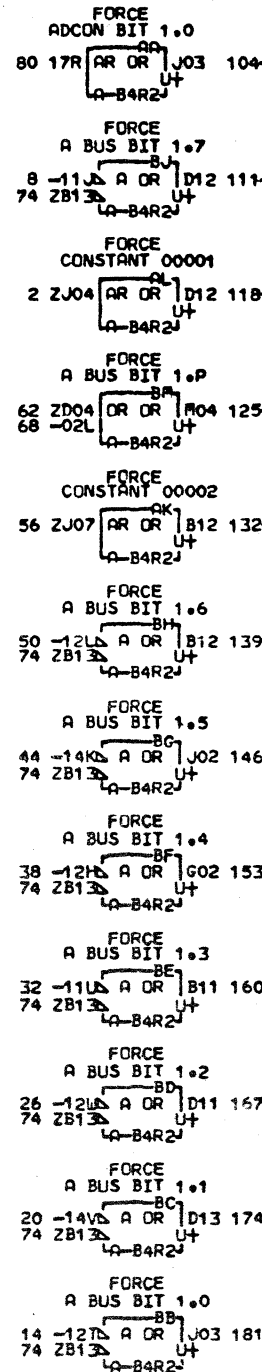
EDGE CONN.
221 A-B3V3D05
01A-B4V3D05

LOC. TYPE
A-B3H2 AB91

CD004
000

INSTRUCTION DECODE			
E ₀ C ₀ -HISTORY	E ₁ NACH-3705	FRARE	01
DATE	LAST EC	IBM CORP.SCD	CD004
10-14-80	244270	P.No. 1850842	000

+ FORCE CONSTANT 00001 — CF002AF2 — 2
 - GENERATE CONST BIT 7 — CF002BC0 — 8
 - GENERATE CONST BIT 0 — CF002BC3 — 14
 - GENERATE CONST BIT 1 — CF002BC4 — 20
 - GENERATE CONST BIT 2 — CF002BC5 — 26
 - GENERATE CONST BIT 3 — CF002BC6 — 32
 - GENERATE CONST BIT 4 — CF002BC7 — 38
 - GENERATE CONST BIT 5 — CF002BC8 — 44
 - GENERATE CONST BIT 6 — CF002BC9 — 50
 + FORCE CONSTANT 00002 — CF002BH6 — 56
 + FORCE CONSTANT 00000 — CF002CG2 — 62
 + GATE GEN CONST BYTE 0 — CF004BJ2 — 68
 - GATE GEN CONST BYTE 1 — CF004BK6 — 74
 + FORCE ADCON BIT 0.6 AND 1.0 — CF004CL2 — 80



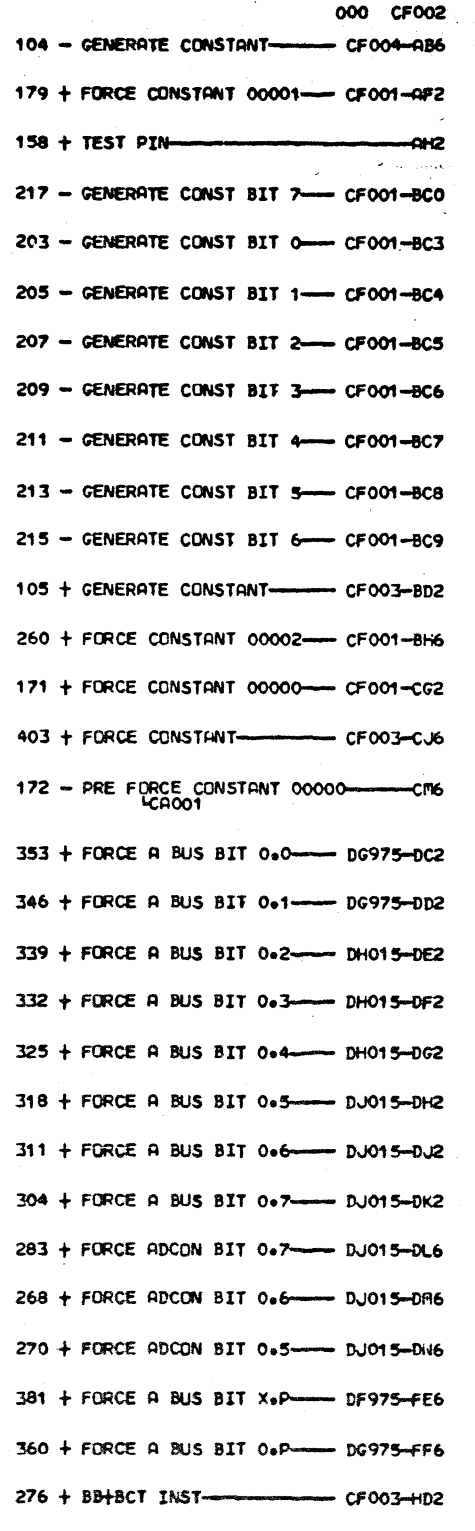
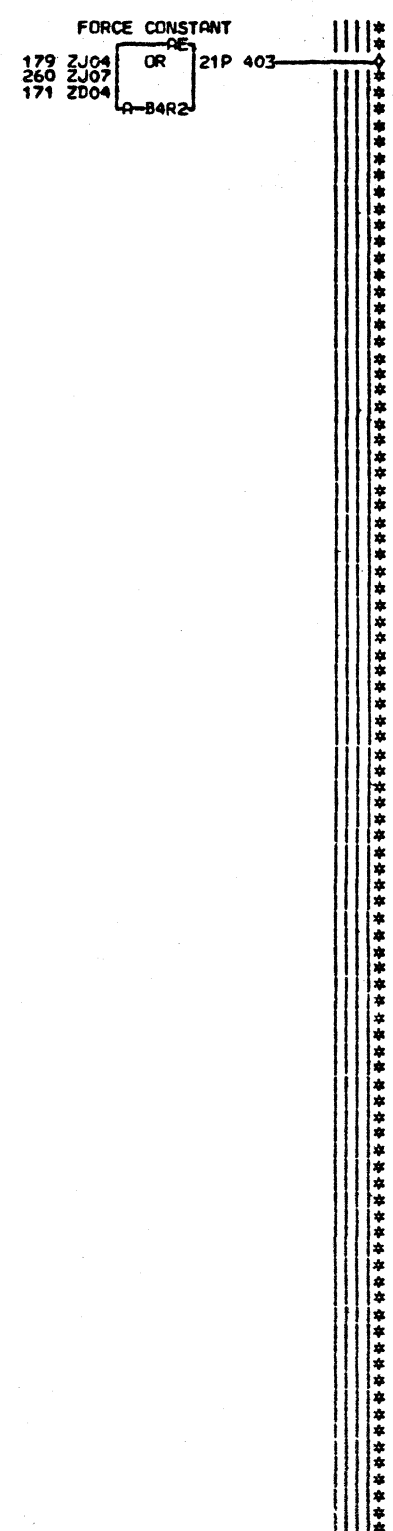
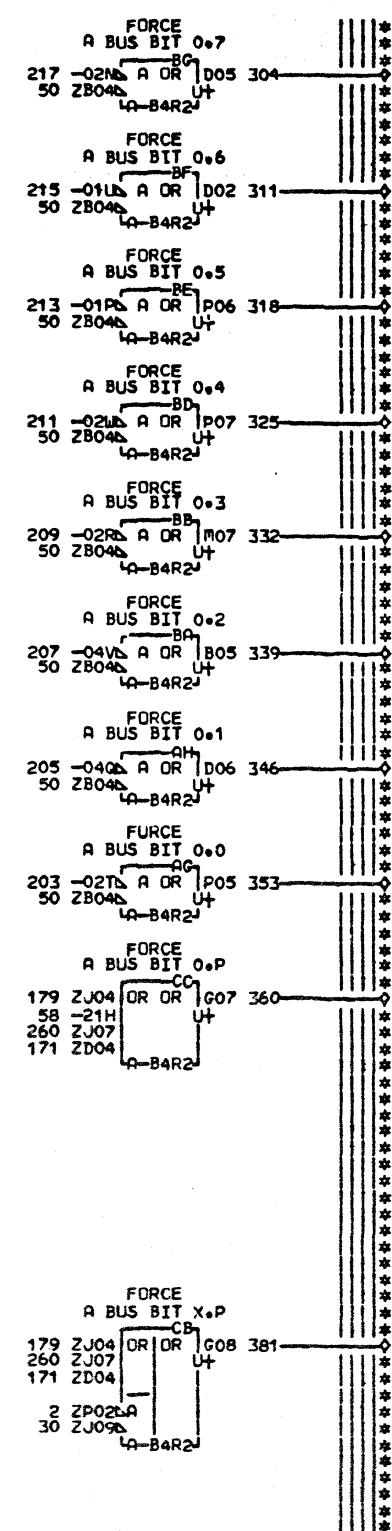
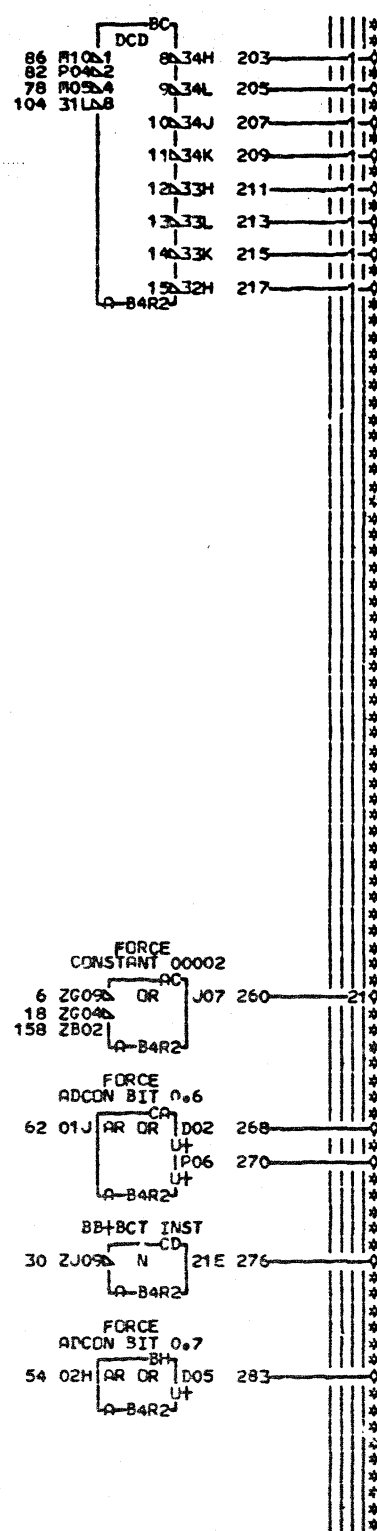
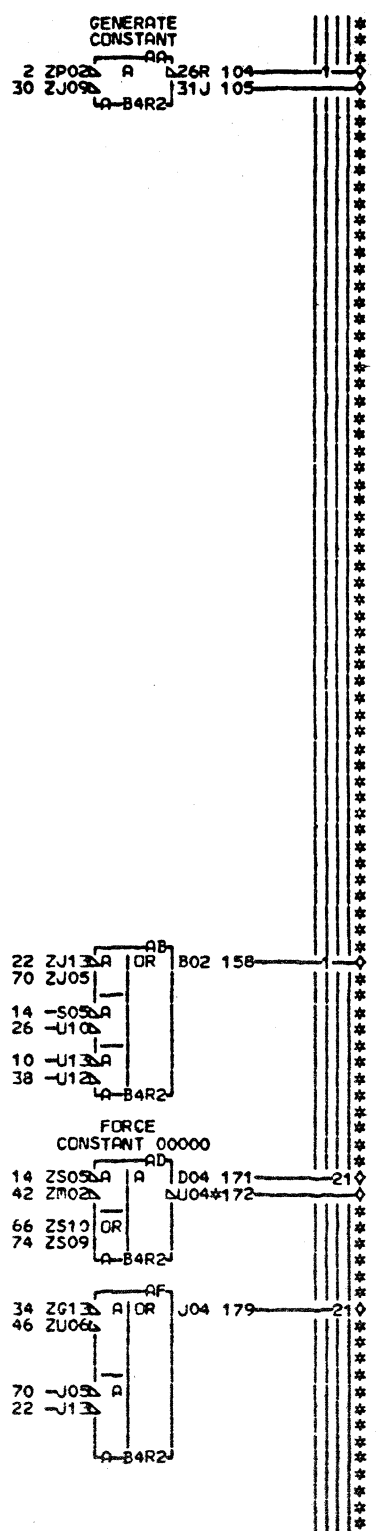
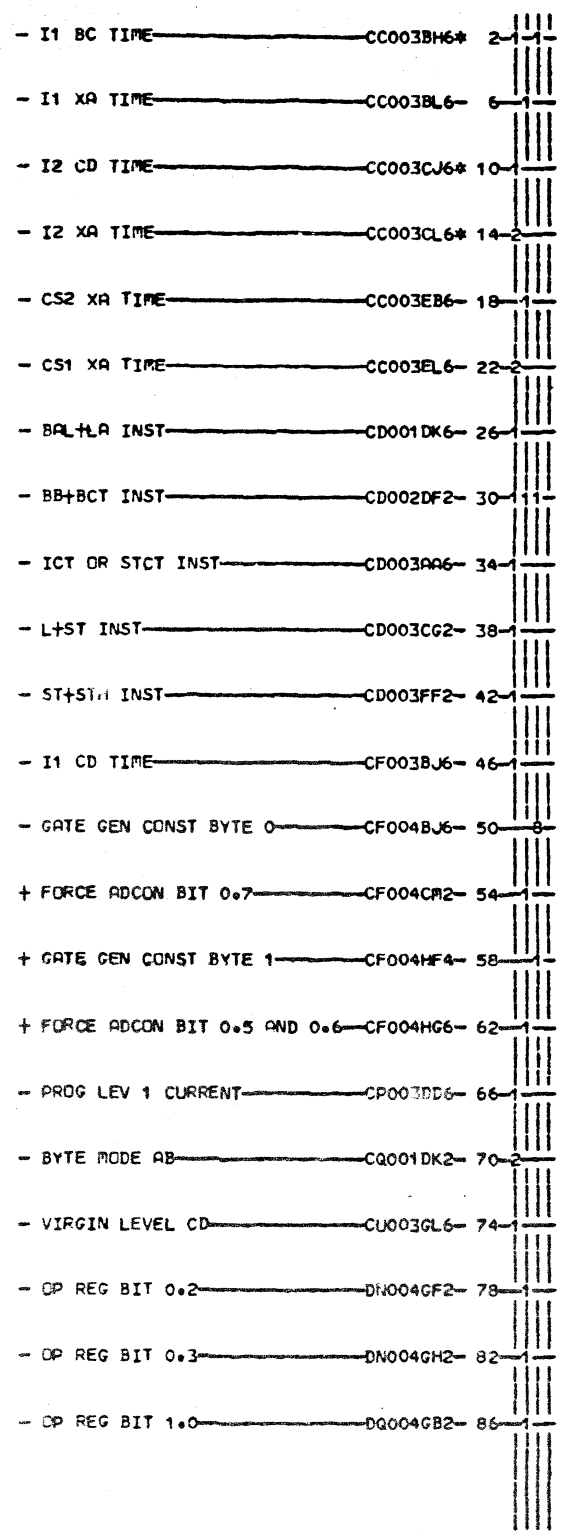
000 CF001

104 + FORCE ADCON BIT 1.0 — DK975-AA6
 132 + FORCE CONSTANT 00002 — DM005-AK6
 118 + FORCE CONSTANT 00001 — DM005-AL6
 181 + FORCE A BUS BIT 1.0 — DK975-BB2
 174 + FORCE A BUS BIT 1.1 — DK975-BC2
 167 + FORCE A BUS BIT 1.2 — DL005-BD2
 160 + FORCE A BUS BIT 1.3 — DL005-BE2
 153 + FORCE A BUS BIT 1.4 — DL005-BF2
 146 + FORCE A BUS BIT 1.5 — DM005-BG2
 139 + FORCE A BUS BIT 1.6 — DM005-BH2
 111 + FORCE A BUS BIT 1.7 — DM005-BJ2
 125 + FORCE A BUS BIT 1.P — DK975-BM6

LDC TYPE
A-B4R2 6807

CF001
000

FORCE CONSTANTS	
E.C. HISTORY	E. MACH. 3705
DATE LAST EC	FRAME 01
10-14-80 344270	IBM CORP. SCD CF001
	PeN. 1850843 000



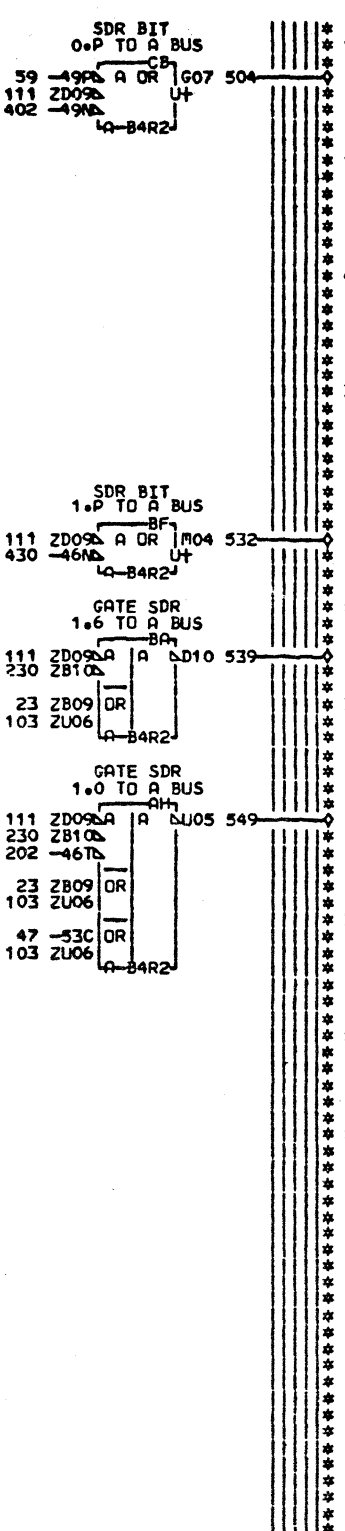
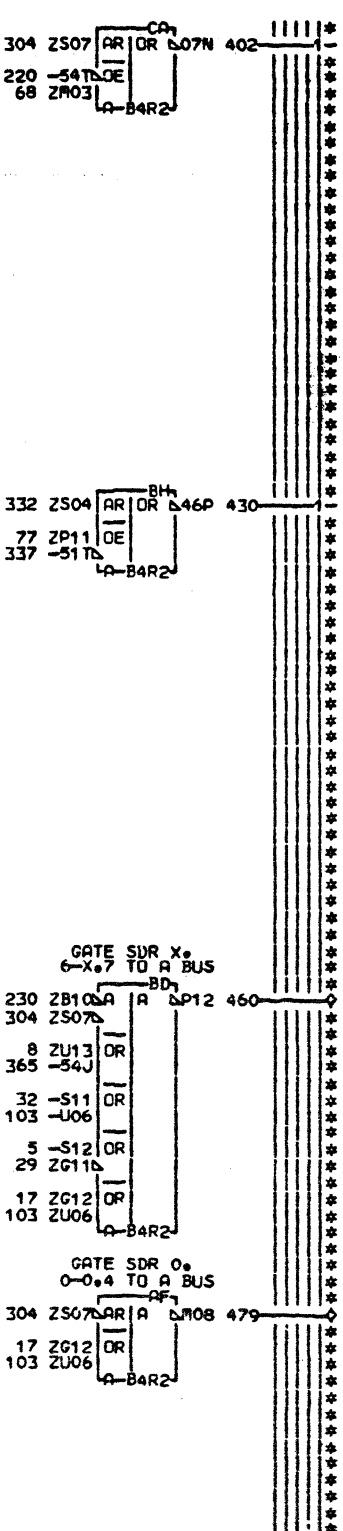
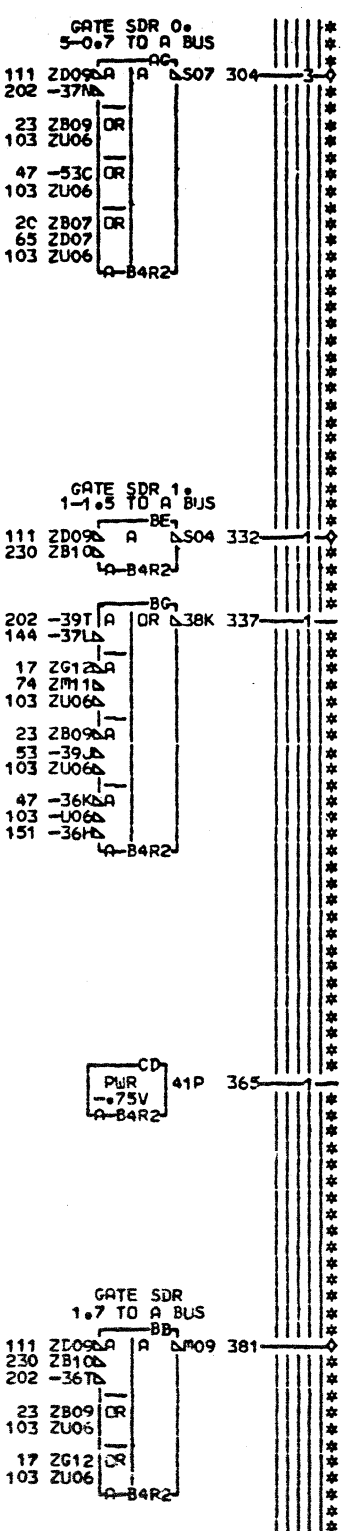
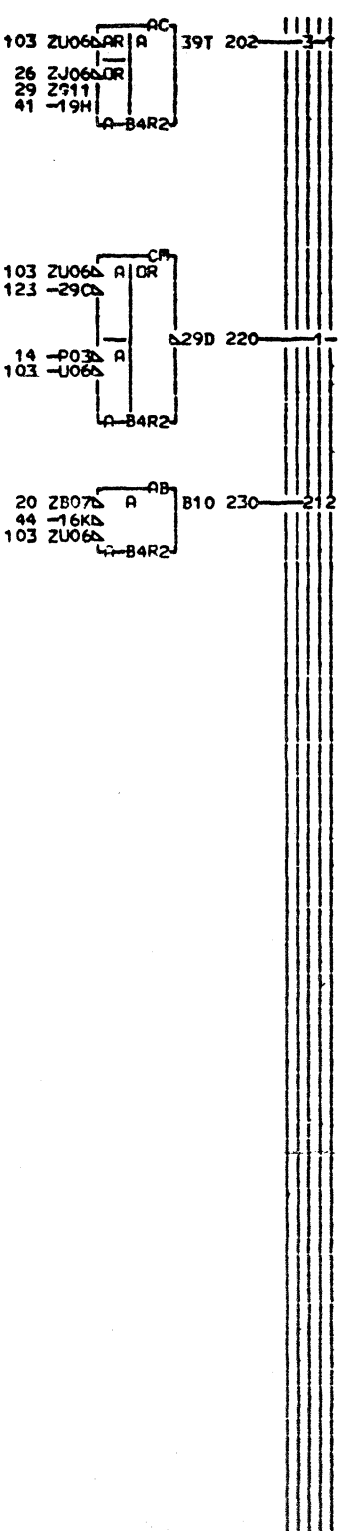
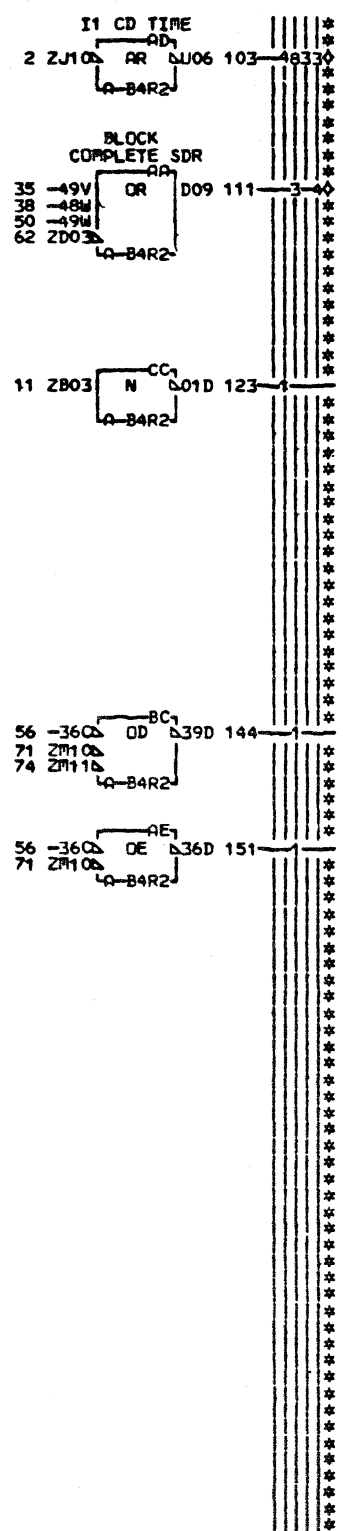
EDGE CONN.
 2 RESISTOR
 A-B4R2P02
 10 RESISTOR
 A-B4R2U13
 14 RESISTOR
 A-B4R2S05
 172 A-B3V3D06
 01A-B4V3D06

LOC. TYPE
 A-B4R2 6807

FORCE CONSTANTS			
E.C. HISTORY	E	MACH. 3705	
		FRAME 01	
DATE	LAST EC	IBM CORP. SCD	CF002
10-14-60	344270	P.N. 1850844	000

CF002
 000

- I1 CD TIME—CC003BJ6* 2-1
 - I2 BC TIME—CC003CH6* 5-1
 - I2 CD TIME—CC003CJ6 8-1
 + B INST—CD002CB2- 11-1
 - BCL INST—CD002CC6- 14-1
 - B+BCL BZL INST—CD002DA2- 17-22
 - RI+R-BYTE INST TYPE—CD002DH2- 20-1
 - L INST—CD003AH6- 23-3-2
 - ST INST—CD003AJ6- 26-1
 + LH+STH INST—CD003BG6- 29-1
 - RR INST TYPE—HALFWORD—CD003CM6- 32-1
 + GENERATE CONSTANT—CF002BD2- 35-1
 + FORCE CONSTANT—CF002CJ6- 38-1
 + BEHCT INST—CF002HD2- 41-1
 + OP REG BIT 0.7—CF004AK2- 44-1
 - IC+STC INST—CF004AL2- 47-2
 + SHIFT RIGHT—CF004CF2- 50-1
 + FORCE ADCON BIT 0.6 AND 1.0—CF004CL2- 53-1
 - FORCE ADCON BITS 0.6 AND 1.0—CF004CL6- 56-2
 + FORCE ADCON BIT 0.5 AND 0.6—CF004HG6- 59-1
 - GATE SAR TO A BUS—CS004BK6- 62-1
 - OP REG BIT 0.7—DP992GA6- 65-1
 + GENERATED SDR BIT ODD 0.P—DP993GV4- 68-1
 - OP REG BIT 1.0—DQ004GB2- 71-2
 - OP REG BIT 1.7—DR992GA6- 74-1
 + GENERATED SDR BIT ODD 1.P—DR993GV4- 77-1



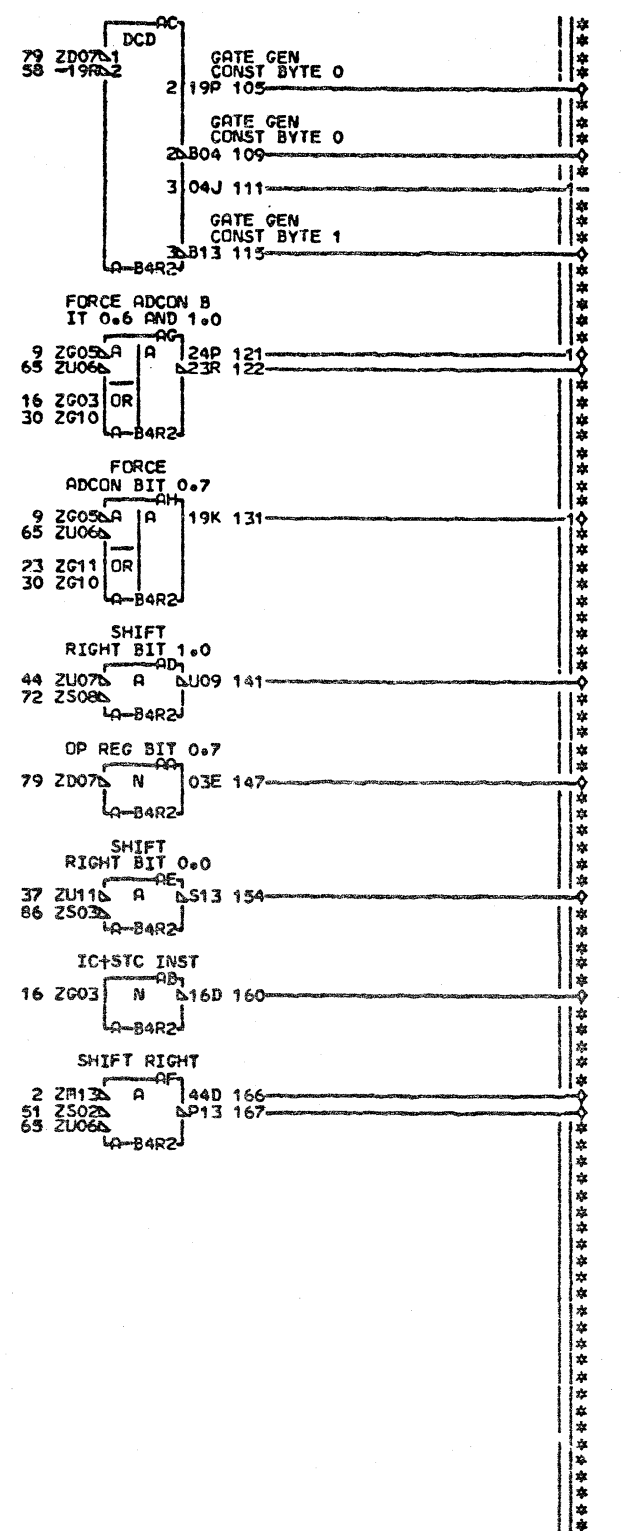
EDGE CONN.
 2 RESISTOR
 A-B4R2J10
 5 RESISTOR
 A-B4R2S12

LOC. TYPE
 A-B4R2 6807

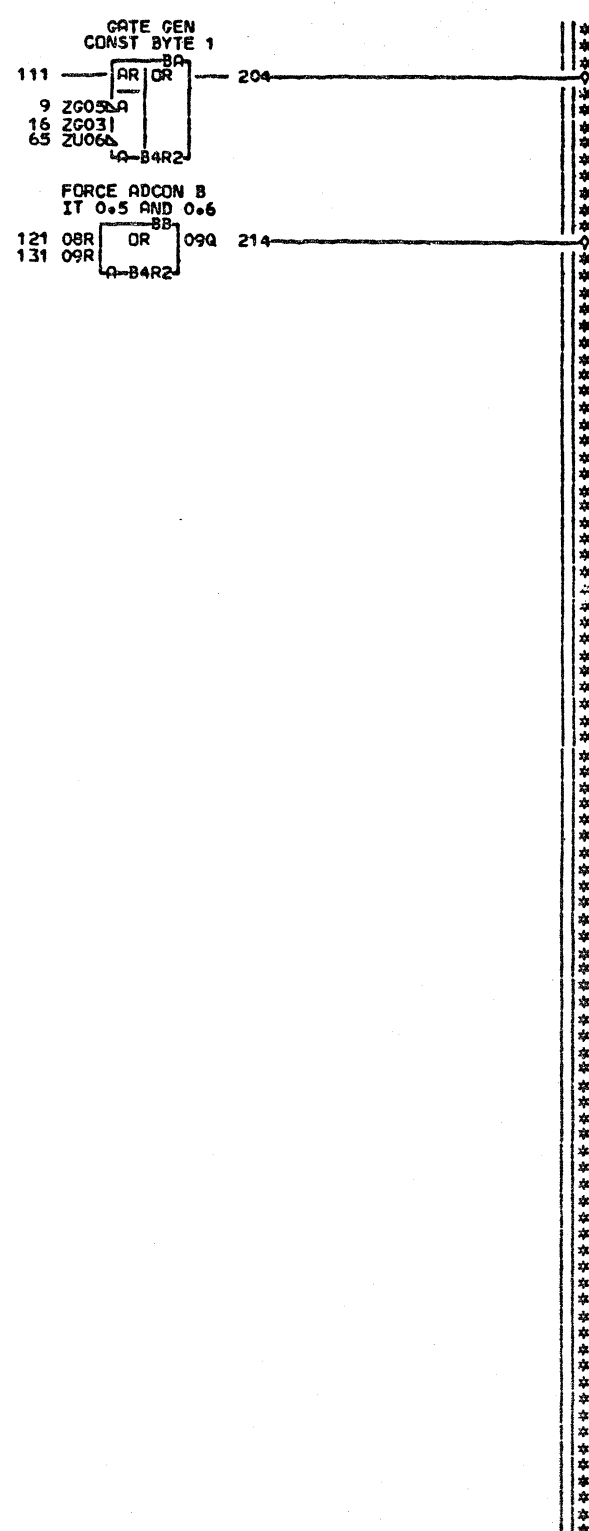
BIT FILTER AND PARITY GENERATI
 -E.C.-HISTORY—E1 MACH.3705
 FRAME 01
 IBA CORP.SCD CF003
 DATE LAST EC 10-14-80 344270 P.N. 1850845 000

CF003
 000

- OP XXXX XXXX X111 XXXX CA003BB0 2-1
 - SELECT LS REG 0 CC006BM6 9-21
 + IC+STC INST CD003BF2 16-21
 + LH+STH INST CD003BG6 23-
 + ABSOLUTE ADDRESS L + ST CD003BK2 30-2
 - RR INST TYPE-ADDRESS CD003BN6 37-
 - RR INST-HW+ADDR CD003CN6 44-
 - RR INST-BYTE+HW+ADDR CD004AB2 51-
 - GENERATE CONSTANT CF002AB6 58-
 - I1 CD TIME CF003BJ6 65-31
 - SDR BIT 0.7 DP992ED2 72-1
 - OP REG BIT 0.7 DP992GA6 79-2
 - SDR BIT X.7 DR992EG2 86-



LOC. TYPE
A-B4R2 6807

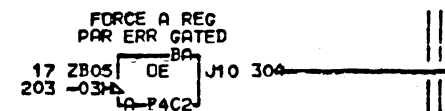
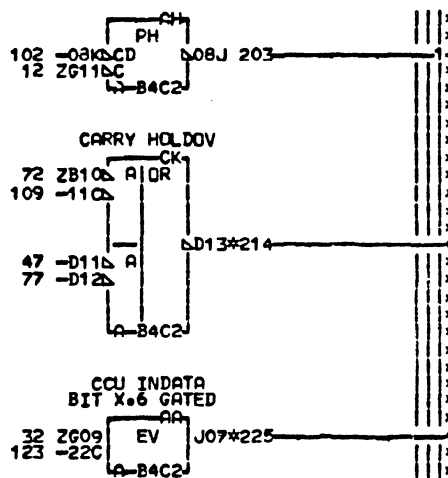
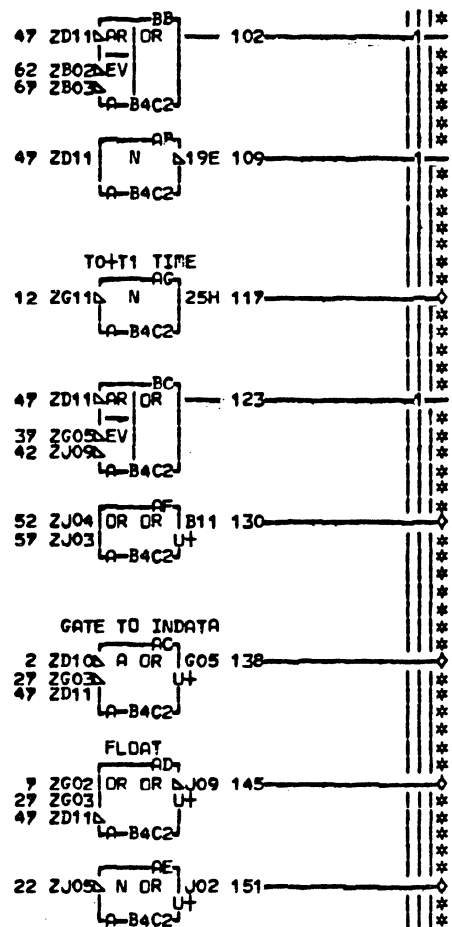


000 CF004
 147 + OP REG BIT 0.7 CF003-AH2
 160 - IC+STC INST CF003-AL2
 105 + GATE GEN CONST BYTE 0 CF001-BJ2
 109 - GATE GEN CONST BYTE 0 CF002-BJ6
 115 - GATE GEN CONST BYTE 1 CF001-BK6
 141 - SHIFT RIGHT BIT 1.0 CC6
 LDG003 LDR992
 154 - SHIFT RIGHT BIT 0.0 CE6
 LDN003 LDP992
 166 + SHIFT RIGHT CF003-CF2
 167 - SHIFT RIGHT CF6
 LDN003 LDP994 LDG003 LDR994
 121 + FORCE ADCON BIT 0.6 AND 1.0 CL2
 LCF001 LCF003
 122 - FORCE ADCON BITS 0.6 AND 1.0 CL6
 LCF003
 131 + FORCE ADCON BIT 0.7 CF002-CM2
 204 + GATE GEN CONST BYTE 1 CF002-4F4
 214 + FORCE ADCON BIT 0.5 AND 0.6 HG6
 LCF002 LCF003

CF004
000

SHIFT RIGHT CONTROLS
 AND FORCE ADCONST
 -E.C.-HISTORY-E-PACH.3705
 FRAME 01
 IBM CORP.SCD CF004
 DATE LAST EC
 10-14-60 344270 P.No. 1850846 000

- HEXSW BYTE X WEIGHT 8 UNUSED-AP001CB4- 2
 - HEXSW BYTE X WEIGHT 4 UNUSED-AP001CC4- 7
 - TO+T1 TIME SET A-B REGS-CC007HK4- 12
 + FORCE A REG PARITY ERROR-CK001GB2- 17
 - GATE INPUT 79-CQ005DK6- 22
 - GATE HEXSW A AND C AND E-CS004GH2- 27
 + CCU INDATA BIT X.6-CU011DA4- 32
 - FLOAT-CU011DQ4- 37
 - FLOAT-CU011DR4- 42
 - FLOAT-DE002GF4- 47
 - FLOAT-DE971EG2- 52
 - FLOAT-DE971EL2- 57
 - FLOAT-DE975DG4- 62
 - FLOAT-DE975DL4- 67
 ALWAYS PLUS-DE977GK6- 72
 + X.6 CARRY HOLDOVER-DF977GK6- 77



000 CG001

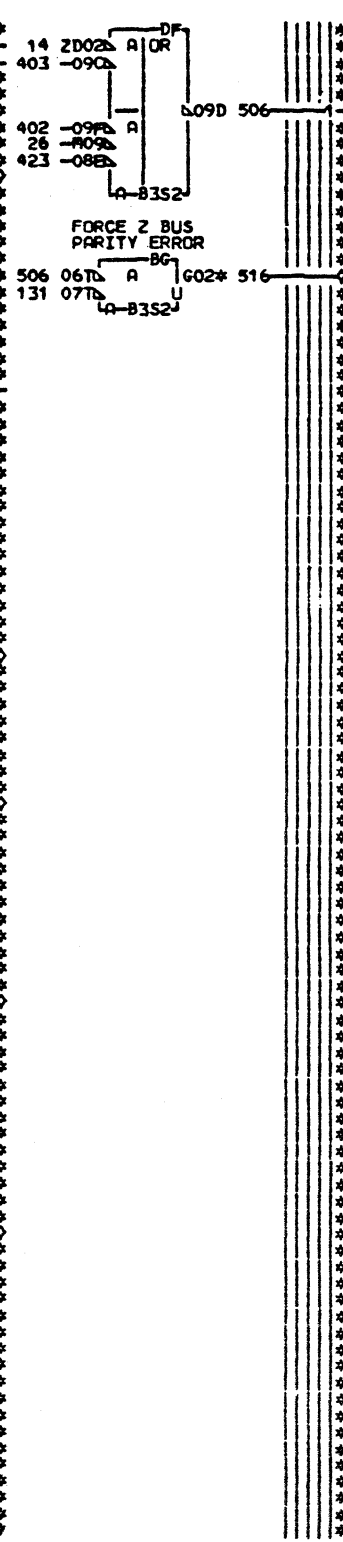
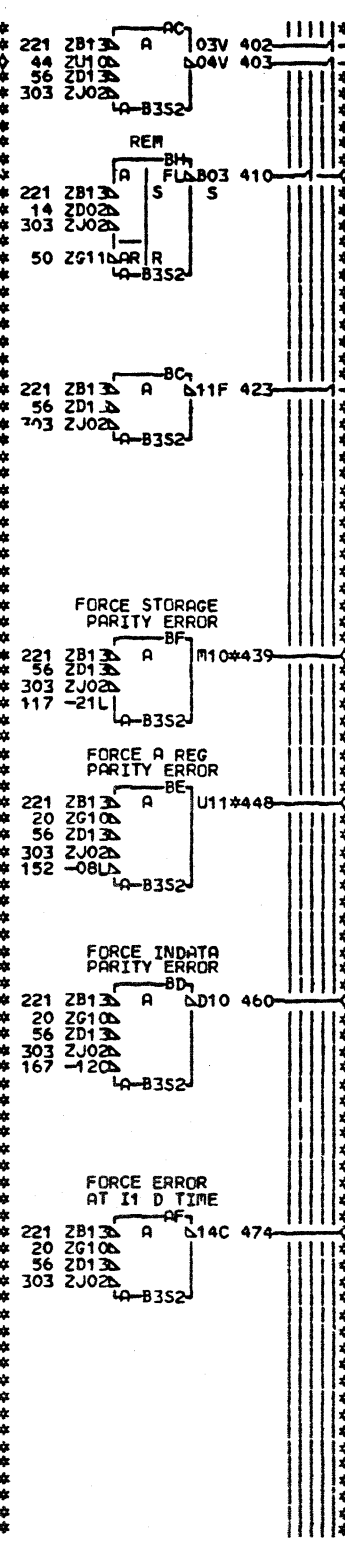
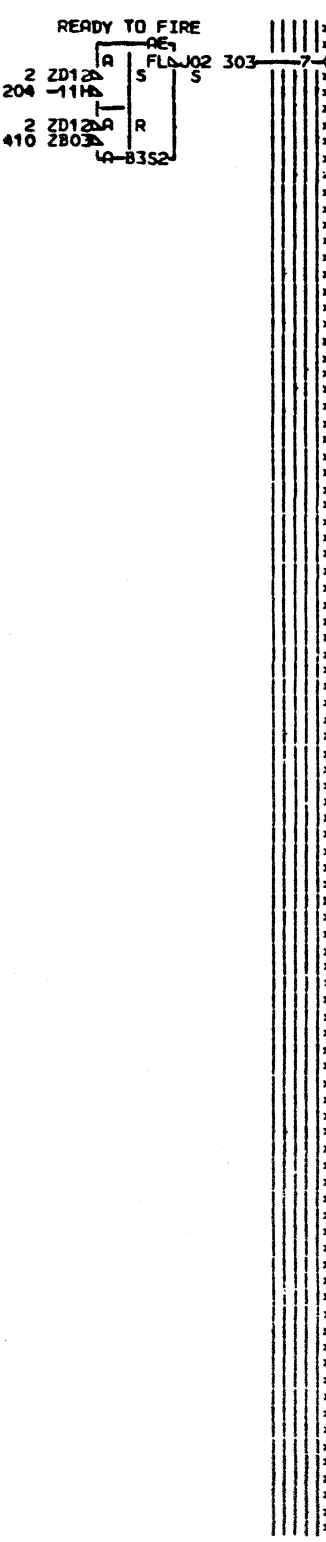
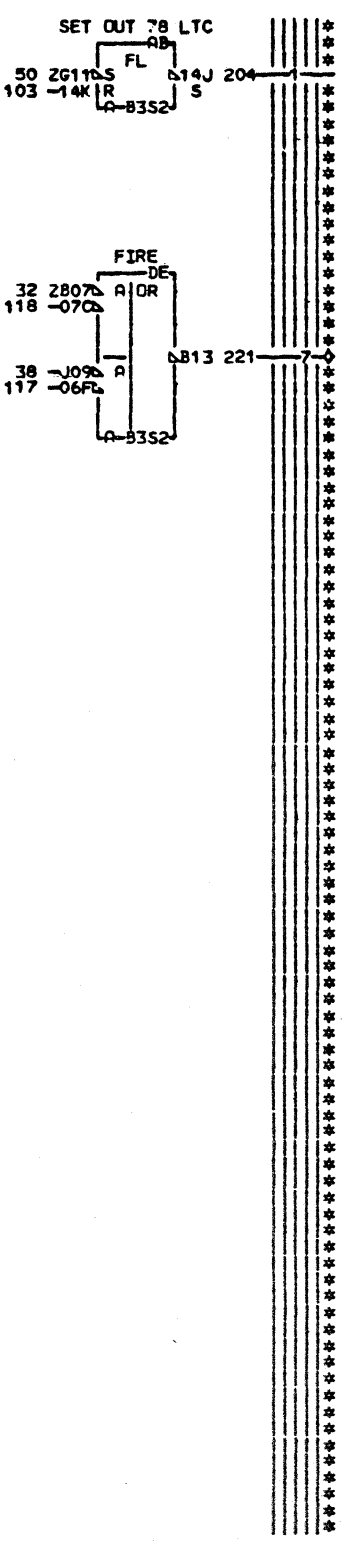
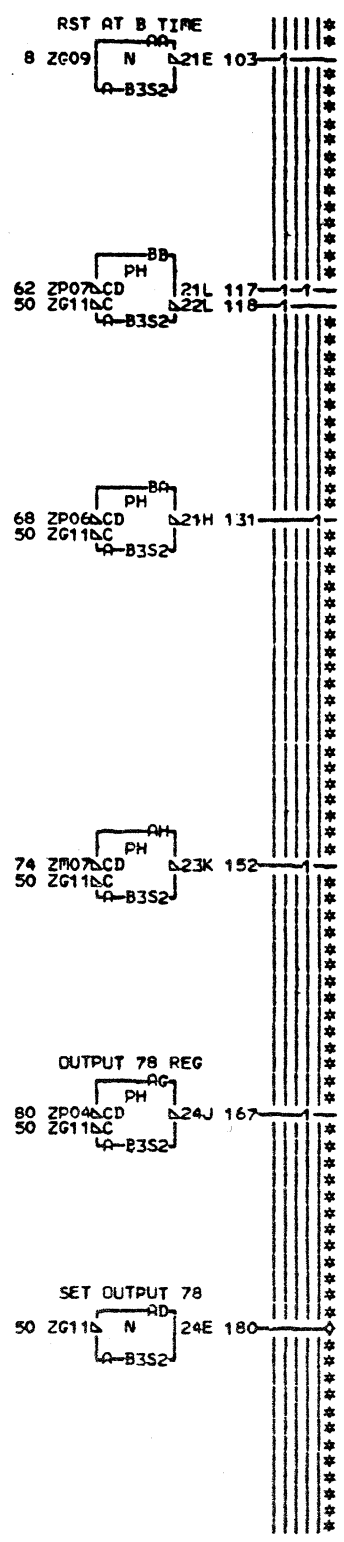
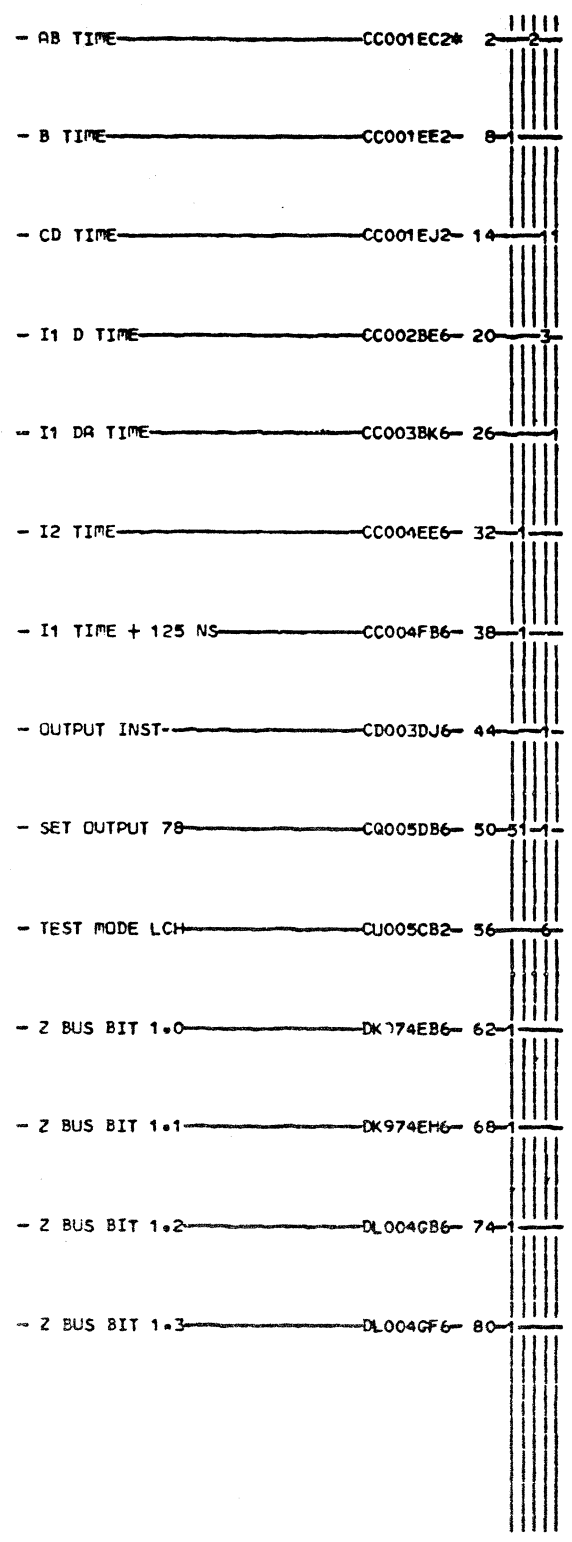
225 + CCU INDATA BIT X.6 GATED-BF2
 LCU013
 138 - FLOAT-CU011-CB2
 145 - FLOAT-CU011-CC2
 214 + X.6 CARRY HOLDOVER GATED-CK6
 LCZ003
 151 ALWAYS PLUS AT IN 79 TIME CU012-ED2
 130 FOLLOWS CV061AF4-CV061-EF6
 117 + TO+T1 TIME-CS005-FN2
 304 + FORCE A REG PAR ERR GATED-SC2
 LDF974

EDGE CONN.
 214 A-B4K1A13
 01A-B3K6A04
 225 A-B4E6D02
 01A-B3E1D11

LOC. TYPE
 A-B4C2 AB93

CG001
 000

X.6 CARRY TO SELECT C AND Z LATCHES	
E-C-HISTORY-344270	E-MACH-3705
DATE LAST EC 06-02-81 344828	FRAME 01 IBM CORP.SCD CG001 P.N. 1850847 000



000 CK001

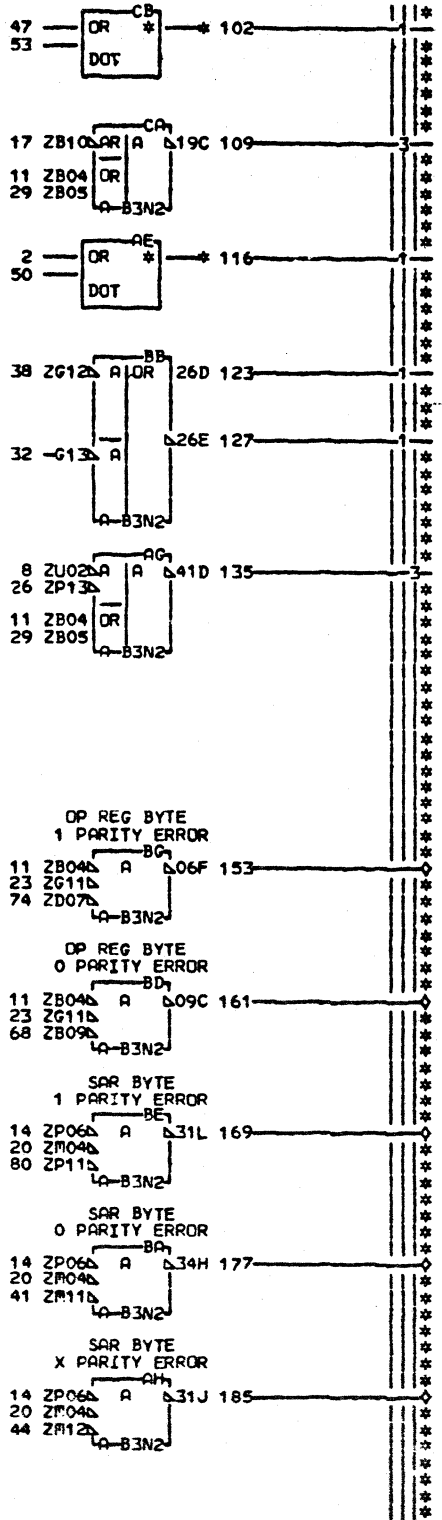
EDGE CONN.
 2 RESISTOR
 A-B352D12
 439 A-B3M1A13
 01A-B4P6A04
 448 A-B3L1D13
 01A-B4L6D04
 516 A-B3L1E11
 01A-B4L6E02

LOC. TYPE
 A-B352 Y703

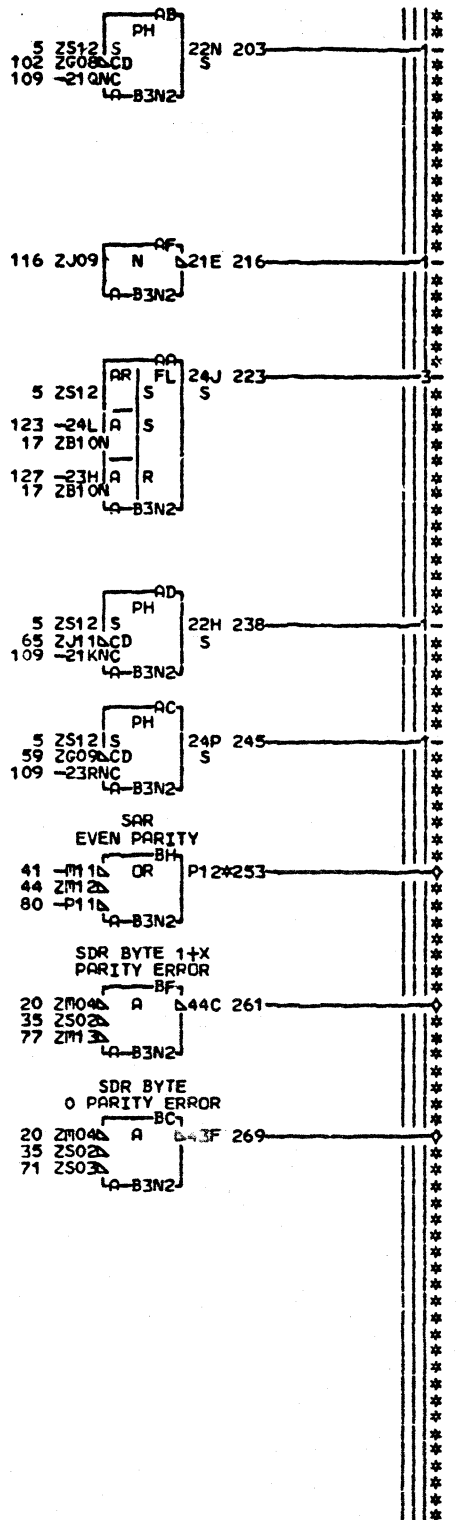
CC CONTROL
 -E-C-HISTORY- E-MACH.3705
 FRAME 01
 IBM CORP.SCD CK001
 DATE LAST EC 10-14-80 344270 P.N. 1850848 000

CK001
 000

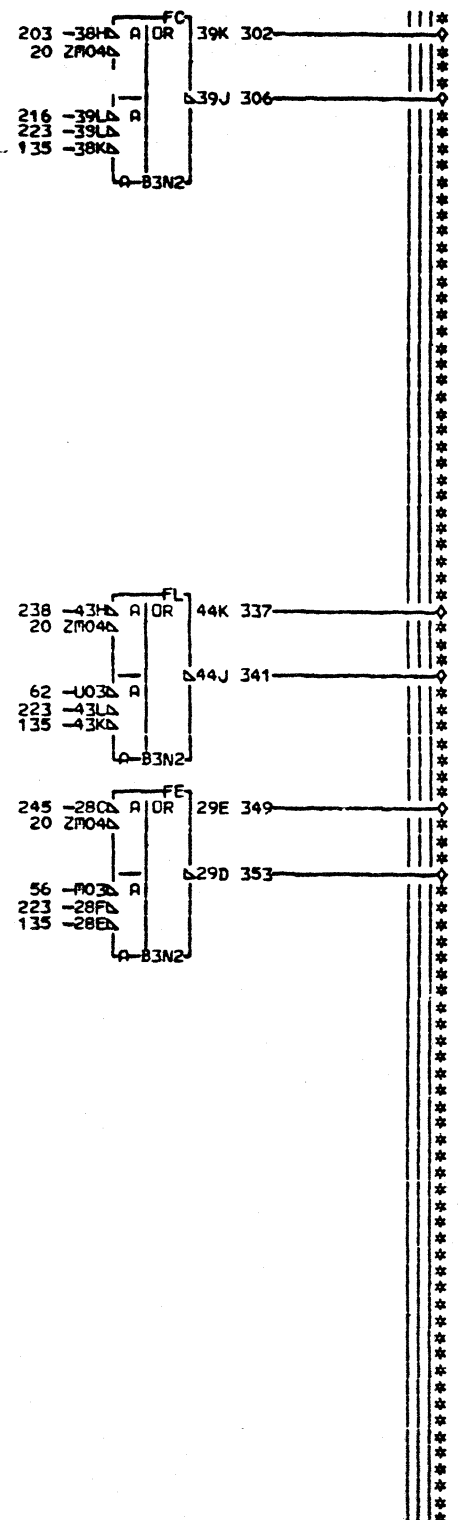
FOLLOWS CK003CC4—AJ001BG4— 2—11
 + POWER ON RESET—AP008DB4— 5—1
 + OUTPUT IAR INHIBIT B REG CHK—CA001FA2— 8—1
 - BC TIME—CC001EG2— 11—1
 - A TIME—CC001FC2— 14—3
 - TO TIME—CC006AF8— 17—2
 - T2 TIME—CC006DF9— 20—323
 - T1 TIME—CC006ED6— 23—2
 - T3 TIME—CC006HB6— 26—1
 - ANY I TIME—CC008BM2— 29—2
 - GATE INPUT 72—C0004DL6— 32—1
 + IPL INHIBIT SDR CK—CR003AE4— 35—2
 - I-O INHIBIT B REG MACH CHK—CS004EF2— 38—1
 - SAR BYTE 0 EVEN PARITY—CV001FG2— 41—1
 - SAR BYTE X EVEN PARITY—CV001HE4— 44—1
 FOLLOWS CK003SA4—DE976SA6— 47—1
 + B REG BYTE X EVEN PARITY—DF974GG2— 50—1
 + ALUHAREG+ZBUS BYTE X ERROR—DF976EJ6— 53—1
 - B REG BYTE 0 EVEN PARITY—DG974GG2— 56—1
 + ALUHAREG+ZBUS BYTE 0 ERROR—DG976EJ6— 59—1
 - B REG BYTE 1 EVEN PARITY—DK974GG2— 62—1
 + ALUHAREG+ZBUS BYTE 1 ERROR—DK976EJ6— 65—1
 - OP REG BYTE 0 P EVEN PARITY—DP992QL2— 68—1
 - SDR BYTE 0 EVEN PARITY—DP993SA4— 71—1
 - OP REG BYTE N EVEN PARITY—DR992QL2— 74—1
 - SDR BYTE 1 OR X EVEN PARITY—DR993SA4— 77—1
 - SAR BYTE 1 PARITY ERROR—DS001EL2— 80—1



EDGE CONN.
 102 RESISTOR
 A-B3N2G08
 01A-B4K6B02
 01A-B3K1B11
 116 A-B3J1D13
 01A-B4J6D04
 253 A-B3N1B11
 01A-B4N6B02



LOC. TYPE
 A-B3N2 6819



000 CK003
 185 - SAR BYTE X PARITY ERROR—EA6
 LCK006 LCK007
 177 - SAR BYTE 0 PARITY ERROR—ED6
 LCK006 LCK007
 269 - SDR BYTE 0 PARITY ERROR—EF6
 LCK006 LCK007
 161 - OP REG BYTE 0 PARITY ERROR—EG6
 LCK006 LCK007
 169 - SAR BYTE 1 PARITY ERROR—EJ6
 LCK006 LCK007
 261 - SDR BYTE 1+X PARITY ERROR—EK6
 LCK006 LCK007
 153 - OP REG BYTE 1 PARITY ERROR—EM6
 LCK006 LCK007
 302 + ALU OR B REG BYTE X ERROR—FC2
 LCK007
 306 - ALU OR B REG BYTE X ERROR—FC6
 LCK006
 349 + ALU OR B REG BYTE 0 ERROR—FE2
 LCK007
 353 - ALU OR B REG BYTE 0 ERROR—FE6
 LCK006
 253 + SAR EVEN PARITY—FF2
 LCK002 LCK003 LCK007
 337 + ALU OR B REG BYTE 1 ERROR—FL2
 LCK007
 341 - ALU OR B REG BYTE 1 ERROR—FL6
 LCK006

CK003
 000

ERROR DETECTION
 E.C.—HISTORY—E1 MACH. 3705
 FRAME 01
 DATE LAST EC IBM CORP. SCD CK003
 10-14-80 344270 P.N. 1850850 000

- BYTE X ERROR CK006GA6 2-2
 - INDATA PARITY ERROR CK006GD6 12-2
 - BYTE 0 ERROR CK006GE2 22-1
 - SAR PARITY ERROR CK006GH6 32-2
 - BYTE 1 ERROR CK006GJ2 42-2
 - OP REG PARITY ERROR CK006GK6 52-2
 - SDR PARITY ERROR CK006GL6 62-2
 - GATE INPUT ?D CQ005FK6 72-7
 - GATE STATUS TO DISPLAY A CU001EH6 82-61

MACH CK TO INDATA BIT 0.7
 12 -14NS A OR 1S08 104
 72 ZD09A UT
 LA-B3N2A
 INDATA PARITY CK TO DISPLY A
 12 -11NS A OR 1B11 111
 82 ZU13A UT
 LA-B3N2A
 MACH CK TO INDATA BIT 0.0
 2 -09NS A OR 1U06 118
 72 ZD09A UT
 LA-B3N2A
 BYTE X MACH CK TO DISPLAY A
 2 -09NS A OR 1D10 125
 82 ZU13A UT
 LA-B3N2A
 MACH CK TO INDATA BIT 0.5
 62 -49NS A OR 1S07 132
 72 ZD09A UT
 LA-B3N2A
 SDR PARITY CK TO DISPLAY A
 62 -49NS A OR 1U11 139
 82 ZU13A UT
 LA-B3N2A
 MACH CK TO INDATA BIT 0.6
 52 -46NS A OR 1U09 146
 72 ZD09A UT
 LA-B3N2A
 OP REG PARITY CK TO DISPLY A
 52 -46NS A OR 1P10 153
 82 ZU13A UT
 LA-B3N2A
 MACH CK TO INDATA BIT 0.2
 42 -54NS A OR 1S04 160
 72 ZD09A UT
 LA-B3N2A
 BYTE 1 MACH CK TO DISPLAY A
 42 -54NS A OR 1S13 167
 82 ZU13A UT
 LA-B3N2A
 MACH CK TO INDATA BIT 0.4
 32 -51NS A OR 1M08 174
 72 ZD09A UT
 LA-B3N2A
 SAR PARITY CK TO DISPLAY A
 32 -51NS A OR 1U10 181
 82 ZU13A UT
 LA-B3N2A
 MACH CK TO INDATA BIT 0.1
 22 -06NS A OR 1U05 188
 72 ZD09A UT
 LA-B3N2A

BYTE 0 MACH CK TO DISPLAY A
 22 -06NS A OR 1B07 204
 82 ZU13A UT
 LA-B3N2A

000 CK004
 125 + BYTE X MACH CK TO DISPLAY A DA2 LAP011
 204 + BYTE 0 MACH CK TO DISPLAY A DC2 LAP012
 167 + BYTE 1 MACH CK TO DISPLAY A DE2 LAP012
 181 + SAR PARITY CK TO DISPLAY A DG2 LAP012
 139 + SDR PARITY CK TO DISPLAY A DJ2 LAP013
 153 + OP REG PARITY CK TO DISPLY A DL2 LAP013
 111 + INDATA PARITY CK TO DISPLY A DN2 LAP012
 118 + MACH CK TO INDATA BIT 0.0 EB2 LCU011
 188 + MACH CK TO INDATA BIT 0.1 EC2 LCU011
 160 + MACH CK TO INDATA BIT 0.2 ED2 LCU011
 174 + MACH CK TO INDATA BIT 0.4 EF2 LCU011
 132 + MACH CK TO INDATA BIT 0.5 EG2 LCU011
 146 + MACH CK TO INDATA BIT 0.6 EH2 LCU011
 104 + MACH CK TO INDATA BIT 0.7 EJ2 LCU011

LOC. TYPE
 A-B3N2 6819

CK004
 000

ERROR DETECTION			
-E.C.-HISTORY-	E	MACH.3705	
	FRAME	01	
DATE	LAST EC	IBM CORP.SCD	CK004
10-14-80	344270	P.No. 1850851	000

- PROG LEV 1 PROG CHECK CK006AM6 2-2
 - CYCLE COUNTER ERROR CK006AM6 12-1
 - CCU CLOCK ERROR CK006BM6 22-1
 - CSB CLOCK ERROR CK006CM6 32-1
 - MACH CHECK CK006GG2 42-1
 - CLOCK ERROR CK006GM6 52-1
 - GATE INPUT 7D CQ005FK6 62-5
 - GATE STATUS TO DISPLAY A CU001EH6 72-2

CCU CYC CLK CK
 TO INDATA 1.0
 12 -29A A OR P04 104
 62 ZD09A UT
 LA-B3N2J
 L1 PROG CHECK
 TO INDATA 0.3
 2 -26A A OR U04 111
 62 ZD09A UT
 LA-B3N2J
 L1 PROG CHK
 TO DISPLAY A
 2 -27A A OR M02 118
 72 ZU13A UT
 LA-B3N2J
 CLOCK CHECK
 TO DISPLAY A
 52 -14A A OR B13 125
 72 ZU13A UT
 LA-B3N2J
 CCU CHECK TO
 INDATA BIT 1.5
 42 ZJ06A A OR M13 132
 62 ZD09A UT
 LA-B3N2J
 CSB CLK CK TO
 INDATA BIT 1.6
 32 -27A A OR G10 139
 62 ZD09A UT
 LA-B3N2J
 CCU CLK CK TO
 INDATA BIT 1.7
 22 -46A A OR S05 146
 62 ZD09A UT
 LA-B3N2J

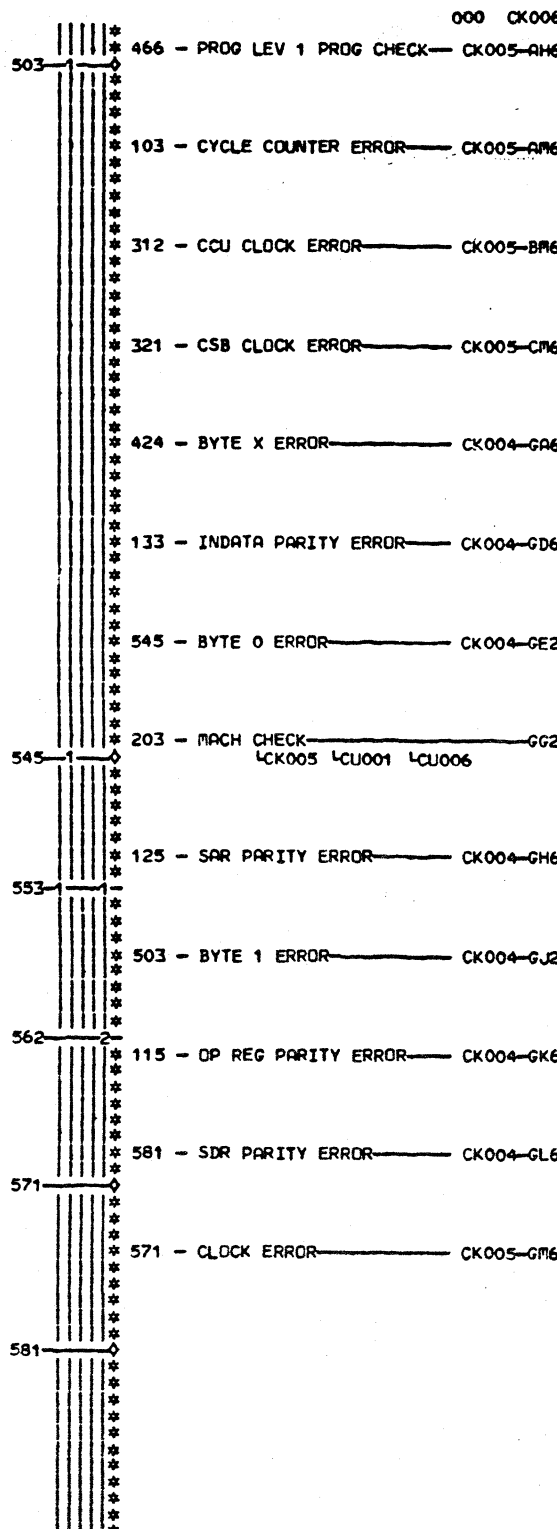
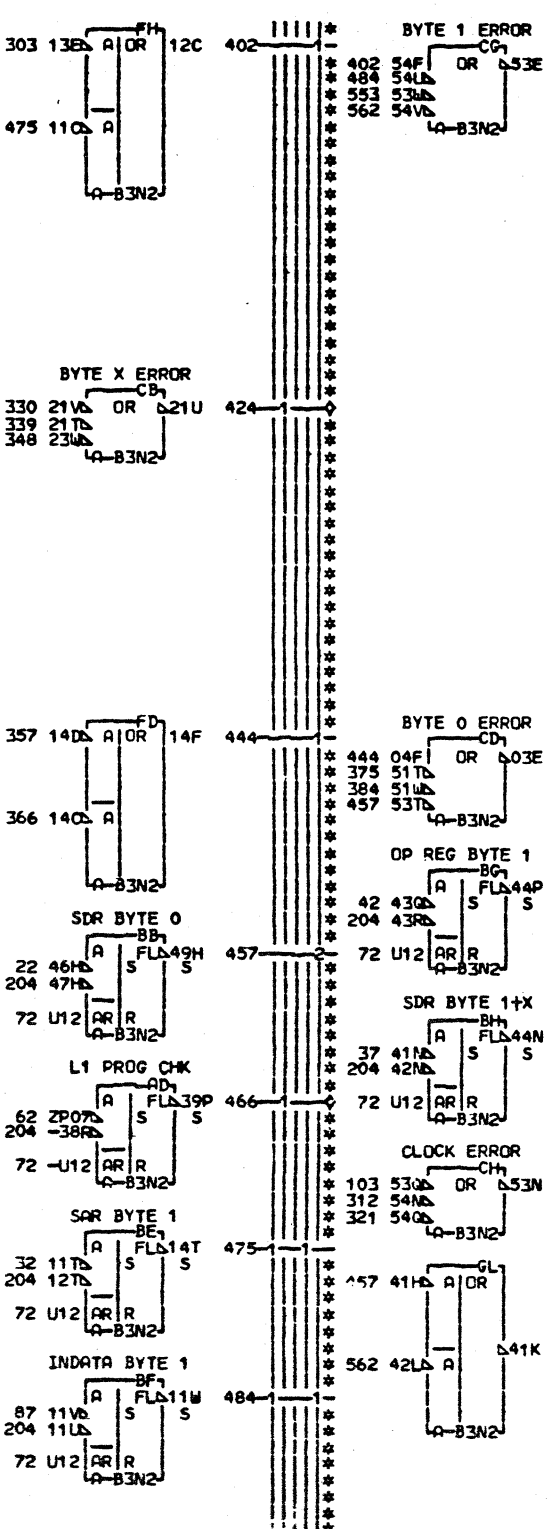
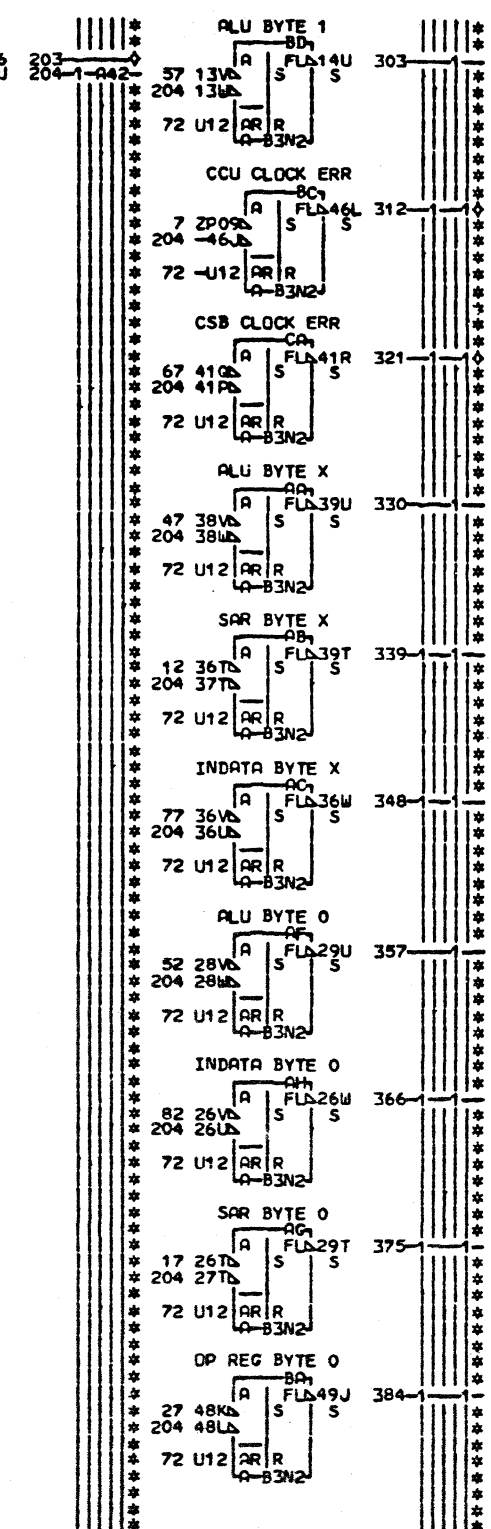
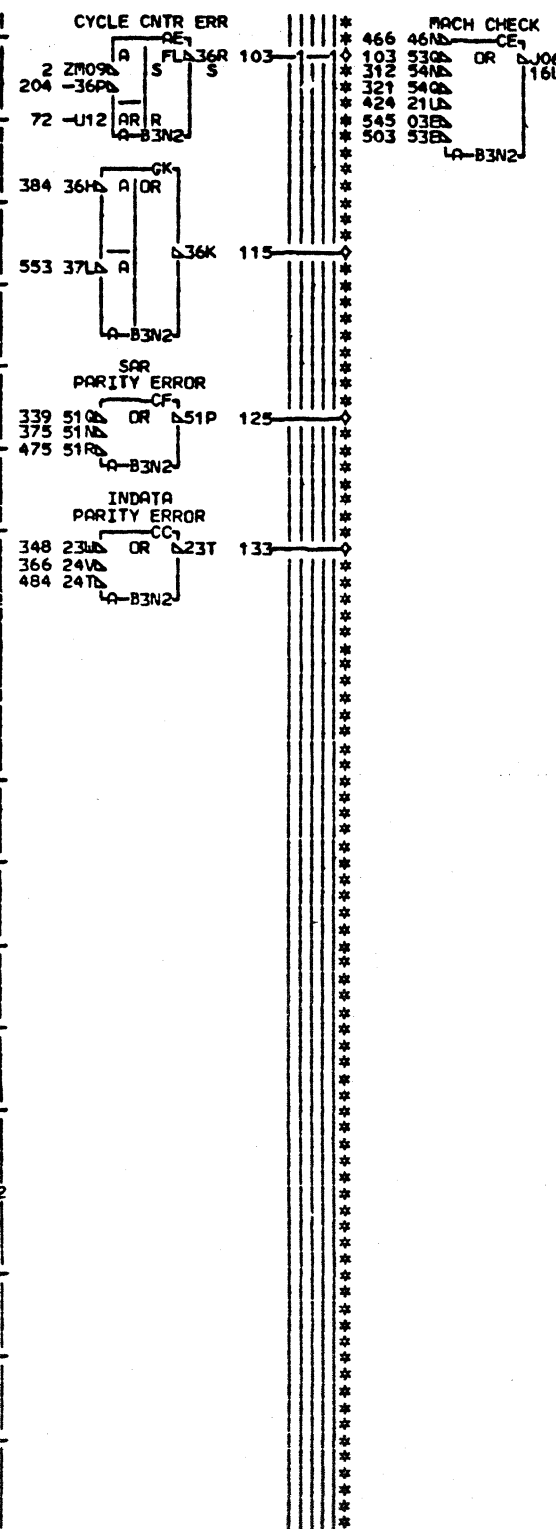
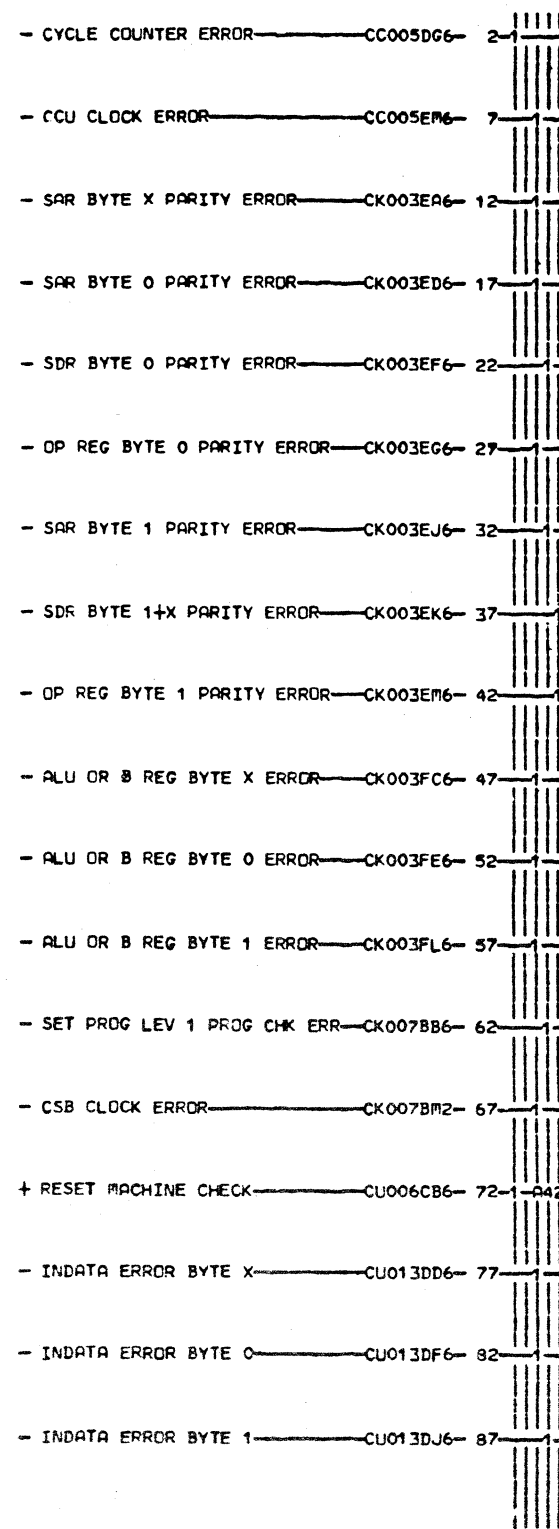
000 CK005

118 + L1 PROG CHK TO DISPLAY A DG2
 LAP013
 125 + CLOCK CHECK TO DISPLAY A DH2
 LAP013
 104 + CCU CYC CLK CK TO INDATA 1.0 EC2
 LCU012
 111 + L1 PROG CHECK TO INDATA 0.3 EG2
 LCU011
 132 + CCU CHECK TO INDATA BIT 1.5 EH2
 LCU012
 139 + CSB CLK CK TO INDATA BIT 1.6 EK2
 LCU013
 146 + CCU CLK CK TO INDATA BIT 1.7 EL2
 LCU013

LOC. TYPE
 A-B3N2 6819

CK005
 000

ERROR DETECTION	
E.C. HISTORY 344270	E. MACH. 3705 FRAME 01
DATE LAST EC 06-02-81 344828	IBM CORP. SCD CK005 P.N. 1850852 000

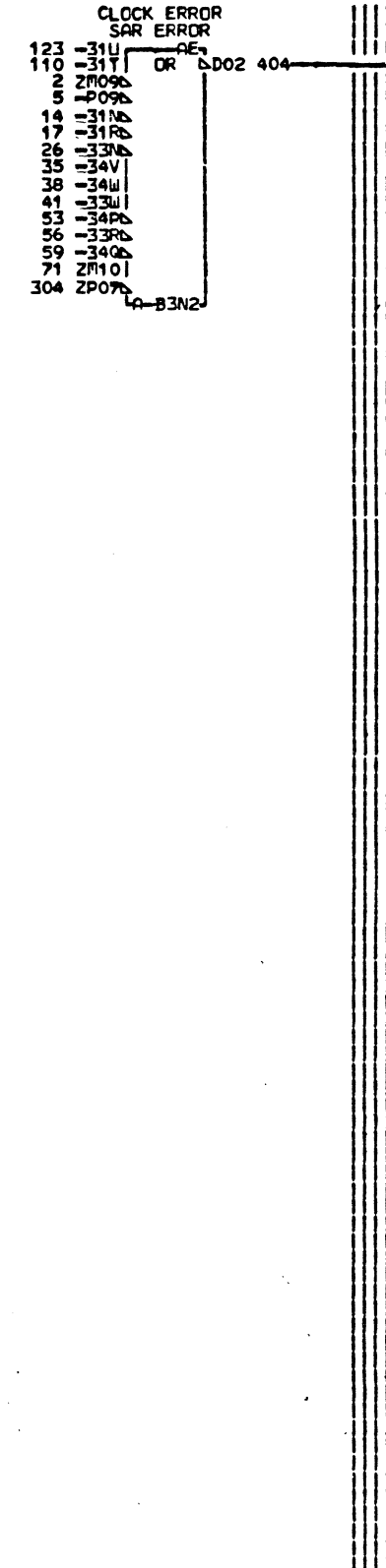
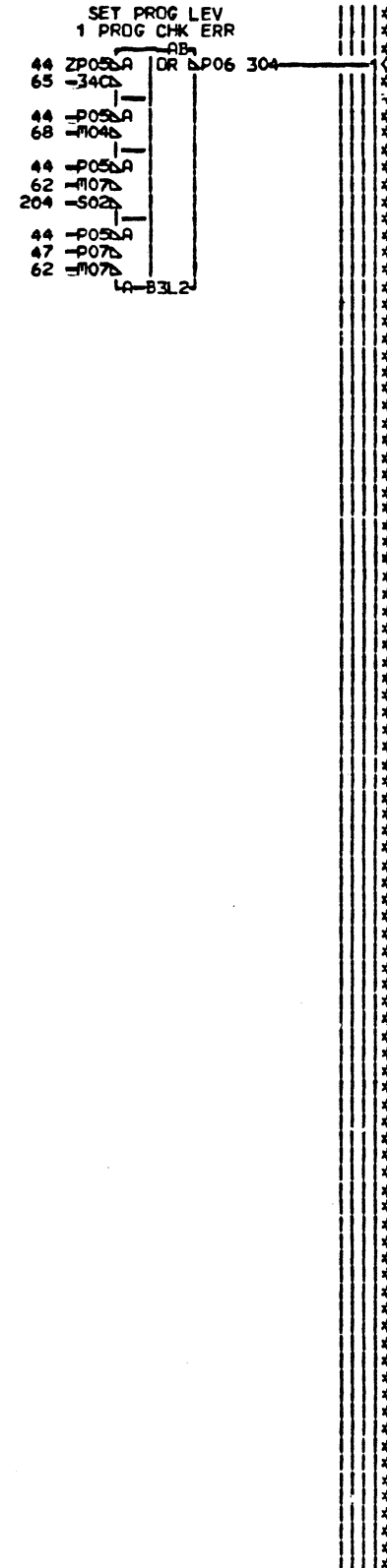
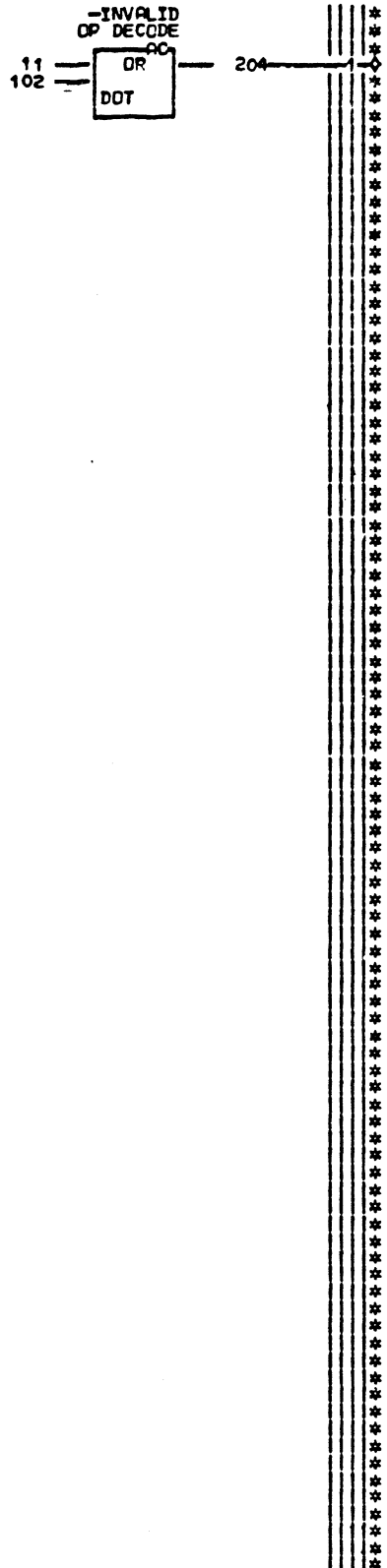
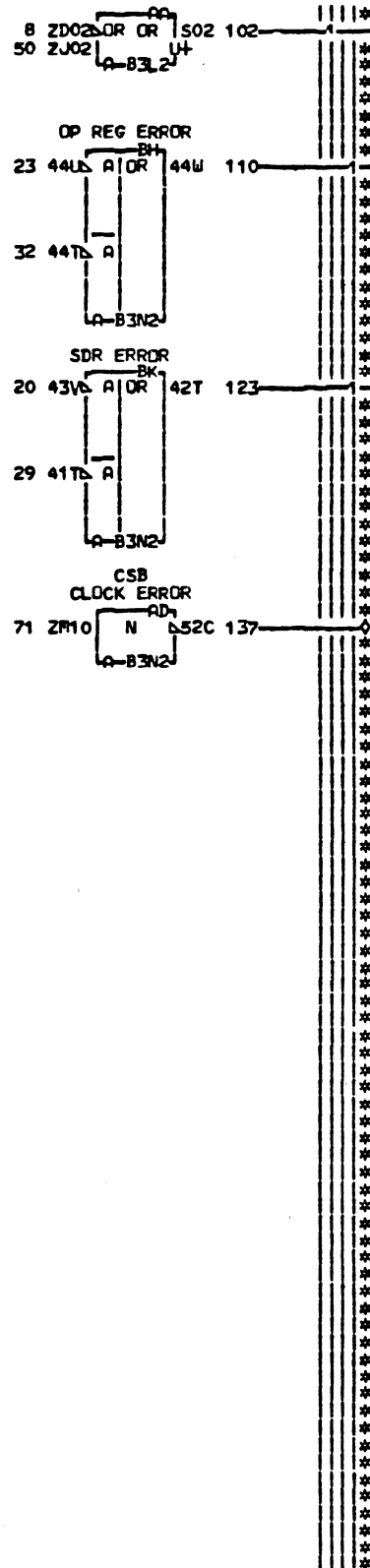


LOC. TYPE
A-B3N2 6819

ERROR REGISTER	
E.C. - HISTORY	E. MACH. 3705
DATE LAST EC	FRAME 01
10-14-80 344270	IBM CORP. SCD CK006
	P.No. 1850853 000

CK006
000

- CYCLE COUNTER ERROR — CC005DG6 — 2
 - CCU CLOCK ERROR — CC005EM6 — 5
 - CYCLE STEAL AB — CC008AE6 — 8
 - INVALID OP DECODE CONDITION — CD004AH2 — 11
 - SAR BYTE X PARITY ERROR — CK003EA6 — 14
 - SAR BYTE 0 PARITY ERROR — CK003ED6 — 17
 - SDR BYTE 0 PARITY ERROR — CK003EF6 — 20
 - OP REG BYTE 0 PARITY ERROR — CK003EG6 — 23
 - SAR BYTE 1 PARITY ERROR — CK003EJ6 — 26
 - SDR BYTE 1+X PARITY ERROR — CK003EK6 — 29
 - OP REG BYTE 1 PARITY ERROR — CK003EM6 — 32
 + ALU OR B REG BYTE X ERROR — CK003FC2 — 35
 + ALU OR B REG BYTE 0 ERROR — CK003FE2 — 38
 + ALU OR B REG BYTE 1 ERROR — CK003FL2 — 41
 - PROG LEV 1 ENTERED — CP003FC2 — 44
 ALWAYS PLUS — CS002BK4 — 47
 + BAD ADDRESS — CS002DK6 — 50
 - INDATA ERROR BYTE X — CU013DD6 — 53
 - INDATA ERROR BYTE 0 — CU013DF6 — 56
 - INDATA ERROR BYTE 1 — CU013DJ6 — 59
 - SAMPLE EXCEPTION — CU014AH6 — 62
 - SET I-O CHECK — CU014EB2 — 65
 - SET PROTECT CHK — CV061EK2 — 68
 + CSB CLOCK ERROR — CX011GL2 — 71



000 CK007
 304 - SET PROG LEV 1 PRG CHK ERR — BB6 LCK006
 204 - INVALID OP DECODE — CU014-BC4
 137 - CSB CLOCK ERROR — CK006-BM2
 404 - SET MACH CHECK — CU006-DK2

LCC. TYPE
 A-B3L2 6823
 A-B3N2 6819

CK007
 000

SECOND ERROR DETECTION AND PRG LEV 1 PRG CHK DETECTION	
E-C-HISTORY 344270	E-MACH-3705
DATE LAST EC 06-02-81 344828	FRAME 01 IBM CORP-SCD CK007 P.N. 1850854 000

- I1 BC TIME — CC003BH6 — 2-2
 - I1 CD TIME — CC003BJ6 — 8-2
 - I2 BC TIME — CC003CH6 — 14-2
 - I2 CD TIME — CC003CJ6 — 20-2
 - I2 XA TIME — CC003CL6 — 26-2
 - I3 BC TIME — CC003DH6 — 32-2
 - LA INST — CD001EM6 — 38-2
 - L INST — CD003AH6 — 44-2
 - INST GROUP 1 — CD004BB2 — 50-2
 - INST GROUP 2 — CD004BD2 — 56-2
 - INST GROUP 3 — CD004BF2 — 62-2
 - INST GROUP 4 — CD004BL2 — 68-2
 - DP REG BIT 0.5 — DP991GB6 — 74-6
 - DP REG BIT 0.6 — DP991GE6 — 80-6

PRE SELECT LS
 R1 REG 2+3+6+7
 8 ZD03A
 50 ZB03A
 80 ZB02A
 20 ZB13A
 38 ZB09A
 80 ZB02A
 32 ZB04A
 44 ZM10A
 80 ZB02A
 2 ZD13A
 62 ZD02A
 80 ZB02A
 26 ZD07A
 68 ZB07A
 80 ZB02A
 14 ZD10A
 56 ZP09A
 80 ZB02A
 A-B3K2

PRE SELECT LS
 R1 REG 4+5+6+7
 8 ZD03A
 50 ZB03A
 74 ZB05A
 20 ZB13A
 38 ZB09A
 74 ZB05A
 32 ZB04A
 44 ZM10A
 74 ZB05A
 2 ZD13A
 62 ZD02A
 74 ZB05A
 26 ZD07A
 68 ZB07A
 74 ZB05A
 14 ZD10A
 56 ZP09A
 74 ZB05A
 A-B3K2

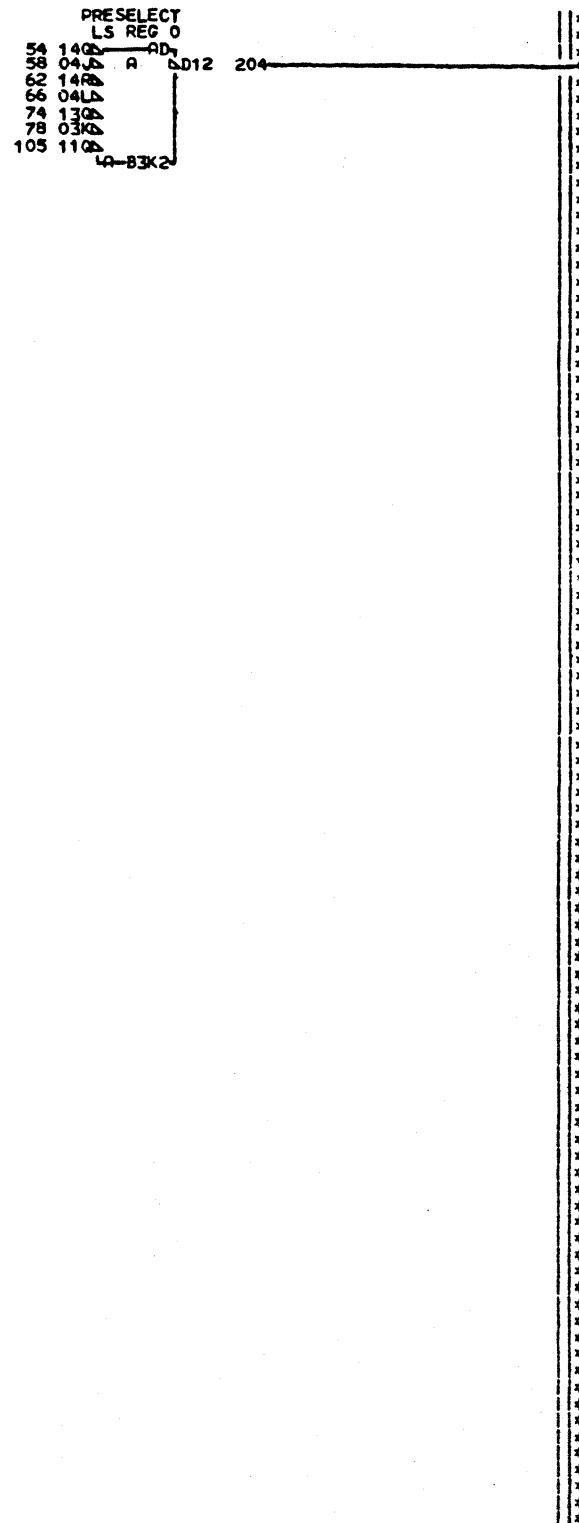
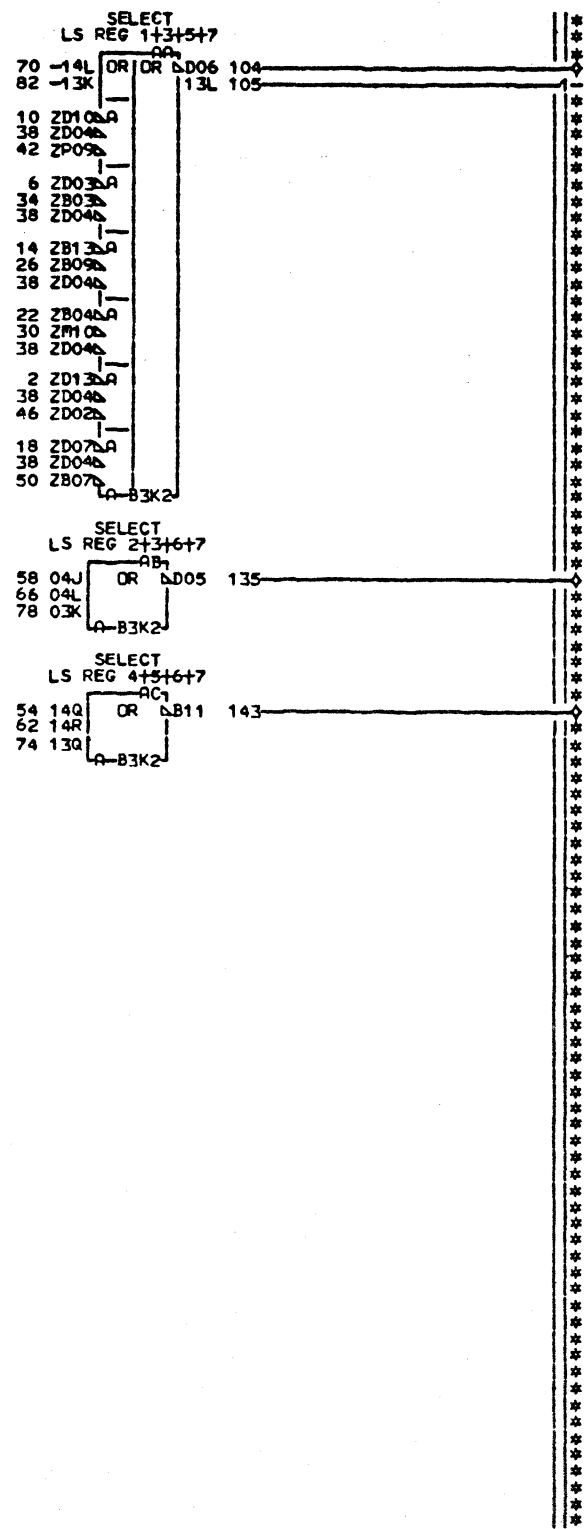
000 CL001
 132 + PRE SELECT LS R1 REG 4+5+6+7 — DE4
 CL002
 104 + PRE SELECT LS R1 REG 2+3+6+7 — GH4
 CL002

CL001
 000

LOC. TYPE
 A-B3K2 6816

LOCAL STORE CONTROL	
R1 SELECT GENERATION	
E-C-HISTORY	E-MACH.3705
FRAME	01
DATE LAST EC	IBM CORP.SCD CL001
10-14-80 344270	P.No. 1850855 000

- I1 BC TIME — CC003BH6 — 2-
 - I1 CD TIME — CC003BJ6 — 6-
 - I2 BC TIME — CC003CH6 — 10-
 - I2 CD TIME — CC003CJ6 — 14-
 - I2 XA TIME — CC003CL6 — 18-
 - I3 BC TIME — CC003DH6 — 22-
 - LA INST — CD001EM6 — 26-
 - L INST — CD003AH6 — 30-
 - INST GROUP 1 — CD004BB2 — 34-
 - INST GROUP 5 — CD004BC2 — 38-
 - INST GROUP 2 — CD004BD2 — 42-
 - INST GROUP 3 — CD004BF2 — 46-
 - INST GROUP 4 — CD004BL2 — 50-
 + PRE SELECT LS R1 REG 4+5+6+7 — CL001DE4 — 54-
 + PRE SELECT LS R1 REG 2+3+6+7 — CL001GH4 — 58-
 + PRE SELECT LS R2 REG 4+5+6+7 — CL003CB4 — 62-
 + PRE SELECT LS R2 REG 2+3+6+7 — CL003GF4 — 66-
 + PRE SELECT LS R2 REG 1+3+5+7 — CL003GK4 — 70-
 + PRE SELECT LS E2 REG 4+5+6+7 — CL004GJ2 — 74-
 + PRE SELECT LS E2 REG 2+3+6+7 — CL004GL2 — 78-
 + PRE SELECT LS E2 REG 1+3+5+7 — CL004GN2 — 82-



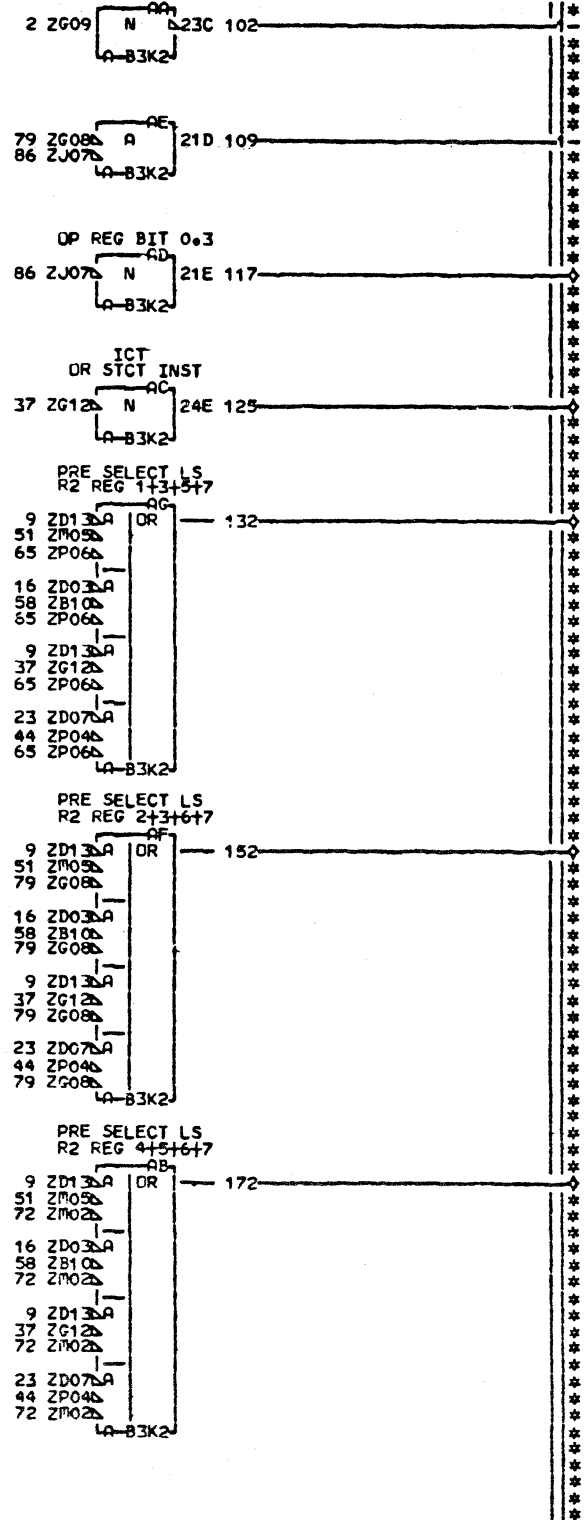
000 CL002
 104 - SELECT LS REG 1+3+5+7 — CC006-CK2
 135 - SELECT LS REG 2+3+6+7 — CC006-CL2
 143 - SELECT LS REG 4+5+6+7 — CC006-CP2
 204 - PRESELECT LS REG 0 — CA001-EH6

LOC. TYPE
 A-B3K2 6816

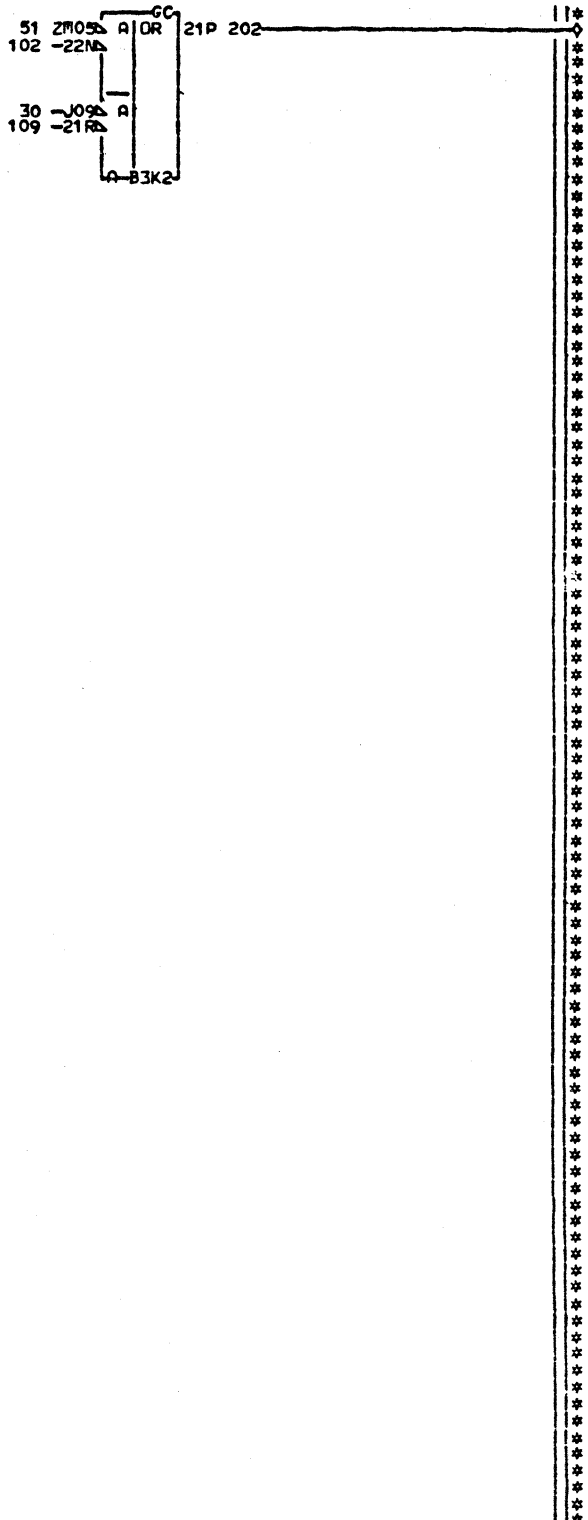
CL002
 000

LOCAL STORE CONTROL
 R2 SEL GENERATION AND REG SEL
 E.C.-HISTORY — E-RACH-3705
 FRAME 01
 DATE LAST EC 10-14-80 344270
 P.N. 1850856
 IBM CORP. SCD CL002
 000

- OP XXXX XXXX X011 XXXX CA003BB6 2-1
 - I1 BC TIME CC003BH6 9-6
 - I1 CD TIME CC003BJ6 16-3
 - I2 XA TIME CC003CL6 23-3
 - RI INST TYPE CD002CF6 30
 - ICT OR STCT INST CD003AA6 37-4
 - BALR INST CD003BM6 44-3
 - RR INST-BYTE+HW+ADDR CD004AB2 51-31
 - LOAD OR STORE INST TYPE CD004AE2 58-3
 - RR INST-BYTE+OP REG BIT 0.3 CD004CK2 65-4
 - OP REG BIT 0.1 DN004GD2 72-4
 - OP REG BIT 0.2 DN004GF2 79-5
 - OP REG BIT 0.3 DN004GH2 86-2



LOC. TYPE
 A-B3K2 6816

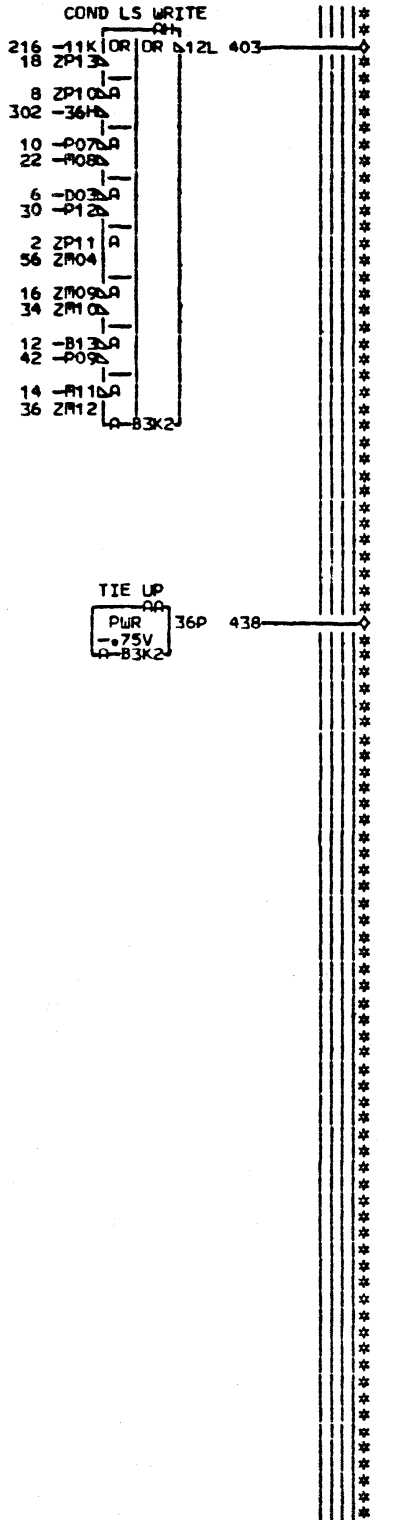
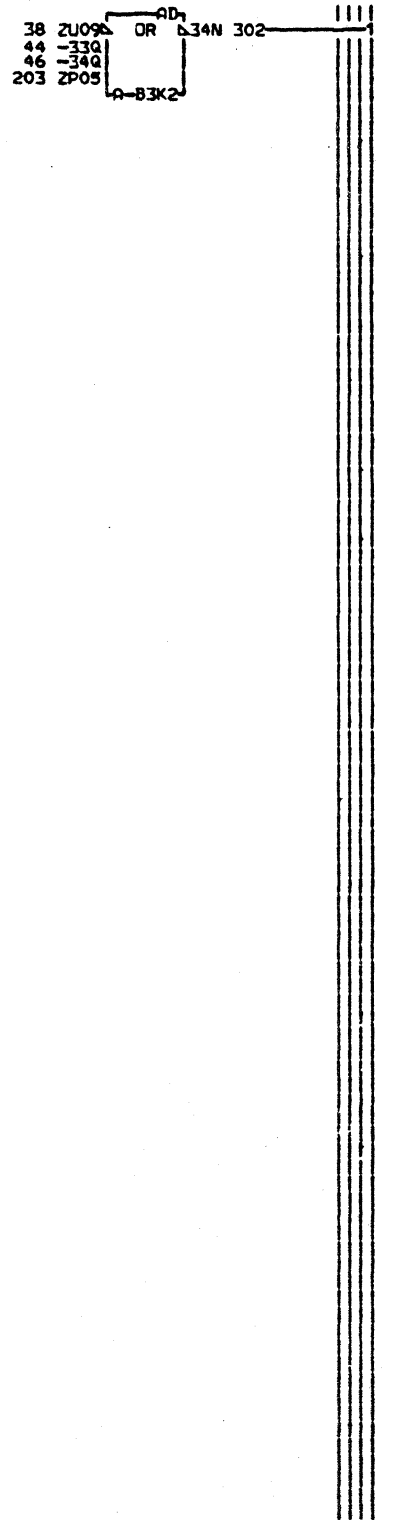
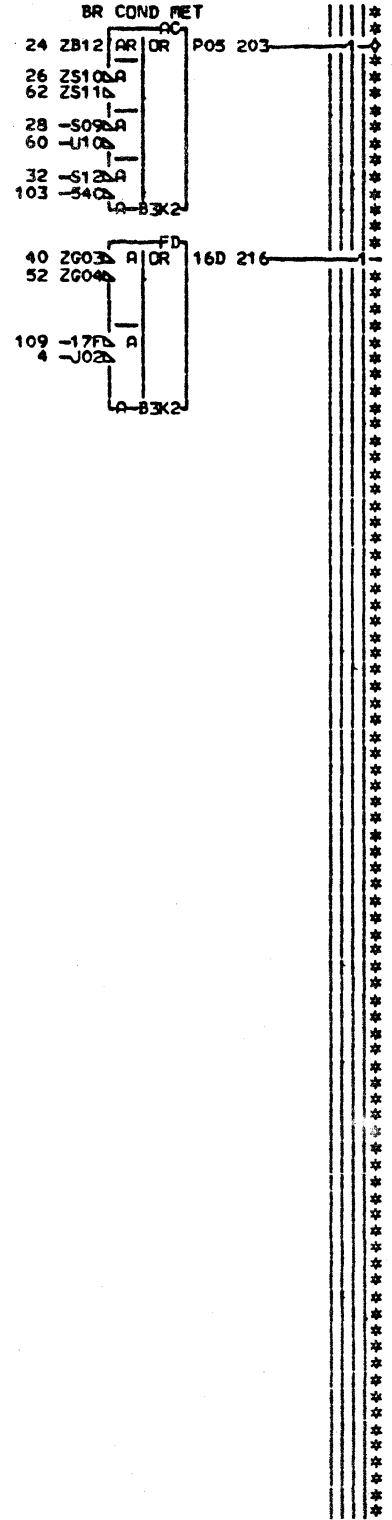
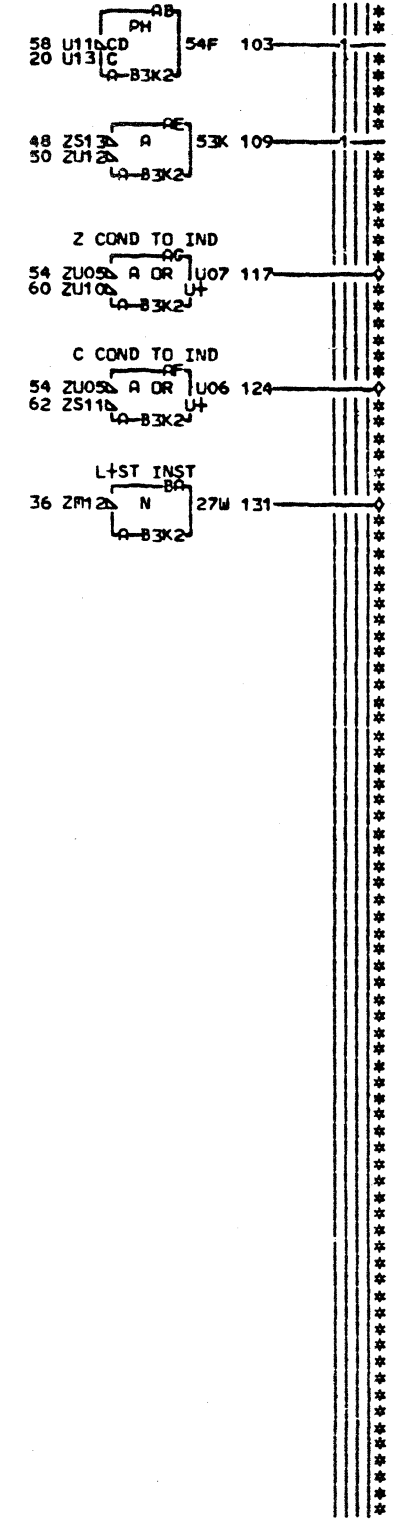


000 CL003
 172 + PRE SELECT LS R2 REG 4+5+6+7 CB4
 LCL002
 125 + ICT OR STCT INST CL005-CK2
 117 + OP REG BIT 0.3 CL004-DD2
 202 + RR WRITE LS CONDITIONS CL005-GC2
 152 + PRE SELECT LS R2 REG 2+3+6+7 GF4
 LCL002
 132 + PRE SELECT LS R2 REG 1+3+5+7 GK4
 LCL002

CL003
 000

LOCAL STORE CONTROL		R2 SELECT GENERATION	
E.C. HISTORY		E. PACH. 3705	
DATE	LAST EC	FRAME	01
10-14-80	344270	IBM CORP. SCD	CL003
		P.N. 1850857	000

- AB TIME CC001EC2- 2
 - I1 AB TIME CC003BB6- 4
 - I1 CD TIME CC003BJ6- 6
 - I1 DA TIME CC003BK6- 8
 - I2 AB TIME CC003CB6- 10
 - I2 CD TIME CC003CJ6- 12
 - I2 DA TIME CC003CK6- 14
 - I3 CD TIME CC003DJ6- 16
 - I3 DA TIME CC003DK6- 18
 + T1 TIME CC006ED2- 20
 - BAL+LA INST CD001DK6- 22
 + B INST CD002CB2- 24
 - BCL INST CD002CC6- 26
 - BZL INST CD002CD6- 28
 - BCT INST CD002CG6- 30
 - BB+BCT INST CD002DF2- 32
 - L INST CD003AH6- 34
 - L+ST INST CD003CG2- 36
 - INPUT + OUTPUT INST CD003CK6- 38
 - LS I-O REG ADDR CD004AK6- 40
 - INST GROUP 2 CD004BD2- 42
 + ICT OR STCT INST CL003CK2- 44
 + RR WRITE LS CONDITIONS CL003GC2- 46
 - PROG LEV 1 NEXT CP002EA2- 48
 - VIRGIN LEVEL CP003BL6- 50
 - COND LS REG WRITE CS003EF6- 52
 - GATE STATUS TO DISPLAY B CU001EJ6- 54
 + RESET CU010FM2- 56
 - 2 BUS BYTE 0 OR 0 AND 1 ZERO C2001ED6- 58
 - Z COND C2004EB2- 60
 - C COND C2005DC2- 62

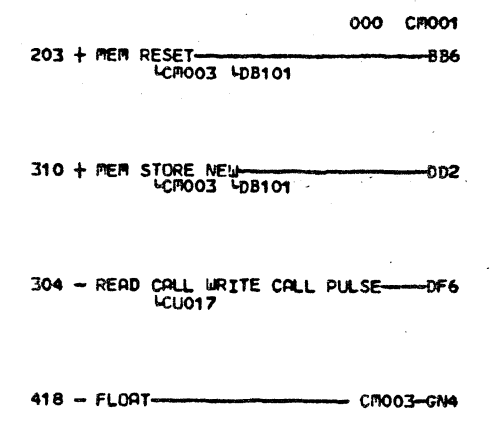
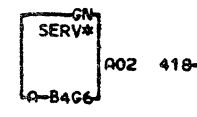
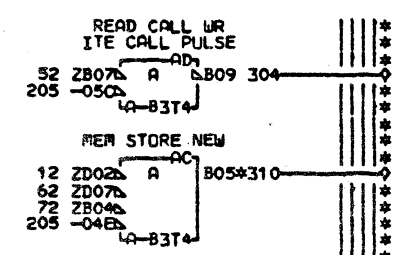
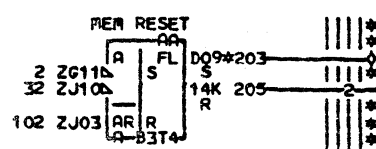
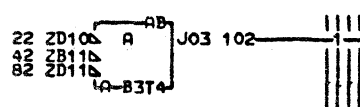
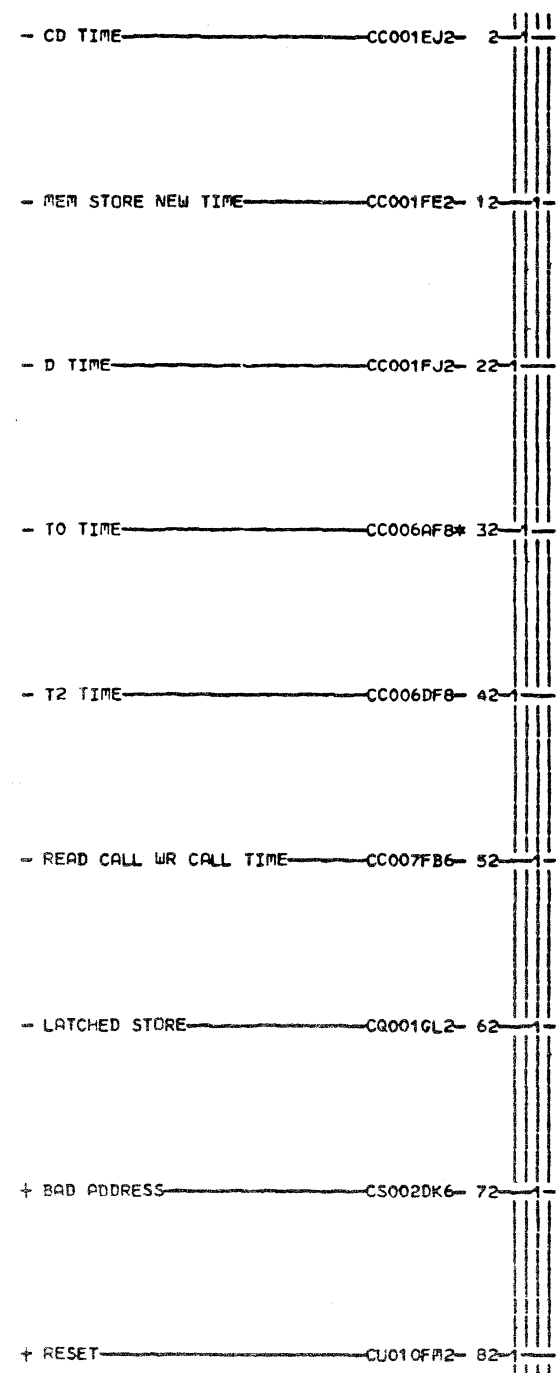


000 CL005
 438 + TIE UP CL004-AC4
 203 + BR COND MET CS003-CC6
 124 + C COND TO IND AP014-EA2
 117 + Z COND TO IND AP014-EB2
 403 - COND LS WRITE CS007-FH2
 131 + L+ST INST CL004-HD2

LDC. TYPE
A-B3K2 6816

CL005
000

LOCAL STORE CONTROL
 WRITE LS GENERATION
 E.C.-HISTORY E. PACH. 3705
 FRAME 01
 IBM CORP. SCD CL005
 DATE LAST EC
 10-14-80 344270 P.N. 1850659 000

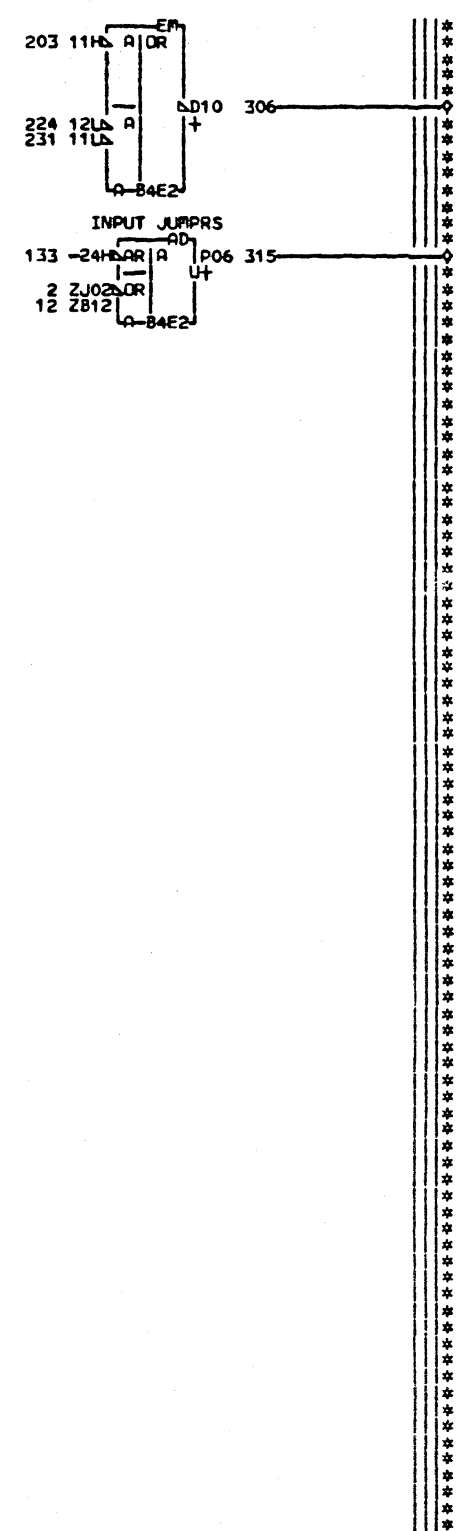
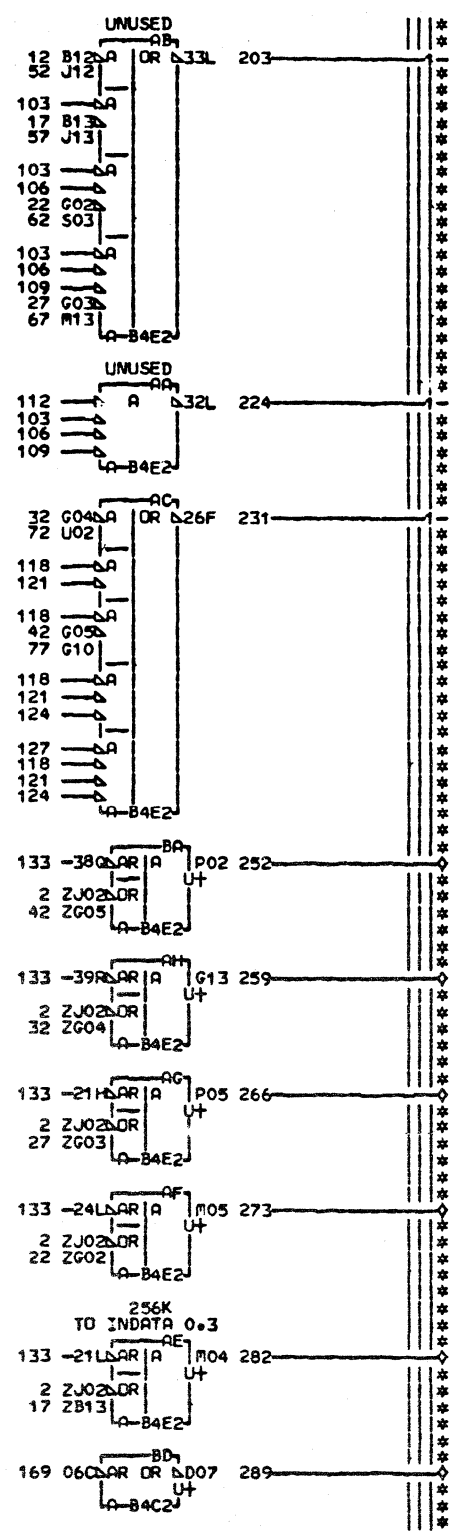
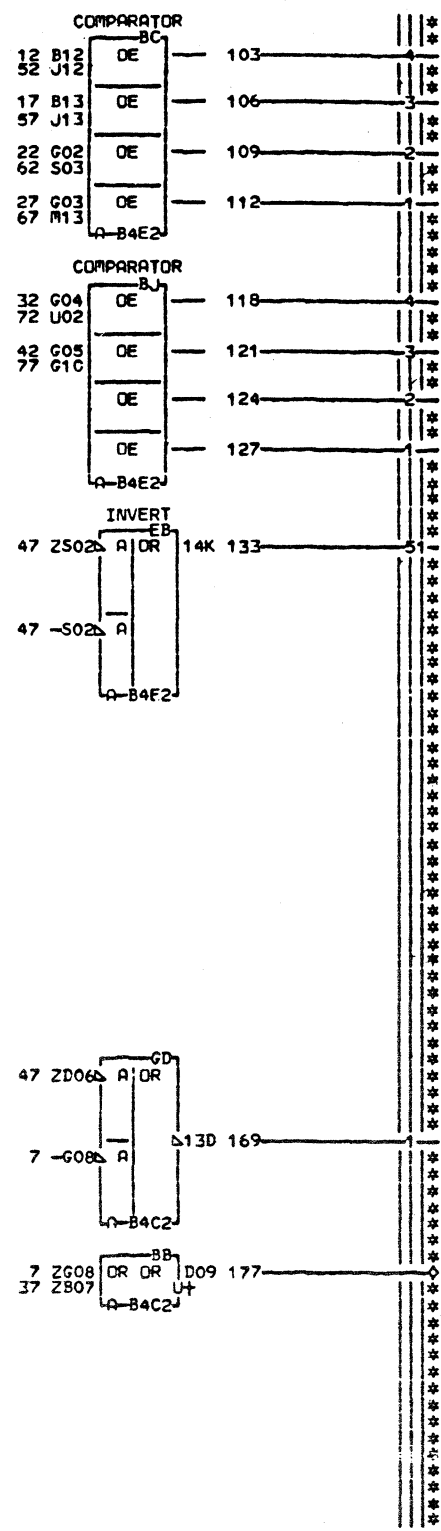
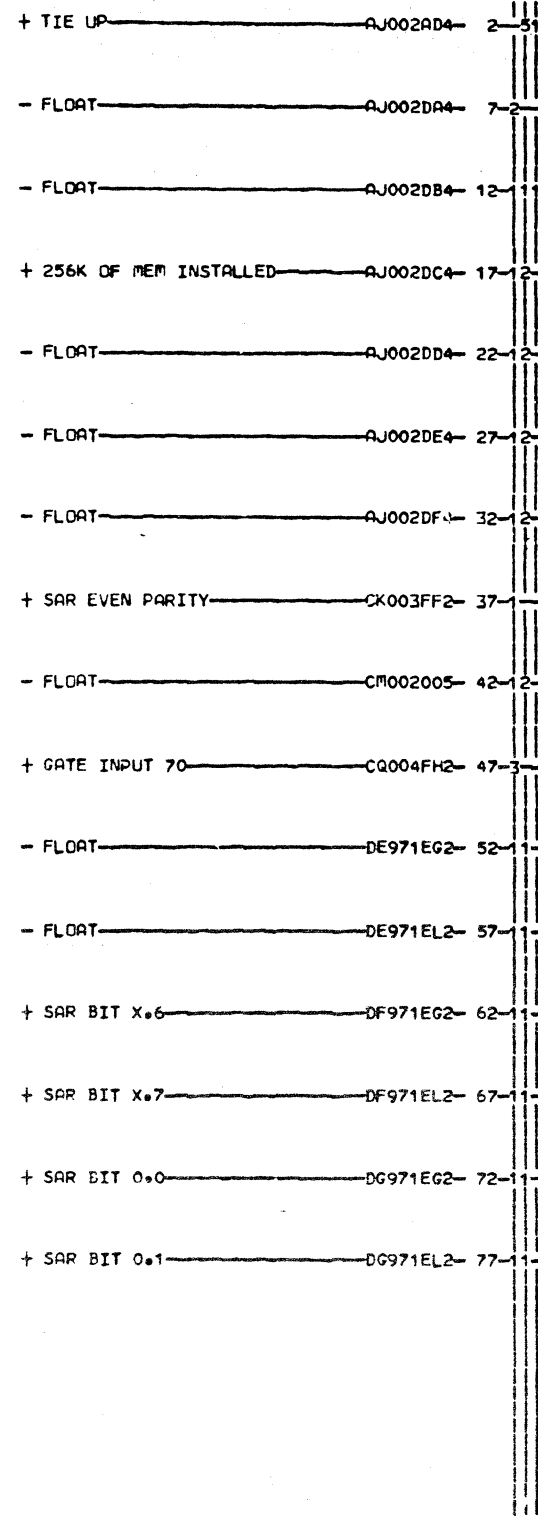


EDGE CONN.
 32 RESISTOR
 A-B3T4J10
 203 A-B3F1D11
 01A-B4F6D02
 310 A-B3F1E13
 01A-B4F6E04

LOC. TYPE
 A-B3T4 6824

CM001
 000

MEMORY CONTROLS CCU	
E.C. HISTORY	E. MACH. 3705
DATE	LAST EC
10-14-80	344270
FRAME	01
IBM CORP. SCD	CM001
P.N. 1850860	000



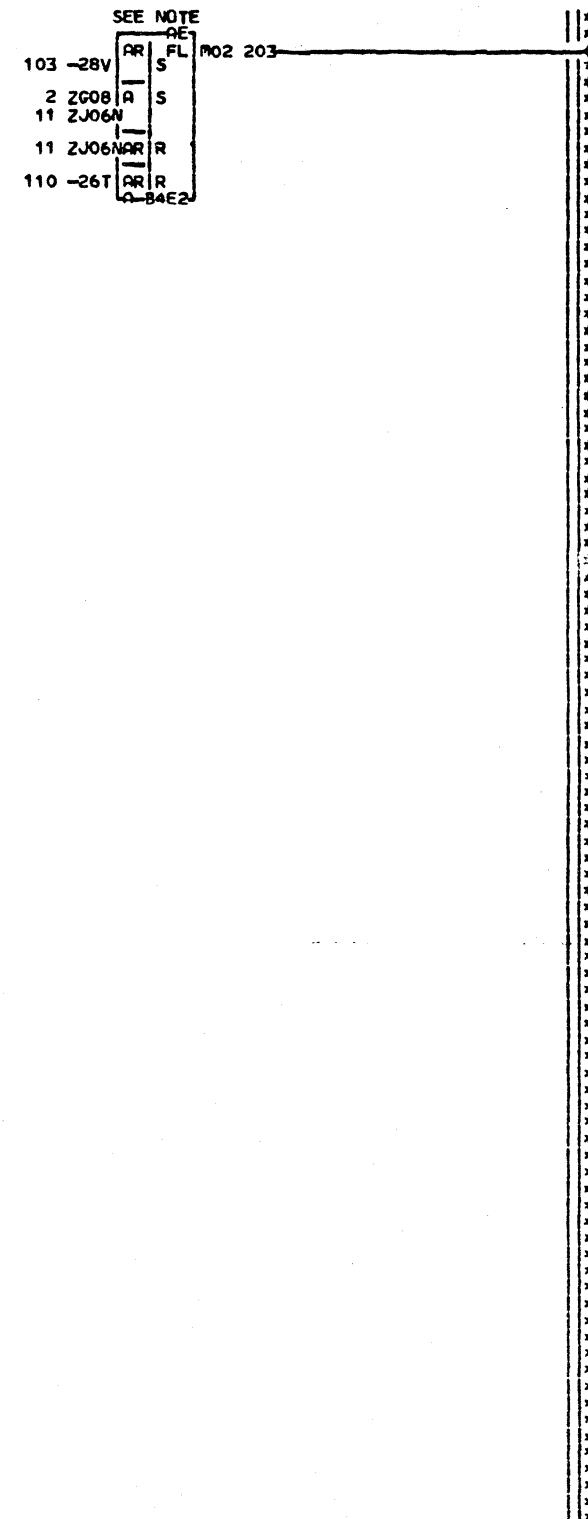
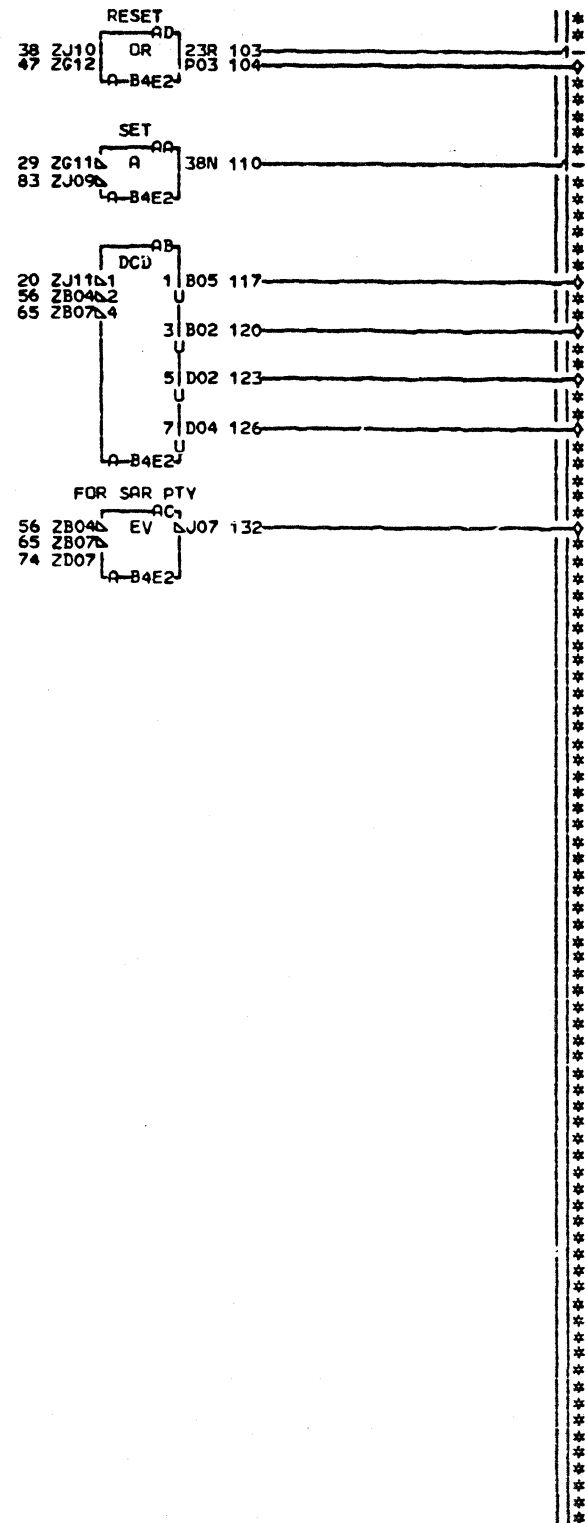
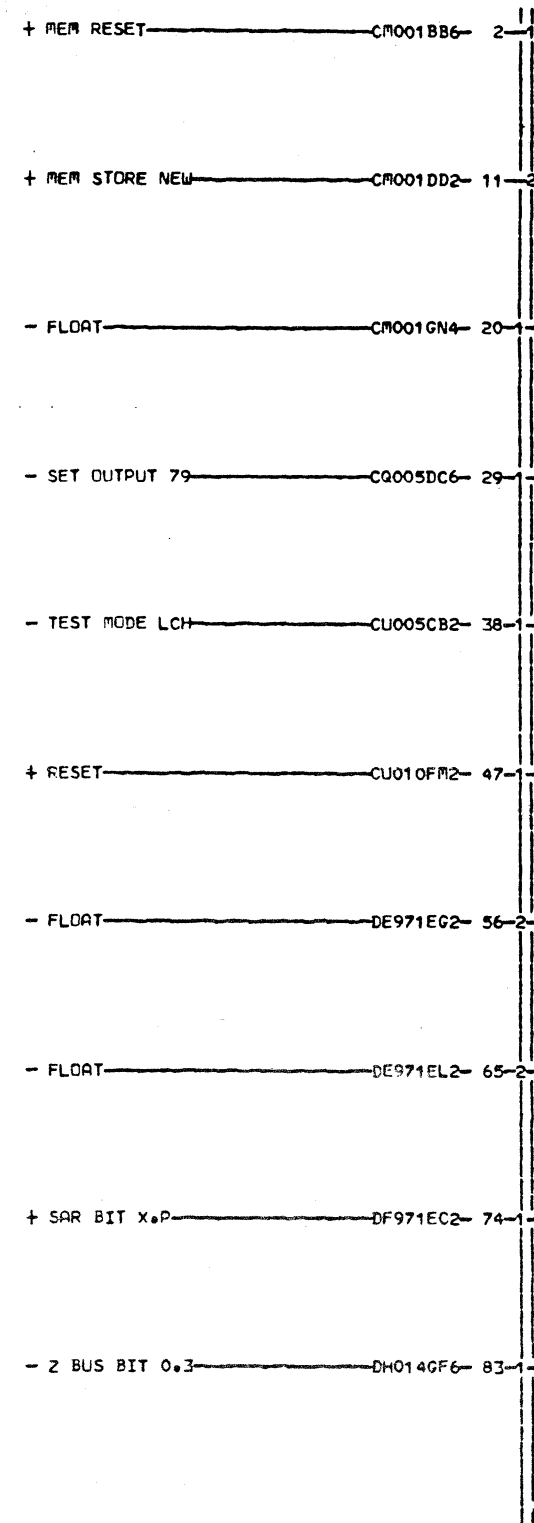
000 CM002

306 ALWAYS PLUS — CS002-EM6
 315 ALWAYS MINUS AT IN X 70 TIME — FC2
 CU011
 282 + 256K TO INDATA 0.3 — CU011-FD2
 273 ALWAYS MINUS AT IN X 70 TIME — FE2
 CU011
 266 ALWAYS MINUS AT IN X 70 TIME — FF2
 CU011
 259 ALWAYS MINUS AT IN X 70 TIME — FG2
 CU011
 252 ALWAYS MINUS AT IN X 70 TIME — FH2
 CU011
 177 ALWAYS PLUS — CS002-GR6
 289 ALWAYS MINUS AT IN 70 TIME — GC6
 CU011

LDC. TYPE
 A-B4C2 AB93
 A-B4E2 AB69

CM002
 000

CC CONTROL	FRAME 01
ADDR EXCPT AND GATE MEM SIZE	IBR CORP.SCD CM002
E.C. HISTORY	PACH.3705
DATE LAST EC	P.No. 1850861 000
10-14-80 344270	



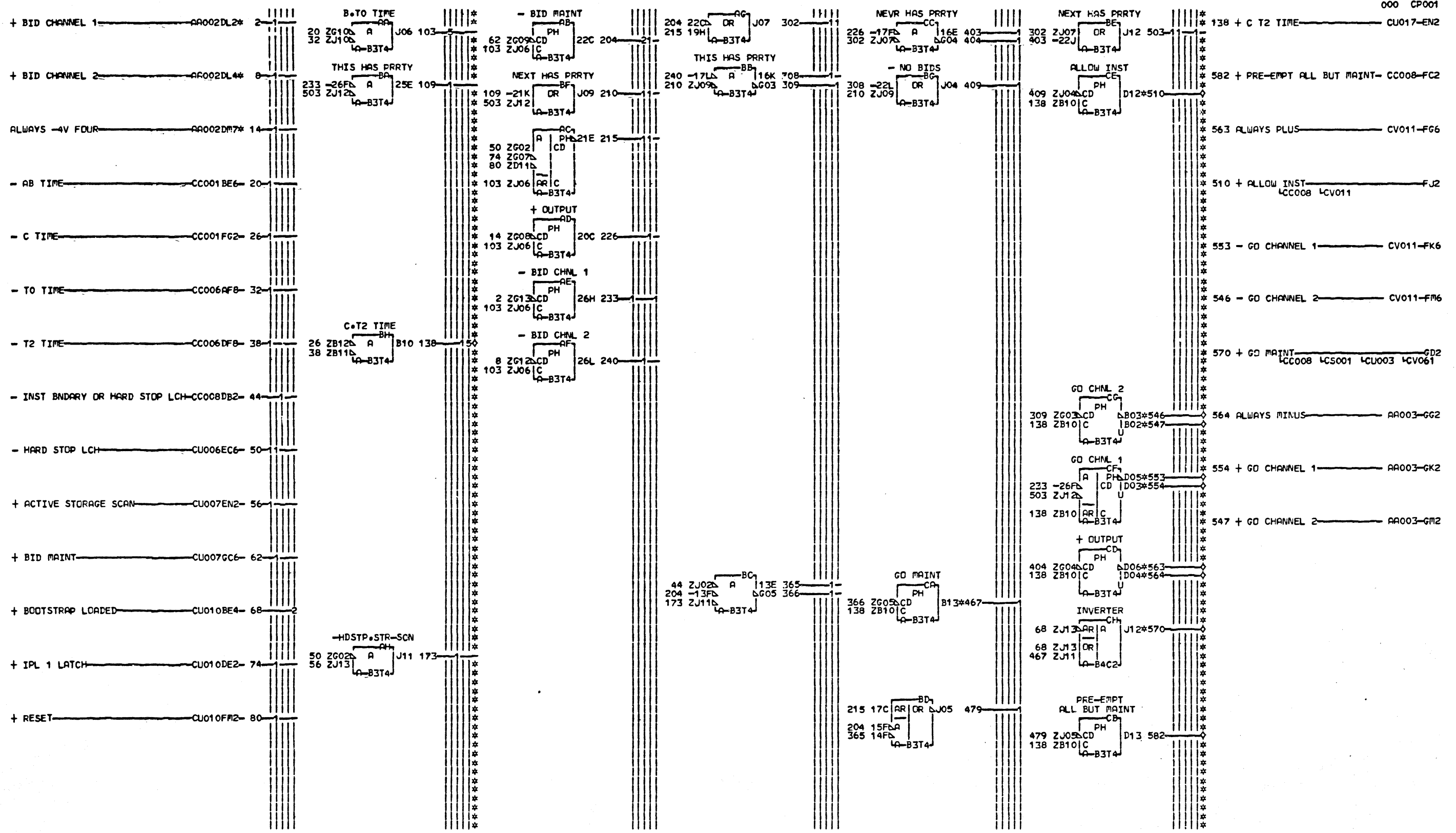
- 000 CM003
- 117 GROUND LEVEL — DV001-DB2
- 123 GROUND LEVEL — DU001-DC2
- 120 GROUND LEVEL — DT001-DD2
- 126 GROUND LEVEL — DS001-DE2
- 132 + SAR X.P REDRIVEN — CV001-DG2
- 104 + RESET MEMORY DIAG REC — DB101-GM6
- 203 - ALLOW SET MEMORY DIAG REC — SA2
LDB101

LOC. TYPE
A-B4E2 AB89

NOTE.
 OUTPUT-79-BIT-0.3 SETS LCH.
 RESET OR NOT-TEST-MODE RESETS
 LCH. MEM-RESET RESETS LCH
 IF IT FOLLOWS A MEMORY-STORE-
 NEW.

CM003
000

READ CALL WRITE CALL AND SAR BYTE X AND 1 PARITY	
E.C.—HISTORY—E	RACH.3705
FRAME	01
DATE LAST EC	IBM CORP.SCD CM003
10-14-80 344270	P.N. 1850862 000



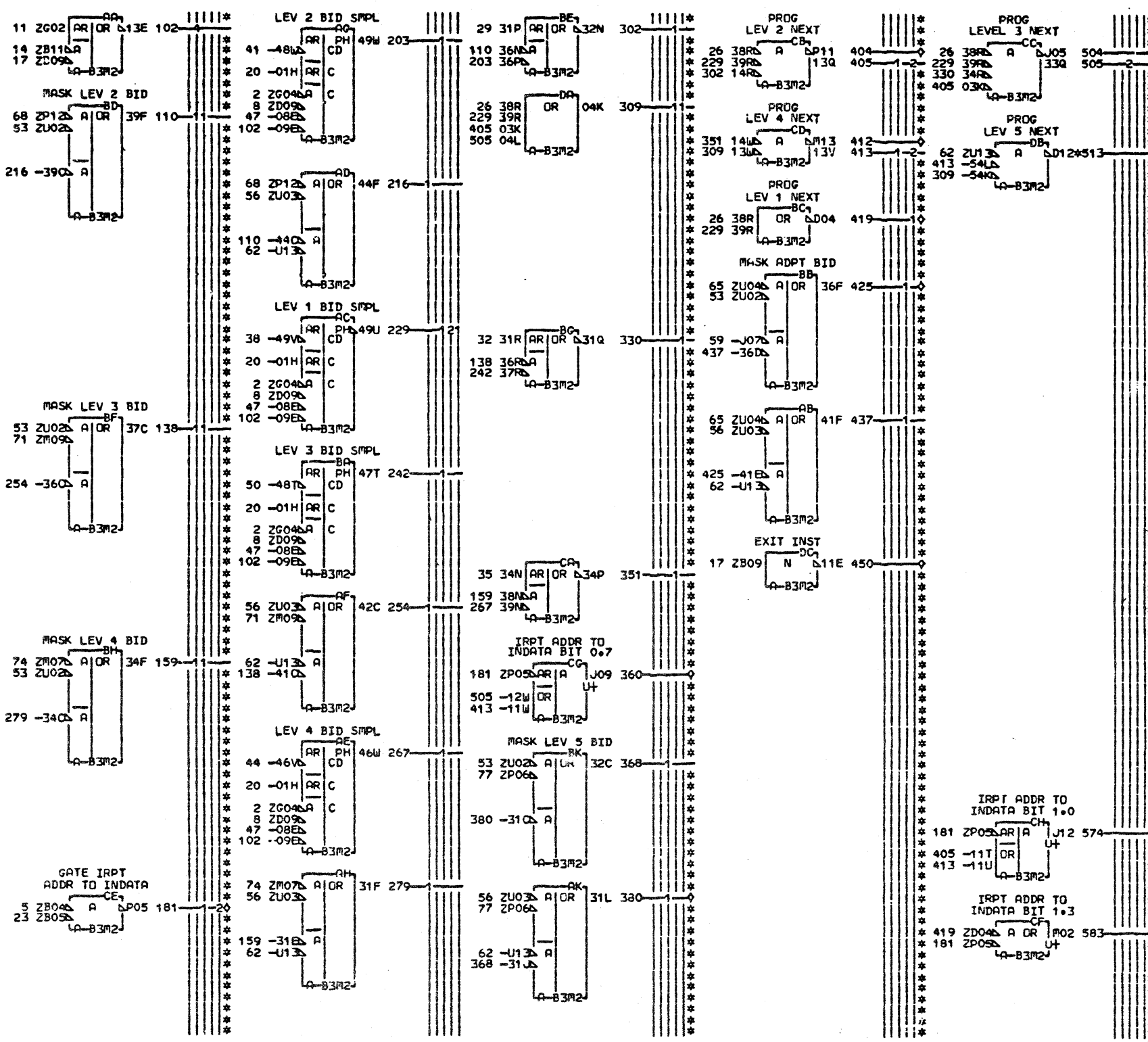
EDGE CONN.	01A-B4U6D02	570 A-B3U1C11
2 RESISTOR	546 A-B3U1E11	01A-B4U6C02
A-B3T4G13	01A-B4U6E02	
8 RESISTOR	547 A-B3A5B12	
A-B3T4G12	553 A-B3U1D13	
14 RESISTOR	01A-B4U6F04	
A-B3T4G08	554 A-B3A5B10	
467 A-B3N1E13	563 A-B3U1C13	
01A-B4N6E04	01A-B4U6C04	
510 A-B3U1D11	564 A-B3A5D09	

LCC. TYPE
 A-B3T4 6824
 A-B4C2 AB93

CP001
 000

CCU PRIORITY CONTROL	I-O PRIORITY SELECT	E-C-HISTORY	E-MACH.3705
344270			
DATE	LAST EC	FRAME	01
06-02-81	344828	IBM CORP.SCD	CP001
		P.N.	1850863
			000

- CD TIME - CC001EJ2- 2
 - I1 XA TIME - CC003BL6- 5
 - TO TIME - CC006AF8- 8
 + NO I TIME - CC008AA2- 11
 - LAST I TIME - CC008BC2- 14
 + EXIT INST - CD001DN2- 17
 + EXIT I1 BC TO TIME - CP003AC2- 20
 - VIRGIN LEVEL - CP003BL6- 23
 + PROG LEV 1 ENTERED - CP003GC2- 26
 + PROG LEV 2 ENTERED - CP003GE2- 29
 + PROG LEV 3 ENTERED - CP003GG2- 32
 + PROG LEV 4 ENTERED - CP003GJ2- 35
 + BID PROGRAM LEVEL 1 ALL - CP005BB6- 38
 + BID PROGRAM LEVEL 2 ALL - CP005BD6- 41
 + BID PROGRAM LEVEL 4 ALL - CP005BM6- 44
 + PREVENT INTERRUPT - CP005CE2- 47
 + BID PROGRAM LEVEL 3 ALL - CP005CH6- 50
 - SET OUTPUT 7E - CQ005FD6- 53
 - SET OUTPUT 7F - CQ005FE6- 56
 - TEST MODE LCH - CU005CB2- 59
 + RESET - CU010FM2- 62
 - Z BUS BIT 1.1 - DK974EH6- 65
 - Z BUS BIT 1.2 - DL004GB6- 68
 - Z BUS BIT 1.3 - DL004GF6- 71
 - Z BUS BIT 1.4 - DL004GK6- 74
 - Z BUS BIT 1.5 - DM004GB6- 77



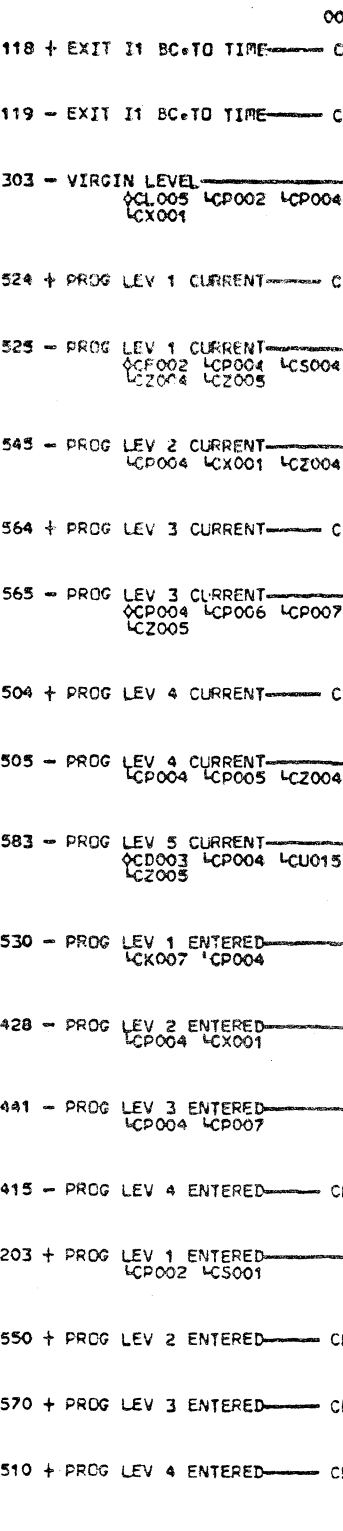
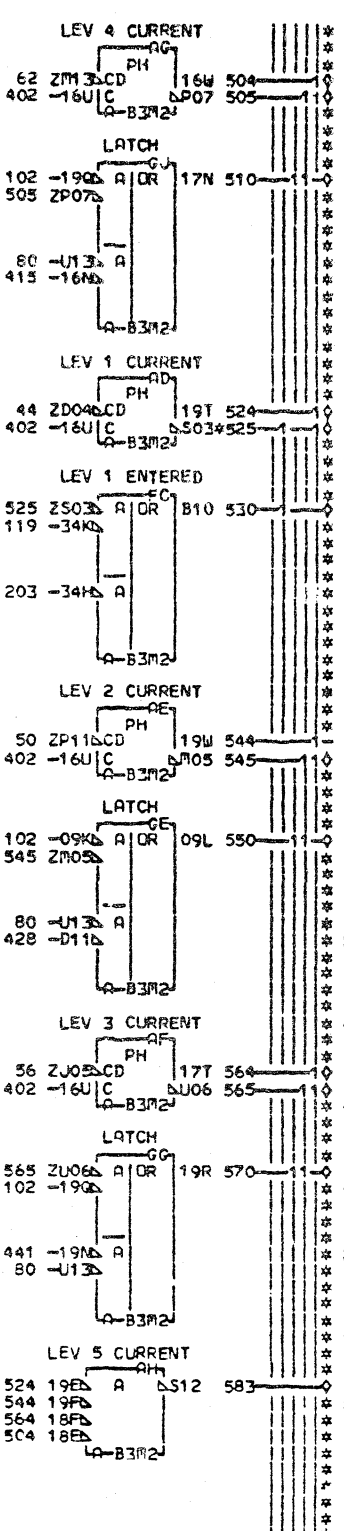
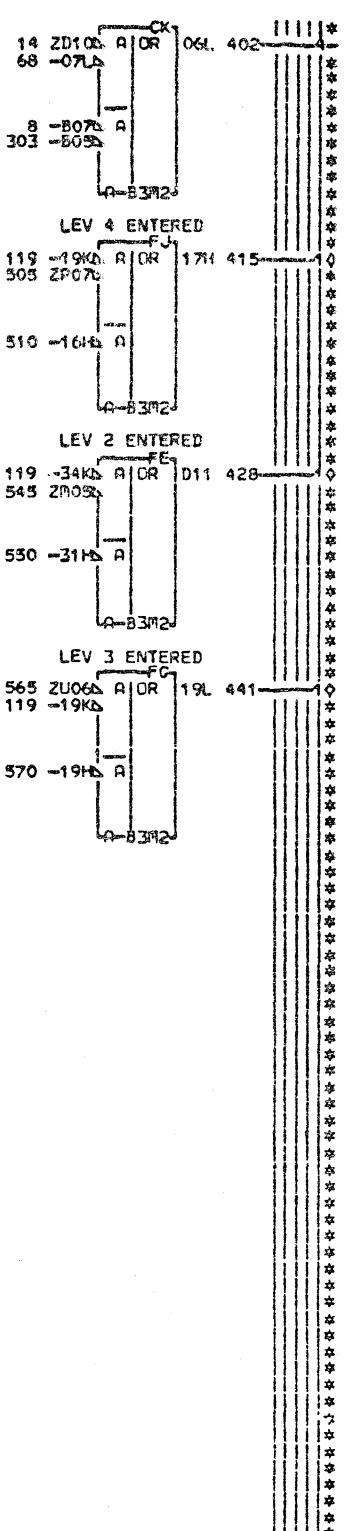
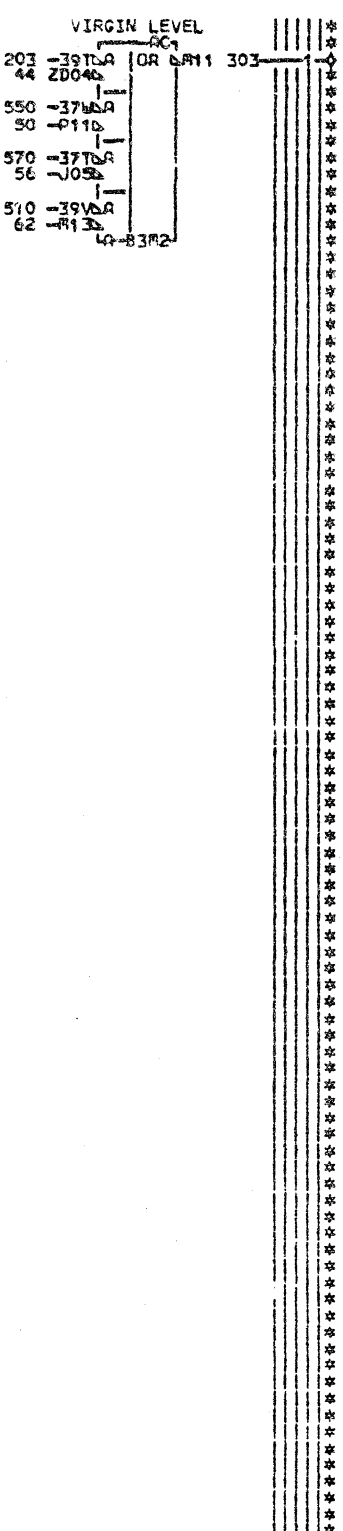
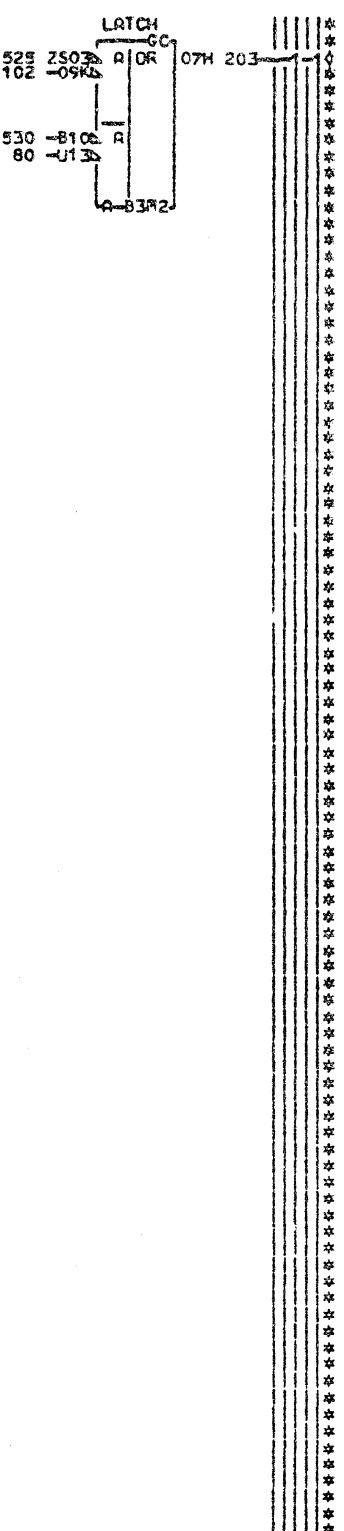
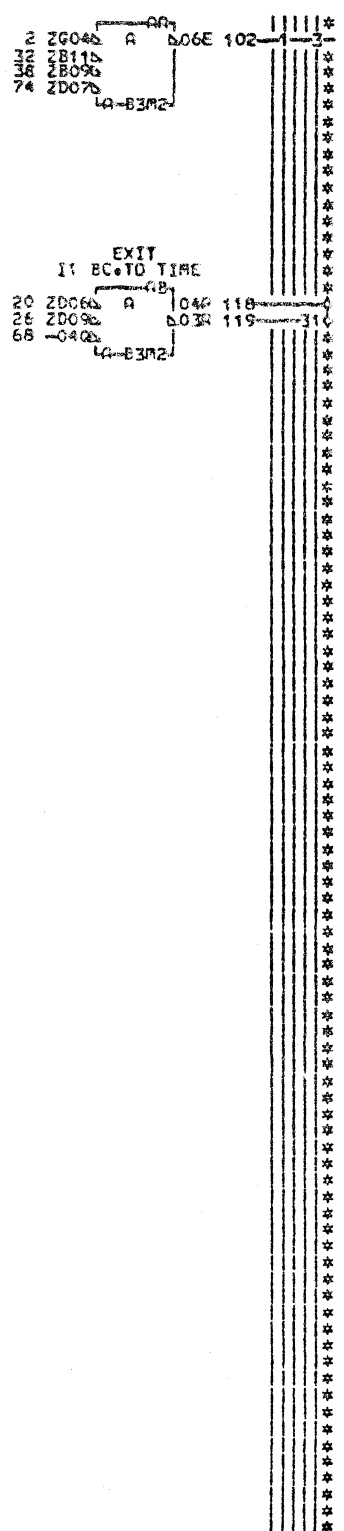
000 CP002
 380 - MASK OFF PROG LEV 5 - CC008-AK2
 425 + MASK ADAPTER LEV 1 BID - CP005-BB2
 419 - PROG LEV 1 NEXT - EA2
 404 - PROG LEV 2 NEXT - FE6
 504 - PROG LEVEL 3 NEXT - FG6
 412 - PROG LEV 4 NEXT - FL6
 181 - GATE IRPT ADDR TO INDATA - FN6
 583 + IRPT ADDR TO INDATA BIT 1.3 - GA2
 360 + IRPT ADDR TO INDATA BIT 0.7 - GB2
 574 + IRPT ADDR TO INDATA BIT 1.0 - GF2
 513 - PROG LEV 5 NEXT - GK6
 450 - EXIT INST - CP003-HN2

EDGE CONN.
 513 A-B3V1A13
 01A-B4V6A04

LOC. TYPE
 A-B3M2 6818

PRIORITY CONTROL
 PROG LEVEL SELECT
 -E.C.-HISTORY- E-MACH-3705
 FRAME 01
 IBM CORP.SCD CP002
 DATE LAST EC 10-14-80 344270
 P.No. 1850864 000

- CD TIME - CC001EJ2- 2-1
 - I1 B TIME - CC002BC6- 8-
 - I1 D TIME - CC002BE6- 14-
 - I1 BC TIME - CC003B46- 20-
 - TO TIME - CC006AF8- 26-
 - LAST I TIME - CC008BC2- 32-
 + EXIT INST - CD001DN2- 38-
 - PRG LEV 1 NEXT - CP002EA2- 44-
 - PRG LEV 2 NEXT - CP002FE6- 50-
 - PRG LEVEL 3 NEXT - CP002FG6- 56-
 - PRG LEV 4 NEXT - CP002FL6- 62-
 - EXIT INST - CP002HN2- 68-
 - VIRGIN LEVEL CD - CU003GL6- 74-
 + RESET - CU010FM2- 80-



000 CP003
 118 + EXIT I1 BC TO TIME - CP002-AC2
 119 - EXIT I1 BC TO TIME - CU015-AC6
 303 - VIRGIN LEVEL - BL6
 524 + PRG LEV 1 CURRENT - CU014-DB2
 525 - PRG LEV 1 CURRENT - DDE
 545 - PRG LEV 2 CURRENT - DF6
 564 + PRG LEV 3 CURRENT - CP005-DH2
 565 - PRG LEV 3 CURRENT - DH6
 504 + PRG LEV 4 CURRENT - CP005-DK2
 505 - PRG LEV 4 CURRENT - DK6
 583 - PRG LEV 5 CURRENT - EM6
 530 - PRG LEV 1 ENTERED - FC2
 428 - PRG LEV 2 ENTERED - FE2
 441 - PRG LEV 3 ENTERED - FG2
 415 - PRG LEV 4 ENTERED - CP004-FJ2
 203 + PRG LEV 1 ENTERED - GC2
 550 + PRG LEV 2 ENTERED - CP002-GE2
 570 + PRG LEV 3 ENTERED - CP002-GG2
 510 + PRG LEV 4 ENTERED - CP002-GJ2

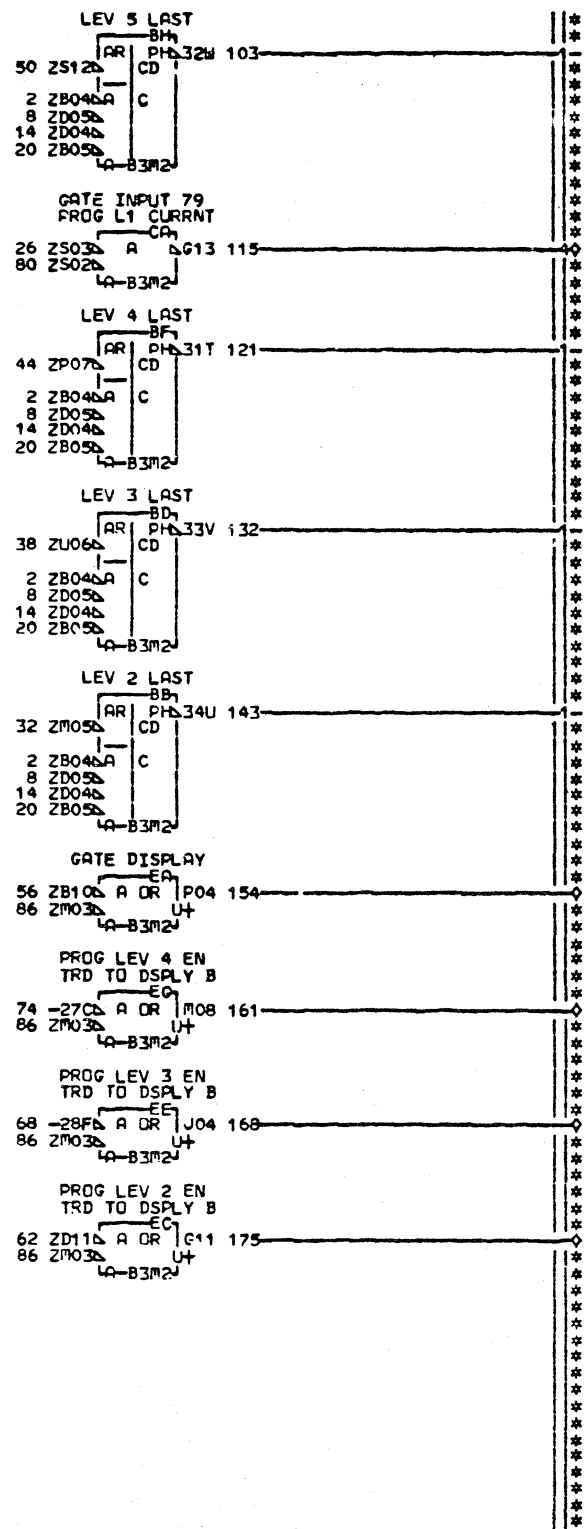
EDGE CONN.
 525 A-B3D1E13
 01A-B4D6E04

LOC. TYPE
 A-B3M2 6818

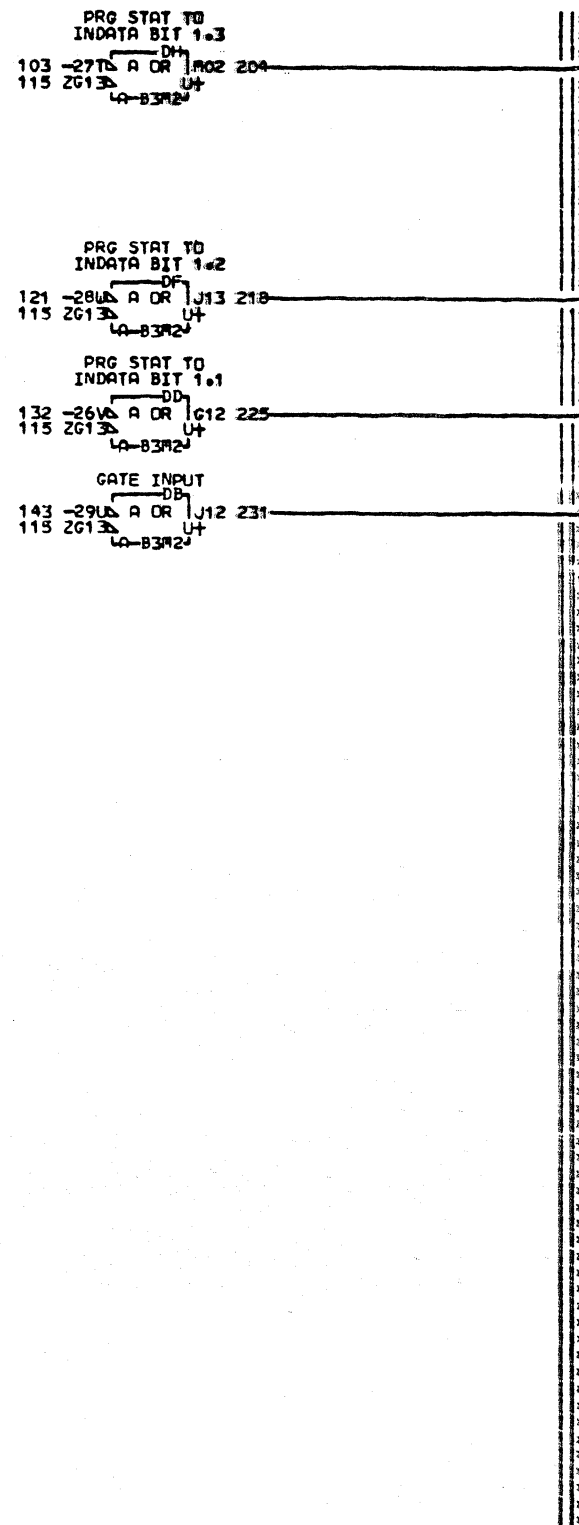
CP003
 000

PRIORITY CONTROL
 PRG LEVEL SELECT
 E.C.-HISTORY- E MACH.3705
 FRAME 01
 IBM CORP.SCD CP003
 DATE LAST EC 10-14-80 344270 P.N. 1769189 000

- I1 XA TIME—CC003BL6— 2
 - TOT1 TIME—CC007HK1— 8
 - PRDG LEV 1 NEXT—CP002EA2— 14
 - VIRGIN LEVEL—CP003BL6— 20
 - PRDG LEV 1 CURRENT—CP003DD6— 26
 - PRDG LEV 2 CURRENT—CP003DF6— 32
 - PRDG LEV 3 CURRENT—CP003DH6— 38
 - PRDG LEV 4 CURRENT—CP003DK6— 44
 - PRDG LEV 5 CURRENT—CP003EM6— 50
 - PRDG LEV 1 ENTERED—CP003FC2— 56
 - PRDG LEV 2 ENTERED—CP003FE2— 62
 - PRDG LEV 3 ENTERED—CP003FG2— 68
 - PRDG LEV 4 ENTERED—CP003FJ2— 74
 - GATE INPUT 79—CQ005DK6— 80
 - GATE STATUS TO DISPLAY B—CU001EJ6— 86



LUC TYPE
A-B3M2 6818

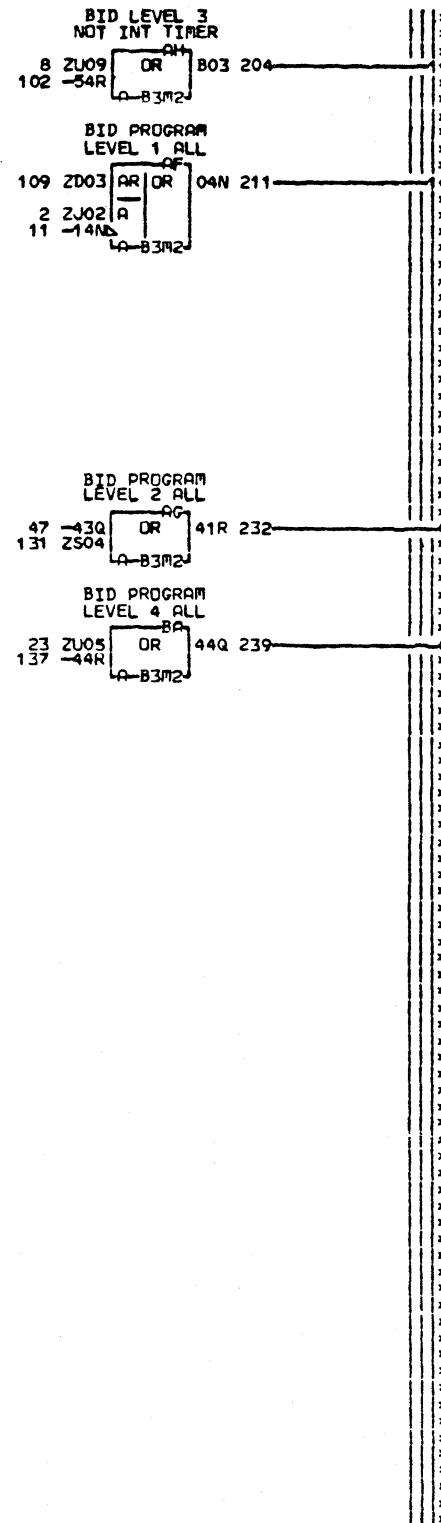
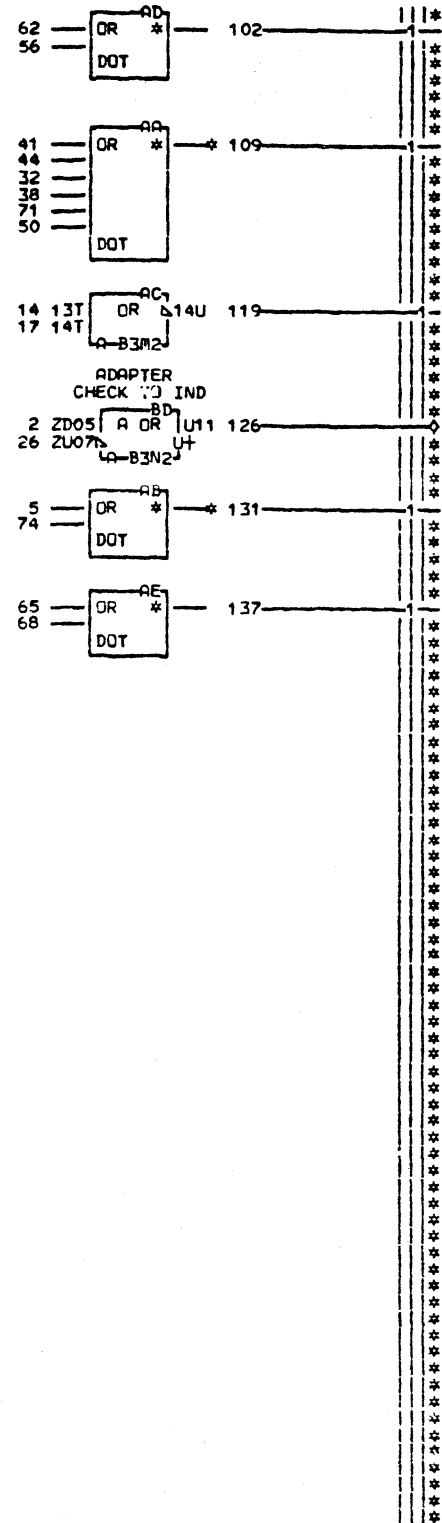


000 CP004
 115 - GATE INPUT 79 PROG L1 CURRENT—CQ6
 LCRO04
 231 + PRG STAT TO INDATA BIT 1.0—CQ2
 LCU012
 225 + PRG STAT TO INDATA BIT 1.1—CQ2
 LCU012
 218 + PRG STAT TO INDATA BIT 1.2—CQ2
 LCU012
 204 + PRG STAT TO INDATA BIT 1.3—CQ2
 LCU012
 154 + PRDG LEV 1 ENTERD TO DSPLY B—EC2
 LAP015
 175 + PRDG LEV 2 ENTERD TO DSPLY B—EC2
 LAP015
 168 + PRDG LEV 3 ENTERD TO DSPLY B—EC2
 LAP015
 161 + PRDG LEV 4 ENTERD TO DSPLY B—EC2
 LAP015

CP004
000

PRIORITY CONTROL		PRDG LEV LAST DETERMINATION	
E.C.—HISTORY—		E.—RACH.—3703	
DATE	LAST EC	FRAME	01
10-14-80	344270	IBM CORP.—SCD	CP004
		P.No.	1769190 000

+ BID PROGRAM LEV 1 — AA002DF1* 2-1-1
 + BID PROGRAM LEV 2 — AA002DF3- 5-1-1
 + BID PROGRAM LEV 3 — AA002DF5* 8-1-1
 + MASK ADAPTER LEV 1 BID — CP002BB2- 11-1-1
 + PROG LEV 3 CURRENT — CP003DH2- 14-1-1
 + PROG LEV 4 CURRENT — CP003DK2- 17-1-1
 - PROG LEV 4 CURRENT — CP003DK6- 20-1-1
 + BID PROGRAM LEV 4 — CP005001* 23-1-1
 - GATE STATUS TO DISPLAY B — CU001EJ6- 26-1-1
 - ACTIV INSN STEP OR CLK STEP — CU006CK2- 29-1-1
 + IPL BID LEV 1 — CU010FF2- 32-1-1
 - DIAGNOSTIC BID PRG LEV 2 — CU014BL6- 35-1-1
 + I-O CHECK BID PRG LEV 1 — CU014CB2- 38-1-1
 + ADDR COMPARE BID PRG LEV 1 — CU014CE2- 41-1-1
 FOLLOWS CP005AB4 — CU014CH2- 44-1-1
 + DIAG PCI BID PRG LEV 2 — CU014CL2- 47-1-1
 + INVALID OP BID PRG LEV 1 — CU014GB2- 50-1-1
 + INTERVAL TIMER BID PRG LEV3 — CU014GH2- 53-1-1
 + INTERRUPT KEY BID PRG LEV 3 — CU014GL2- 56-1-1
 - PCI 3 — CU015BB6- 59-1-1
 + PCI BID PRG LEV 3 — CU015CB2- 62-1-1
 + PCI BID PRG LEV 4 — CU015CE2- 65-1-1
 + SVC BID PRG LEV 4 — CU015CH2- 68-1-1
 + PROT CHECK BID PRG LEV 1 — CV061GE2- 71-1-1
 + CSB SUPPORT BID PRG LEV 2 — CX003EL6- 74-1-1



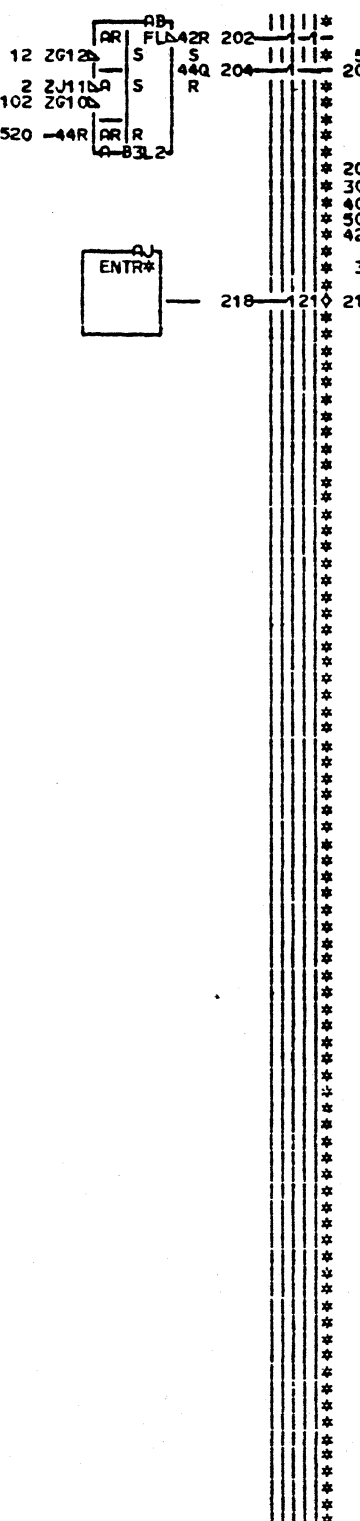
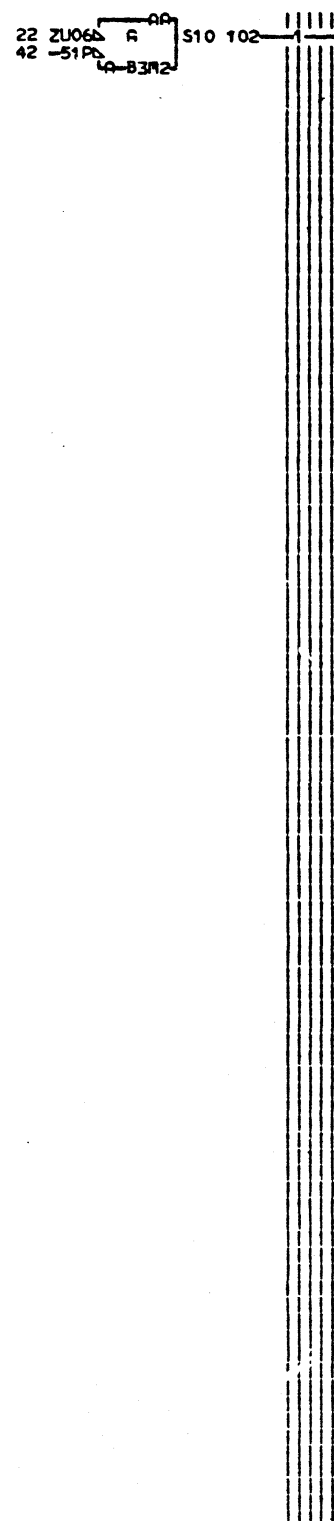
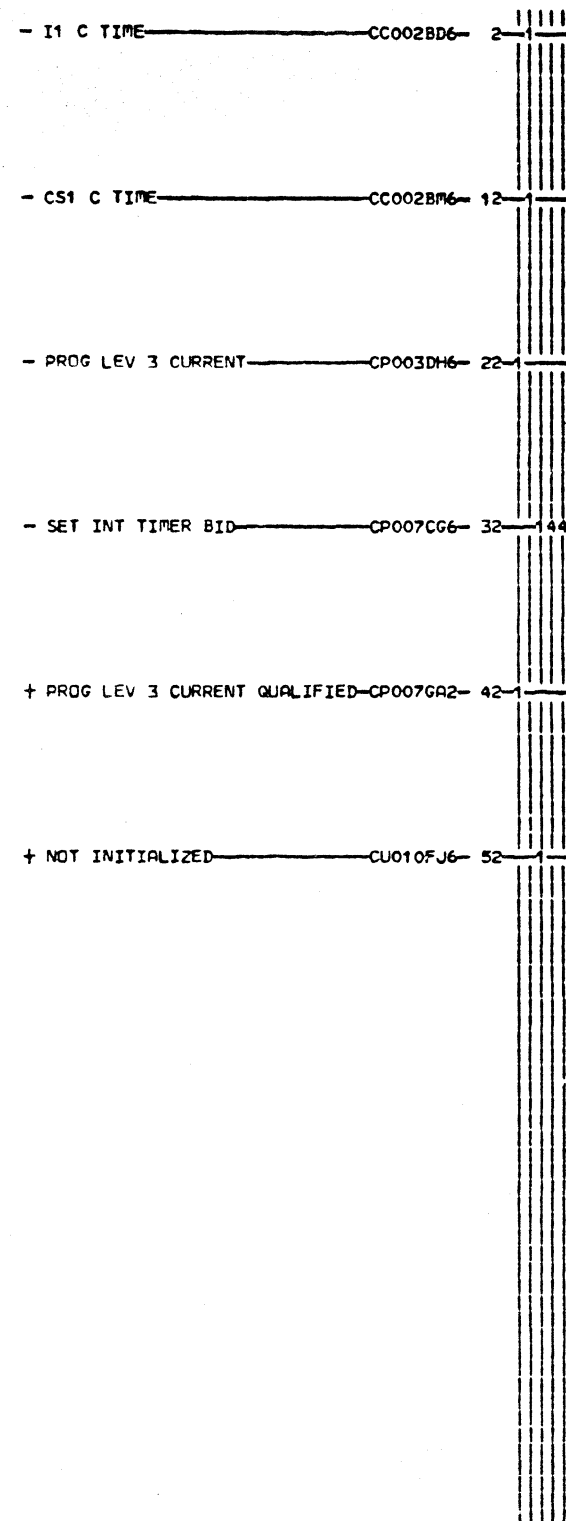
000 CP005
 211 + BID PROGRAM LEVEL 1 ALL — BB6
 CP002 LC5001
 232 + BID PROGRAM LEVEL 2 ALL CP002-BB6
 204 + BID LEVEL 3 NOT INT TIMER — BJ6
 CP007
 239 + BID PROGRAM LEVEL 4 ALL CP002-BB6
 311 + PREVENT INTERRUPT — CP002-CE2
 304 + BID PROGRAM LEVEL 3 ALL CP002-CH6
 126 + ADAPTER CHECK TO IND — AP013-HE2

EDGE COIN# 01A-B3C1A11
 2 RESISTOR 131 RESISTOR
 A-B3M2 J02 A-B3M2S04
 8 RESISTOR 01A-B4V5D11
 A-B3M2U09 01A-B4B6C02
 23 RESISTOR 01A-B3B1C11
 A-B3M2U05
 109 RESISTOR
 A-B3M2D03
 01A-B4C6A02

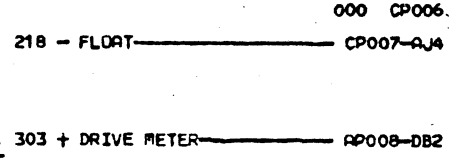
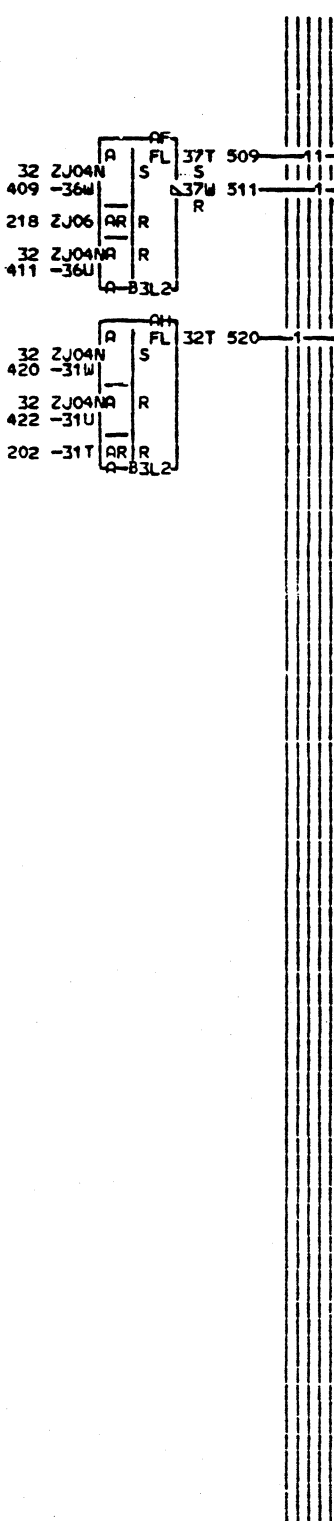
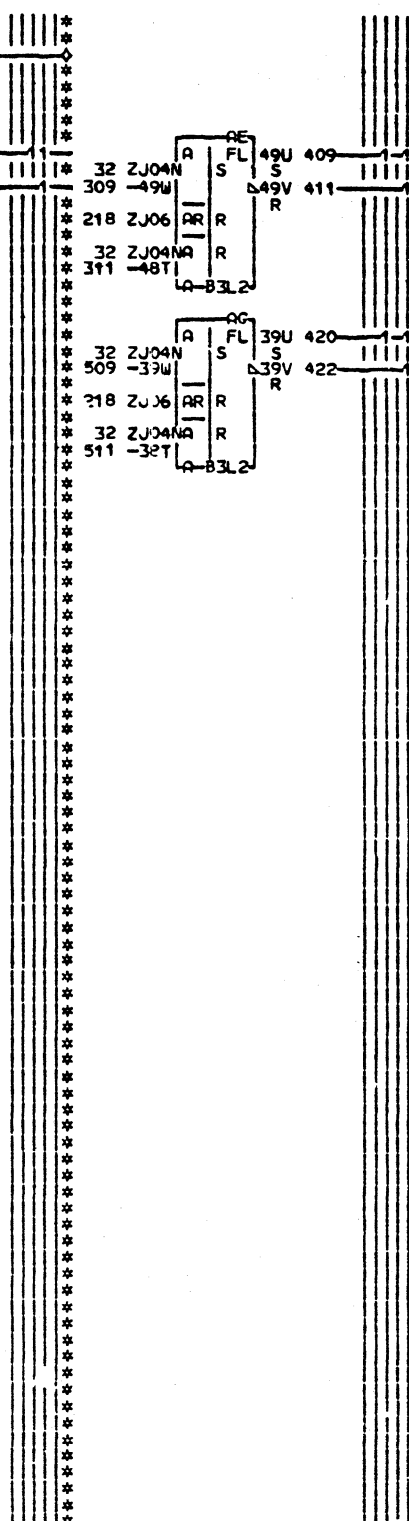
LOC. TYPE
 A-B3M2 6810
 A-B3N2 6819

CP005
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PRIORITY CONTROL	
E.C. HISTORY	E. MACH. 3705
DATE	FRAME 01
10-14-80 344270	IBM CORP. SCD CP005
	P.N. 1769191 000



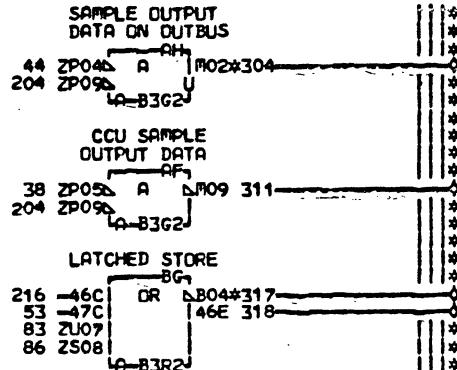
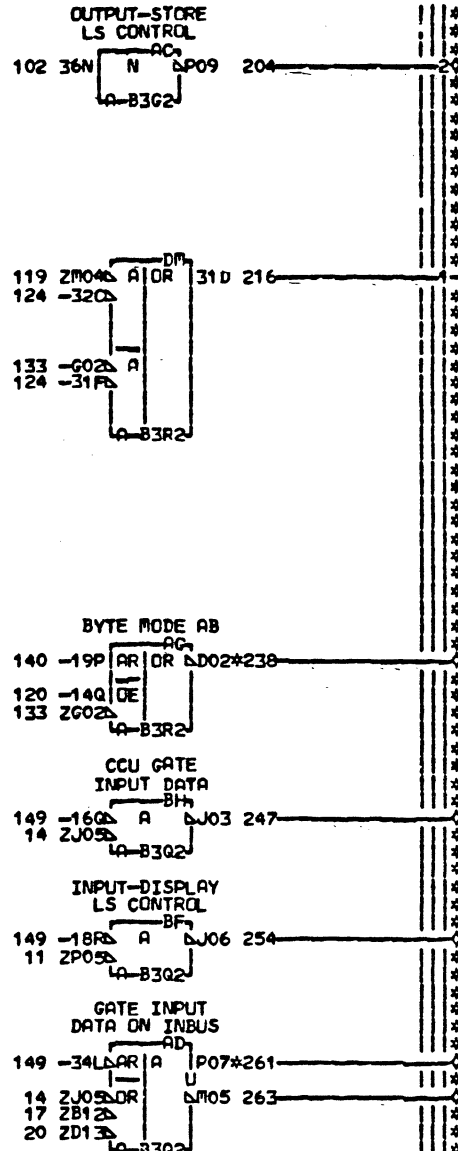
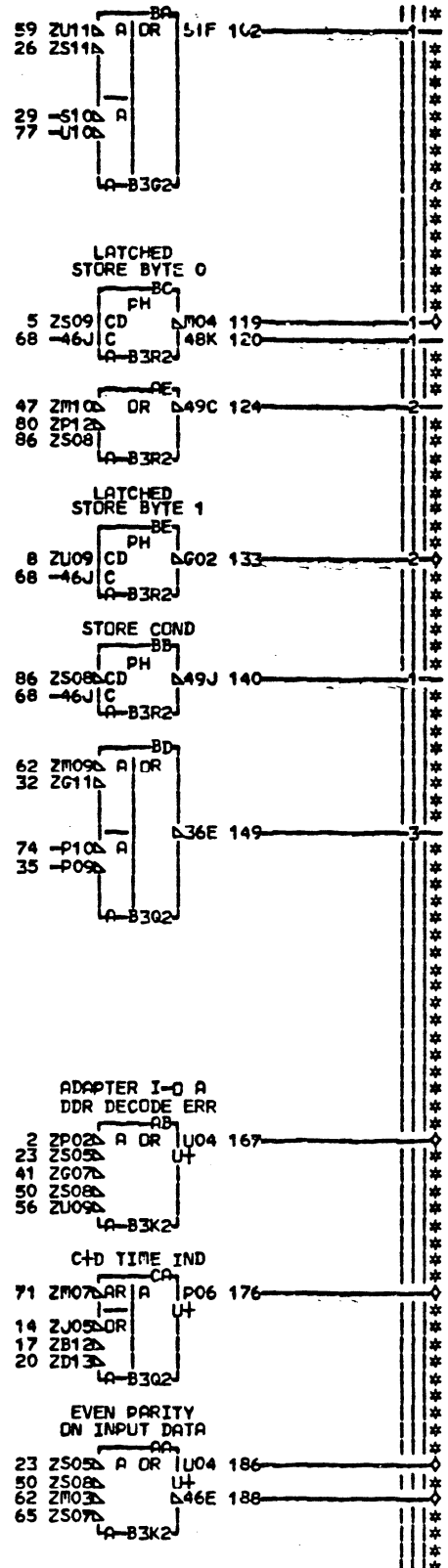
LOC. TYPE
P-B3L2 6823
P-B3P2 6818



CP006
000

METER AND INTERVAL TIMER
E.C.-HISTORY — E-PACH.3705
FRAME 01
DATE LAST EC IBM CORP.SCD CP006
10-14-80 344270 P.N. 1769192 000

- + ADAPTER I-O ADDRESS DECODE—AA002DF7— 2
- + STORE BYTE 0—AA003DM3— 5
- + STORE BYTE 1—AA003DM5— 8
- BC TIME—CC001BJ6— 11
- CD TIME—CC001EJ2— 14
- C TIME—CC001FG2— 17
- D TIME—CC001FJ2— 20
- I1 D TIME—CC002BE6— 23
- I1 CD TIME—CC003BJ6— 26
- CS1 CD TIME—CC003EJ6— 29
- I1 TIME—CC004EA6— 32
- CS1 TIME—CC004EL6— 35
- T0 TIME—CC006AFB— 38
- T2 TIME—CC006DFB— 41
- T3+T0 TIME—CC007HJ1— 44
- CYCLE STEAL AB—CC008AE6— 47
- + CCU I-O REG ADDR—CD001EN6— 50
- + INST LATCHED STORE COND—CD002DM2— 53
- INPUT + OUTPUT INST—CD003CK6— 56
- OUTPUT INST—CD003DJ6— 59
- INPUT INST—CD003DK6— 62
- BYTE 0 OR 1 B REG PARITY ERR—CQ002AJ6— 65
- + SET SAR—CS007EA2— 68
- GATE STATUS TO DISPLAY A—CU001EH6— 71
- DISPLAY REGISTER CD—CU003GD6— 74
- STORE REGISTER CD—CU003GF6— 77
- CYCLE STEAL CD—CU003GK6— 80
- + CS1 PANEL STORE FUNCTIONS—CU007GG2— 83
- + BTSTRP MODE FORCE LTCH STORE—CU016FB2— 86



- 186 + EVEN PARITY ON INPUT DATA—BF2 LCU014
- 188 - INT SCOPING POINT UNLOADED—BF6 LAU001
- 167 + ADAPTER I-O ADDR DECODE ERR—BG2 LCU014
- 204 - OUTPUT-STORE LS CONTROL—CA2 LCU004 LCU005
- 261 + GATE INPUT DATA ON INBUS—CD2 LA002
- 263 - GATE INPUT DATA ON INBUS—CD6 LCU005
- 311 - CCU SAMPLE OUTPUT DATA—DA6 LCU004 LCU001 LCU001
- 238 - BYTE MODE AB—CF002-DK2
- 304 + SAMPLE OUTPUT DATA ON OUTBUS—EA2 LA002
- 119 - LATCHED STORE BYTE 0—CS005-FL2
- 133 - LATCHED STORE BYTE 1—CS005-FM2
- 254 - INPUT-DISPLAY LS CONTROL—GE6 LCU004
- 317 - LATCHED STORE—GL2 CQ007 LCU001 LCU002 LCU017 LCU061
- 318 + LATCHED STORE—GL6 CQ002-GL6
- 247 - CCU GATE INPUT DATA—HB6 LCU004 LCU005 LCU004 LCU001
- 176 + C+D TIME IND—HP015-HQ2

EDGE CONN.

304	A-B306C02
5	RESISTOR 01A-B401C11
	A-B3R2S09 01A-B4V5D02
8	RESISTOR 317 A-B3G1A13
	A-B3R2U09 01A-B4G6A04
238	A-B3V3D07
	01A-B4V3D07
261	A-B3A1D13
	01A-B4A6D04
	01A-B4V5D03

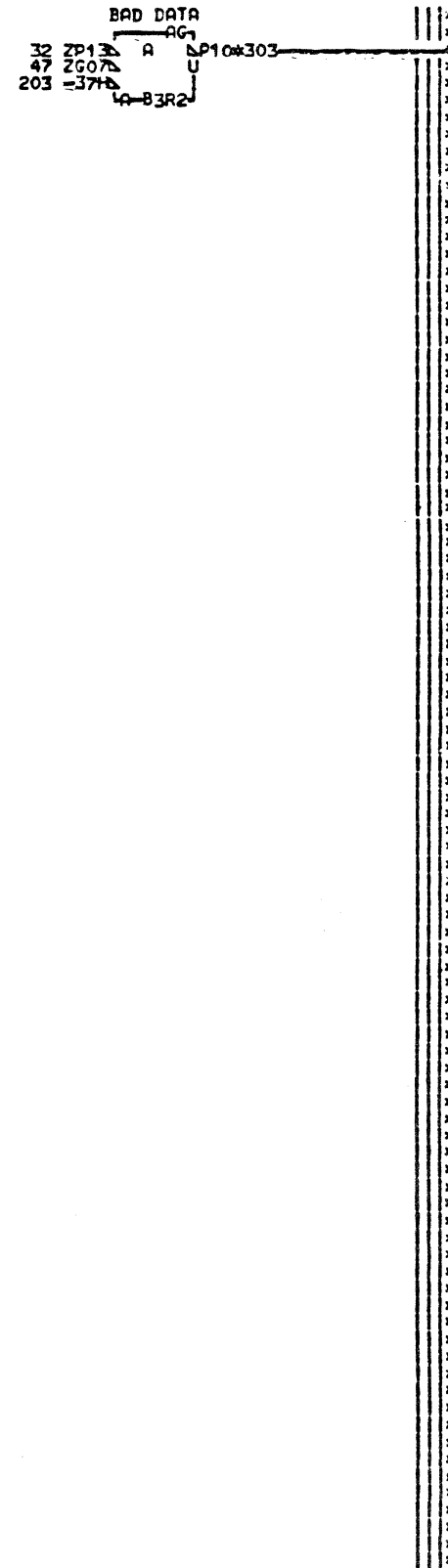
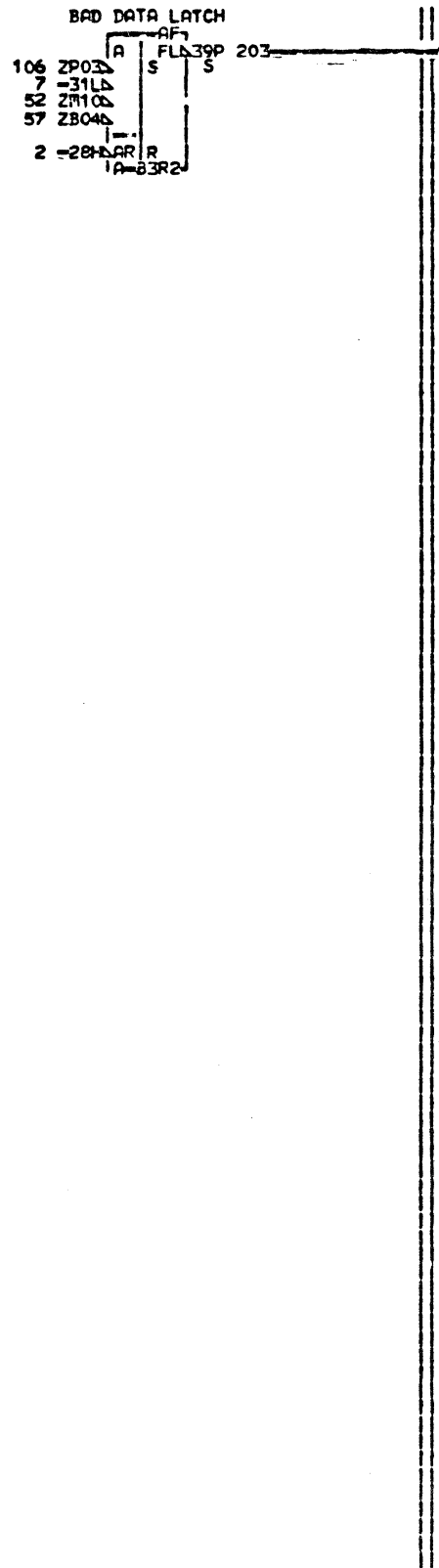
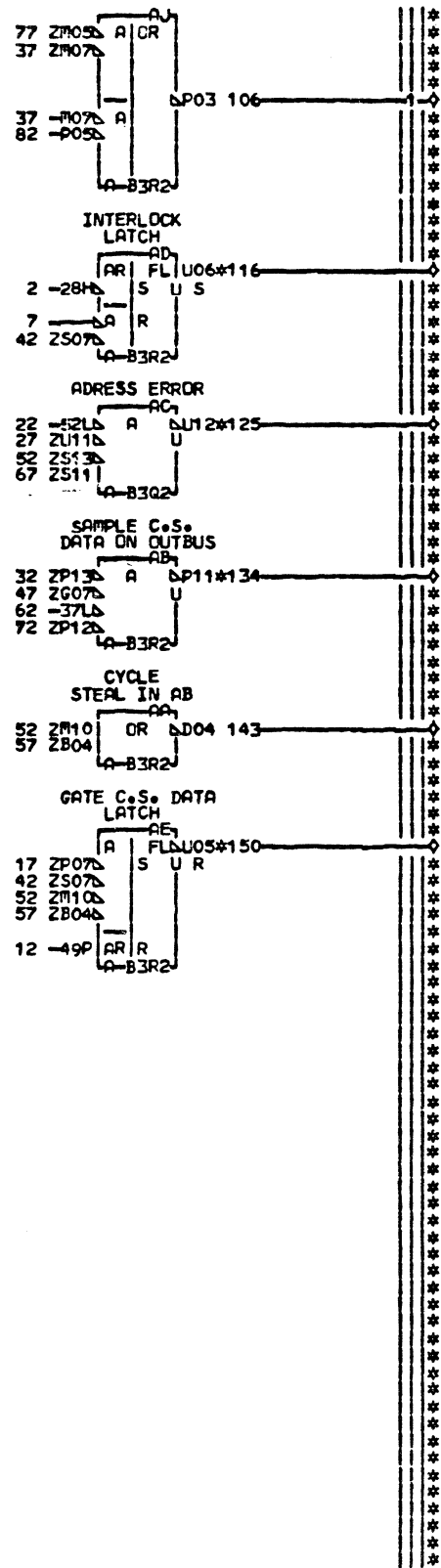
LOC. TYPE

A-B3G2	Y702
A-B3K2	6816
A-B3Q2	6821
A-B3R2	AB92

ADAPTER INTERFACE CONTROLS

E.C.—HISTORY—E3MACH.3705	344270	FRAME	01
DATE	LAST EC	IBM CORP.—SCD	CQ001
06-02-81	344828	P.No. 1769194	000

- A TIME — CC001CA6 — 2-1
 - B TIME — CC001CE6 — 7-1
 + C TIME — CC001CJ2 — 12-1
 - CS1 A TIME — CC002BK6 — 17-1
 - CS1 B TIME — CC002BL6 — 22-1
 - CS1 AB TIME — CC003EC6 — 27-1
 - CS1 CD TIME — CC003EJ6 — 32-1
 - T2 TIME — CC006DF8 — 37-2
 - T1 TIME — CC006ED6 — 42-2
 - T3+T0 TIME — CC007HJ1 — 47-1
 - CYCLE STEAL AB — CC008AE6 — 52-3-1
 - LATCHED STORE — CQ001GL2 — 57-2-1
 + LATCHED STORE — CQ001GL6 — 62-1
 + BAD ADDRESS — CS002IK6 — 67-1
 - CYCLE STEAL CD — CU003GK6 — 72-1
 - B REG BYTE 0 EVEN PARITY — DG974GG2 — 77-1
 - B REG BYTE 1 EVEN PARITY — DK974GG2 — 82-1



000 CQ002

143 - CYCLE STEAL IN AB — CS004AF2
 106 - BYTE 0 OR 1 B REG PARITY ERR — AJ6
 LA0001
 134 - SAMPLE C.S. DATA ON OUTBUS — BC6
 LA0003
 125 - ADDRESS ERROR — AA003-BK6
 116 + A TO THRU B TO TIME — AA003-CA6
 150 - GATE C.S. DATA ON INBUS AA003-DD6
 303 - BAD DATA — AA003-DF6

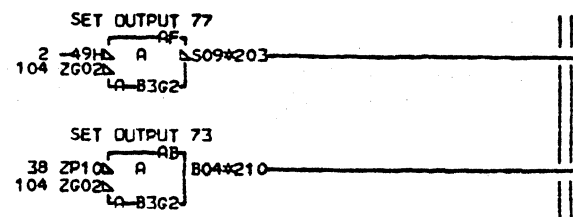
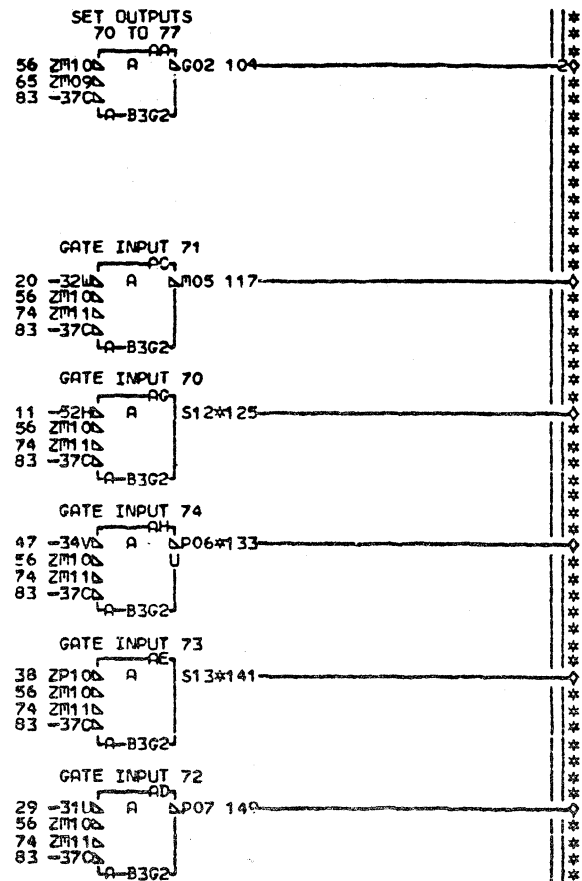
EDGE CONN.
 116 A-B3A5B06
 125 A-B3A5D02
 134 A-B3A5D06
 150 A-B3A5D05
 303 A-B3A5B13

LOC. TYPE
 A-B3Q2 6821
 A-B3R2 AB92

CQ002
 000

ADAPTER INTERFACE CONTROLS	
E.C. HISTORY 344270	E. MACH. 3705 FRAME 01
DATE LAST EC 06-02-81 344828	IBR CORP. SCD P.No. 1769195 CQ002 000

- DP XXXX XXXX X111 XXXX - CD001BB0 - 2
 - JP XXXX XXXX X000 XXXX - CD001BB3 - 11
 - DP XXXX XXXX X001 XXXX - CD001BB4 - 20
 - DP XXXX XXXX X010 XXXX - CD001BB5 - 29
 - DP XXXX XXXX X011 XXXX - CD001BB6 - 38
 - DP XXXX XXXX X100 XXXX - CD001BB7 - 47
 - DP X111 XXXX XXXX XXXX - CD001BH0 - 56
 - CCU SAMPLE OUTPUT DATA - CQ001DA6 - 65
 - CCU GATE INPUT DATA - CQ001HB6 - 74
 + DP REG BIT 1.0 - CQ005HG2 - 83



000 CQ004
 104 - SET OUTPUTS 70 TO 77 - BA6
 LCU007 LCU006
 210 + SET OUTPUT 73 - DE2
 LCU001 LCU061
 117 - GATE INPUT 71 - CS004-DK6
 149 - GATE INPUT 72 - DL6
 LCP005 LCK003 LCU003
 141 + GATE INPUT 73 - CV051-DR2
 203 - SET OUTPUT 77 - FE6
 LCU006 LCU010 LCU014 LCU015
 LCV061
 125 + GATE INPUT 70 - CM002-FH2
 133 - GATE INPUT 74 - FJ6
 LVC001 LDE002 LDF002 LDG002
 LDH002 LDJ002 LDK002 LDL002
 LDM002 LDW001

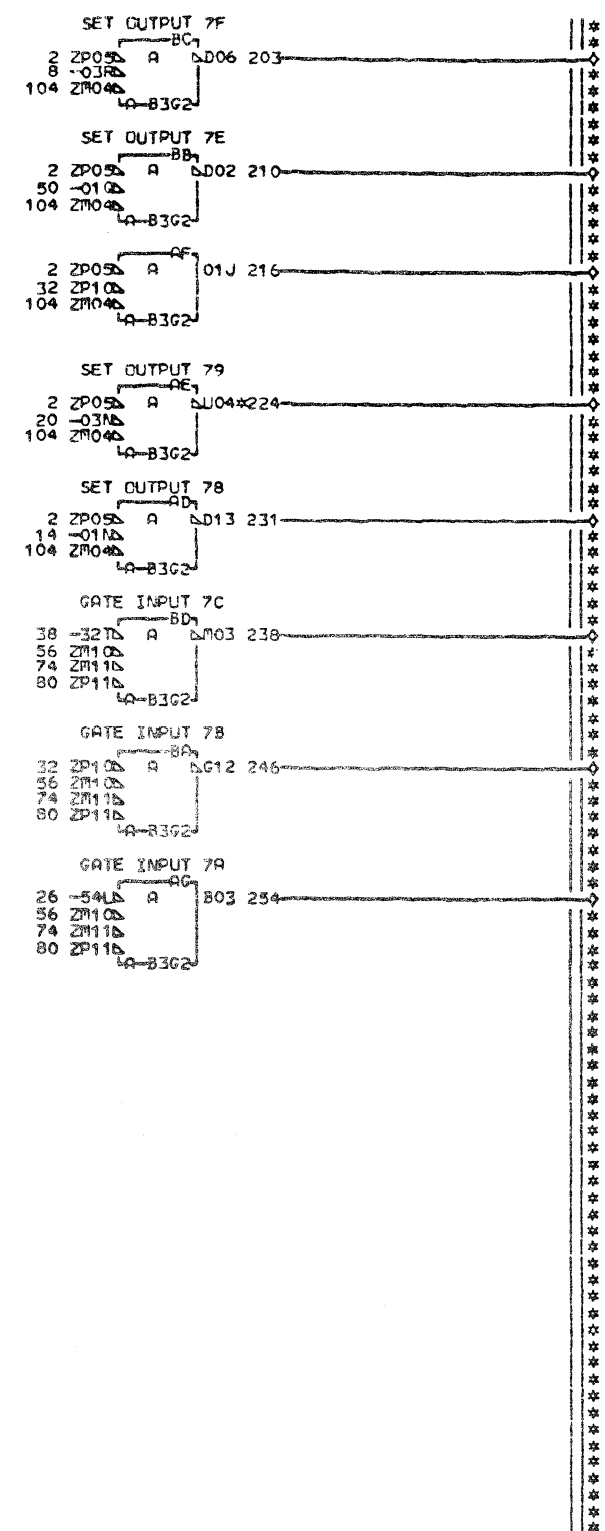
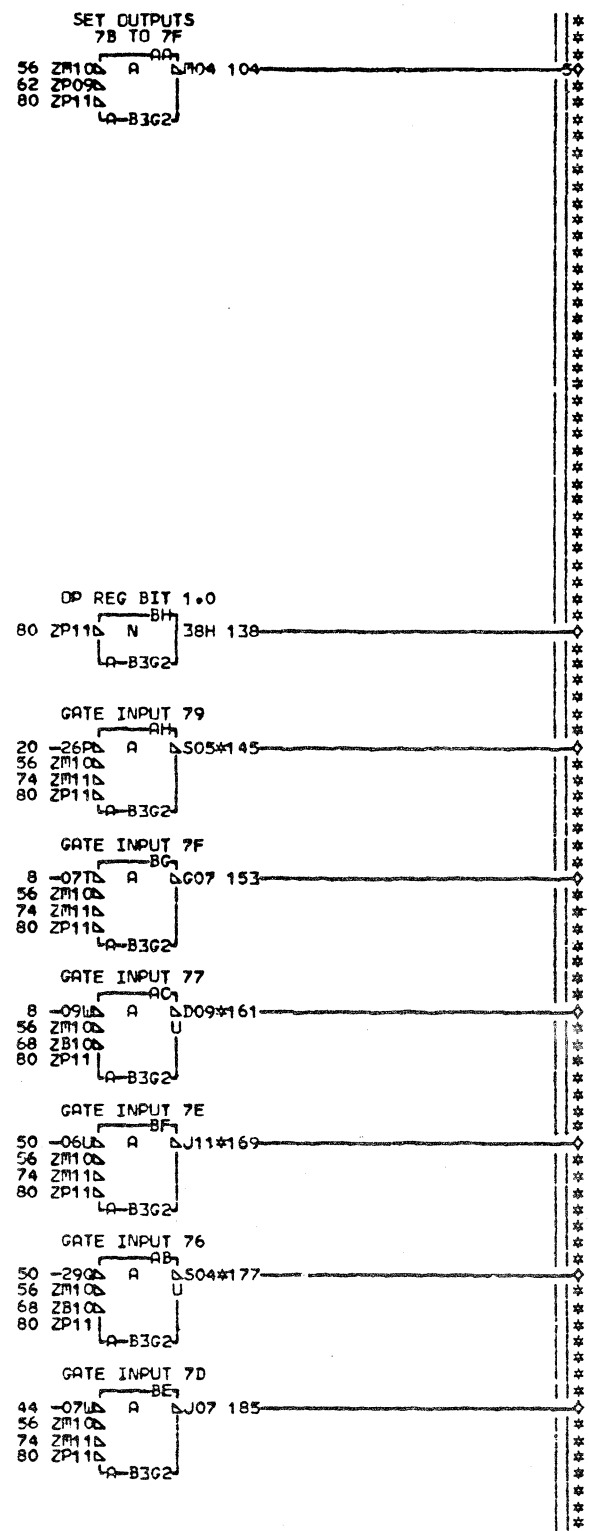
EDGE CONN. 01A-B4R1E13
 125 A-B3C1E11
 01A-B4C6E02
 133 A-B3H1R13
 01A-B4H6A04
 141 A-B3C1D13
 01A-B4C6D04
 203 A-B3N6A02
 01A-B4N1R11
 210 A-B3M6E04

LDC. TYPE
 A-B3G2 Y702

CQ004
 000

PULSED INPUTS AND OUTPUTS			
HEX 70-77			
-E.C.-HISTORY-	E.	RACH.3705	
	FRAME	01	
DATE	LAST EC	IBM CORP.SCD	CQ004
10-14-80	344270	P.N. 1769196	000

- TO TIME ----- CC006AF8- 2-5
 - DP XXXX XXXX X111 XXXX ----- CD001BB0- 8-21
 - DP XXXX XXXX X000 XXXX ----- CD001BB3- 14-
 - DP XXXX XXXX X001 XXXX ----- CD001BB4- 20-11
 - DP XXXX XXXX X010 XXXX ----- CD001BB5- 26-
 - DP XXXX XXXX X011 XXXX ----- CD001BB6- 32-2
 - DP XXXX XXXX X100 XXXX ----- CD001BB7- 38-
 - DP XXXX XXXX X101 XXXX ----- CD001BB8- 44-
 - DP XXXX XXXX X110 XXXX ----- CD001BB9- 50-21
 - DP X111 XXXX XXXX XXXX ----- CD001BH0- 56-73
 - OUTPUT-STORE LS CONTROL ----- CQ001CA2- 62-1
 - GATE INPUT DATA ON INBUS ----- CQ001CD6- 68-2
 - CCU GATE INPUT DATA ----- CQ001HB6- 74-43
 - DP REG BIT 1.0 ----- DQ004GB2- 80-83



000 CQ005

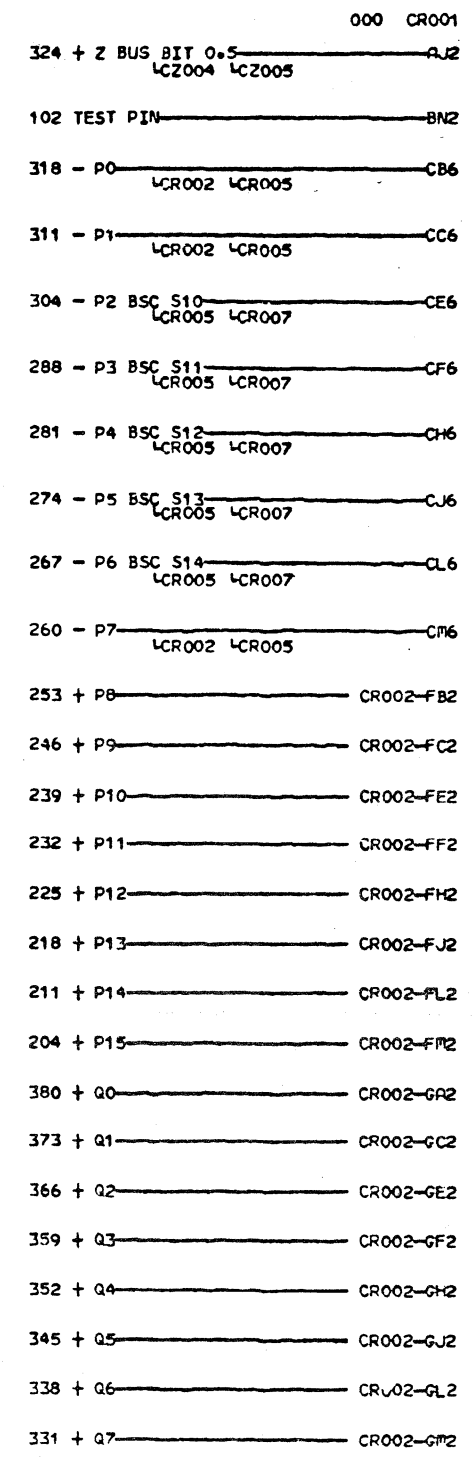
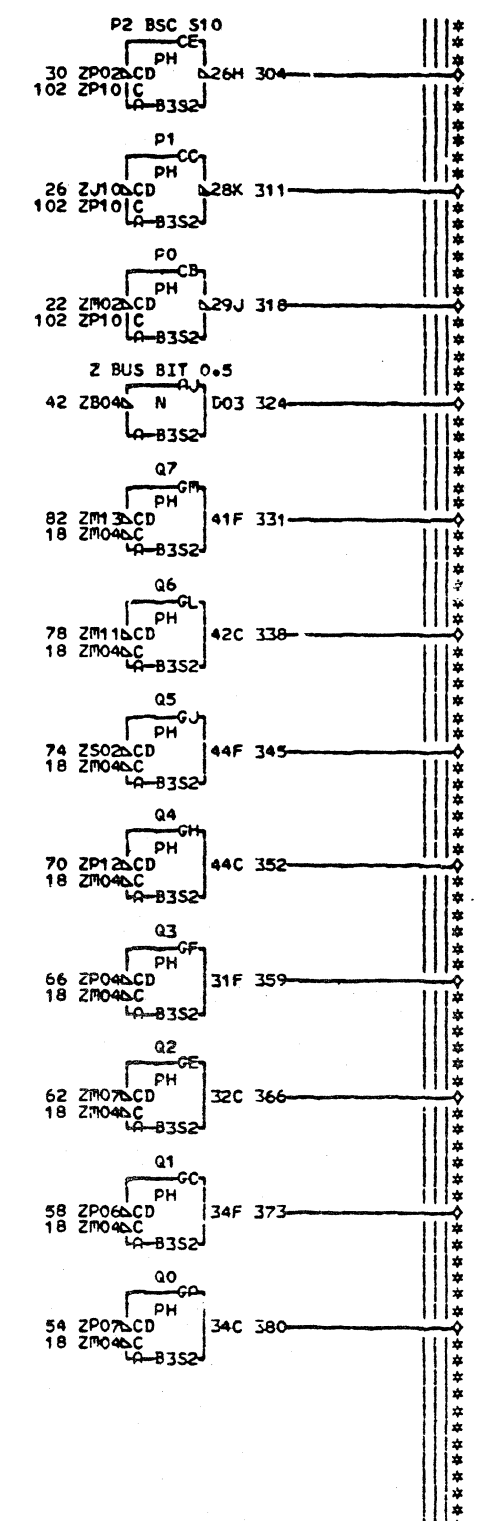
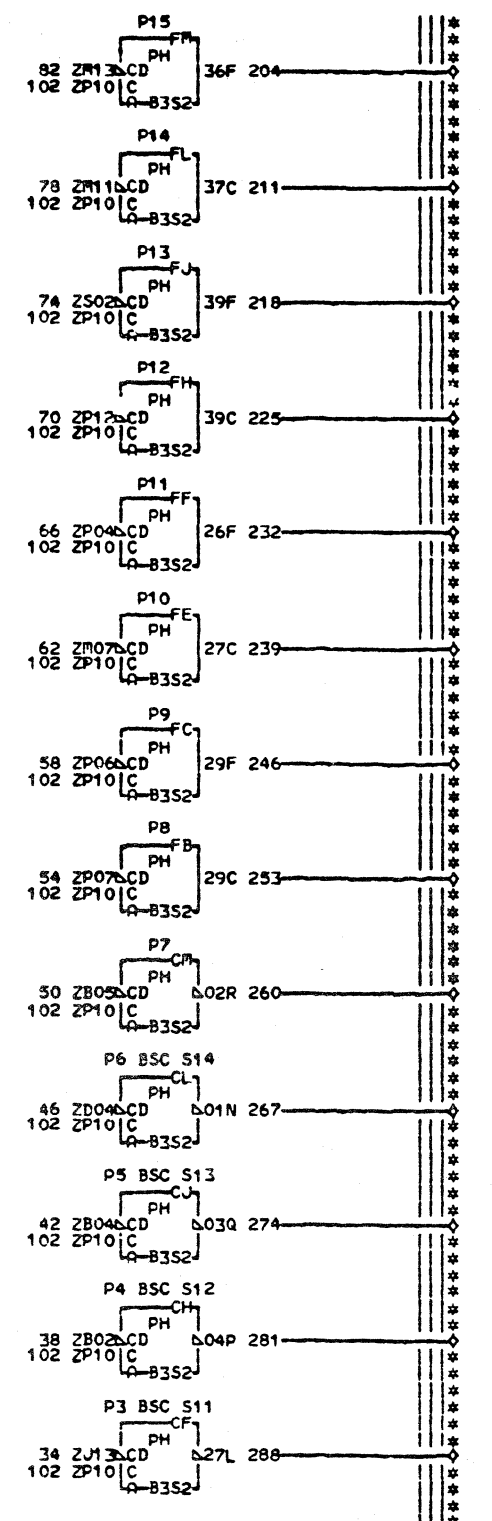
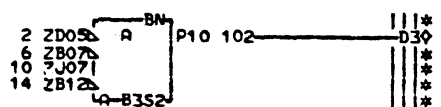
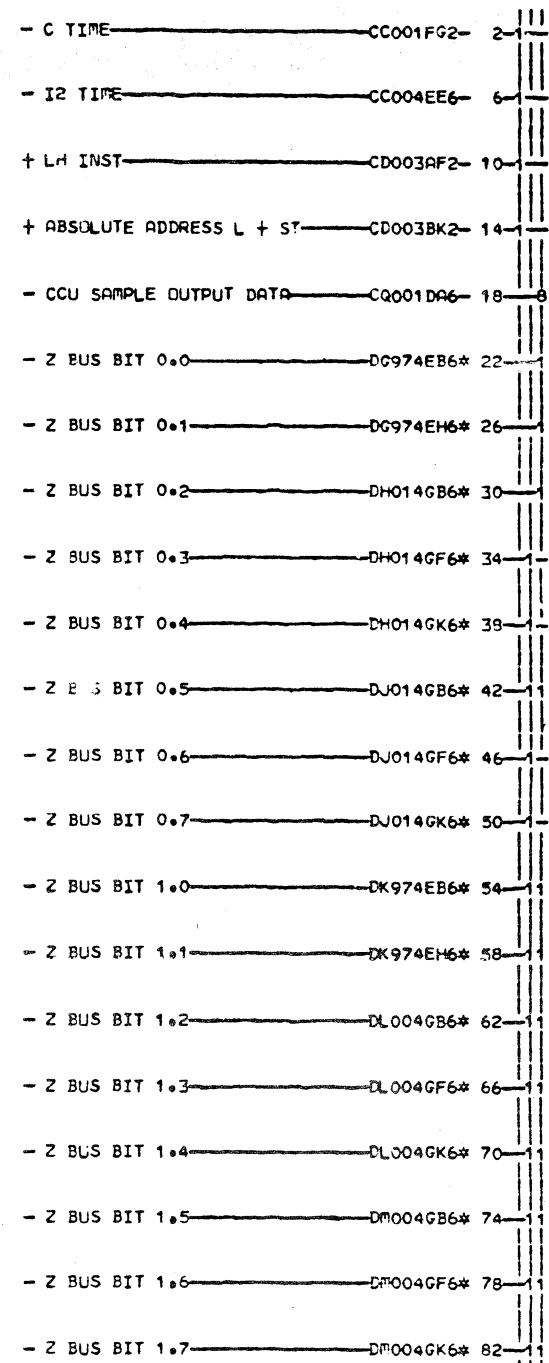
104 - SET OUTPUTS 7B TO 7F ----- CU015-BA6
 177 - GATE INPUT 76 ----- AA002-CJ6
 161 - GATE INPUT 77 ----- CL6
 LA002 LCX003
 231 - SET OUTPUT 78 ----- CK001-DB6
 224 - SET OUTPUT 79 ----- DC6
 QCM003 LCR006 LCU005 LCU006
 LCU010 LCU015 LCZ004 LCZ005
 216 + SET OUTPUT 7B ----- DE2
 254 + GATE INPUT 7A ----- CR004-DJ2
 145 - GATE INPUT 79 ----- DK6
 LCG001 LCP004 LCZ004 LCZ005
 246 - GATE INPUT 7B ----- DM6
 LCR006 LCR007
 210 - SET OUTPUT 7E ----- CP002-FD6
 203 - SET OUTPUT 7F ----- CP002-FE6
 238 - GATE INPUT 7C ----- FJ6
 LCR006 LCR007
 185 - GATE INPUT 7D ----- FK6
 LCK004 LCK005
 169 - GATE INPUT 7E ----- FL6
 LCU010 LCU014 LCV061
 153 - GATE INPUT 7F ----- FN6
 LCU014 LCU015
 138 + DP REG BIT 1.0 ----- CQ004-HG2

EDGE CONV. 01A-B4V5D05
 145 A-B3E1D13 224 A-B3F1A13
 01A-B4E6D04 01A-B4F6A04
 161 A-B3B1A13
 01A-B4B6A04
 01A-B4V5D06
 169 A-B3C1E13
 01A-B4C6E04
 177 A-B3A1E11
 01A-B4A6E02

LOC. TYPE
 A-B3G2 Y702

CQ005
 000

PULSED INPUTS AND OUTPUTS
 HEX 7B-7F
 -E.C.-HISTORY-E. PACH.3705
 FRAME 01
 IBA CORP.SCD CQ005
 DATE LAST EC P.No 1769197 000
 10-14-80 344270

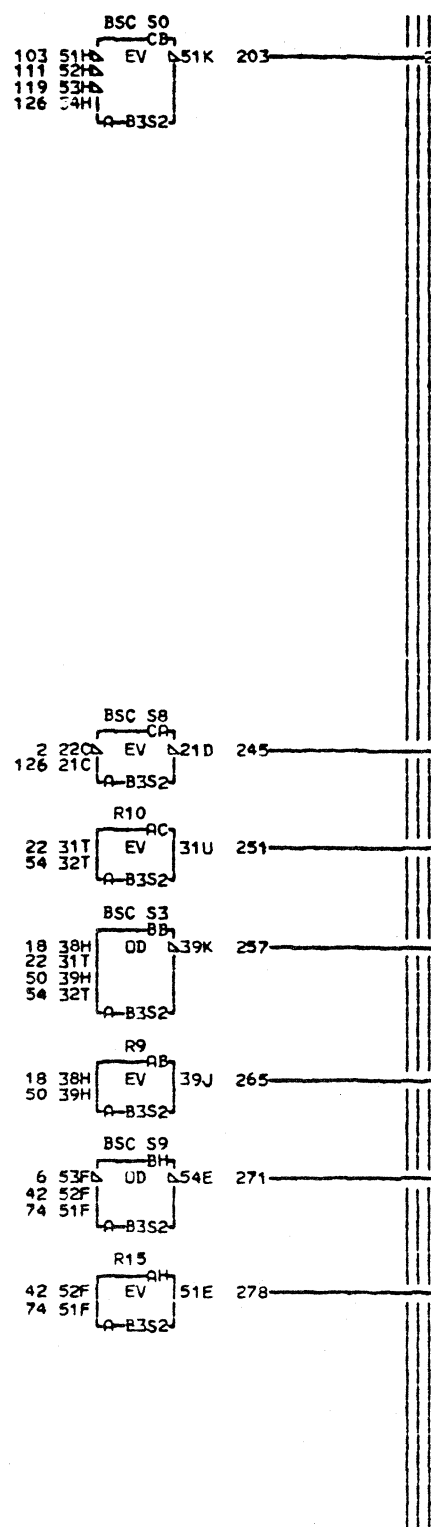
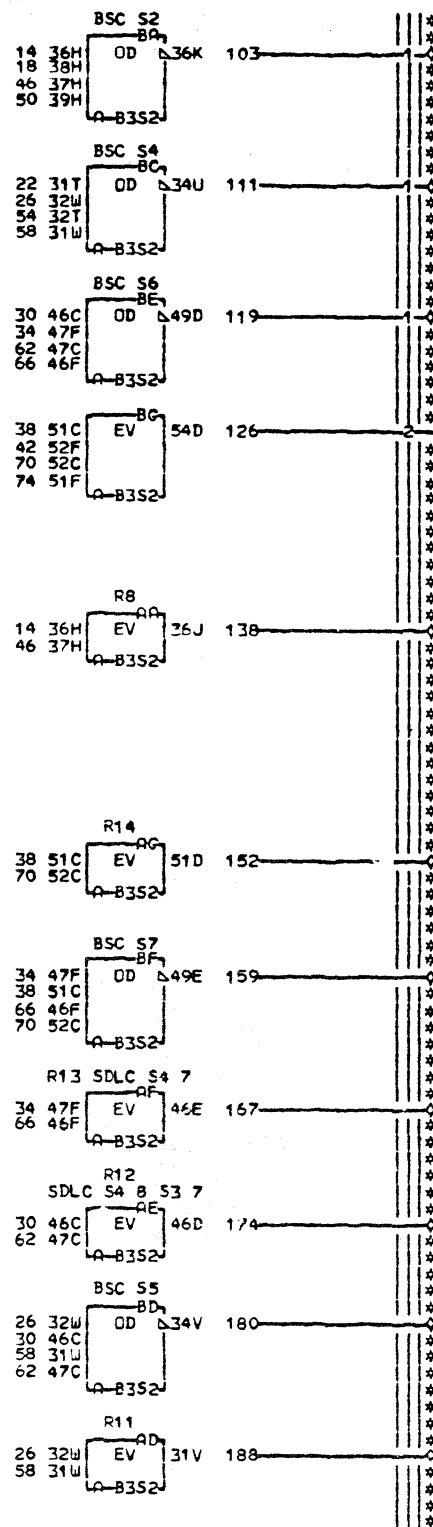
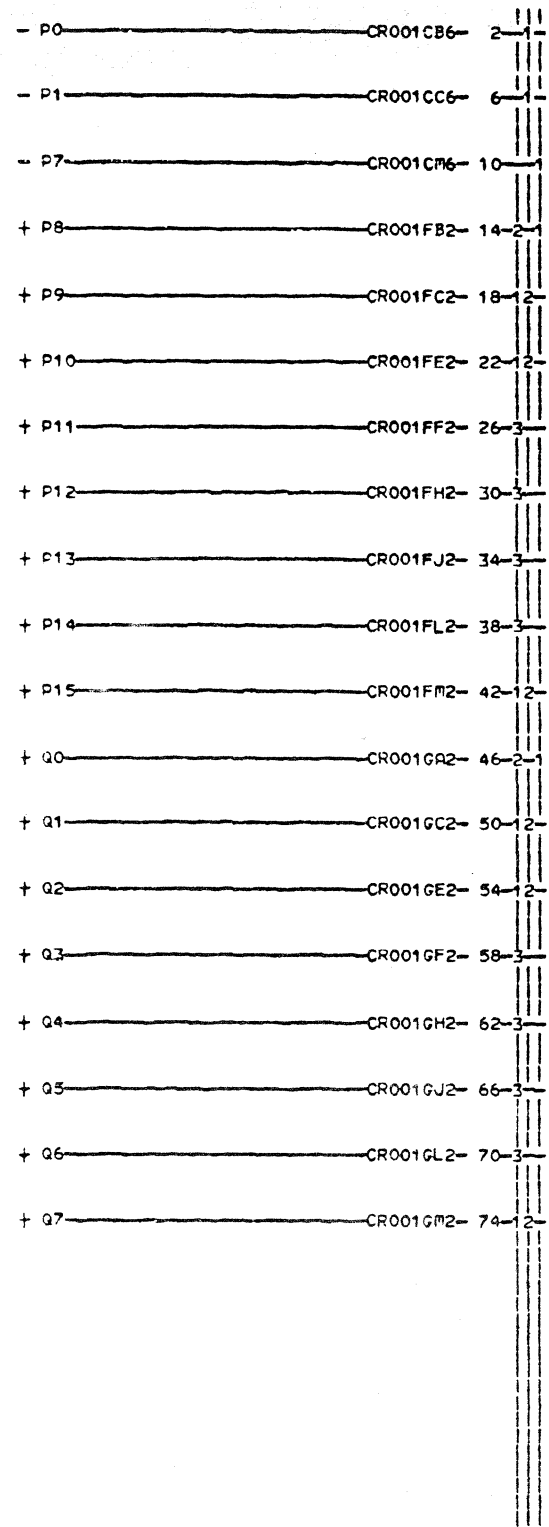


EDGE CONN.	A-B3S2B02	A-B3S2P06	A-B3S2M11
22 RESISTOR	A-B3S2B02	62 RESISTOR	82 RESISTOR
A-B3S2M02	A-B3S2B04	A-B3S2M07	A-B3S2M13
26 RESISTOR	A-B3S2D04	66 RESISTOR	
A-B3S2J10	A-B3S2D04	A-B3S2P04	
30 RESISTOR	A-B3S2B05	70 RESISTOR	
A-B3S2P02	A-B3S2B05	A-B3S2P12	
34 RESISTOR	A-B3S2P07	74 RESISTOR	
A-B3S2J13	A-B3S2P07	A-B3S2S02	
38 RESISTOR	58 RESISTOR	78 RESISTOR	

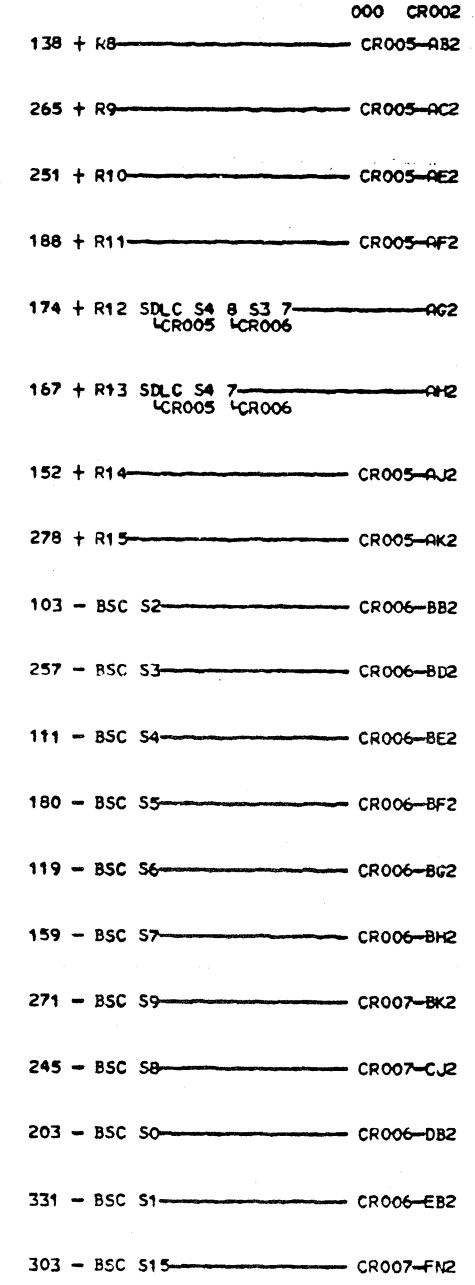
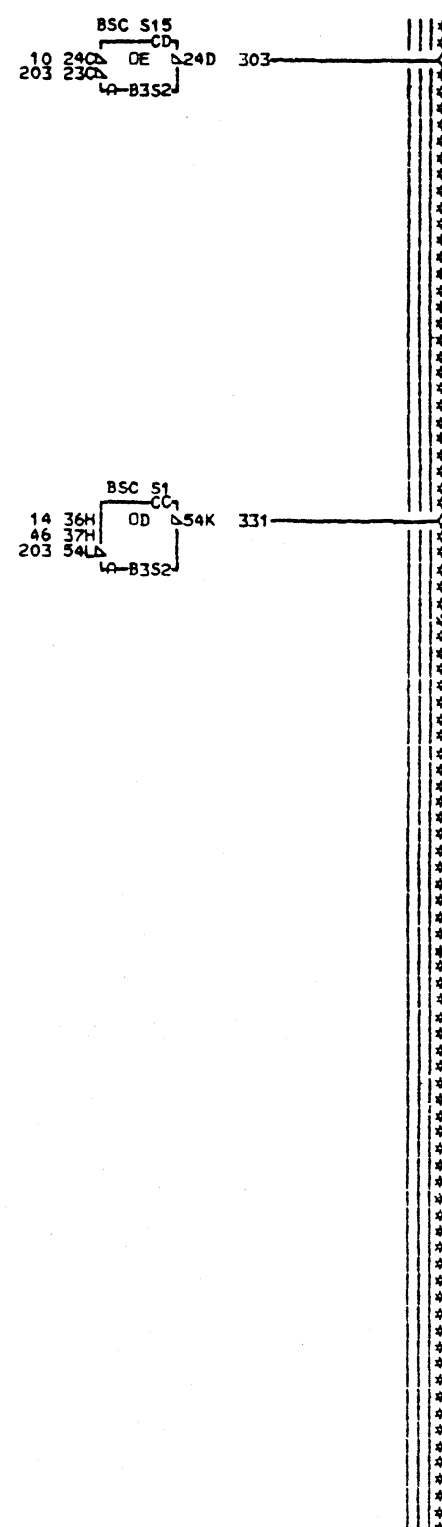
LOC. TYPE
A-B3S2 Y703

CR001
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CC CONTRL
 BSC AND DLC CRC
 E.C. HISTORY — E-MACH. 3705
 FRAME 01
 DATE LAST EC 10-14-60 344270
 IBM CORP. SCD P.N. 1769198
 CR001 000



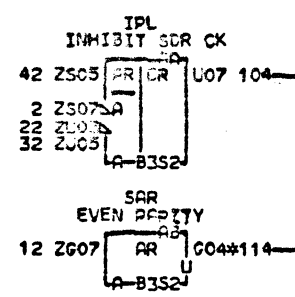
LOC. TYPE
A-B3S2 Y703



CR002
000

CC CONTROL
 BSC AND SDLC CRC
 E.C.HISTORY E MACH. 3705
 FRAME 01
 IBM CORP. SCD CR002
 DATE LAST EC 10-14-80 344270
 P.No. 1769199 000

- STORE INST TYPE - CD002CN2 - 2-
 + SAR EVEN PARITY - CK003FF2 - 12-
 + MEM TESTS OR SCANS - CU007FC6 - 22-
 + IPL 2 LATCH - CU010DH2 - 32-
 + BOOTSTRAP MODE - CU010GD2 - 42-



000 CR003
 104 + IPL INHIBIT SDR CK - CK003-RE4
 114 + SAR EVEN PARITY - AP003-CC6

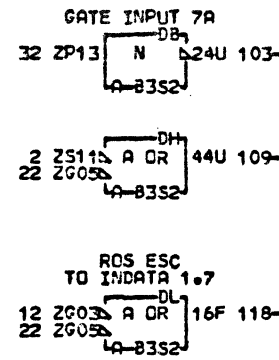
EDGE CONN.
 114 A-83A5D03

LOC. TYPE
 A-B352 Y703

CR003
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CC CONTRL	E. MACH. 3705
BSC AND DLC CRC	
E.C. HISTORY	
344270	FRAME 01
DATE LAST EC	IBM CORP. SCD CR003
06-02-81 344828	P.No. 1769200 000

- FLOAT - AJ001BH4 2
 + ROS ESCAPE - AJ001EC4 12
 - GATE INPUT 79 PROG L1 CURRNT-CP004CA6 22-2
 + GATE INPUT 7A - CQ005DJ2 32

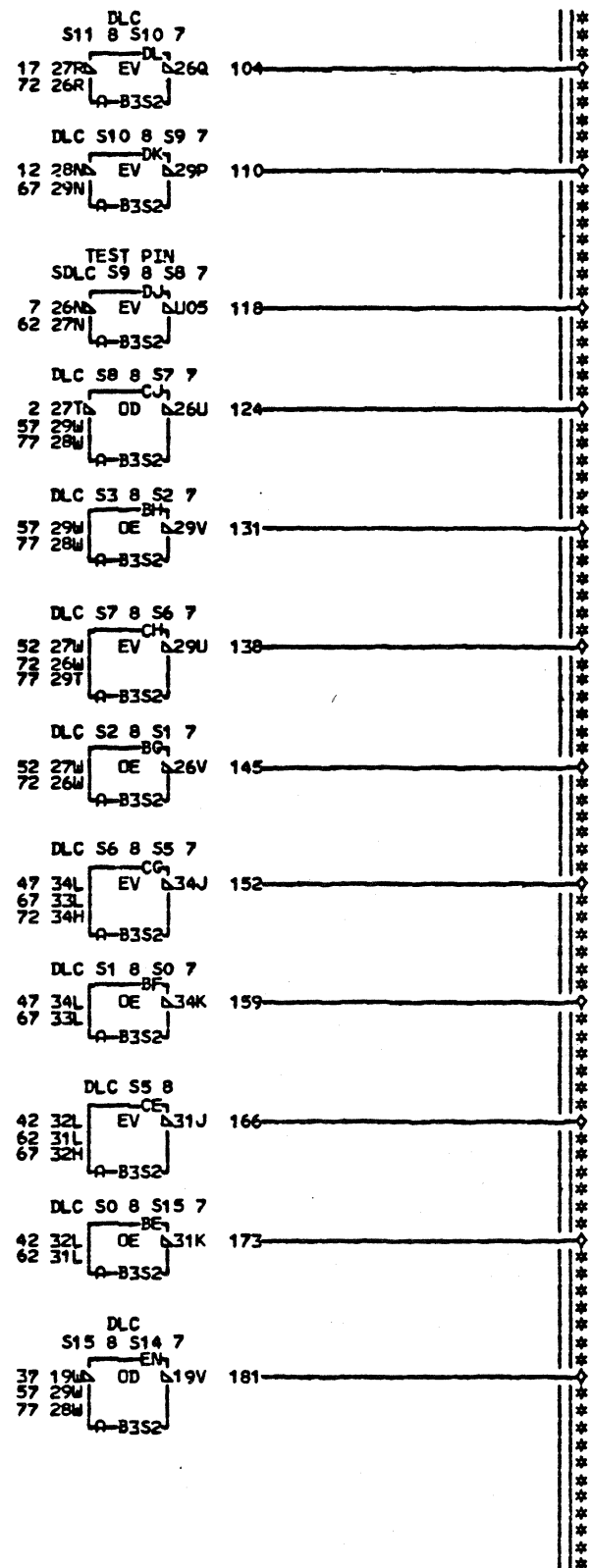
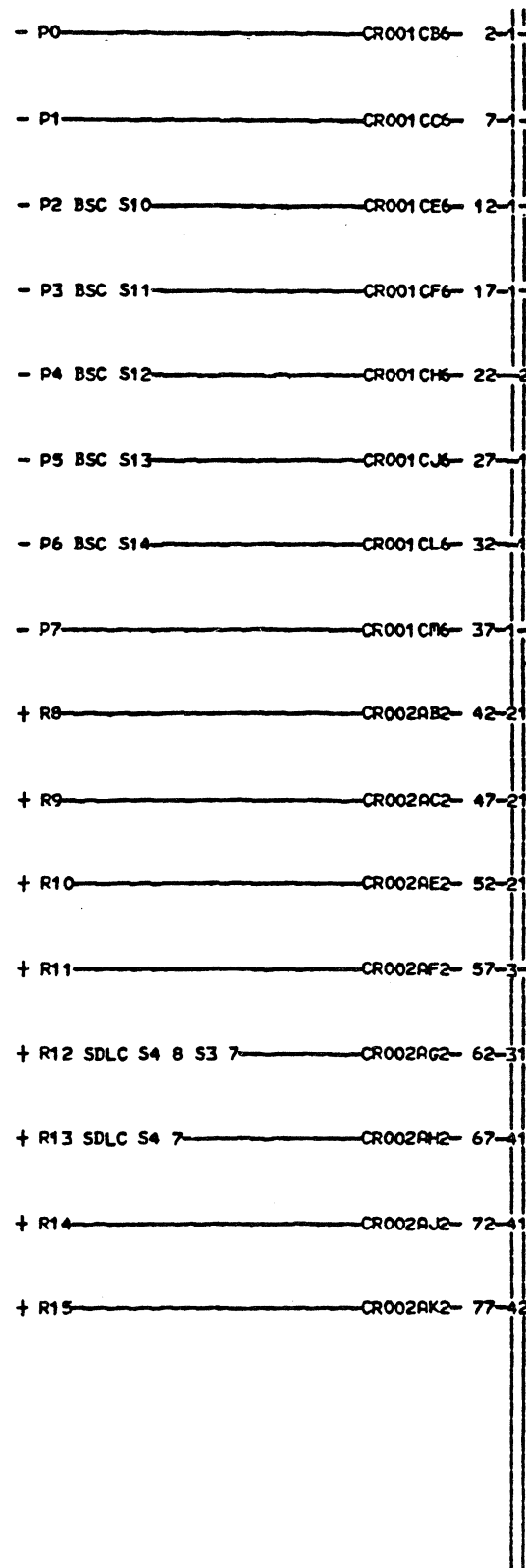


000 CR004
 103 - GATE INPUT 7A - DB2
 LCR006 LCR007
 109 ALWAYS + AT IN 79 AND L1 TIME - DH2
 LCR007
 118 + ROS ESC TO INDATA 1.7 - CR007-DL2

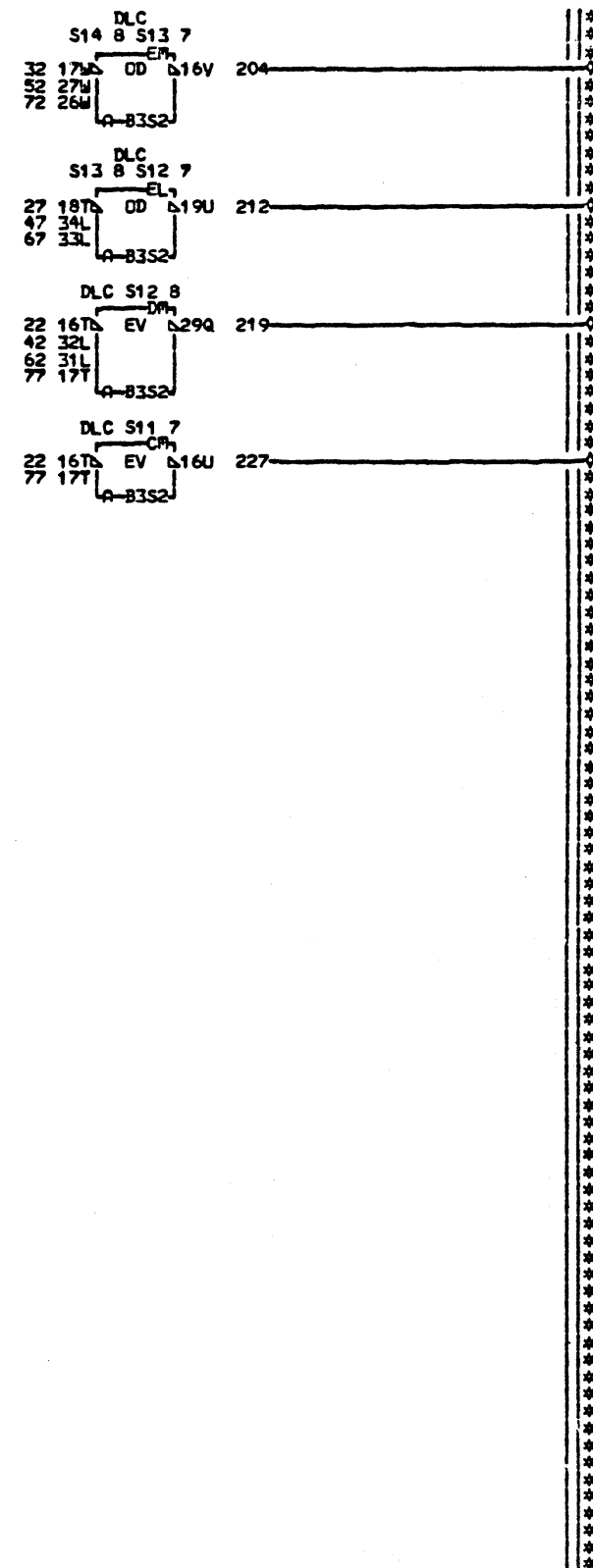
LDC. TYPE
 A-83S2 Y703

CR004
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CC CONTROL BSC AND DLC CRC E.C. HISTORY		E. MACH. 3705	
DATE	LAST EC	FRAME	01
10-14-80	344270	IBM CORP. SCD	CR004
		P. N. 1769201	000



LDC TYPE
A-B3S2 Y703

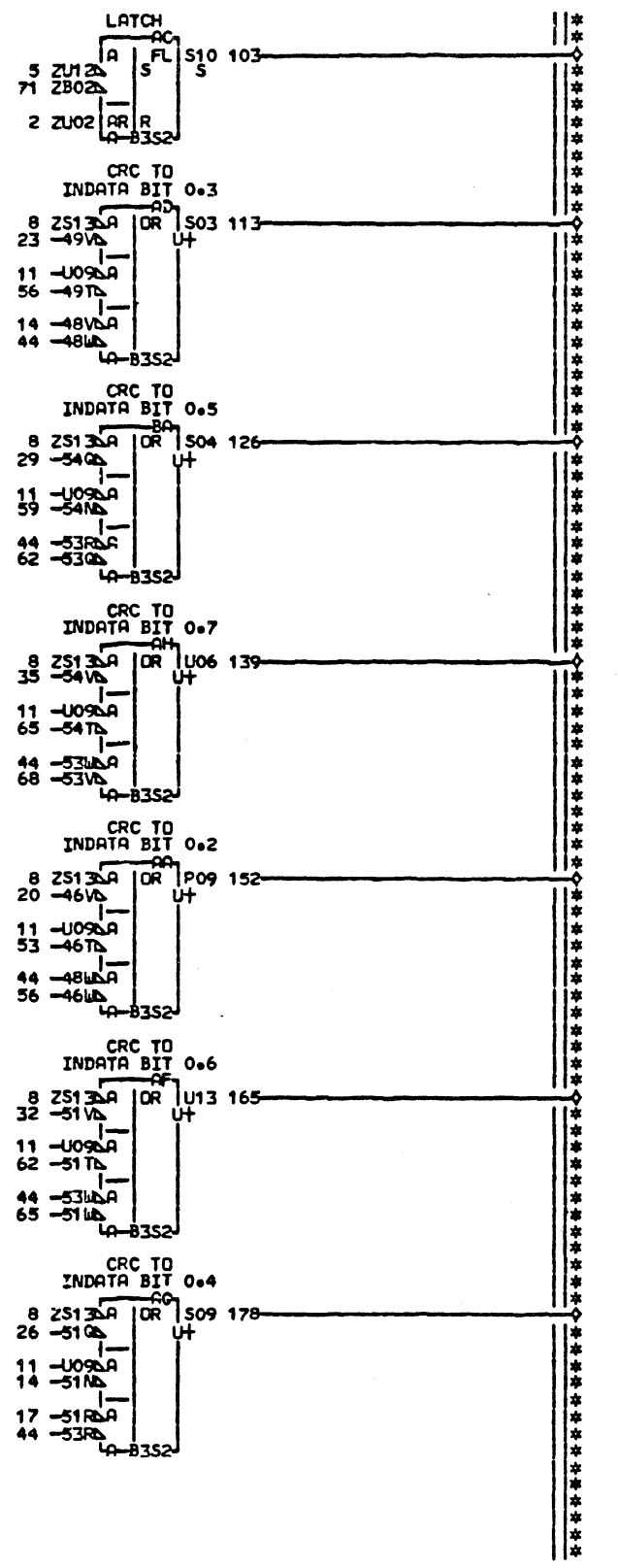


- 000 CR005
- 173 - DLC S0 8 S15 7 — BE2
LCR006 LCR007
 - 159 - DLC S1 8 S0 7 — CR006-BF2
 - 145 - DLC S2 8 S1 7 — CR006-BG2
 - 131 - DLC S3 8 S2 7 — CR006-BH2
 - 166 - DLC S5 8 — CR006-CE2
 - 152 - DLC S6 8 S5 7 — CR006-CG2
 - 138 - DLC S7 8 S6 7 — CR006-CH2
 - 124 - DLC S8 8 S7 7 — CJ2
LCR006 LCR007
 - 227 - DLC S11 7 — CR007-CF2
 - 118 - TEST PIN SDLC S9 8 S8 7 CR007-DJ2
 - 110 - DLC S10 8 S9 7 — CR007-DK2
 - 104 - DLC S11 8 S10 7 — CR007-DL2
 - 219 - DLC S12 8 — CR007-DM2
 - 212 - DLC S13 8 S12 7 — CR007-EL2
 - 204 - DLC S14 8 S13 7 — CR007-EM2
 - 181 - DLC S15 8 S14 7 — CR007-EN2

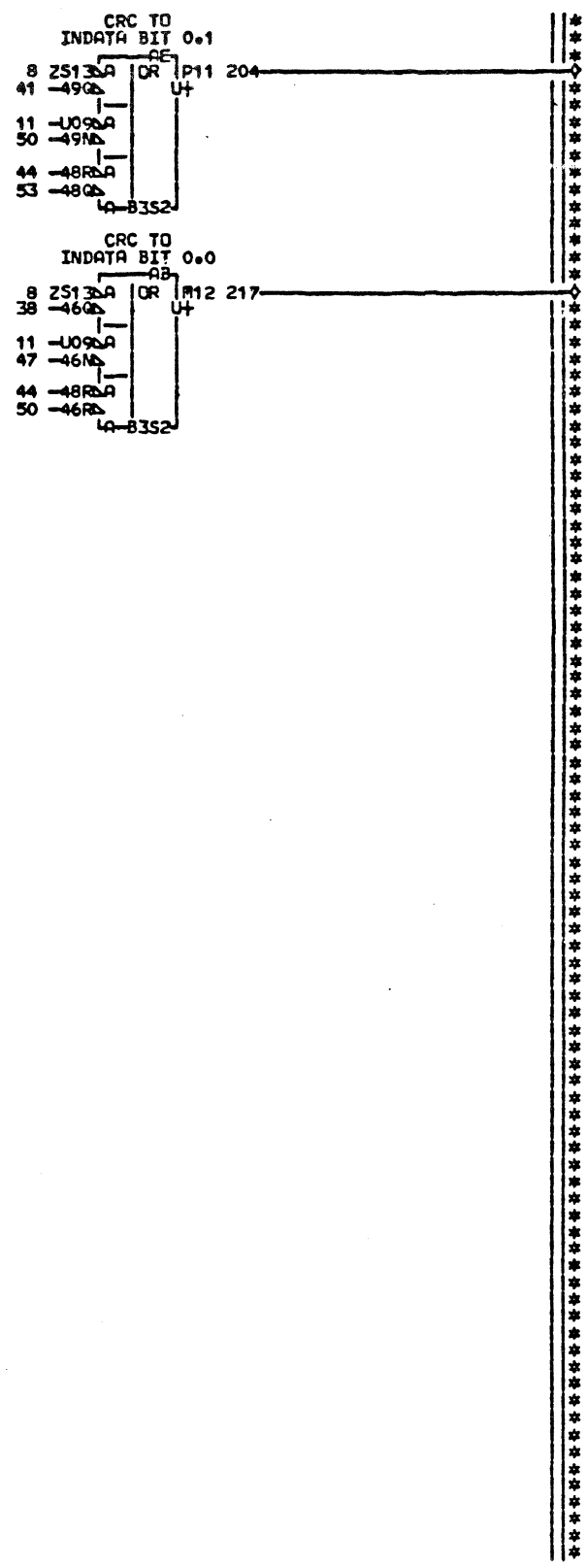
CR005
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CC CONTROL		MACH.3705	
BSC AND DLC CRC		FRAME 01	
EoC-HISTORY		IBM CORP.SCD CR005	
DATE	LAST EC	PoNo	000
10-14-80	344270	1769202	

- + POWER ON RESET — AP008DB4 — 2-1
- SET OUTPUT 79 — CQ005DC6 — 5-1
- GATE INPUT 7B — CQ005DM6 — 8-62
- GATE INPUT 7C — CQ005FJ6 — 11-62
- + R12 SDLC S4 8 S3 7 — CR002AG2 — 14-2
- + R13 SDLC S4 7 — CR002AH2 — 17-1
- BSC S2 — CR002BB2 — 20-1
- BSC S3 — CR002BD2 — 23-1
- BSC S4 — CR002BE2 — 26-1
- BSC S5 — CR002BF2 — 29-1
- BSC S6 — CR002BG2 — 32-1
- BSC S7 — CR002BH2 — 35-1
- BSC S0 — CR002DB2 — 38-1
- BSC S1 — CR002EB2 — 41-1
- GATE INPUT 7A — CR004DB2 — 44-62
- DLC S0 8 S15 7 — CR005BE2 — 47-1
- DLC S1 8 S0 7 — CR005BF2 — 50-2
- DLC S2 8 S1 7 — CR005BG2 — 53-1
- DLC S3 8 S2 7 — CR005BH2 — 56-2
- DLC S5 8 — CR005CE2 — 59-1
- DLC S6 8 S5 7 — CR005CG2 — 62-2
- DLC S7 8 S6 7 — CR005CH2 — 65-2
- DLC S8 8 S7 7 — CR005CJ2 — 68-1
- Z BUS BIT 0.4 — DH014GK6 — 71-1



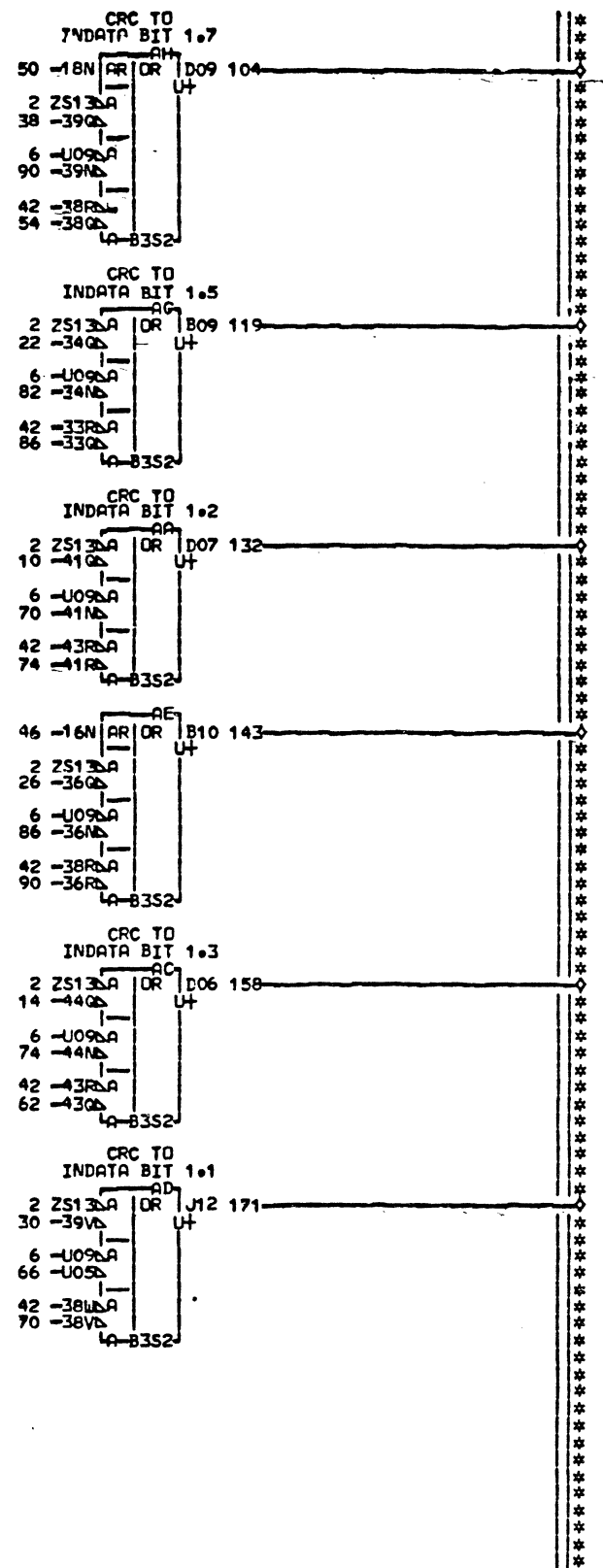
LOC. TYPE
A-83S2 Y703



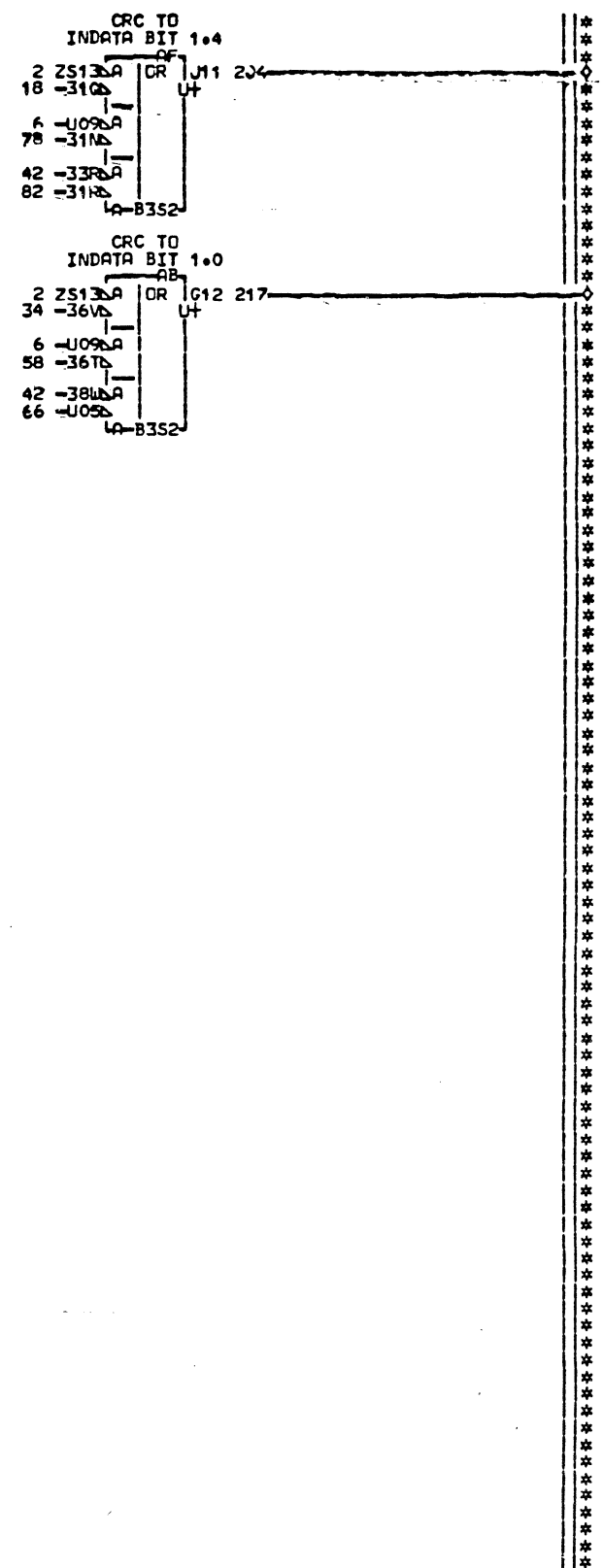
- 103 TEST PIN — AE6
- 217 + CRC TO INDATA BIT 0.0 — CU011-AF6
- 204 + CRC TO INDATA BIT 0.1 — CU011-CF6
- 178 + CRC TO INDATA BIT 0.4 — CU011-DF6
- 165 + CRC TO INDATA BIT 0.6 — CU011-DG6
- 152 + CRC TO INDATA BIT 0.2 — CU011-EF6
- 139 + CRC TO INDATA BIT 0.7 — CU011-EG6
- 126 + CRC TO INDATA BIT 0.5 — CU011-EH6
- 113 + CRC TO INDATA BIT 0.3 — CU011-GF6

CC CONTROL		E-MACH. 3705	
BSC AND DLC CRC		FRAME	01
E-C-HISTORY		IBM CORP. SCD	CR006
DATE	LAST EC	PoN.	1769203 000
10-14-80	344270		

- GATE INPUT 7B - CR005DM6 - 2-62
 - GATE INPUT 7C - CR005FJ6 - 6-62
 - P2 BSC S10 - CR001CE6 - 10-1
 - P3 BSC S11 - CR001CF6 - 14-1
 - P4 BSC S12 - CR001CH6 - 18-1
 - P5 BSC S13 - CR001CJ6 - 22-1
 - P6 BSC S14 - CR001CL6 - 26-1
 - BSC S9 - CR002BK2 - 30-1
 - BSC S8 - CR002CJ2 - 34-1
 - BSC S15 - CR002FN2 - 38-1
 - GATE INPUT 7A - CR004DB2 - 42-62
 ALWAYS + AT IN 79 AND L1 TIME - CR004DH2 - 46-1
 + ROS ESC TO INDATA 1.7 - CR004DL2 - 50-1
 - DLC S0 8 S15 7 - CR005BE2 - 54-1
 - DLC S8 8 S7 7 - CR005CJ2 - 58-1
 - DLC S11 7 - CR005CM2 - 62-1
 - TEST PIN SDLC S9 8 S8 7 - CR005DJ2 - 66-1
 - DLC S10 8 S9 7 - CR005DK2 - 70-2
 - DLC S11 8 S10 7 - CR005DL2 - 74-2
 - DLC S12 8 - CR005DM2 - 78-1
 - DLC S13 8 S12 7 - CR005EL2 - 82-1
 - DLC S14 8 S13 7 - CR005EM2 - 86-2
 - DLC S15 8 S14 7 - CR005EN2 - 90-2



LDC. TYPE
A-B352 Y703



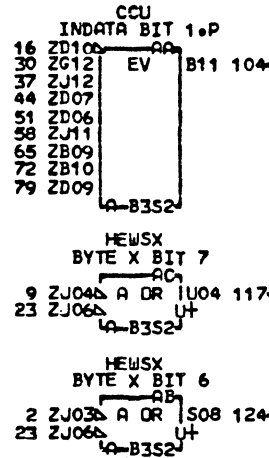
000 CR007

217 + CRC TO INDATA BIT 1.0 - CU012-AF6
 204 + CRC TO INDATA BIT 1.4 - CU012-BF6
 171 + CRC TO INDATA BIT 1.1 - CU012-CF6
 158 + CRC TO INDATA BIT 1.3 - CU012-DF6
 143 +CRC OR IN 79 L1 TO INDATA 1.6 -DG6
 CU013
 132 + CRC TO INDATA BIT 1.2 - CU012-EF6
 119 + CRC TO INDATA BIT 1.5 - CU012-FF6
 104 + CRC TO INDATA BIT 1.7 - CU013-GF6

CR007
000

CC CONTROL	E-MACH.3705	
BSC AND DLC CRC	HISTORY	
344270	FRAME	01
DATE	LAST EC	IBM CORP.SCD
06-02-81	344828	P.N. 1769204
		CR007
		000

- HEXSW BYTE X BIT 6 FROM CV - APO01EC4 - 2-1
 - HEXSW BYTE X BIT 7 FROM CV - APO01ED4 - 9-1
 - FORCE INDATA PARITY ERROR - CK001GA6 - 16-1
 - GATE HEXSW A AND C AND E - CS004GH2 - 23-2
 + CCU INDATA BIT 1.0 - CU012DC4 - 30-1
 + CCU INDATA BIT 1.1 - CU012DD4 - 37-1
 + CCU INDATA BIT 1.2 - CU012DE4 - 44-1
 + CCU INDATA BIT 1.3 - CU012DG4 - 51-1
 + CCU INDATA BIT 1.4 - CU012DJ4 - 58-1
 + CCU INDATA BIT 1.5 - CU012DK4 - 65-1
 + CCU INDATA BIT 1.6 - CU013CJ4 - 72-1
 + CCU INDATA BIT 1.7 - CU013CL4 - 79-1



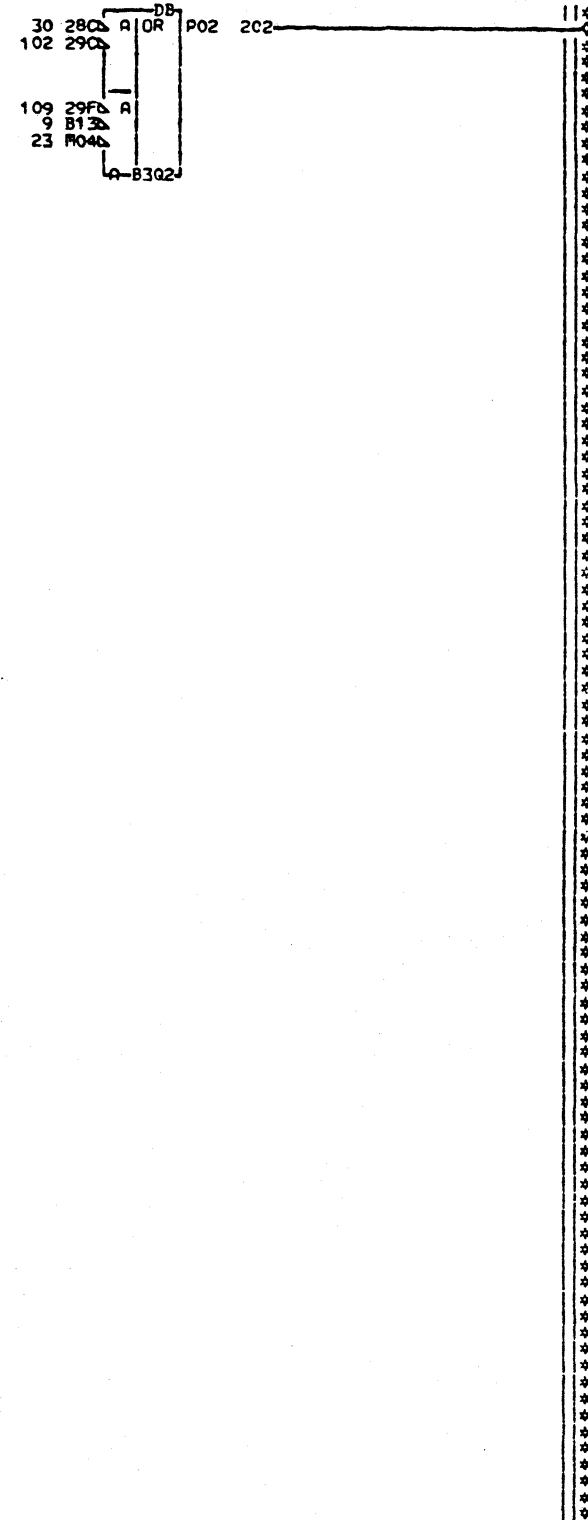
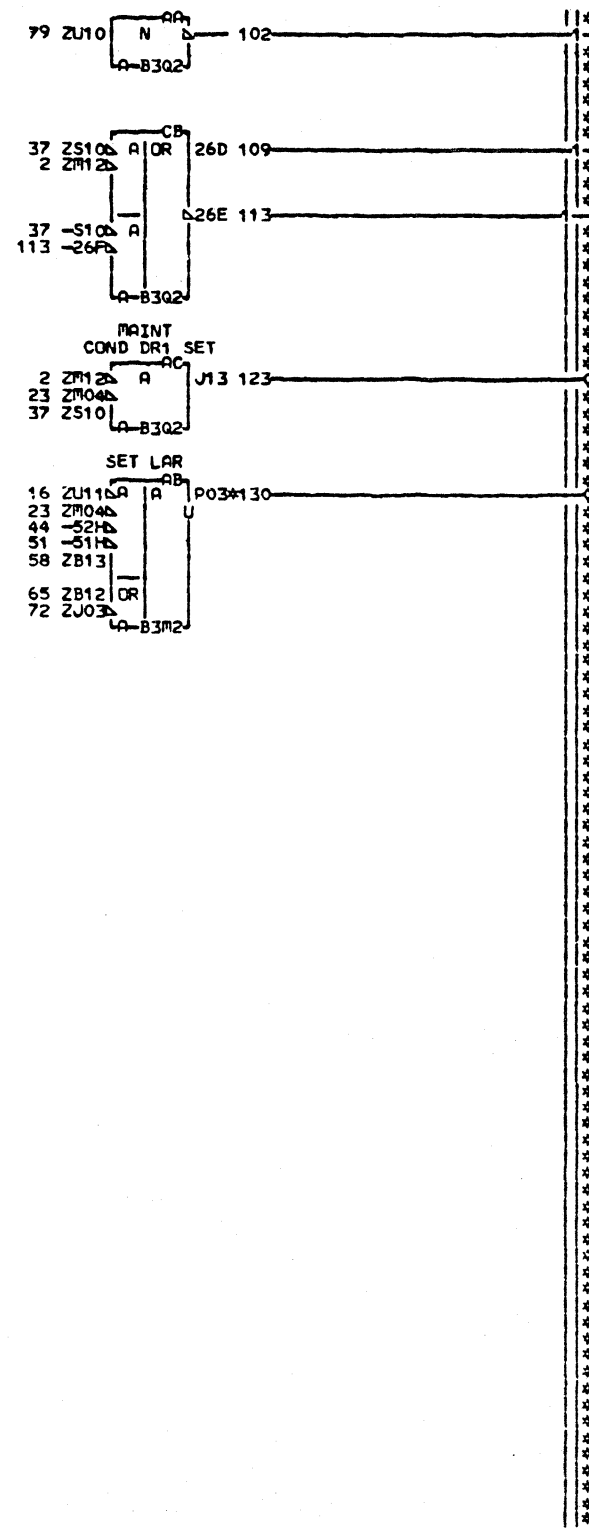
000 CRO08
 104 + CCU INDATA BIT 1.P - CU013-CG2
 124 + HEWSX BYTE X BIT 6 - CU011-DD2
 117 + HEWSX BYTE X BIT 7 - CU011-DE2

CRO08
 000

LOC. TYPE
 A-B3S2 Y703

CC CONTROL BSC AND DLC CRC E.C. HISTORY	MACH. 3705
344270	FRAME 01
DATE LAST EC 06-02-81 344828	IBM CORP. SCD P.N. 1769205 CRO08 000

- AB TIME — CC001EC2 — 2-3
 - DP TIME — CC001EL2 — 9
 - I1 CD TIME — CC003BJ6 — 16
 - T3 TIME — CC006HB6* — 23-21
 - ANY USEFUL CYCLE — CC008CN2 — 30
 + GO MAINT — CP001GD2 — 37-3
 + PROG LEV 1 ENTERED — CP003GC2 — 44
 + BID PROGRAM LEVEL 1 ALL — CP005BB6 — 51
 - GATE INPUT 74 — CQ004FJ6 — 58
 - PROGRAM STOP LATCH — CU001FK6 — 65
 - ACTIV INSN STEP OR CLK STEP — CU006CK2 — 72
 + ACTIVE CLOCK STEP GATED — CU017GC6 — 79



000 CS001
 202 + MAINT COND DR2 SET — CS007-DB2
 130 + SET LAR — DM2
 DE002 LDF002 LDC002 LDH002
 DJ002 DK002 DLL002 LDM002
 LDW001
 123 + MAINT COND DR1 SET — CS007-FC2

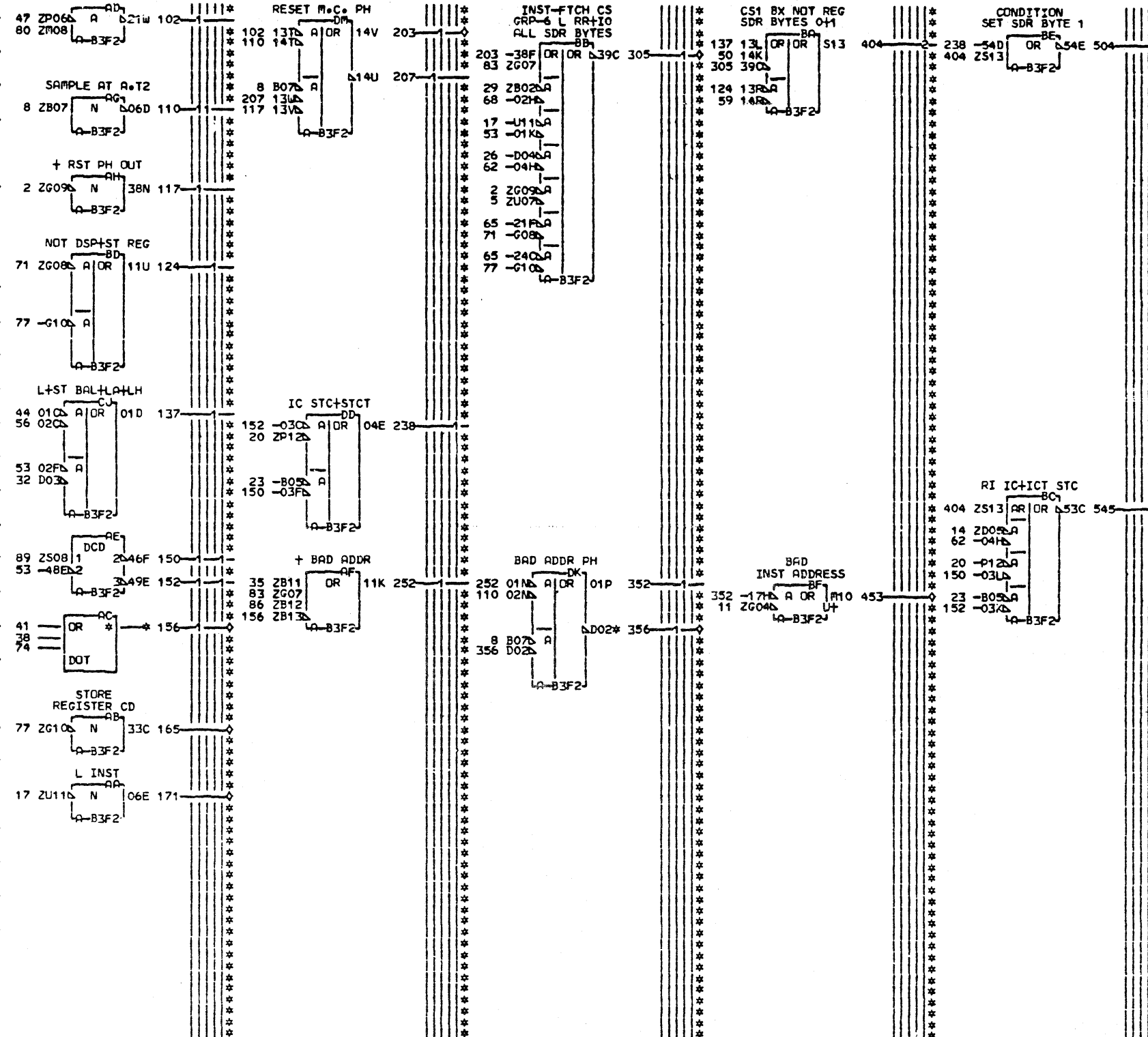
EDGE CONN.
 23 RESISTOR
 A-B3M2M04
 130 A-B3L6D02
 01A-B4L1D11

LOC. TYPE
 A-B3M2 6818
 A-B3Q2 6821

CS001
 000

DATA FLOW REGISTER CONTROL
 E.C. HISTORY — E. MACH. 3705
 FRAME 01
 IBM CORP. SCD CS001
 DATE LAST EC
 10-14-80 344270 P.N. 1769206 000

- BC TIME - CC001EG2- 2-1-1
 - I1 TIME + 125 NS - CC004FB6- 5-1-1
 + A+T2 TIME - CC008AM2- 8-1-1
 - ANY I TIME - CC008BM2- 11-1-1
 - RI INST TYPE - CD002CF6- 14-1-1
 - L INST - CD003AH6- 17-1-1
 - IC+ICT INST - CD003CC2- 20-1-1
 - STCT+STC INST - CD003CE2- 23-1-1
 - RR OR I-O INST TYPE - CD004BG2- 26-1-1
 - INST GROUP 6 - CD004BH2- 29-1-1
 - BAL+LALH INST - CD004BK2- 32-1-1
 + SAR EVEN PARITY - CK003FF2- 35-1-1
 ALWAYS PLUS - CM002EM6- 38-1-1
 ALWAYS PLUS - CM002GA6- 41-1-1
 - L+ST INST - CS004HM2- 44-1-1
 + ANY USEFUL TIME + 125 NS - CS006BD2- 47-1-1
 + CS2 BC TIME - CS006BF2- 50-1-1
 - I2 BC TIME - CS006BH6- 53-2-1
 - I3 BC TIME - CS006BK6- 56-1-1
 - CS1 BC TIME - CS006BM6- 59-1-1
 - I1 CD TIME - CS006DF6- 62-1-1
 - CS1 CD TIME - CS006DM6- 65-2-1
 - I2 AB TIME - CS006EH6- 68-1-1
 - DISPLAY REGISTER CD - CU003GD6- 71-1-1
 ALWAYS PLUS - CU003GE6- 74-1-1
 - STORE REGISTER CD - CU003GF6- 77-2-1
 + RESET MACHINE CHECK - CU006CB6- 80-1-1
 + RESET - CU010FM2- 83-1-1
 + PROTECTION ERROR - CV061FB2- 86-1-1
 + SAR BIT 1.7 - DM001EK2- 89-1-1



000 CS002
 * 171 + L INST - CS004-AD2
 * 165 + STORE REGISTER CD - CS004-AF2
 * 156 ALWAYS PLUS - BK4
 * 305 - CONDITION SET SDR BYTE X - DF2
 * 356 + BAD ADDRESS - DK6
 * 203 + RESET MACHINE CK SDR OP REG - DM2
 * 545 - CONDITION SET SDR BYTE 0 - EB2
 * 504 - CONDITION SET SDR BYTE 1 - ED2
 * 453 + BAD INST ADDRESS - CS007-EJ2

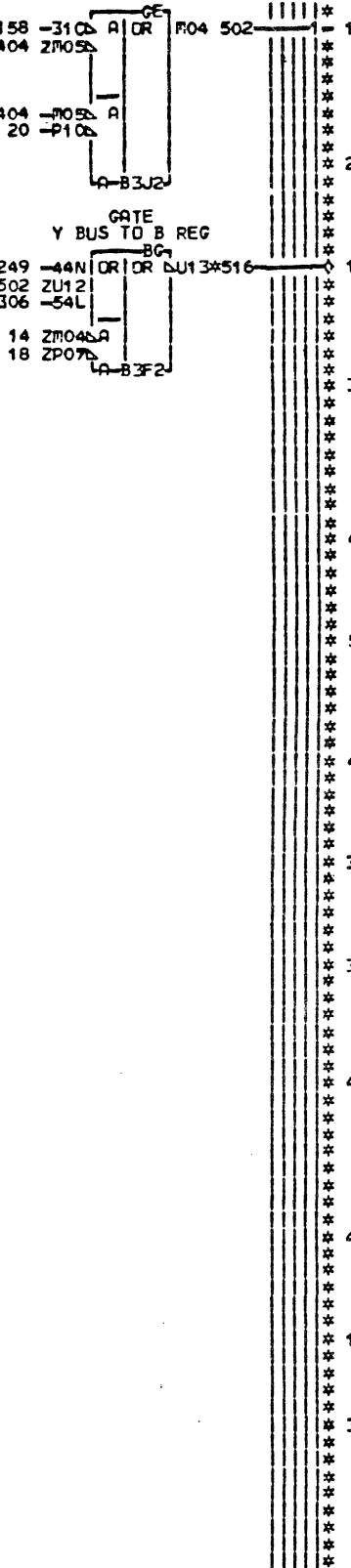
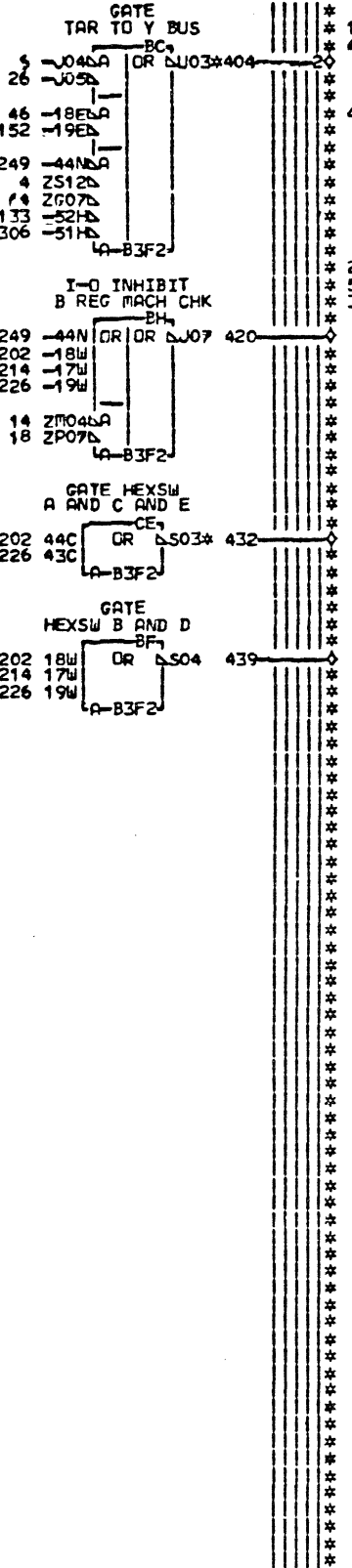
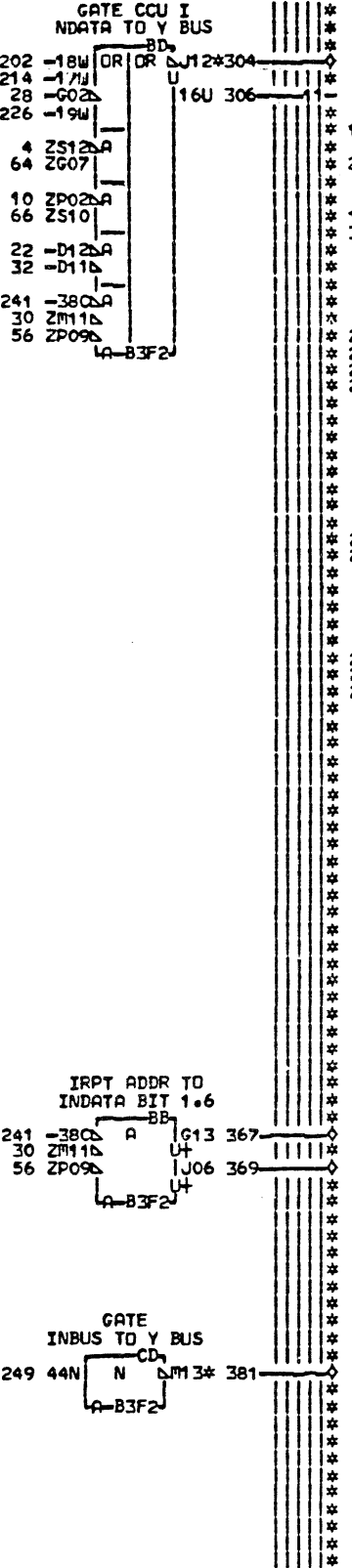
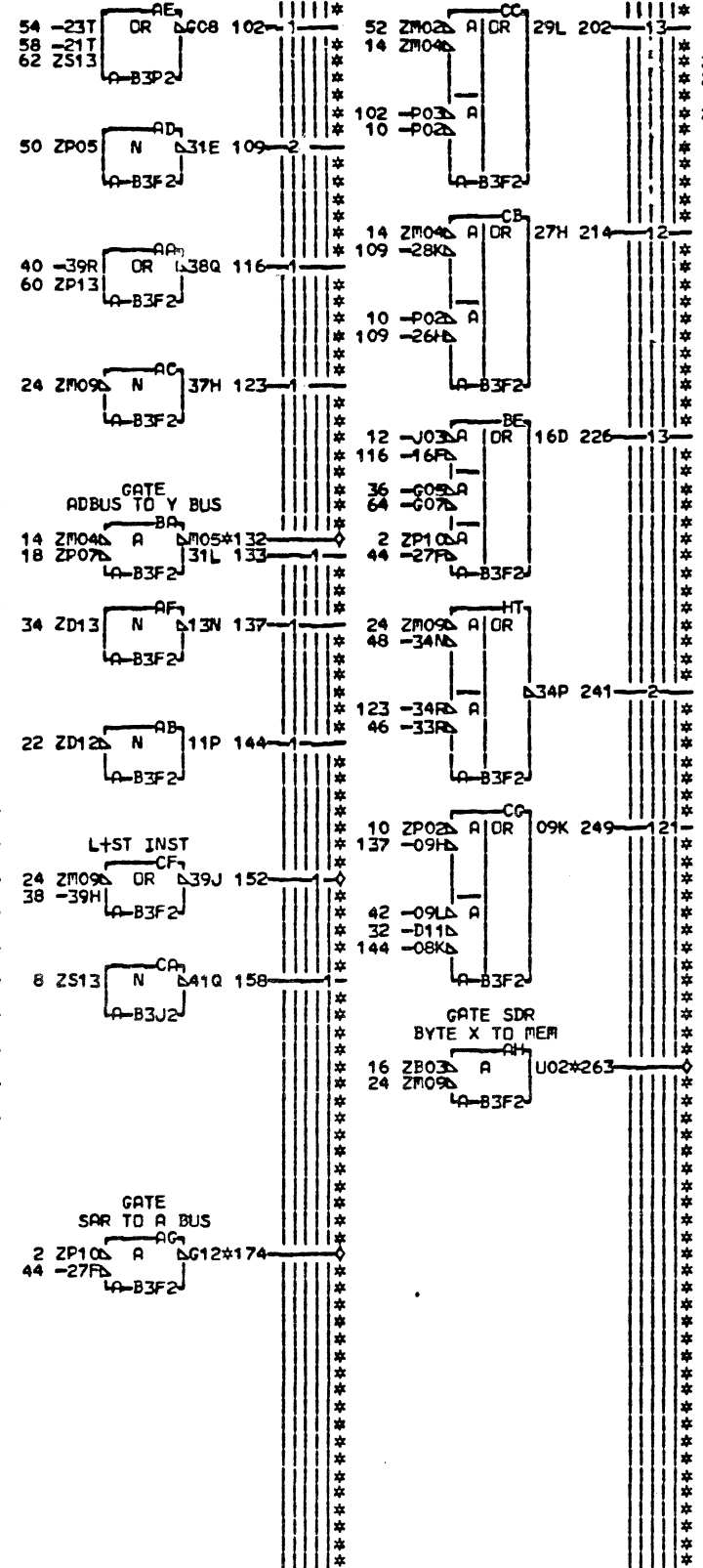
EDGE CONN.
 156 A-B4D6E02
 01A-B3D1E11
 356 A-B3B1E13
 01A-B4B6E04

LOC. TYPE
 A-B3F2 6810

CS002
 000

DATA FLOW REGISTER CONTROL			
-E.C.-HISTORY-		-MACH-3705	
DATE	LAST EC	FRAME	01
10-14-80	344270	IBM CORP.SCD	CS002
		P.No. 1769207	000

- AB TIME - CC001BE6 - 2-11
- DA TIME - CC001EL2 - 4-11
- I2 BC TIME - CC003CH6 - 6-11
- I2 XA TIME - CC003CL6 - 8-11
- CS1 AB TIME - CC003EC6 - 10-31
- CS1 BC TIME - CC003EH6 - 12-11
- CS1 XA TIME - CC003EL6 - 14-12-11
- I3 TIME - CC004EJ6 - 16-11
- CYCLE STEAL AB - CC008AE6 - 18-11
- BALR INST - CD001DK6 - 20-11
- INDATA I-O REG ADDR - CD001GG6 - 22-11
- ST INST - CD003AJ6 - 24-22
- BALR INST - CD003BM6 - 26-11
- GATE IRPT ADDR TO INDATA - CP002FN6 - 28-11
- PROG LEV 1 CURRENT - CP003DD6 - 30-2
- CCU GATE INPUT DATA - CQ001HB6 - 32-11
- CYCLE STEAL IN AB - CQ002AF2 - 34-11
- GATE INPUT 71 - CQ004DK6 - 36-11
- + L INST - CS002AD2 - 38-11
- + STORE REGISTER CD - CS002AF2 - 40-11
- + LS I-O REG ADDR - CS003GA2 - 42-11
- ANY I TIME + 125 NS - CS006AD6 - 44-11
- I2 CD TIME - CS006DH6 - 46-11
- I3 CD TIME - CS006DK6 - 48-11
- + DISPLAY REGISTER AB - CU003FC2 - 50-11
- DISPLAY STORAGE AB - CU003FD6 - 52-11
- + STORE STORAGE AB - CU003FG2 - 54-11
- VIRGIN LEVEL CD - CU003GL6 - 56-2
- + ACTIVE SGL ADDR TEST - CU007EE2 - 58-11
- + ACTIVE SGL ADDR SCAN - CU007EL2 - 60-11
- + ACTIVE STORAGE TEST - CU007FF4 - 62-11
- + RESET - CU010FM2 - 64-11
- + BOOTSTRAP MODE GATED - CU016GE2 - 66-11



- 174 - GATE SAR TO A BUS - BK6
 CCF003 LDF975 LDF975 LDF975
 LDH015 LDJ015 LDK975 LDL005
- 263 + GATE SDR BYTE X TO MEM - BM2
 LCS006 LDR993
- 132 - GATE ADBUS TO Y BUS - CA6
 ODE971 LDF971 LDF971 LDH011
 LDJ011 LDK971 LDL001 LDM001
- 304 - GATE CCU INDATA TO Y BUS - DB2
 ODU013 LCU001 LCU011 LCU013
 ODE971 LDF971 LDF971 LDH011
 ODU011 LDK971 LDL001 LDM001
- 439 - GATE HEXSW B AND D - EB2
 LAP001 LAP002
- 516 - GATE Y BUS TO B REG - ED2
 ODE002 LDF002 LDF002 LDH002
 LDJ002 LDK002 LDL002 LDM002
- 420 - I-O INHIBIT B REG MACH CHK - EF2
 LCR003
- 367 + IRPT ADDR TO INDATA BIT 1.6 - FD6
 LCU013
- 381 - GATE INBUS TO Y BUS - FG2
 ODE971 LDF971 LDF971 LDH011
 LDJ011 LDK971 LDL001 LDM001
- 404 - GATE TAR TO Y BUS - FJ2
 ODE971 LDF971 LDF976 LDF971
 ODF976 LDH003 LDH011 LDJ003
 ODU011 LDK971 LDK976 LDL001
 LDL003 LDM001 LDM003
- 432 - GATE HEXSW A AND C AND E - GH2
 LAP001 LAP002 LCG001 LCR008
- 152 - L+ST INST - CS002-HM2
- 369 + IRPT ADDR TO INDATA BIT 1.3 - HL2
 LCU012

EDGE CONN.

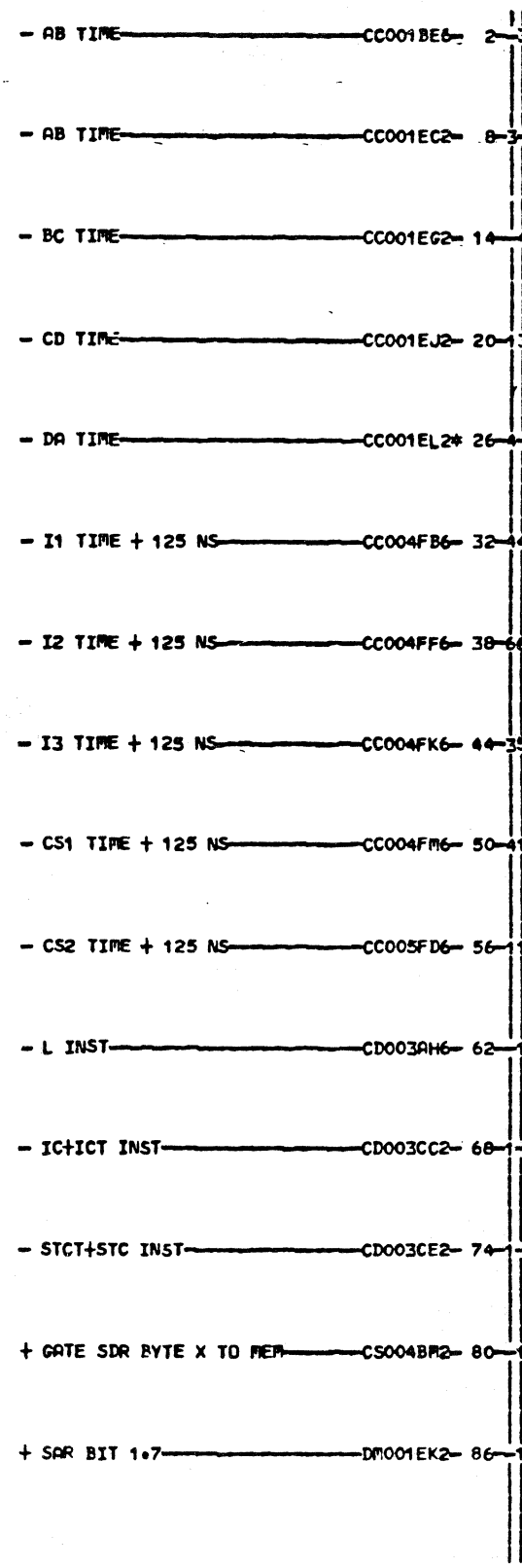
132 A-B3H1D11	01A-B4H6E04
01A-B4H6D02	01A-B4J6A02
174 A-B3H1B11	432 A-B3E1E11
01A-B4H6B02	01A-B4E6E02
263 A-B3H1C13	516 A-B3H1E11
01A-B4H6C04	01A-B4H6E02
304 A-B3H1D13	
01A-B4H6D04	
381 A-B3H1E13	

LOC. TYPE

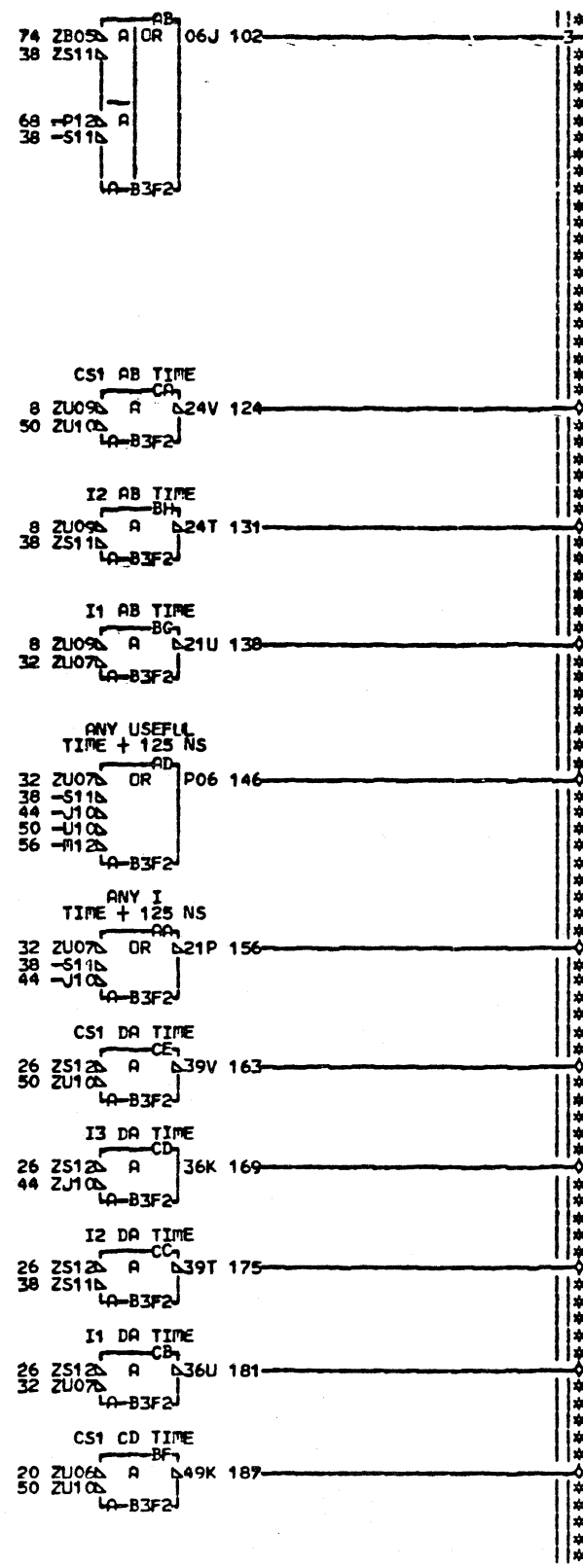
A-B3F2	6810
A-B3J2	6815
A-B3P2	6820

DATA FLOW REGISTER CONTROL

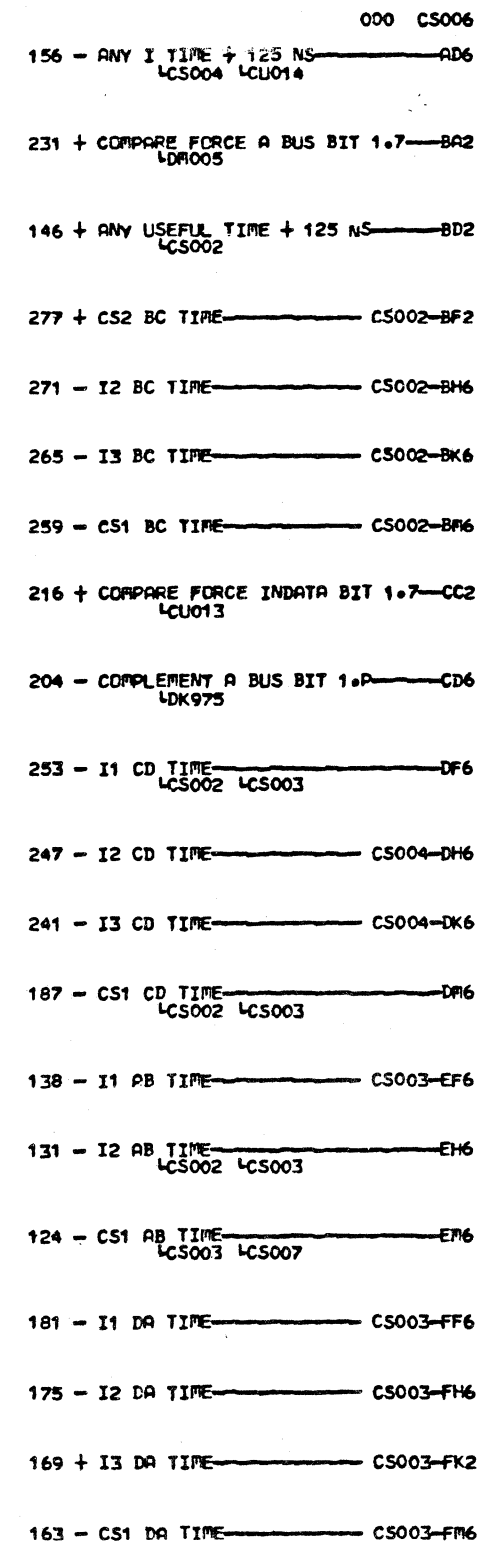
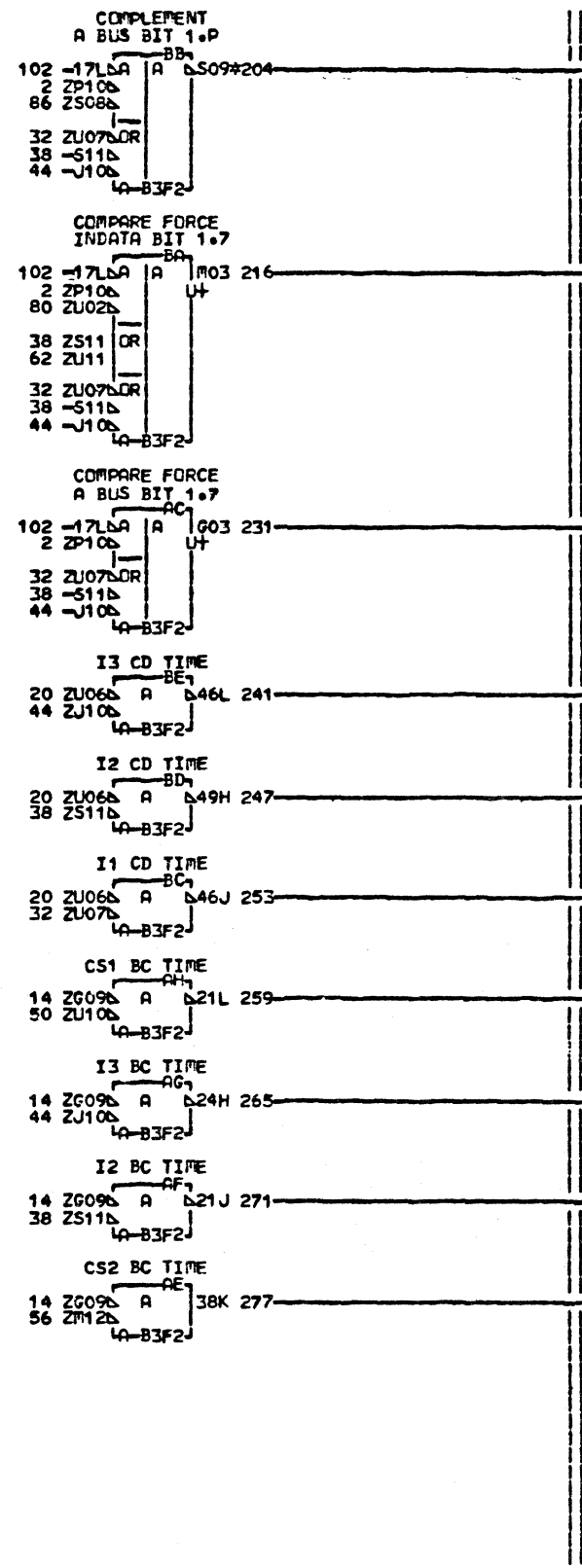
E.C. HISTORY	E1 MACH. 3705
344270	FRAME 01
DATE LAST EC	IBM CORP. SCD CS004
06-02-81 344828	P.N. 1769209 000



EDGE CONN.
26 RESISTOR
A-B3F2S12
204 A-B3V3D11
01A-B4V3D11



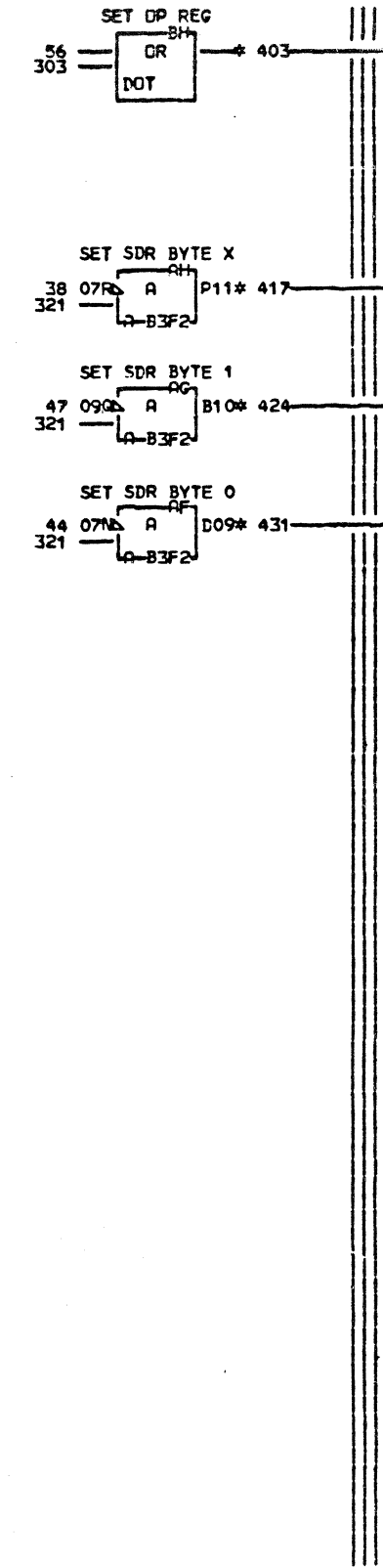
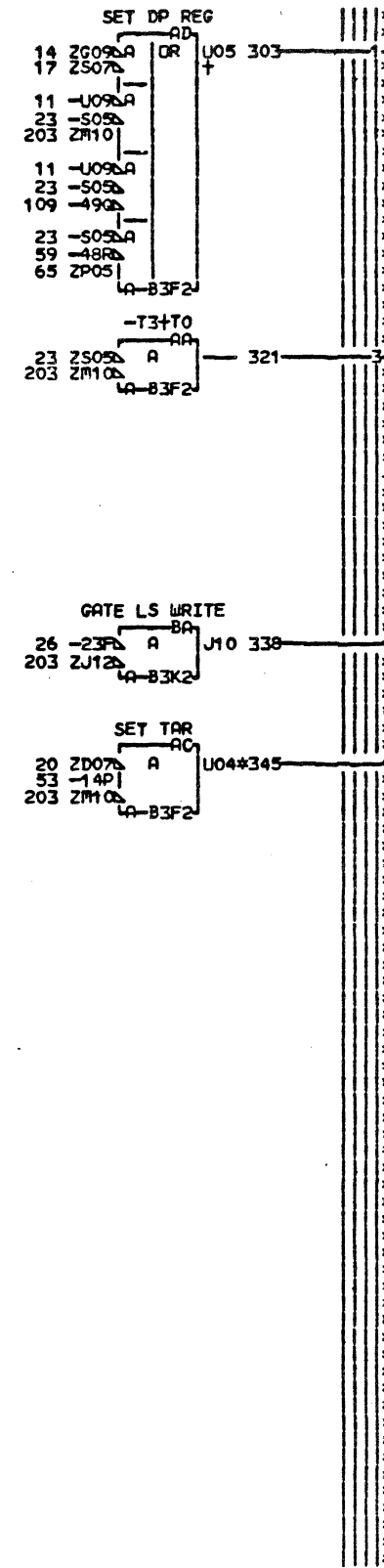
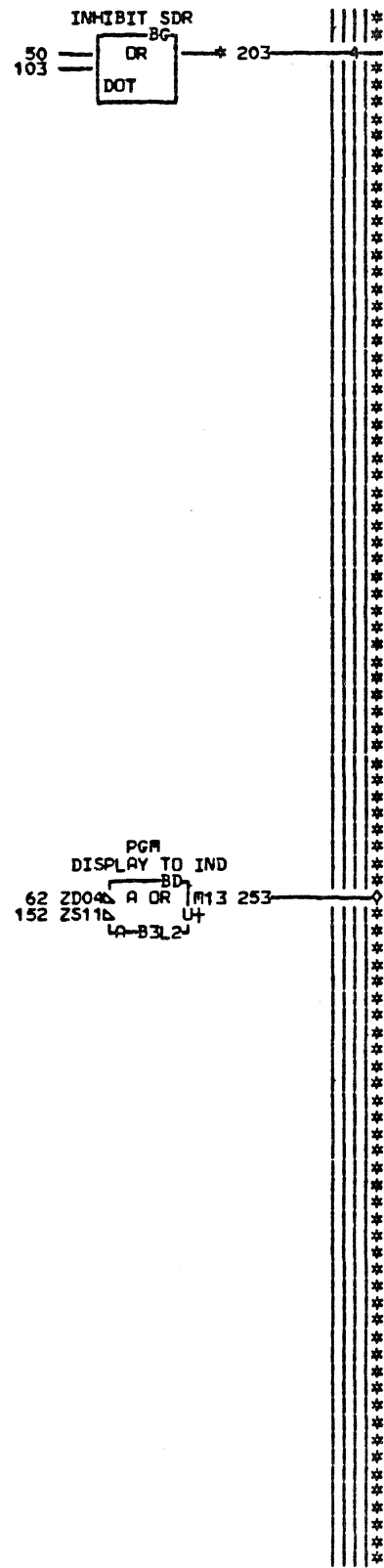
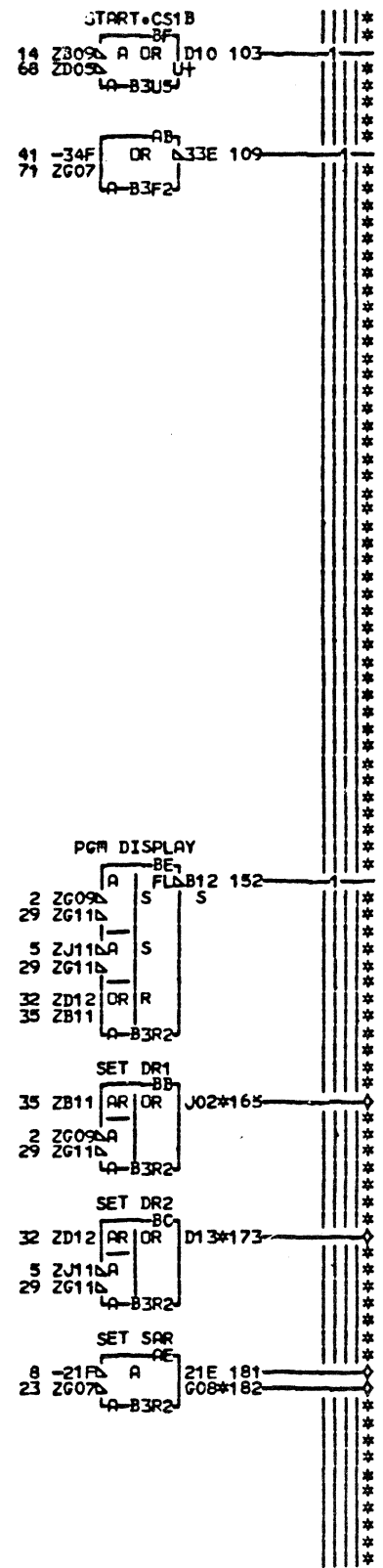
LOC. TYPE
A-B3F2 6810



CS006
000

DATA FLOW REGISTER CONTROL
TIMING GENERATION
E-C-HISTORY E-MACH.3705
344270
FRAME 01
DATE LAST EC IBM CORP.SCD CS006
06-02-81 344828 P.No. 1769211 000

- DP XXXX XXXX X001 XXXX CA003BB4- 2-2
 - DP XXXX XXXX X010 XXXX CA003BB5- 5-2
 - DA TIME CC001DM6- 8-1
 - AB TIME CC001EC2- 11-2
 - BC TIME CC001EG2- 14-1
 - I1 B TIME CC002BC6- 17-1
 - T3 TIME CC006HB6- 20-1
 - T3+T0 TIME CC007HJ1* 23-1
 - COND LS WRITE CL005FH2- 26-1
 - SET OUTPUTS 70 TO 77 CQ004BA6- 29-1
 + MAINT COND DR2 SET CS001DB2- 32-2
 + MAINT COND DR1 SET CS001FC2- 35-2
 - CONDITION SET SDR BYTE X CS002DF2- 38-1
 + RESET MACHINE CK SDR DP REG CS002DM2- 41-1
 - CONDITION SET SDR BYTE 0 CS002EB2- 44-1
 - CONDITION SET SDR BYTE 1 CS002ED2- 47-1
 + BAD INST ADDRESS CS002EJ2- 50-1
 + CONDITION TAR SET CS003GK4- 53-1
 + I1.B TIME TO DP REG CS005SJ1- 56-1
 - CS1 AB TIME CS006EM6- 59-1
 - PHASE B SAMPLE PULSE CU001CB2- 62-1
 + DISPLAY REGISTER AB CU003FC2- 65-1
 - START CD CU003GB6- 68-1
 + RESET CU010FM2- 71-1



000 CS007

345 + SET TAR CH2
 QDC971 QDF971 LDG971 LDH011
 LDJ011 LDK971 LDL001 LDM001

131 + SET SAR CQ001-EA2

182 + SET SAR EB2
 QCU007 QDE971 QDF971 QDG971
 QDH011 QDJ011 QDK971 QDL001
 QDM001

431 + SET SDR BYTE 0 EE2
 QDN001 QDN002 QDP991 QDP992

424 + SET SDR BYTE 1 EF2
 QDQ001 QDQ002 QDR991 QDR992

417 + SET SDR BYTE X EG2
 QDN002 QDP991 QDP992 QDQ002
 QDR991 QDR992

338 + GATE LS WRITE CC006-EM2

165 + SET DR1 FC6
 QDE003 QDF003 QDG003 QDH003
 QDJ003 QDK003 QDL003 QDM003

173 + SET DR2 FD6
 QDE003 QDF003 QDG003 QDH003
 QDJ003 QDK003 QDL003 QDM003

253 + PGM DISPLAY TO IND AP009-FK2

403 + SET DP REG SD4
 QDN001 QDN002 QDP991 QDP992
 QDQ001 QDQ002 QDR991 QDR992

EDGE CONN. A-B3F2M10 01A-B4M1B11

23 RESISTOR	345	A-B3L6D04
	01	A-B4L1D13
165	A-B3M6C04	403 A-B3L6E02
01	A-B4M1C13	01 A-B4L1E11
173	A-B3M6D02	417 A-B3M6C02
01	A-B4M1D11	01 A-B4M1C11
182	A-B3M6A04	424 A-B3M6B04
01	A-B4M1A13	01 A-B4M1B13
203 RESISTOR	431	A-B3M6B02

LOC. TYPE

A-B3F2	6810
A-B3K2	6816
A-B3L2	6823
A-B3R2	AB92
A-B3U5	AB85

SET REGISTER TIMING

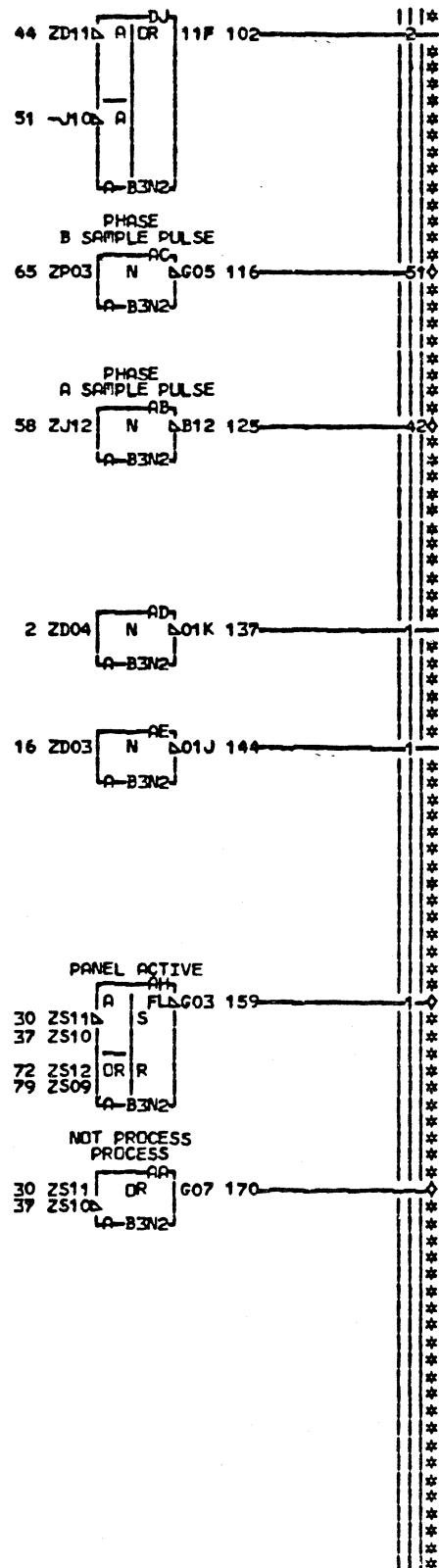
E.C.-HISTORY E.MACH.#3705

FRAME 01

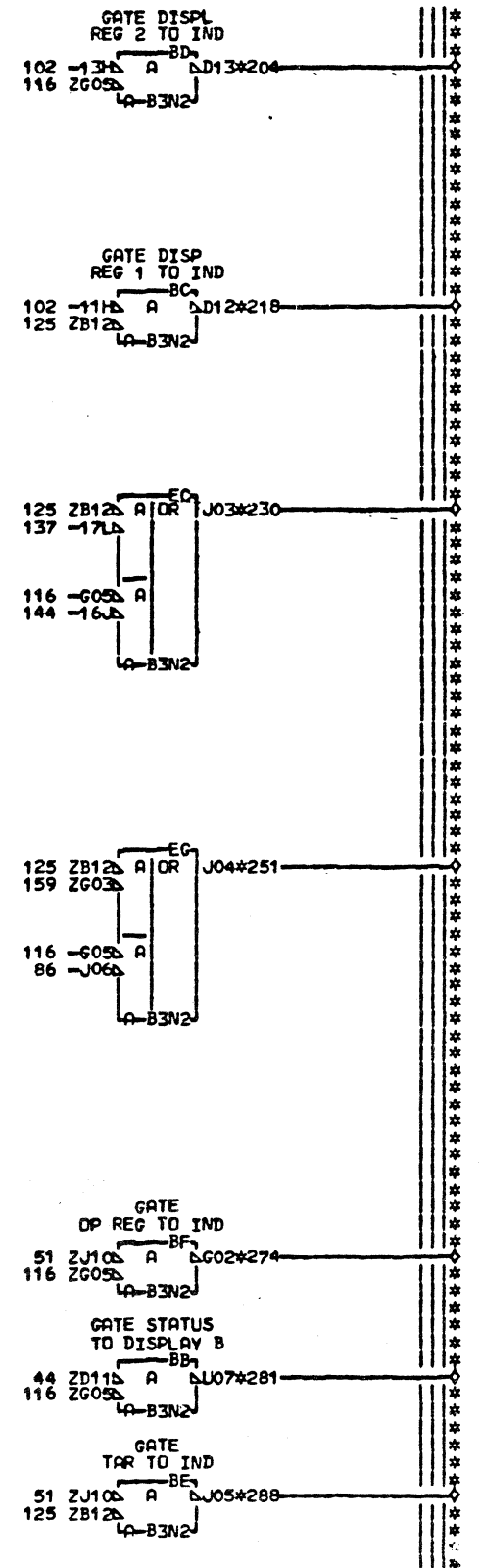
IBM CORP.SCD CS007

DATE LAST EC 10-14-80 344270 P.N. 1769212 000

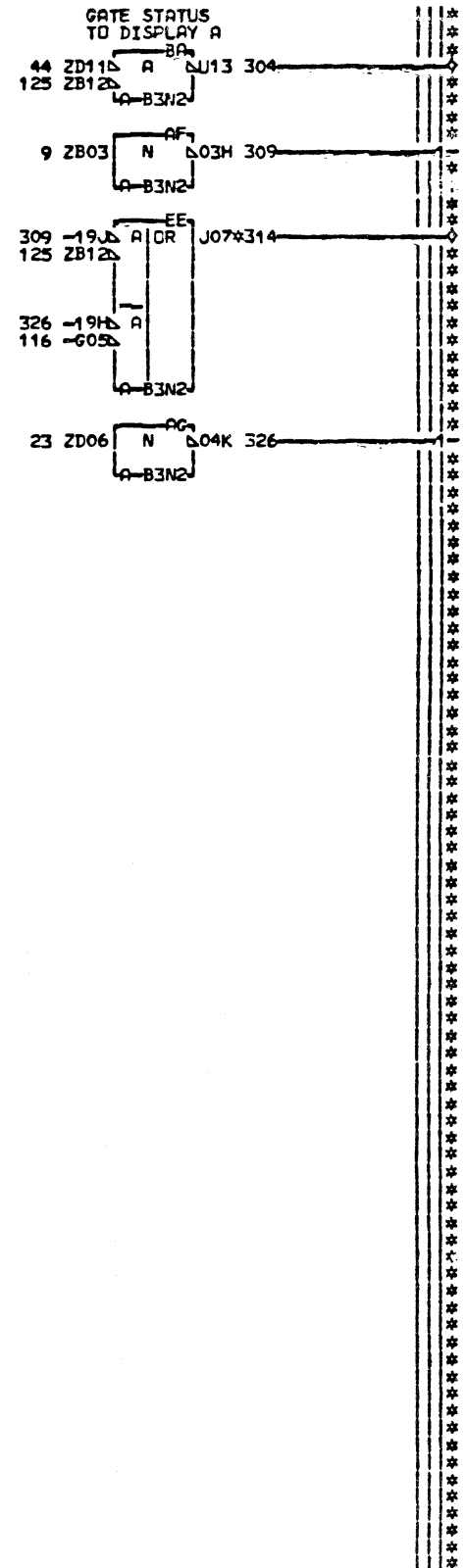
+ CHANNEL 1 INTF A ENABLED—AR002DL6* 2-1
 + CHANNEL 2 INTF A ENABLED—AR002DM1* 9
 + CHANNEL 1 INTF B ENABLED—AR002DM3* 16-1
 ALWAYS -4V THREE—AR002DM5* 23
 - DIAGNOSTIC PROCESS POSITION—AP003DD4— 30-2
 + MODE PROCESS POS—AP004DD4— 37-2
 - STATUS POSITION—AP005DD4— 44-11
 - TAR AND OP REG POSITION—AP005DF4— 51-12
 + PHASE A SAMPLE PULSE—AP008BJ4— 58-1
 + PHASE B SAMPLE PULSE—AP008BK4— 65
 + POWER ON RESET—AP008DB4— 72-1
 + PANEL DISABLE POS—AP008DG4— 79-1
 - MACH CHECK—CK006GG2— 86



EDGE CONN.	01A-B4U6B04	01A-B4T6A02
2 RESISTOR	218 A-B3U1A11	288 A-B3T1A13
	A-B3N2D04	01A-B4T6A04
9 RESISTOR	230 A-B3T1C13	314 A-B3T1D11
	A-B3N2B03	01A-B4T6C04
16 RESISTOR	251 A-B3T1D13	01A-B4T6D02
	A-B3N2D03	01A-B4T6D04
23 RESISTOR	274 A-B3T1B11	
	A-B3N2D06	01A-B4T6B02
204	A-B3U1B13	281 A-B3T1A11



LOC. TYPE
A-B3N2 6819

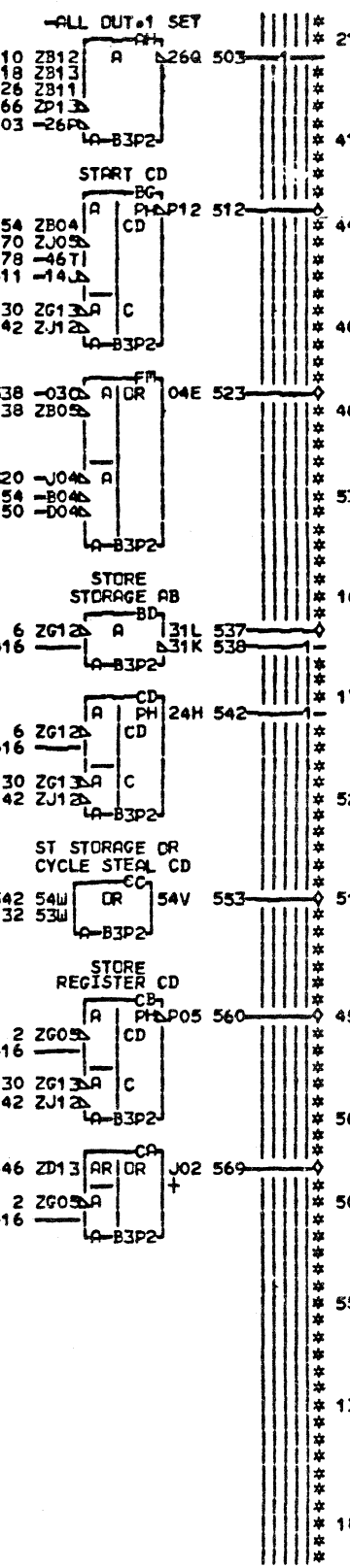
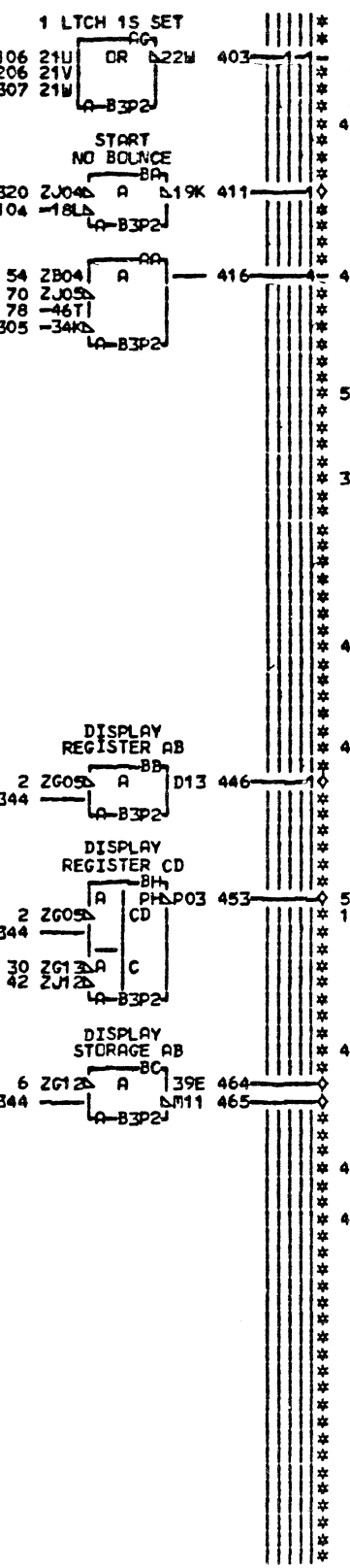
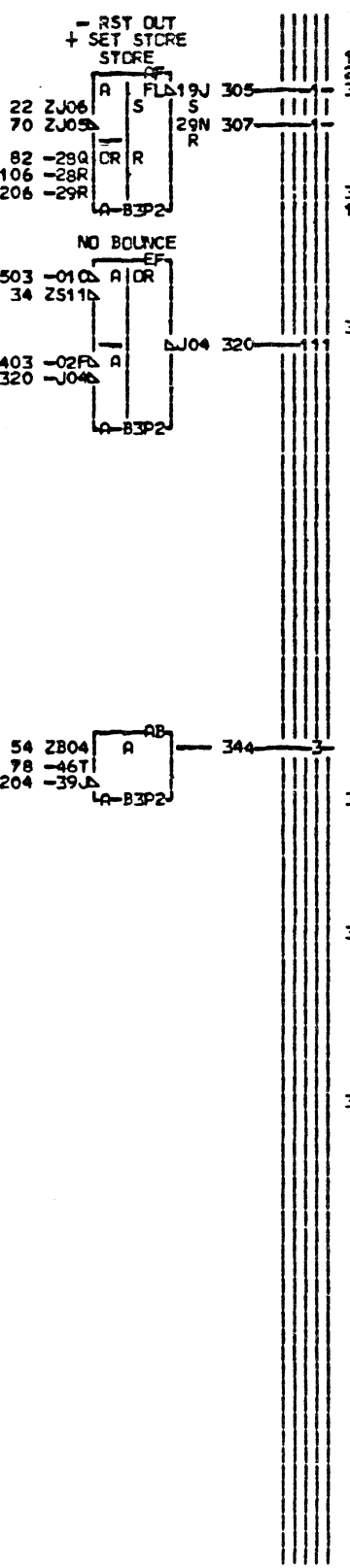
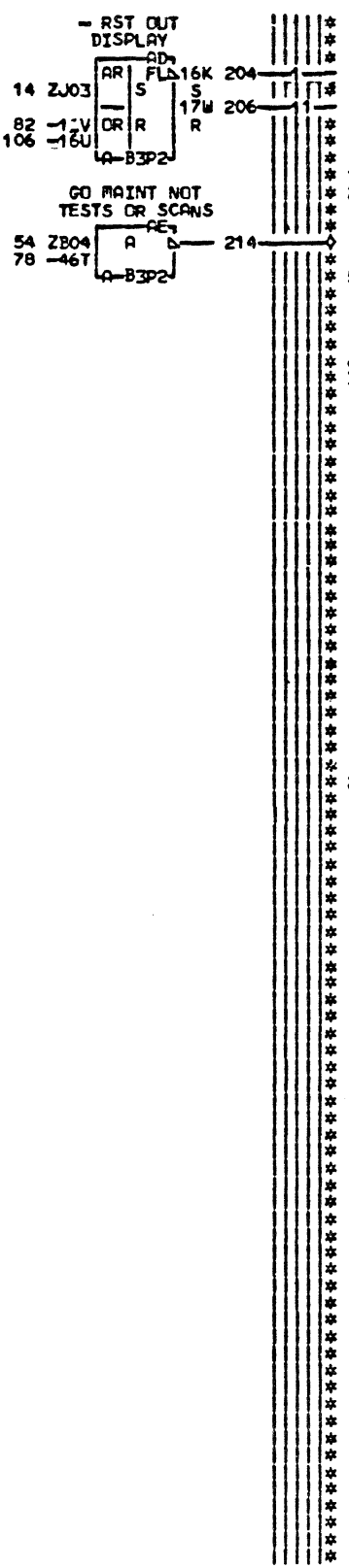
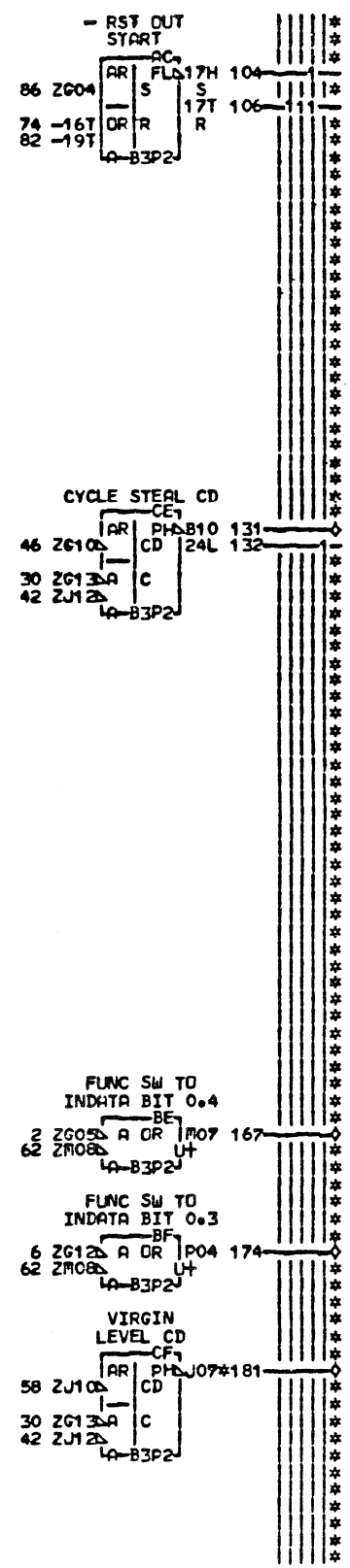


000 CU001
 170 + NOT PROCESS PROCESS—CU005-AL6
 159 - PANEL ACTIVE—BM6
 CU003 LCU004 LCU006 LCU007
 LCU009 LCU010 LCU014
 125 - PHASE A SAMPLE PULSE—CA2
 LCC008 LCP007 LCU006
 116 - PHASE B SAMPLE PULSE—CB2
 LCP007 LCS007 LCU004 LCU005
 230 + INDICATOR CHAN 1—AP009-EC2
 314 + INDICATOR CHAN 2—AP009-EE2
 251 + PANEL ACTIVE-CCU CHECK IND—EG2
 LAP009
 304 - GATE STATUS TO DISPLAY A—EH6
 CA001 LCC001 LCC002 LCK004
 LCK005 LCR001
 281 - GATE STATUS TO DISPLAY B—EJ6
 CL005 LCP004 LCP005 LCU004
 LCU010 LCU014 LCV061
 218 - GATE DISP REG 1 TO IND—EK6
 DE003 LDF003 LDG003 LDH003
 LDJ003 LDK003 LDL003 LDM003
 204 - GATE DISPL REG 2 TO IND—EL6
 DE003 LDF003 LDG003 LDH003
 LDJ003 LDK003 LDL003 LDM003
 288 - GATE TAR TO IND—EP6
 DE003 LDF003 LDG003 LDH003
 LDJ003 LDK003 LDL003 LDM003
 274 - GATE OP REG TO IND—DN005-FM6

CU001
000

PANEL CONTROLS			
E.C.—HISTORY—	E1 MACH#3705	FRAME	01
344270		IBM CORP-SCD	CU001
DATE	LAST EC	P.N.	1769213
06-02-81	344828		000

- REGISTER ADDRESS POSITION - AP005DC4 - 2-1-22
- STORAGE ADDRESS POSITION - AP005DE4 - 6-1-12
- + START PUSHBUTTON OUT - AP006DC4 - 10
- + DISPLAY PUSHBUTTON IN - AP006DF4 - 14
- + DISPLAY PUSHBUTTON OUT - AP006DG4 - 19
- + STORE PUSHBUTTON IN - AP006DH4 - 22
- + STORE PUSHBUTTON OUT - AP006DJ4 - 26
- AB TIME - CC001BE6 - 30-2-13
- BC TIME - CC001EG2 - 34-1-1
- CS1 TIME - CC004EL6 - 38
- TOT1 TIME - CC007HK1* - 42-2-13
- CYCLE STEAL AB - CC008AE6 - 46-1
- INST BNDARY OR HARD STOP LCH - CC008DB2 - 50
- + GO MAINT - CP001GD2 - 54-1-12
- VIRGIN LEVEL - CP003BL6 - 58-1
- GATE INPUT 72 - CQ004DL6 - 62-2
- PANEL ACTIVE - CU001BM6 - 66-1
- PROGRAM STOP LATCH - CU004FK6 - 70-1-1
- + RESET START LCH - CU006EF2 - 74-1-1
- MEM TESTS OR SCANS - CU007FC2 - 78-1-1
- + RESET START DISPLAY STORE - CU007GB6 - 82-1-1
- + START PUSH BUTTON IN GATED - CU017CF6 - 86-1



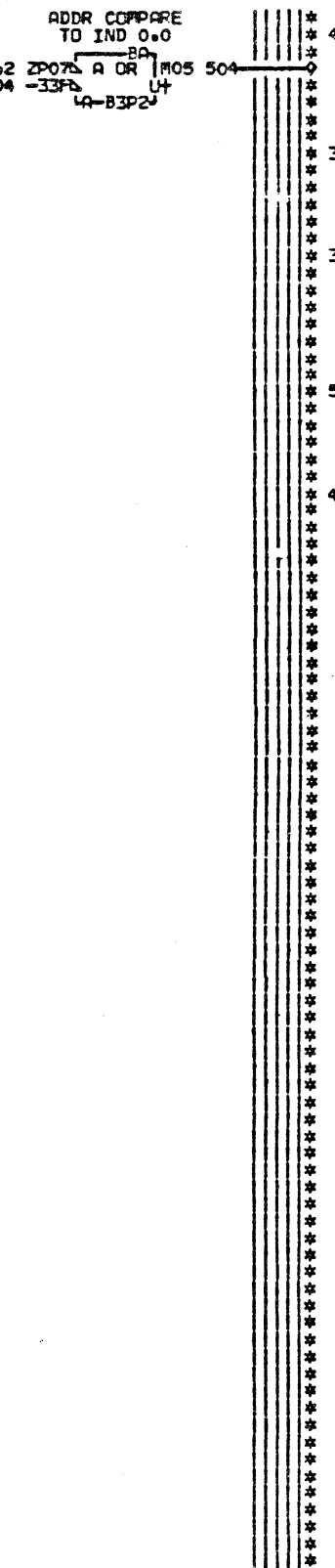
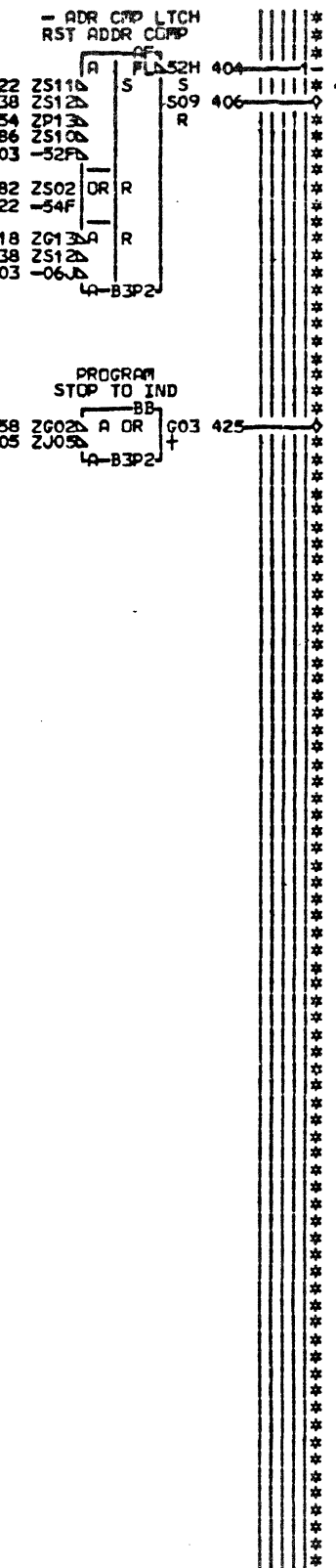
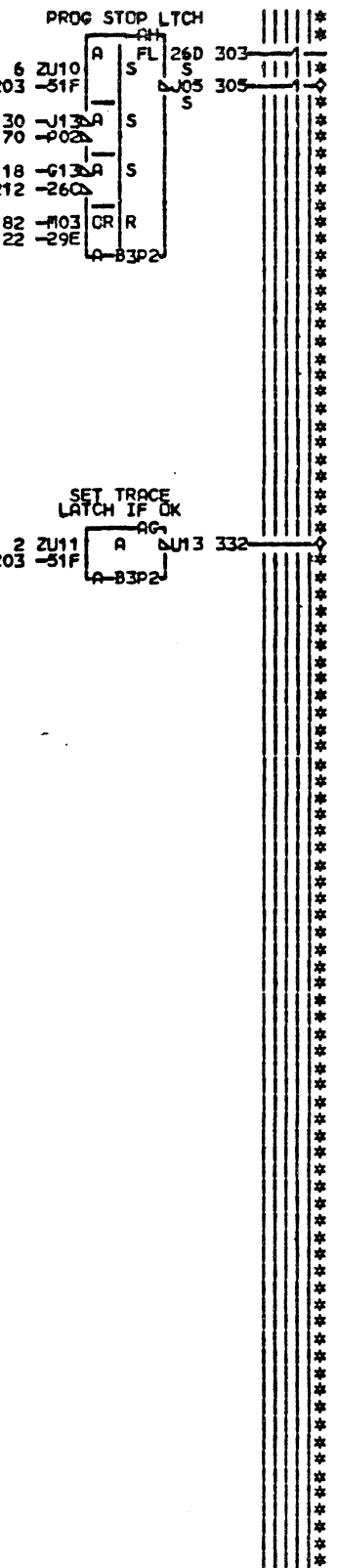
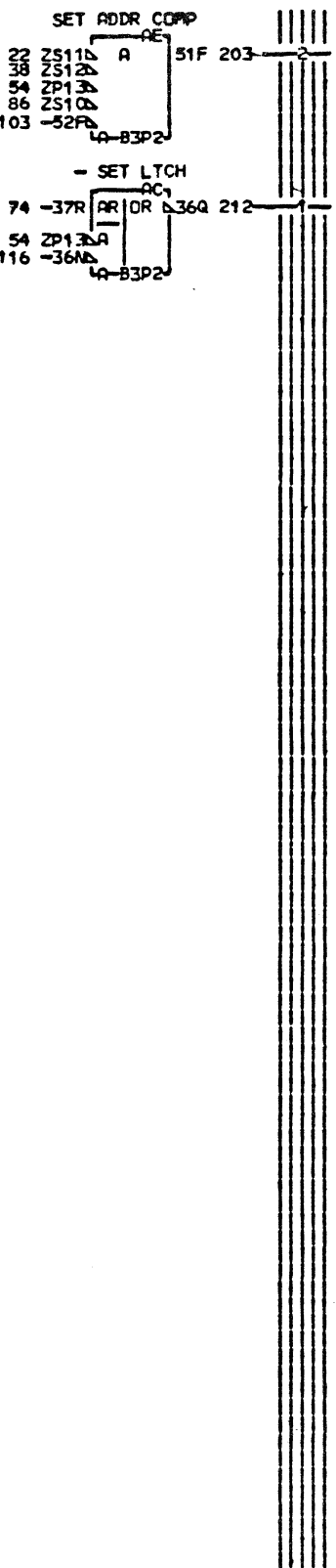
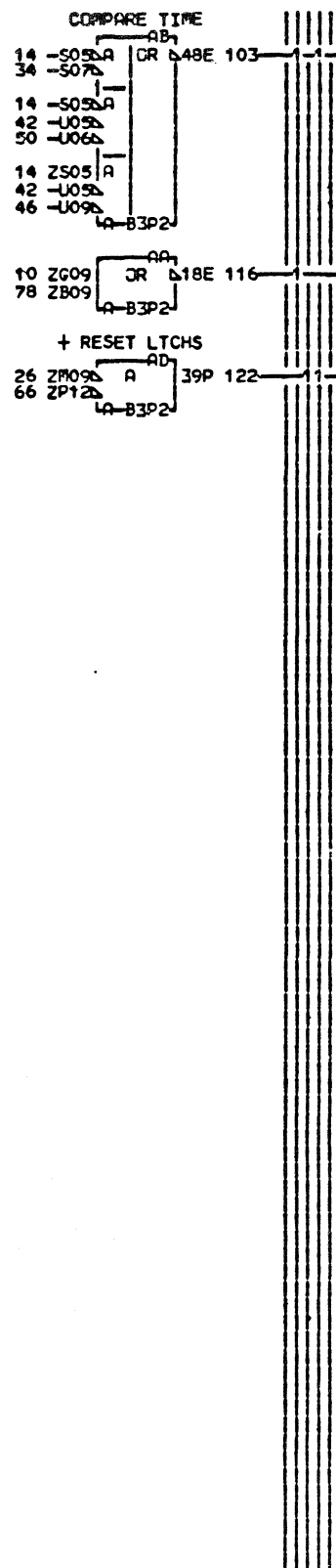
- 214 - GO MAINT NOT TESTS OR SCANS - CK4 LCU007
- 411 - START NO BOUNCE - CU007-FB6
- 446 + DISPLAY REGISTER AB - FC2 LCU003 LCU004 LCU007
- 464 + DISPLAY STORAGE AB - CS003-FD2
- 465 - DISPLAY STORAGE AB - CS004-FD6
- 537 + STORE STORAGE AB - FG2 LCU003 LCU004 LCU007
- 167 + FUNC SW TO INDATA BIT 0.4 - FH2 LCU011
- 174 + FUNC SW TO INDATA BIT 0.3 - FJ2 LCU011
- 523 + BUTTON BID MAINT - FM2 LCU006 LCU007
- 512 - START CD - GB6 LCU001 LCU003 LCU007 LCU004 LCU006
- 453 - DISPLAY REGISTER CD - GD6 LCU001 LCU001 LCU002
- 569 ALWAYS PLUS - C5002-GE6
- 560 - STORE REGISTER CD - GF6 LCU001 LCU002 LCU003
- 553 + ST STORAGE OR CYCLE STEAL CD - GH6 LCU007
- 131 - CYCLE STEAL CD - GK6 LCU001 LCU001 LCU002 LCU005 LCU017
- 181 - VIRGIN LEVEL CD - GL6 LCU002 LCU003 LCU004

EDGE CONN.
42 RESISTOR
A-B3P2J12
181 A-B3V3D09
01A-B4V3D09

LDC. TYPE
A-B3P2 6820

PANEL CONTROLS
E-C-HISTORY - MACH.3705
344270 FRAME 01
DATE LAST EC IBM CORP. SCD CU003
06-02-81 344828 P.N. 1769214 000

- + ADDR COMPARE INT POS — AP004DC4 — 2
- + ADDR COMP PROG STOP POS — AP004DE4 — 6
- + STOP PUSHBUTTON IN — AP006DD4 — 10
- + COMPARE SW STORE POS — AP008FA4 — 14
- AB TIME — CC001BE6 — 18
- BC TIME — CC001EG2 — 22
- CS1 C TIME — CC002BM6 — 26
- I1 AB TIME — CC003BC6 — 30
- I1 TIME — CC004ER6 — 34
- TO TIME — CC006AF0 — 38
- I2+I3 TIME — CC008AL2 — 42
- STORE INST TYPE — CD002CN2 — 46
- LOAD INST TYPE — CD004DN2 — 50
- PANEL ACTIVE — CU001BM6 — 54
- PHASE B SAMPLE PULSE — CU001CB2 — 58
- GATE STATUS TO DISPLAY B — CU001EJ6 — 62
- START CD — CU003GB6 — 66
- ACTIVE INSN STEP — CU006BK6 — 70
- + HARD STOP LCH — CU006FC2 — 74
- + MEM TESTS OR SCANS — CU007FC6 — 78
- + RESEY — CU010FM2 — 82
- Z BUS ZERO — CZ001BM6 — 86

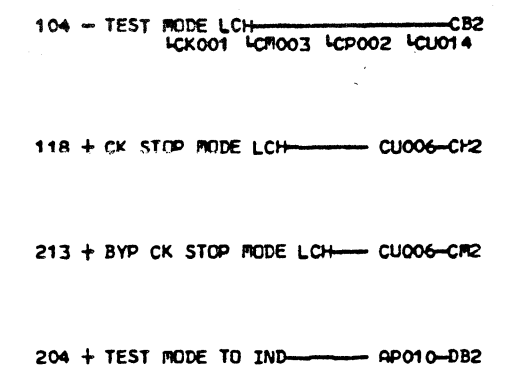
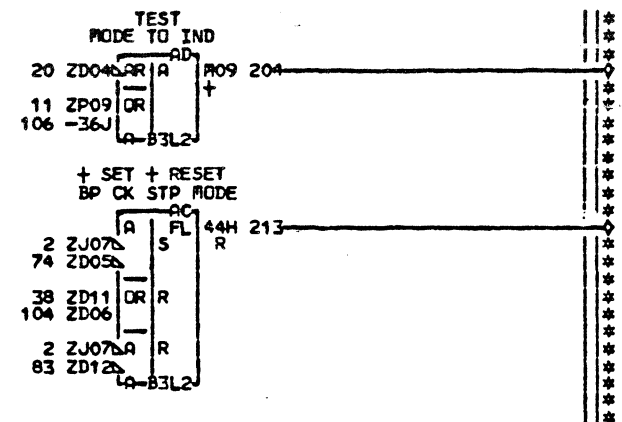
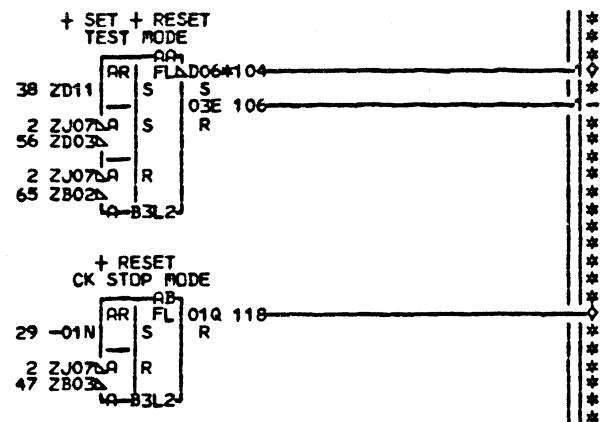
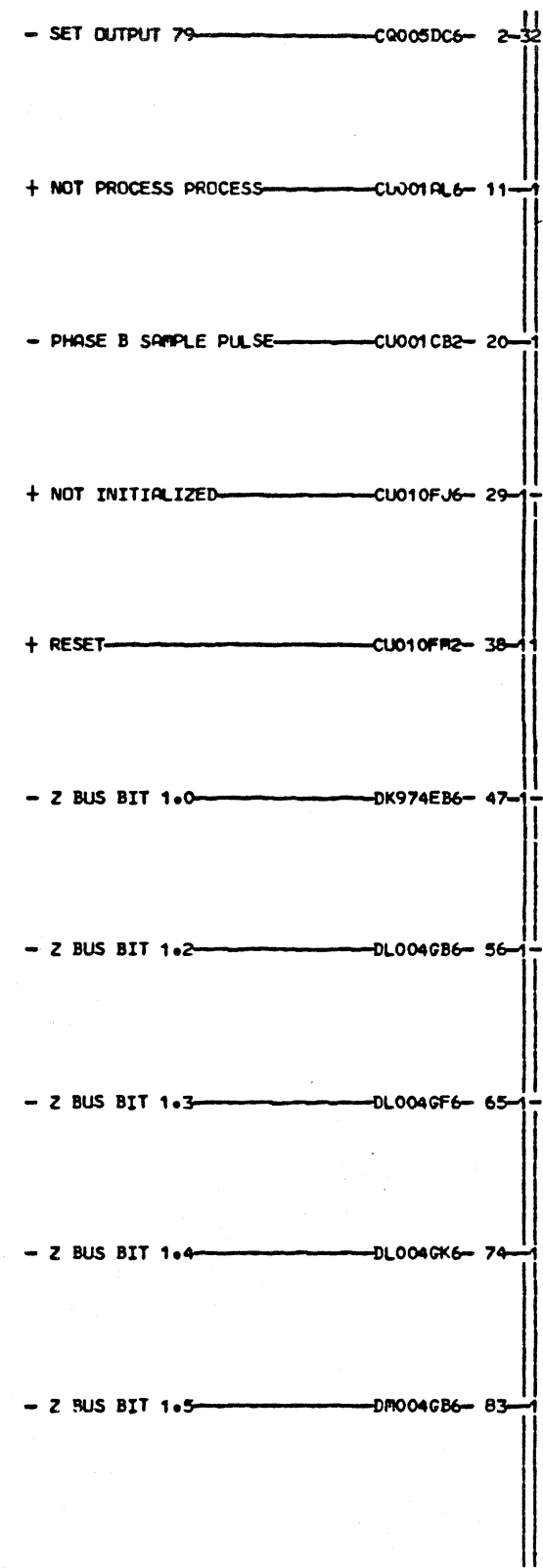


- 406 + ADDRESS COMPARE TEST PIN — FF2 LAA005
- 332 - SET TRACE LATCH IF OK — CU014-FH6
- 305 - PROGRAM STOP LATCH — FK6
CC008 LXS001 LXS003 LCU003
LCU014 LXC001
- 504 + ADDR COMPARE TO IND 0.0 AP012-GD2
- 425 + PROGRAM STOP TO IND — AP010-GL2

LDC. TYPE
A-B3P2 6820

CU004
000

PANEL CONTROLS			
-E.C.-HISTORY	E1	MACH.3705	
		FRAME	01
DATE	LAST EC	IBM CORP.SCD	CU004
10-14-80	344270	P.N. 1769215	000



NOTE. + RESET CU010FR2 SETS TEST MODE LATCH. EITHER TEST MODE LATCH OR NOT PROCESS PROCESS TURNS ON TEST MODE LITE.

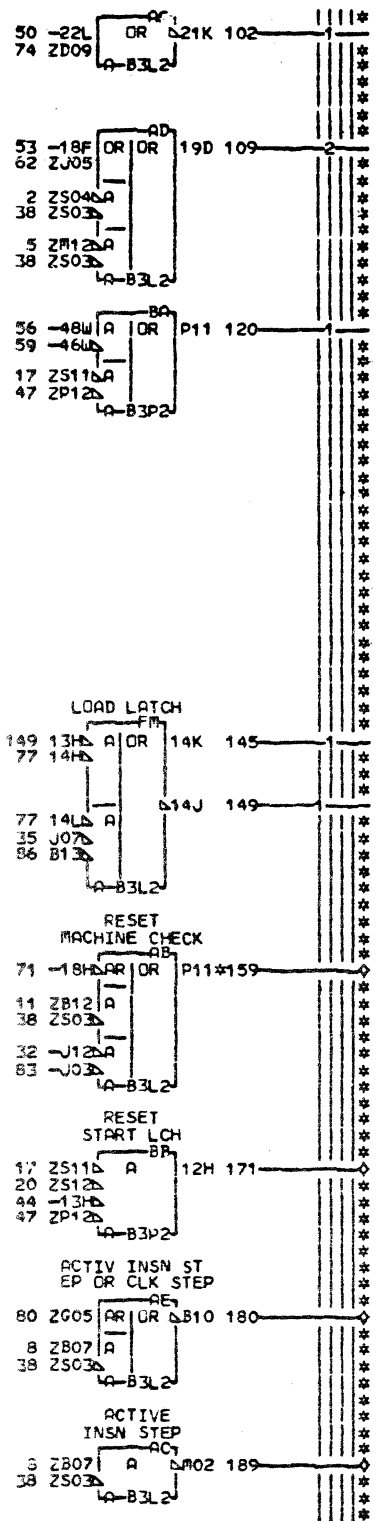
EDGE CONN.
 104 A-B3F1B11
 01A-B4F6B02

LDC. TYPE
 A-B3L2 6823

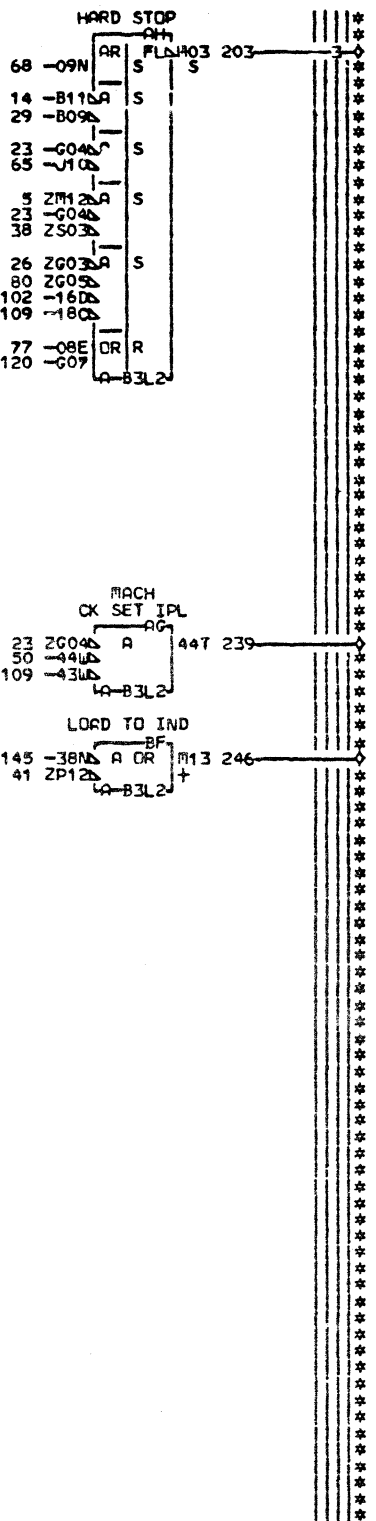
CU005
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TEST MODE AND CK STOP MODE	
E.C.-HISTORY	E-MACH.3705
DATE	LAST EC
10-14-80	344270
FRAME	01
IBP CORP.SCD	CU005
P.N.	1769216
	000

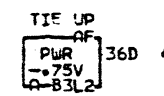
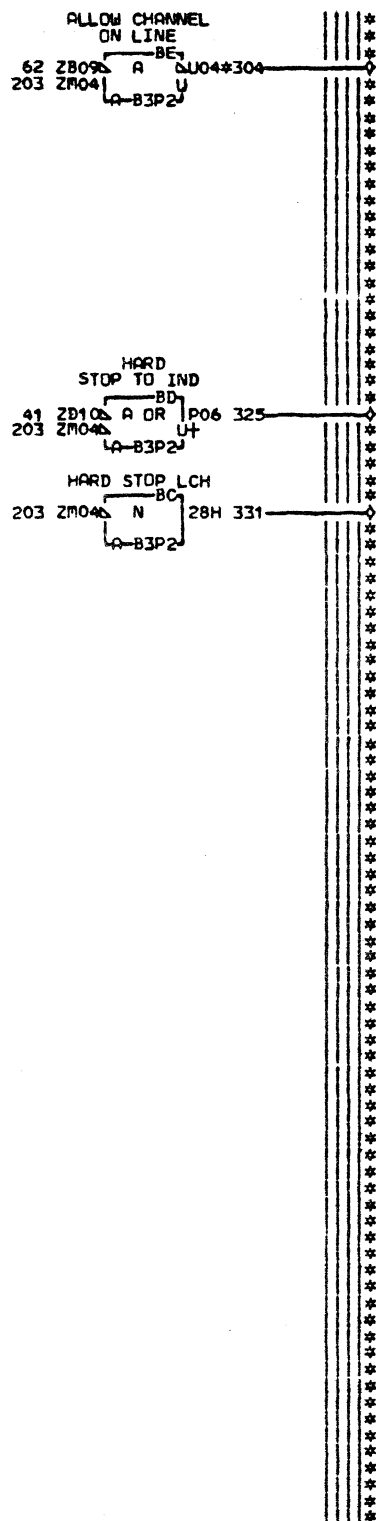
- BYPASS CHECK STOP POSITION - AP003DB4- 2
- CC CHECK HARD STOP POSITION - AP003DC4- 5
- + INSTRUCTION STEP POS - AP004DF4- 8
- + CHECK RESET PUSHBUTTON IN - AP006DM4- 11
- OP XXXX XXXX X000 XXXX - CA003BB3- 14
- BC TIME - CC001EG2- 17
- TO TIME - CC006AF04- 20
- MACH CHECK - CK006GG2- 23
- SET MACH CHECK - CK007DK2- 26
- SET OUTPUTS 70 TO 77 - CQ004BA6- 29
- SET OUTPUT 77 - CQ004FE6- 32
- SET OUTPUT 79 - CQ005DC6- 35
- PANEL ACTIVE - CU001BM6- 38
- PHASE SAMPLE PULSE - CU001CA2- 41
- + BUTTON BID MAINT - CU003FM2- 44
- START CD - CU003GB6- 47
- + CK STOP MODE LCH - CU005CH2- 50
- + BYP CK STOP MODE LCH - CU005CM2- 53
- + SET STEP OR MEM TESTS - CU007CD2- 56
- MEM TESTS GR SCANS - CU007FC2- 59
- + MEM TESTS OR SCANS - CU007FC6- 62
- ACTIVE STORAGE SCAN OR TEST - CU007FD2- 65
- + ACTIVE RESET PUSHBUTTON - CU010AK2- 68
- POR OR RESET SW - CU010BM2- 71
- + IPL 2 LATCH - CU010DH2- 74
- + POWER ON OR IPL RESET - CU010FL6- 77
- + ACTIVE CLOCK STEP GATED - CU017GC6- 80
- Z BUS BIT 0.1 - DG974EH6- 83
- Z BUS BIT 1.1 - DK974EH6- 86



EDGE CONN.
20 RESISTOR
A-B3P2512
159 A-B3V4B08
304 A-B3ASB02



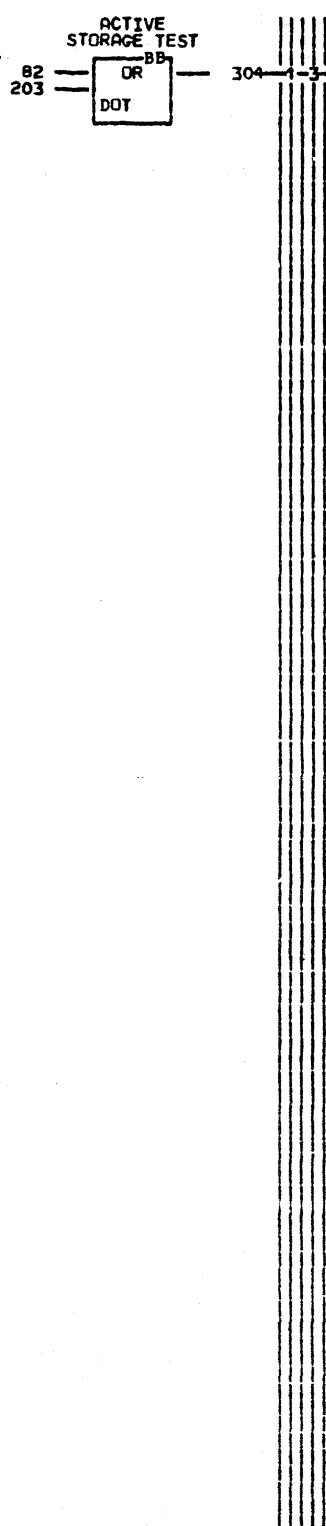
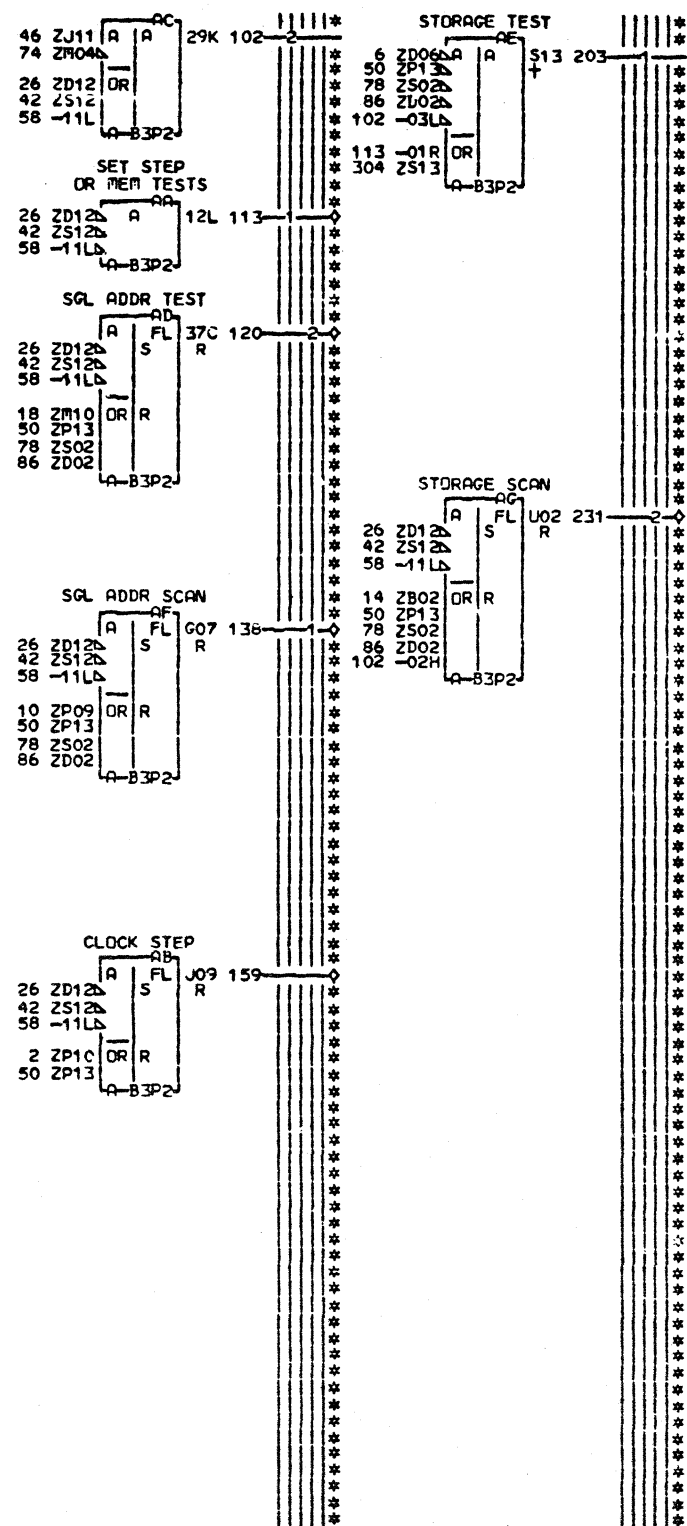
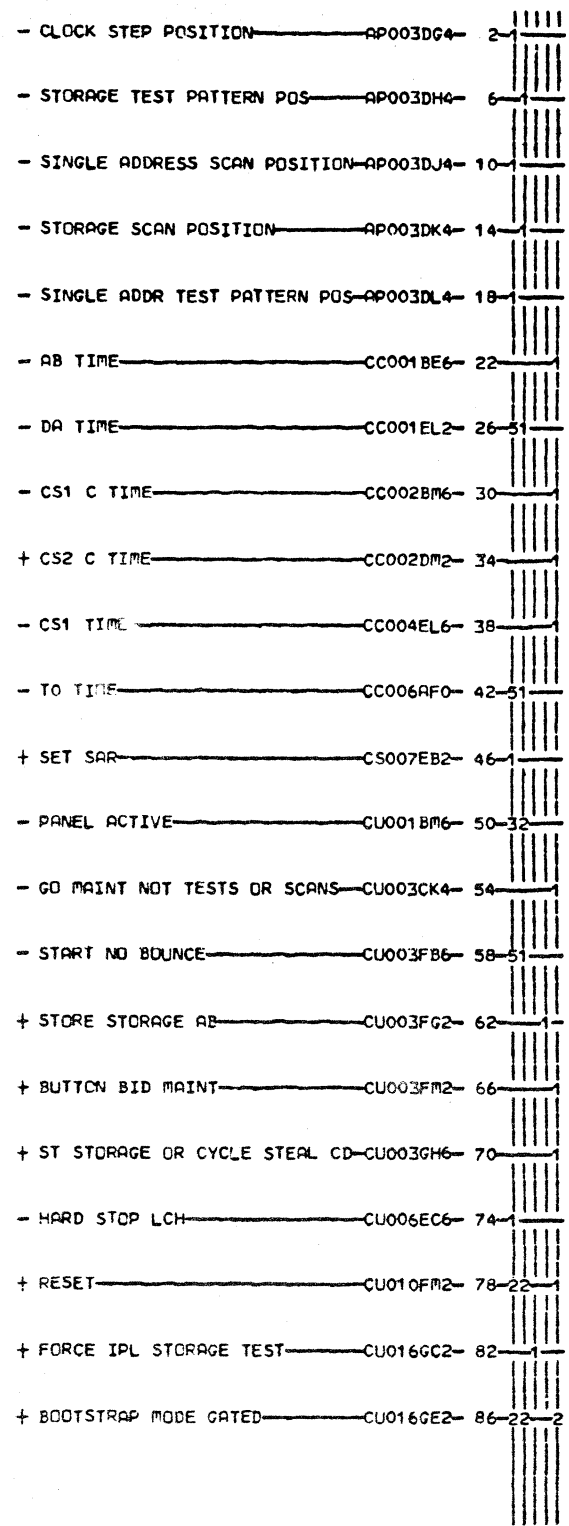
LDC. TYPE
A-B3L2 6823
A-B3P2 6820



- 000 CU006
- 189 - ACTIVE INSN STEP - CU004-BK6
- 159 + RESET MACHINE CHECK - CB6
M003 MCK006 MCK002
- 180 - ACTIV INSN STEP OR CLK STEP - CK2
MCK005 MCK001 MCK014
- 480 + TIE UP - CU010-DC4
- 239 + MACH CK SET IPL - CU010-DJ2
- 203 - HARD STOP LCH - EC6
MCK008 MCK001 MCK007
- 171 + RESET START LCH - CU003-EF2
- 331 + HARD STOP LCH - CU004-FC2
- 325 + HARD STOP TO IND - AP010-GB2
- 304 - ALLOW CHANNEL ON LINE - AP003-GC6
- 246 + LOAD TO IND - AP009-GM2

CU006
000

PANEL CONTROLS	
E.C. HISTORY	MACH. 3705
DATE LAST EC	FRAME 01
10-14-80 344270	IBR CORP. SCD
	P.No. 1769217
	CU006 000



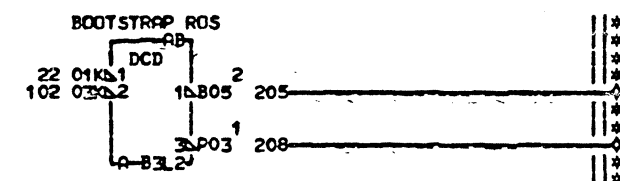
LDC TYPE
A-B3P2 6820

CU007
000

PANEL CONTROLS	
E.C.-HISTORY	E-MACH.3705
	FRAME 01
DATE LAST EC	IBM CORP.SCD CU007
10-14-80 344270	P.No 1769218 000

+ FORCE ALT — AJ001FD4 — 2 —
 - PANEL ACTIVE — CU001BM6 — 12 —
 - BOOTSTRAP MODE — CU010GD6 — 22 —

2 ZB04b OR 103Q 102
 12 ZS03
 A-B3L2



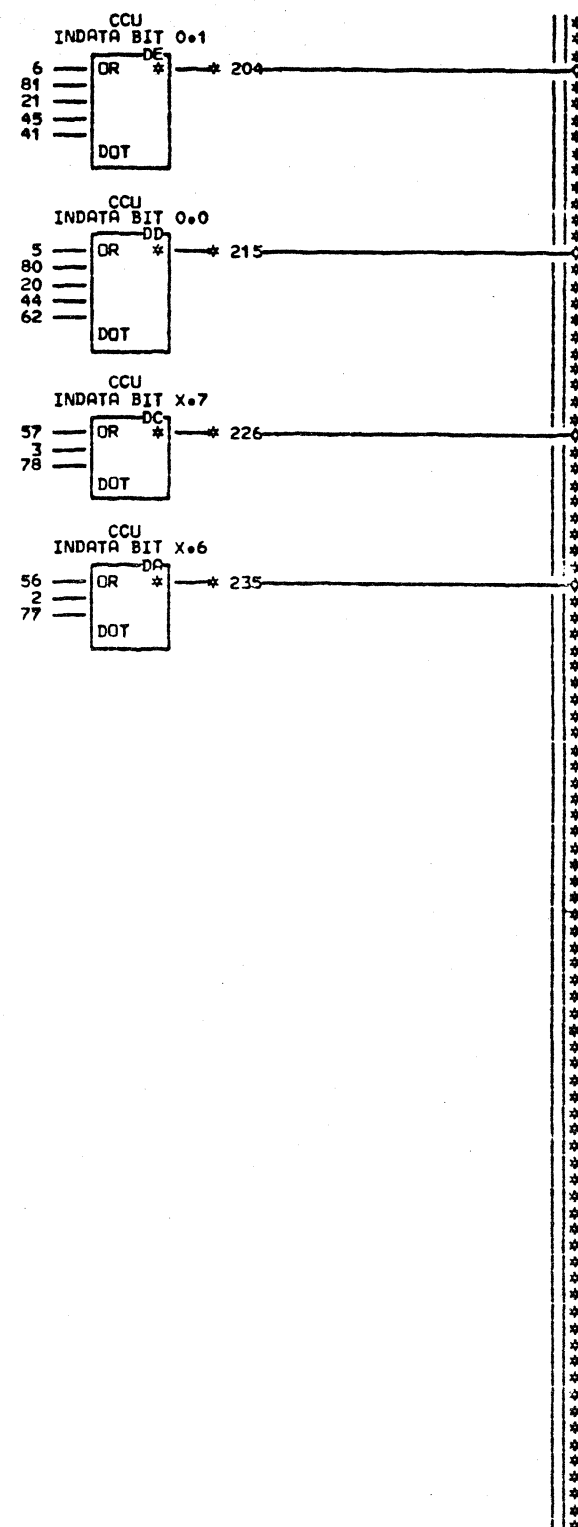
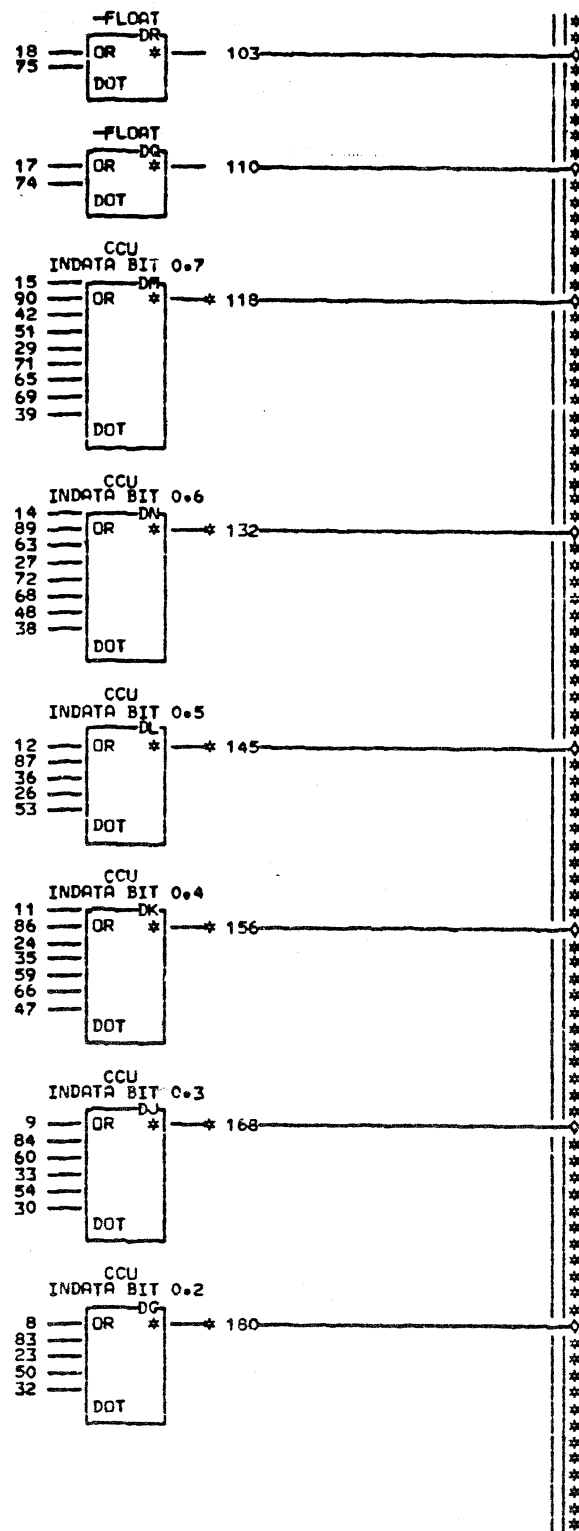
000 CU009
 208 - BOOTSTRAP ROS 1 — CU016-ED6
 205 - BOOTSTRAP ROS 2 — CU016-EE6

LOC. TYPE
 A-B3L2 6823

CU009
 000

PANEL CONTROLS	
E.C. HISTORY 344270	MACH. 3705 FRAME 01
DATE LAST EC 06-02-81 344828	IBM CORP. SCD P.No 1769219
	CU009 000

FOLLOWS CU011DA4 AJ001FK4 2
 FOLLOWS CU011DC4 AJ001FM4 3
 + HEXSW BYTE 0 BIT 0 AP001DJ2 5
 + HEXSW BYTE 0 BIT 1 AP001DK2 6
 + HEXSW BYTE 0 BIT 2 AP001DL2 8
 + HEXSW BYTE 0 BIT 3 AP001DM2 9
 + HEXSW BYTE 0 BIT 4 AP001GE2 11
 + HEXSW BYTE 0 BIT 5 AP001GF2 12
 + HEXSW BYTE 0 BIT 6 AP001GG2 14
 + HEXSW BYTE 0 BIT 7 AP001GH2 15
 - FLOAT CG001CB2 17
 - FLOAT CG001CC2 18
 + MACH CK TO INDATA BIT 0.0 CK004EB2 20
 + MACH CK TO INDATA BIT 0.1 CK004EC2 21
 + MACH CK TO INDATA BIT 0.2 CK004ED2 23
 + MACH CK TO INDATA BIT 0.4 CK004EF2 24
 + MACH CK TO INDATA BIT 0.5 CK004EG2 26
 + MACH CK TO INDATA BIT 0.6 CK004EH2 27
 + MACH CK TO INDATA BIT 0.7 CK004EJ2 29
 + L1 PROG CHECK TO INDATA 0.3 CK004EK2 30
 ALWAYS MINUS AT IN X 70 TIME CM002FC2 32
 + 256K TO INDATA 0.3 CM002FD2 33
 ALWAYS MINUS AT IN X 70 TIME CM002FE2 35
 ALWAYS MINUS AT IN X 70 TIME CM002FF2 36
 ALWAYS MINUS AT IN X 70 TIME CM002FG2 38
 ALWAYS MINUS AT IN X 70 TIME CM002FH2 39
 ALWAYS MINUS AT IN 70 TIME CM002FC6 41
 + IRPT ADDR TO INDATA BIT 0.7 CP002GB2 42
 + CRC TO INDATA BIT 0.0 CRO06FF6 44
 + CRC TO INDATA BIT 0.1 CRO06CF6 45
 + CRC TO INDATA BIT 0.4 CRO06DF6 47
 + CRC TO INDATA BIT 0.6 CRO06DG6 48
 + CRC TO INDATA BIT 0.2 CRO06EF6 50
 + CRC TO INDATA BIT 0.7 CRO06EG6 51
 + CRC TO INDATA BIT 0.5 CRO06EH6 53
 + CRC TO INDATA BIT 0.3 CRO06F6 54
 + HEXSW BYTE X BIT 6 CRO08DD2 56
 + HEXSW BYTE X BIT 7 CRO08DE2 57
 + FUNC SW TO INDATA BIT 0.4 CU003FH2 59
 + FUNC SW TO INDATA BIT 0.3 CU003FJ2 60
 + PCT L2 TO INDATA 0.0 CU014CM2 62
 + INTERRUPT KEY TO INDATA 0.6 CU014DM2 63
 + 2ND T.P. TO INDATA BIT 0.7 CU015CF2 65
 + BAR TO INDATA BIT 0.4 CX009GA2 66
 + BAR TO INDATA BIT 0.6 CX009GB2 68
 + BAR TO INDATA BIT 0.7 CX009GC2 69
 + LEV 5 Z CCND TO INDATA 0.7 C2004EK2 71
 + LEV 5 C CCND TO INDATA 0.6 C2005EK2 72
 - FLOAT DE002EB2 74
 - FLOAT DE002ED2 75
 + LAR TO INDATA BIT X.6 DFO02EB2 77
 + LAR TO INDATA BIT X.7 DFO02ED2 78
 + LAR TO INDATA BIT 0.0 DG002EB2 80
 + LAR TO INDATA BIT 0.1 DG002ED2 81
 + LAR TO INDATA BIT 0.2 DH002EB2 83
 + LAR TO INDATA BIT 0.3 DH002ED2 84
 + LAR TO INDATA BIT 0.4 DH002EF2 86
 + LAR TO INDATA BIT 0.5 DJ002EB2 87
 + LAR TO INDATA BIT 0.6 DJ002ED2 89
 + LAR TO INDATA BIT 0.7 DJ002EF2 90



000 CU011
 235 + CCU INDATA BIT X.6 DA4
 LCG001 LDF971
 226 + CCU INDATA BIT X.7 DC4
 LCU013 LDF971
 215 + CCU INDATA BIT 0.0 DD4
 LCU013 LDF971
 204 + CCU INDATA BIT 0.1 DE4
 LCU013 LDF971
 180 + CCU INDATA BIT 0.2 DG4
 LCU013 LDH011
 168 + CCU INDATA BIT 0.3 DJ4
 LCU013 LDH011
 156 + CCU INDATA BIT 0.4 DK4
 LCU013 LDH011
 145 + CCU INDATA BIT 0.5 DL4
 LCU013 LDJ011
 118 + CCU INDATA BIT 0.7 DM4
 LCU013 LDJ011
 132 + CCU INDATA BIT 0.6 DN4
 LCU013 LDJ011
 110 - FLOAT DQ4
 LCG001 LDE971
 103 - FLOAT DR4
 LCG001 LDE971

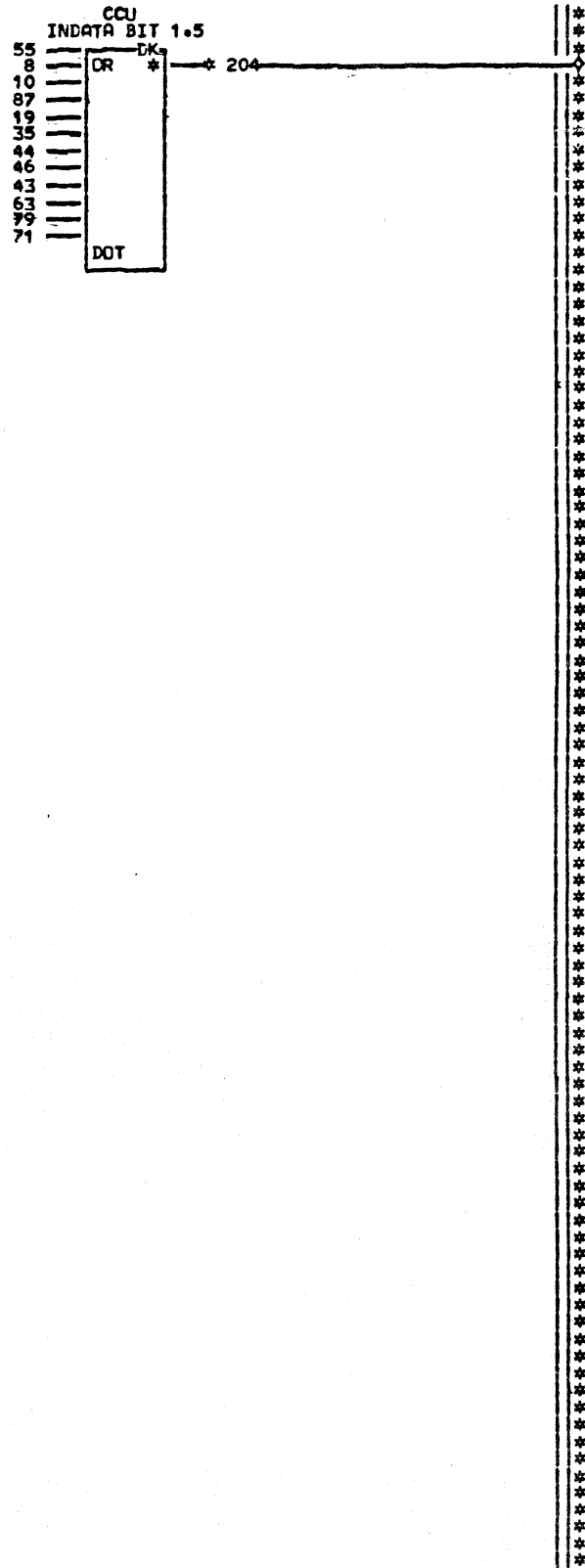
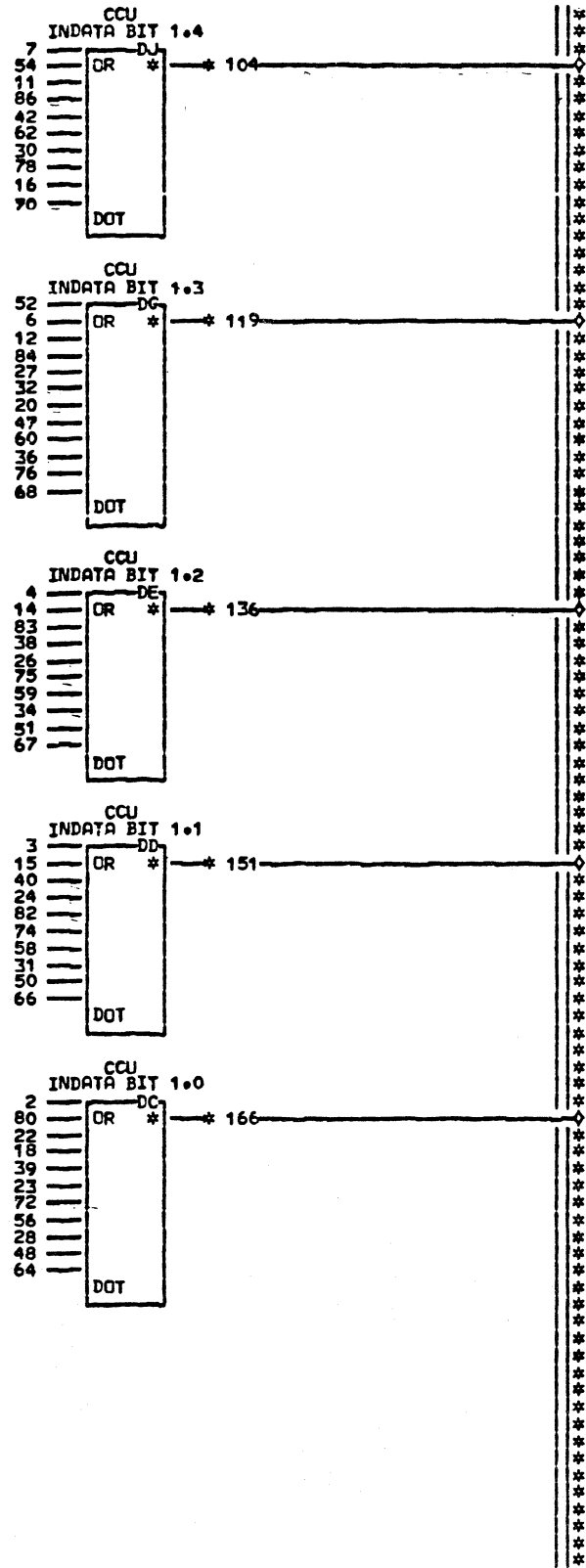
EDGE CONN. 01A-B4T1C13
 118 A-B3T6E02 180 A-B3T6B02
 01A-B4T1E11 01A-B4T1B11
 132 A-B3T6E04 204 A-B3T6A04
 01A-B4T1E13 01A-B4T1A13
 145 A-B3T6D04 215 A-B3T6A02
 01A-B4T1D13 01A-B4T1A11
 156 A-B3T6D02 226 A-B3S6A02
 01A-B4T1D11 01A-B4S1A11
 168 A-B3T6C04 235 A-B3R6E04

CU011
 000

LOC. TYPE

CC INDATA BUS
 -E.C.-HISTORY-E MACH.3705
 FRAME 01
 DATE LAST EC 10-14-80 344270
 P.N. 1769221 CU011
 000

† HEXSW BYTE 1 BIT 0 — APO02EB2 — 2-1
 † HEXSW BYTE 1 BIT 1 — APO02EC2 — 3-1
 † HEXSW BYTE 1 BIT 2 — APO02ED2 — 4-1
 † HEXSW BYTE 1 BIT 3 — APO02EE2 — 6-1
 † HEXSW BYTE 1 BIT 4 — APO02EF2 — 7-1
 † HEXSW BYTE 1 BIT 5 — APO02EJ2 — 8-1
 † FUNC SEL TO INDATA BIT 1.5 — APO05EH2 — 10-1
 † FUNC SEL TO INDATA BIT 1.4 — APO05EJ2 — 11-1
 † FUNC SEL TO INDATA BIT 1.3 — APO05EK2 — 12-1
 † FUNC SEL TO INDATA BIT 1.2 — APO05EL2 — 14-1
 † FUNC SEL TO INDATA BIT 1.1 — APO05EM2 — 15-1
 ALWAYS PLUS AT IN 79 TIME — CG001ED2 — 16-1
 † CCU CYC CLK CK TO INDATA 1.0 — CK005EC2 — 18-1
 † CCU CHECK TO INDATA BIT 1.5 — CK005EH2 — 19-1
 † IRPT ADDR TO INDATA BIT 1.3 — CP002GA2 — 20-1
 † IRPT ADDR TO INDATA BIT 1.0 — CP002GF2 — 22-1
 † PRG STAT TO INDATA BIT 1.0 — CP004DB2 — 23-1
 † PRG STAT TO INDATA BIT 1.1 — CP004DD2 — 24-1
 † PRG STAT TO INDATA BIT 1.2 — CP004DF2 — 26-1
 † PRG STAT TO INDATA BIT 1.3 — CP004DH2 — 27-1
 † CRC TO INDATA BIT 1.0 — CR007CF6 — 29-1
 † CRC TO INDATA BIT 1.4 — CR007BF6 — 30-1
 † CRC TO INDATA BIT 1.1 — CR007CF6 — 31-1
 † CRC TO INDATA BIT 1.3 — CR007DF6 — 32-1
 † CRC TO INDATA BIT 1.2 — CR007EF6 — 34-1
 † CRC TO INDATA BIT 1.5 — CR007FF6 — 35-1
 † IRPT ADDR TO INDATA BIT 1.3 — CS004HS2 — 36-1
 † I-O CK TO INDATA BIT 1.2 — CU014CC2 — 38-1
 † ADDR COMPARE TO INDATA 1.0 — CU014CF2 — 39-1
 ALWAYS MINUS AT IN X 7E TIME — CU014CJ2 — 40-1
 † INVALID OP TO INDATA 1.4 — CU014GC2 — 42-1
 † INT TIMER TO INDATA 1.5 — CU014GJ2 — 43-1
 † STORE KEY TO INDATA BIT 1.5 — CV051FA2 — 44-1
 † PROT KEY TO INDATA BIT 1.5 — CV051FG2 — 46-1
 † PROTECT CHK TO INDATA 1.3 — CV061GF2 — 47-1
 FOLLOWS CU012DC4 — CW001GB6 — 48-1
 FOLLOWS CU012DD4 — CW001GC6 — 50-1
 FOLLOWS CU012DE4 — CW001GD6 — 51-1
 FOLLOWS CU012DG4 — CW001GE6 — 52-1
 FOLLOWS CU012DJ4 — CW001GG6 — 54-1
 FOLLOWS CU012DK4 — CW001GH6 — 55-1
 † RDS 1 TO INDATA DOT BIT 1.0 — CW011GB2 — 56-1
 † RDS 1 TO INDATA DOT BIT 1.1 — CW011GC2 — 58-1
 † RDS 1 TO INDATA DOT BIT 1.2 — CW011GD2 — 59-1
 † RDS 1 TO INDATA DOT BIT 1.3 — CW011GE2 — 60-1
 † RDS 1 TO INDATA DOT BIT 1.4 — CW011GG2 — 62-1
 † RDS 1 TO INDATA DOT BIT 1.5 — CW011GH2 — 63-1
 † N RDS TO INDATA DOT BIT 1.0 — CW013GB6 — 64-1
 † N RDS TO INDATA DOT BIT 1.1 — CW013GC6 — 66-1
 † N RDS TO INDATA DOT BIT 1.2 — CW013GD6 — 67-1
 † N RDS TO INDATA DOT BIT 1.3 — CW013GE6 — 68-1
 † N RDS TO INDATA DOT BIT 1.4 — CW013GG6 — 70-1
 † N RDS TO INDATA DOT BIT 1.5 — CW013GH6 — 71-1
 † BAR TO INDATA BIT 1.0 — CX009GD2 — 72-1
 † BAR TO INDATA BIT 1.1 — CX009GF2 — 74-1
 † BAR TO INDATA BIT 1.2 — CX009GG2 — 75-1
 † BAR TO INDATA BIT 1.3 — CX009GH2 — 76-1
 † BAR TO INDATA BIT 1.4 — CX009GK2 — 78-1
 † BAR TO INDATA BIT 1.5 — CX009GL2 — 79-1
 † LAR TO INDATA BIT 1.0 — DK002EB2 — 80-1
 † LAR TO INDATA BIT 1.1 — DK002ED2 — 82-1
 † LAR TO INDATA BIT 1.2 — DL002EB2 — 83-1
 † LAR TO INDATA BIT 1.3 — DL002ED2 — 84-1
 † LAR TO INDATA BIT 1.4 — DL002EF2 — 86-1
 † LAR TO INDATA BIT 1.5 — DM002EB2 — 87-1



000 CU012
 166 + CCU INDATA BIT 1.0 — DC4
 LCR008 LDK971
 151 + CCU INDATA BIT 1.1 — DD4
 LCR008 LDK971
 136 + CCU INDATA BIT 1.2 — DE4
 LCR008 LDK001
 119 + CCU INDATA BIT 1.3 — DG4
 LCR008 LDK001
 104 + CCU INDATA BIT 1.4 — DJ4
 LCR008 LDK001
 204 + CCU INDATA BIT 1.5 — DK4
 LCR008 LDK001

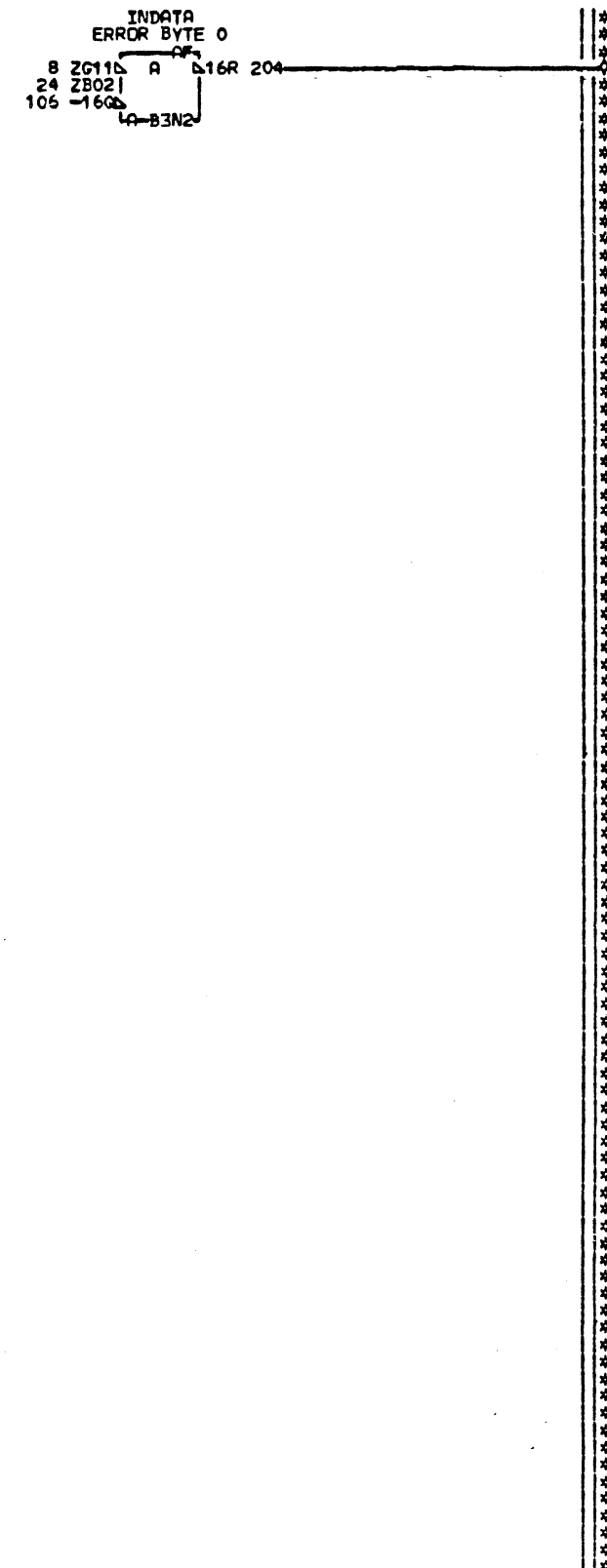
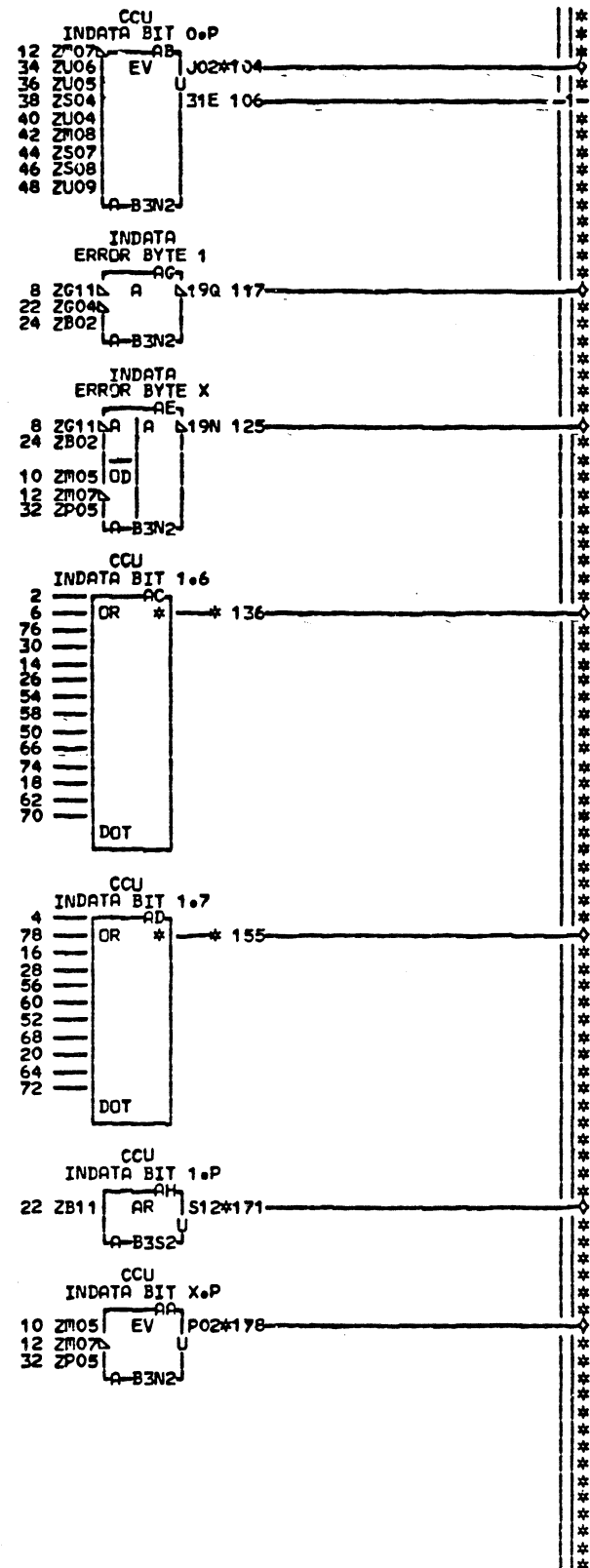
EDGE CONN. 01A-B4U1B13
 104 A-B3U6D04 204 A-B3U6E02
 01A-B4U1D13 01A-B4U1E11
 119 A-B3U6D02
 01A-B4U1D11
 136 A-B3U6C04
 01A-B3U1C13
 151 A-B3U6C02
 01A-B4U1C11
 166 A-B3U6B04

LOC. TYPE

CU012
000

CC INDATA BUS
 —E.C.—HISTORY—E-MACH.3705
 344270 FRAME 01
 DATE LAST EC IBM CORP. SCD CU012
 06-02-81 344228 P.No. 1769222 000

+ HEXSW BYTE 1 BIT 6 — APO02EK2 — 2
 + HEXSW BYTE 1 BIT 7 — APO02EL2 — 4
 + FUNC SEL TO INDATA BIT 1.6 — APO05EG2 — 6
 - T1 TIME — CC006ED6 — 8
 + CCU INDATA BIT X.6 GATED — CG001BF2 — 10
 - FORCE INDATA PARITY ERROR — CK001GA6 — 12
 + CSB CLK CK TO INDATA BIT 1.6 — CK005EK2 — 14
 + CCU CLK CK TO INDATA BIT 1.7 — CK005EL2 — 16
 + CRC DR IN 79 L1 TO INDATA 1.6 — CR007DG6 — 18
 + CRC TO INDATA BIT 1.7 — CR007GF6 — 20
 + CCU INDATA BIT 1.P — CR008CG2 — 22
 - GATE CCU INDATA TO Y BUS — CS004DB2 — 24
 + IRPY ADDR TO INDATA BIT 1.6 — CS004FD6 — 26
 + COMPARE FORCE INDATA BIT 1.7 — CS006CC2 — 28
 + IPL TO INDATA BIT 1.6 — CU010GG2 — 30
 + CCU INDATA BIT X.7 — CU011DC4 — 32
 + CCU INDATA BIT 0.0 — CU011DD4 — 34
 + CCU INDATA BIT 0.1 — CU011DE4 — 36
 + CCU INDATA BIT 0.2 — CU011DG4 — 38
 + CCU INDATA BIT 0.3 — CU011DJ4 — 40
 + CCU INDATA BIT 0.4 — CU011DK4 — 42
 + CCU INDATA BIT 0.5 — CU011DL4 — 44
 + CCU INDATA BIT 0.7 — CU011DM4 — 46
 + CCU INDATA BIT 0.6 — CU011DN4 — 48
 + 2ND T.P. TO INDATA BIT 1.6 — CU015CC2 — 50
 + 2ND T.P. TO INDATA BIT 1.7 — CU015CJ2 — 52
 + STORE KEY TO INDATA BIT 1.6 — CV051FC2 — 54
 + STORE KEY TO INDATA BIT 1.7 — CV051FE2 — 56
 + PROT KEY TO INDATA BIT 1.6 — CV051FJ2 — 58
 + PROT KEY TO INDATA BIT 1.7 — CV051FL2 — 60
 FOLLOWS CU012DL4 — CW001GJ6 — 62
 FOLLOWS CU012DN4 — CW001GK6 — 64
 + ROS 1 TO INDATA DOT BIT 1.6 — CW011GJ2 — 66
 + ROS 1 TO INDATA DOT BIT 1.7 — CW011GK2 — 68
 + N ROS TO INDATA DOT BIT 1.6 — CW013GJ6 — 70
 + N ROS TO INDATA DOT BIT 1.7 — CW013GK6 — 72
 + BAR TO INDATA BIT 1.6 — CX009GM2 — 74
 + LAR TO INDATA BIT 1.6 — DMO02ED2 — 76
 + LAR TO INDATA BIT 1.7 — DMO02EF2 — 78



000 CU013
 178 + CCU INDATA BIT X.P — DF971-CF6
 136 + CCU INDATA BIT 1.6 — CJ4
 LCR008 LDM001
 155 + CCU INDATA BIT 1.7 — CL4
 LCR008 LDM001
 125 - INDATA ERROR BYTE X — DD6
 LCK006 LCK00P
 204 - INDATA ERROR BYTE 0 — DF6
 LCK006 LCK007
 117 - INDATA ERROR BYTE 1 — DJ6
 LCK006 LCK007
 104 + CCU INDATA BIT 0.P — DG971-EF6
 171 + CCU INDATA BIT 1.P — DK971-GF6

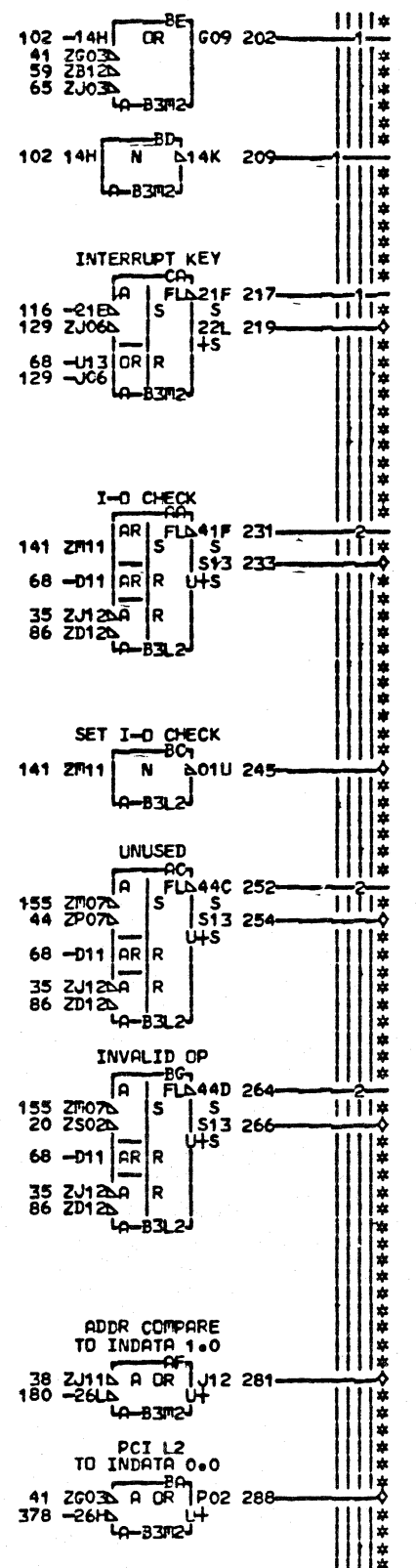
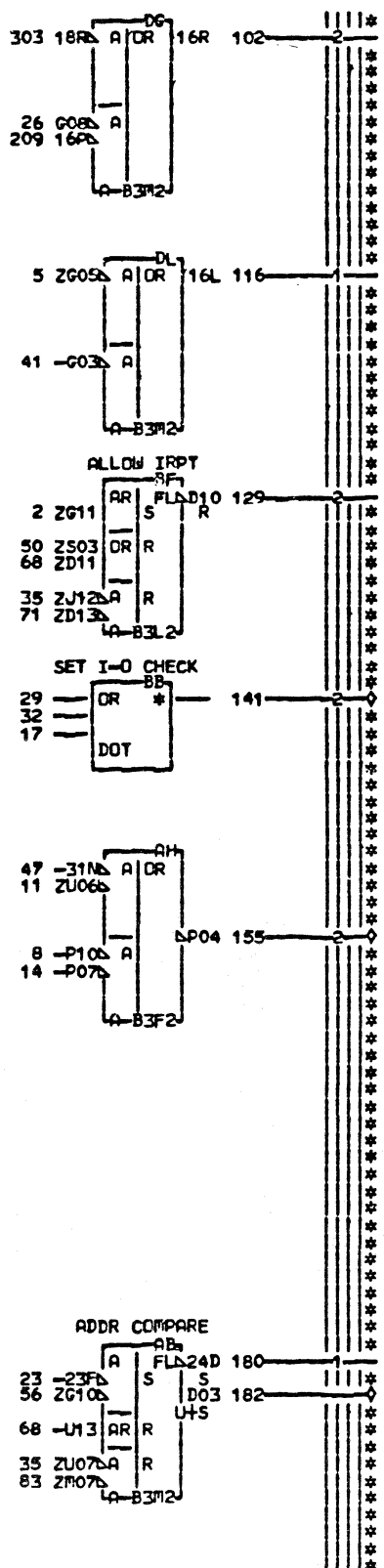
EDGE CONN.
 104 A-B3U6A02
 01A-B4U1A11
 136 A-B3V6A04
 01A-B4V1A13
 155 A-B3V6B02
 01A-B4V1B11
 171 A-B3V6B04
 01A-B4V1B13
 178 A-B3S6A04

LOC. TYPE
 A-B3N2 6819
 A-B3S2 Y703

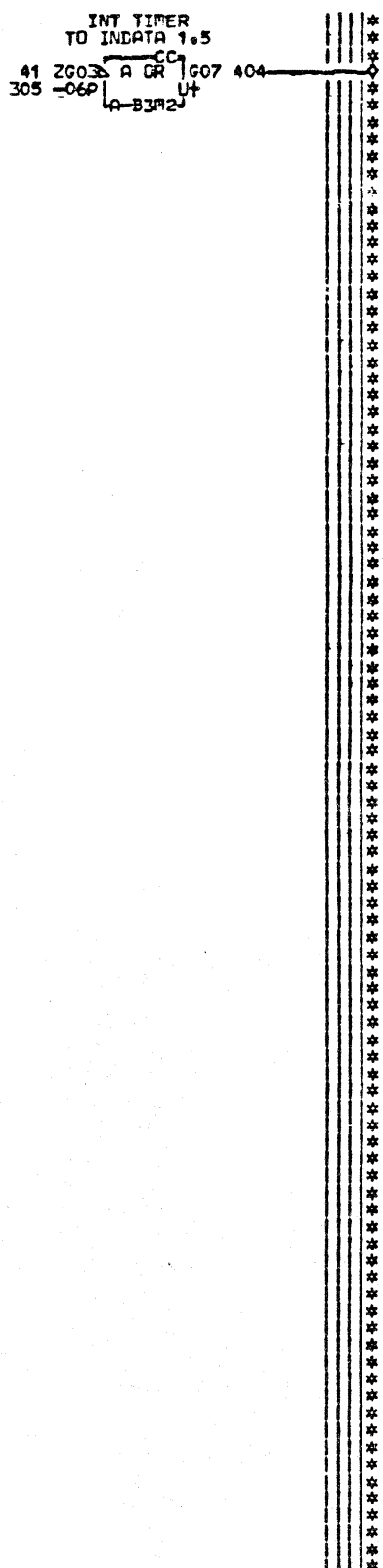
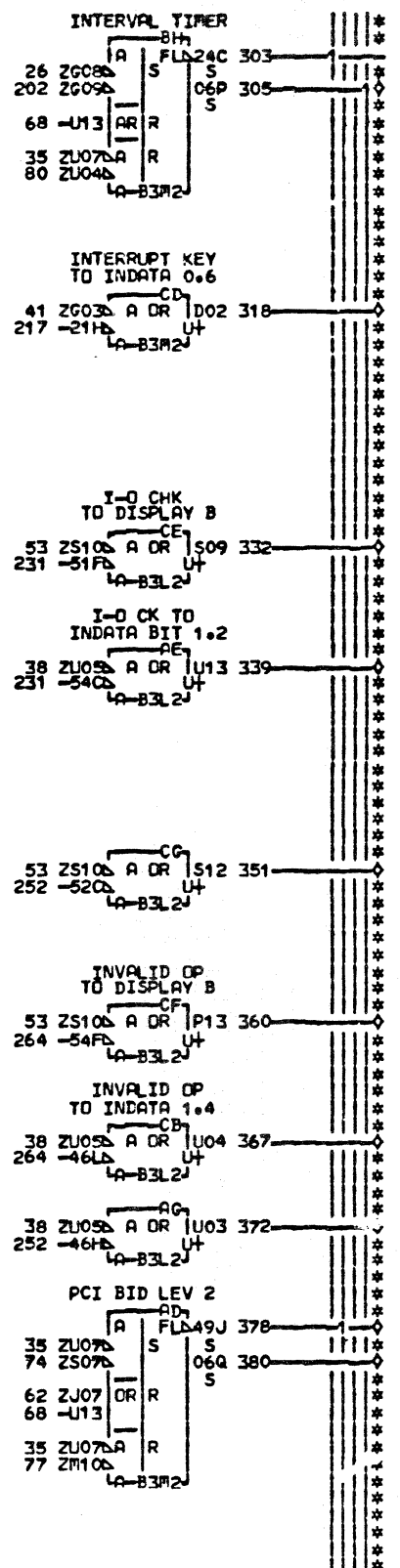
CCU INDATA BUS	
E.C.-HISTORY 344270	E. MACH. 3705
DATE LAST EC 06-02-81 344828	FRAME 01 IBM CORP. SCD CU013 P.N. 1769223 000

CU013
000

+ INTERRUPT PUSHBUTTON IN — AP007EA4 — 2
 + INTERRUPT PUSHBUTTON OUT — AP007EB4 — 5
 - AB TIME — CC001BE6 — 8
 - CD TIME — CC001EJ2 — 11-1
 - CYCLE STEAL AB — CC008AE6 — 14
 + LEV 5 I-O INST ATTEMPT — CD003HB2 — 17-1
 - INVALID OP DECODE — CK007BC4 — 20
 + PROG LEV 1 CURRENT — CP003DD2 — 23-1
 - SET INT TIMER BID — CP007CG6 — 26-1
 + EVEN PARITY ON INPUT DATA — CQ001BF2 — 29
 + ADAPTER I-O ADDR DECODE ERR — CQ001BG2 — 32
 - SET OUTPUT 77 — CQ004FE6 — 35-233
 - GATE INPUT 7E — CQ005FL6 — 38-13
 - GATE INPUT 7F — CQ005FM6 — 41-211
 ALWAYS PLUS — CS002BK4 — 44
 - ANY I TIME + 125 NS — CS006AD6 — 47-1
 - PANEL ACTIVE — CU001BM6 — 50-1
 - GATE STATUS TO DISPLAY B — CU001EJ6 — 53-3
 - SET TRACE LATCH IF CK — CU004FH6 — 56-1
 - PROGRAM STOP LATCH — CU004FK6 — 59-1
 - TEST MODE LCH — CU005CB2 — 62-1
 - ACTIV INSN STEP OR CLK STEP — CUC06CK2 — 65-1
 + RESET — CU010FM2 — 68-242
 - Z BUS BIT 0.2 — DH014GB6 — 71-1
 - Z BUS BIT 0.6 — DJ014GF6 — 74-1
 - Z BUS BIT 0.7 — DJ014GK6 — 77-1
 - Z BUS BIT 1.1 — DK974EH6 — 80-1
 - Z BUS BIT 1.4 — DL004GK6 — 83-1
 - Z BUS BIT 1.5 — DM004GB6 — 86-3



LOC. TYPE
 A-B3F2 6810
 A-B3L2 6823
 A-B3M2 6818

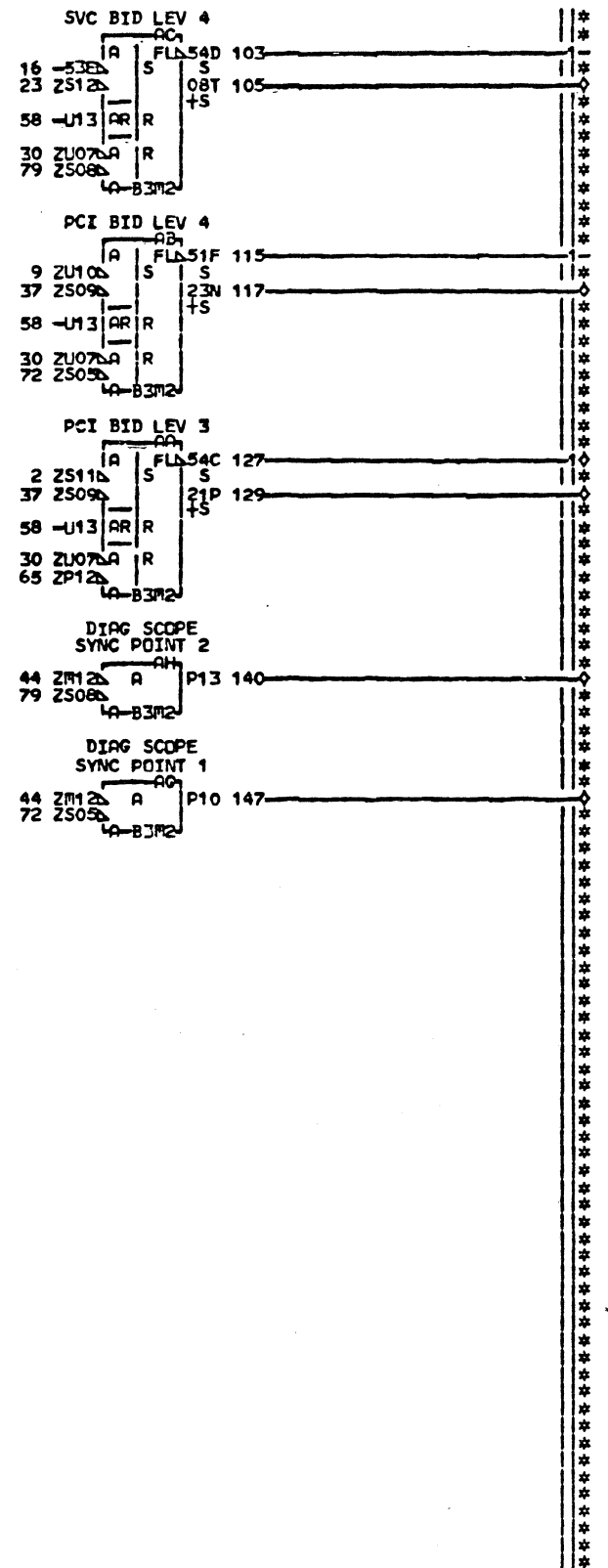


000 CU014
 155 - SAMPLE EXCEPTION — CK007-AH6
 378 - DIAGNOSTIC BID PROG LEV 2 — BL6
 LCP005
 233 + I-O CHECK BID PROG LEV 1 — CB2
 LCP005
 339 + I-O CK TO INDATA BIT 1.2 — CC2
 LCU012
 182 + ADDR COMPARE BID PROG LEV 1 — CE2
 LCP005
 261 + ADDR COMPARE TO INDATA 1.0 — CF2
 LCU012
 254 FOLLOWS CP005AB4 — CP005-CH2
 372 ALWAYS MINUS AT IN X 7E TIME — CJ2
 LCU012
 380 + DIAG PCI BID PROG LEV 2 CP005-CL2
 288 + PCI L2 TO INDATA 0.0 — CU011-CM2
 141 + SET I-O CHECK — CC008-DA4
 245 - SET I-O CHECK — CK007-EB2
 266 + INVALID OP BID PROG LEV 1 — GB2
 LCP005
 367 + INVALID OP TO INDATA 1.4 — GC2
 LCU012
 305 + INTERVAL TIMER BID PROG LEV3 — GH2
 LCP005
 404 + INT TIMER TO INDATA 1.5 CU012-GJ2
 219 + INTERRUPT KEY BID PROG LEV 3 — GL2
 LCP005
 318 + INTERRUPT KEY TO INDATA 0.6 — GM2
 LCU011
 332 + I-O CHK TO DISPLAY B — AP013-GQ2
 360 + INVALID OP TO DISPLAY B AP014-GR2
 351 FOLLOWS AP013AJ4 — AP013-GS2

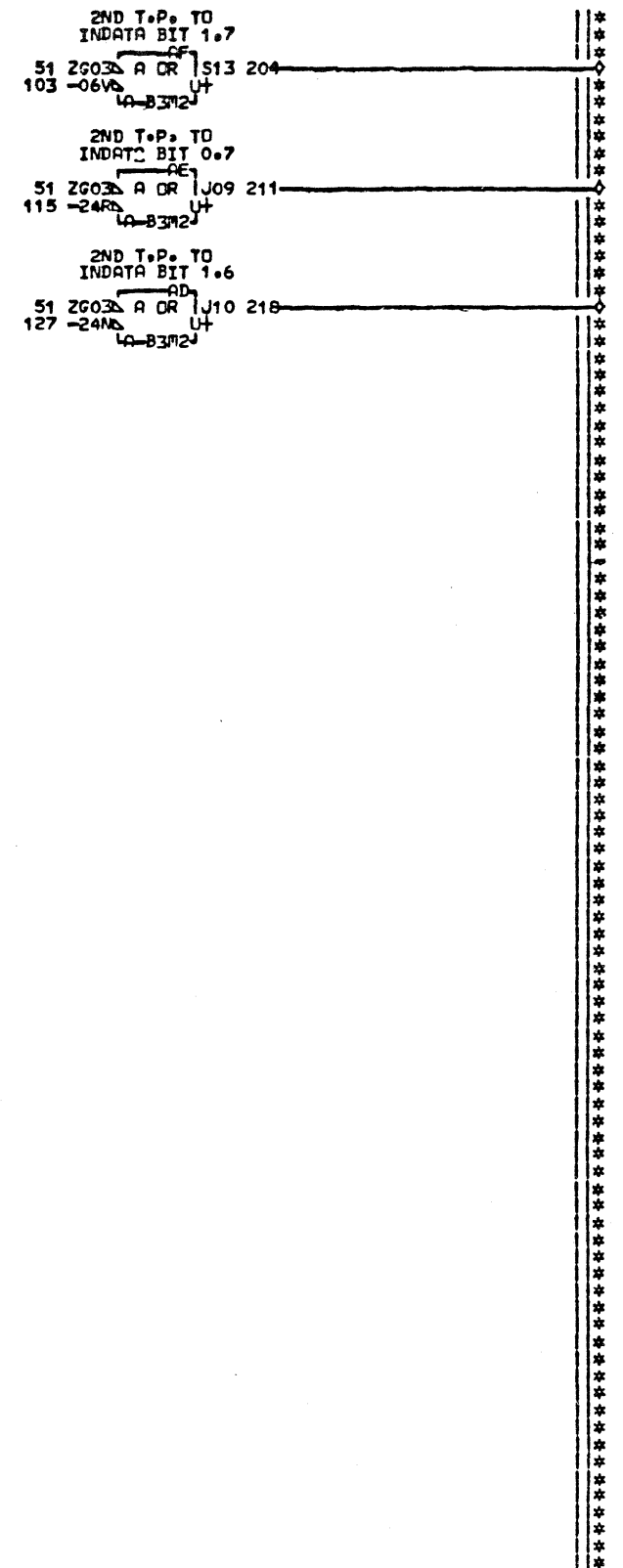
CU014
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PANEL CONTROL
 E.C. HISTORY — E.MACH.3705
 344270
 FRAME 01
 DATE LAST EC IBM CORP. SCD CU014
 06-02-81 344828 P.N. 1769224 000

- OP XXXX XXXX X100 XXXX — CP003BB7 — 2-11
 - OP XXXX XXXX X101 XXXX — CP003BB8 — 9-
 - EXIT I1 BC TO TIME — CP003AC6 — 16-
 - PROG LEV 5 CURRENT — CP003EM6 — 23-
 - SET OUTPUT 77 — CQ004FE6 — 30-3
 - SET OUTPUTS 78 TO 7F — CQ005BA6 — 37-2
 - SET OUTPUT 79 — CQ005DC6 — 44-2
 - GATE INPUT 7F — CQ005FM6 — 51-3
 + RESET — CU010FM2 — 58-3
 - Z BUS BIT 1.2 — DL004GB6 — 65-
 - Z BUS BIT 1.6 — DP004GF6 — 72-2
 - Z BUS BIT 1.7 — DP004GK6 — 79-2



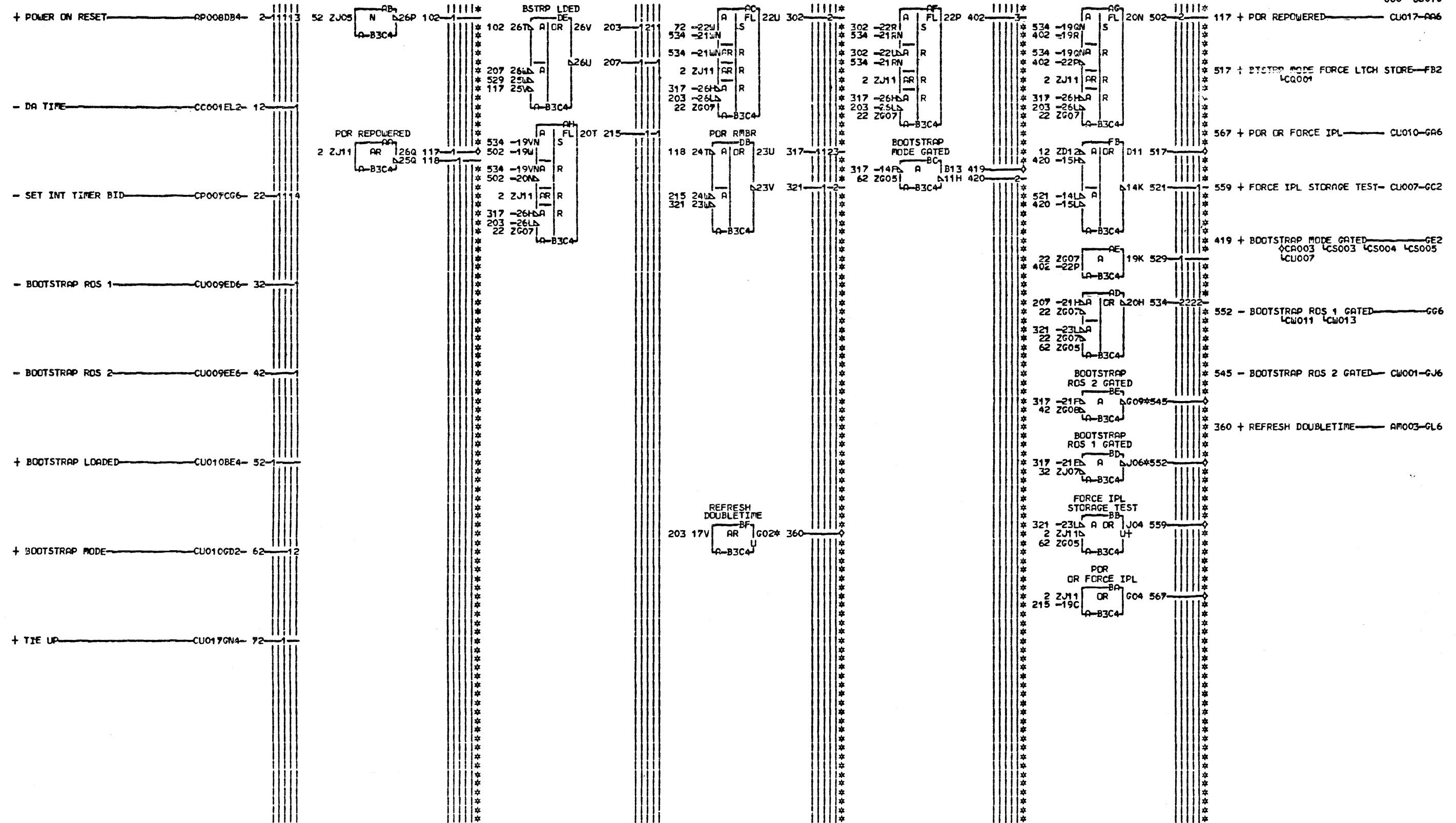
LOC. TYPE
A-B3M2 6818



000 CU015
 127 - PCI 3 — CP005-BB6
 129 + PCI BID PROG LEV 3 — CP005-CB2
 218 + 2ND T.P. TO INDATA BIT 1.6 — CC2
 LCU013
 117 + PCI BID PROG LEV 4 — CP005-CE2
 211 + 2ND T.P. TO INDATA BIT 0.7 — CF2
 LCU011
 105 + SVC BID PROG LEV 4 — CP005-CH2
 204 + 2ND T.P. TO INDATA BIT 1.7 — CJ2
 LCU013
 147 + DIAG SCOPE SYNC POINT 1 AA005-CL2
 140 + DIAG SCOPE SYNC POINT 2 AA005-CN2

CU015
000

PANEL CONTROL	
E.C. HISTORY 344270	E. MACH. 3705 FRAME 01
DATE LAST EC 06-02-81 344828	IBM CORP. SCD P.No. 1769225 CU015 000

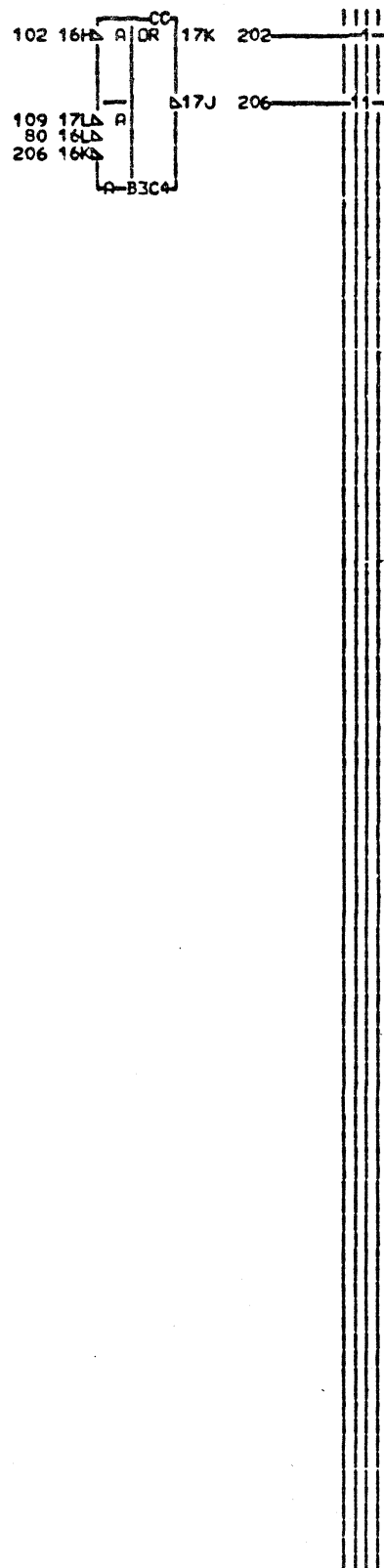
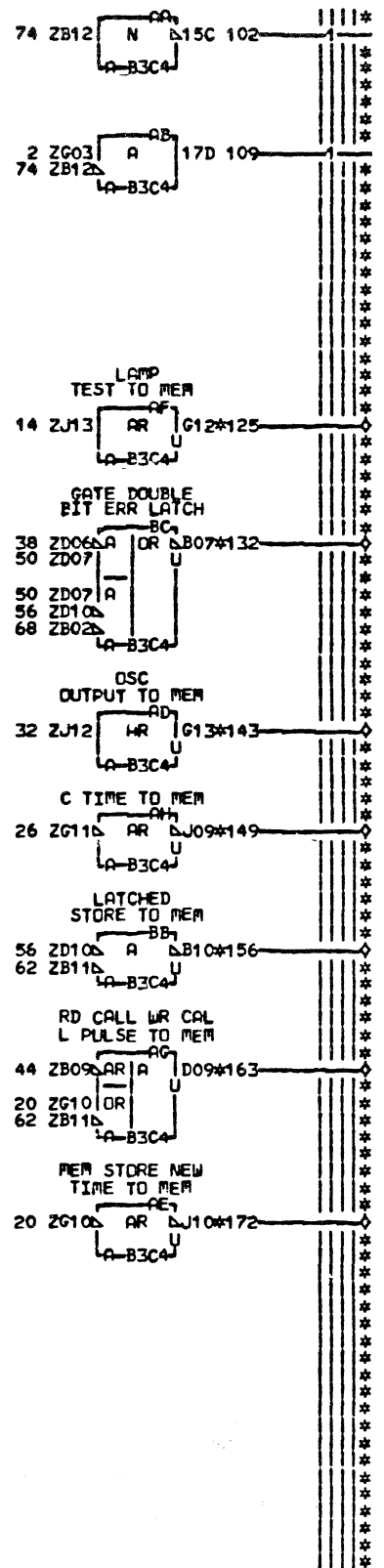


EDGE CONN.
 360 A-B3V4B13
 545 A-B3V3D13
 01A-B4V3D13
 552 A-B3T1E11
 01A-B4T5E02

LOC. TYPE
 A-B3C4 CF99

MEM INITIALIZATION AFTER POR AND REFRESH	
E.C. HISTORY 344270	MACH.3705
DATE LAST EC 06-02-81 344828	FRAME 01
P.N. 1769226	IBM CORP.SCD CU016
	000

- REFRESH PEND IF CLK STEP — AP003DM1* 2-1
 + START PUSHBUTTON IN — AP006DB4 8-1
 + LAMP TEST PUSHBUTTON IN — AP008DF4 14-1
 - MEM STORE NEW TIME — CC001FE2 20-2
 - C TIME — CC001FG2 26-1
 + OSCILLATOR OUTPUT — CC007AA2 32-1
 - ST+STH INST — CD003FF2 38-1
 - READ CALL WRITE CALL PULSE — CP001DF6 44-1
 + C T2 TIME — CP001EN2 50-2
 - LATCHED STORE — CQ001GL2 56-2
 + BAD ADDRESS — CS002DK6 62-2
 - CYCLE STEAL CD — CU003GK6 68-1
 + ACTIVE CLOCK STEP — CU007DE2 74-2
 + PDR REPOWERED — CU016AA6 80-1



LOC. TYPE
A-B3C4 CF99



000 CU017

318 + START PUSH BUTTON IN GATED — CF6
 LCC007 LCU003
 143 + OSC OUTPUT TO MEM — AP003-EA6
 172 - MEM STORE NEW TIME TO MEM — FA6
 LAM003
 125 + LAMP TEST TO MEM — AP003-FC6
 163 + RD CALL WR CALL PULSE TO MEM — FG2
 LAM003
 149 - C TIME TO MEM — AP003-GA6
 304 + ACTIVE CLOCK STEP GATED — GC6
 LAM003 LCC007 LCS001 LCU006
 156 - LATCHED STORE TO MEM — AM003-GE6
 132 - GATE DOUBLE BIT ERR LATCH — GJ2
 LAM003
 473 + TIE UP — CU016-GN4

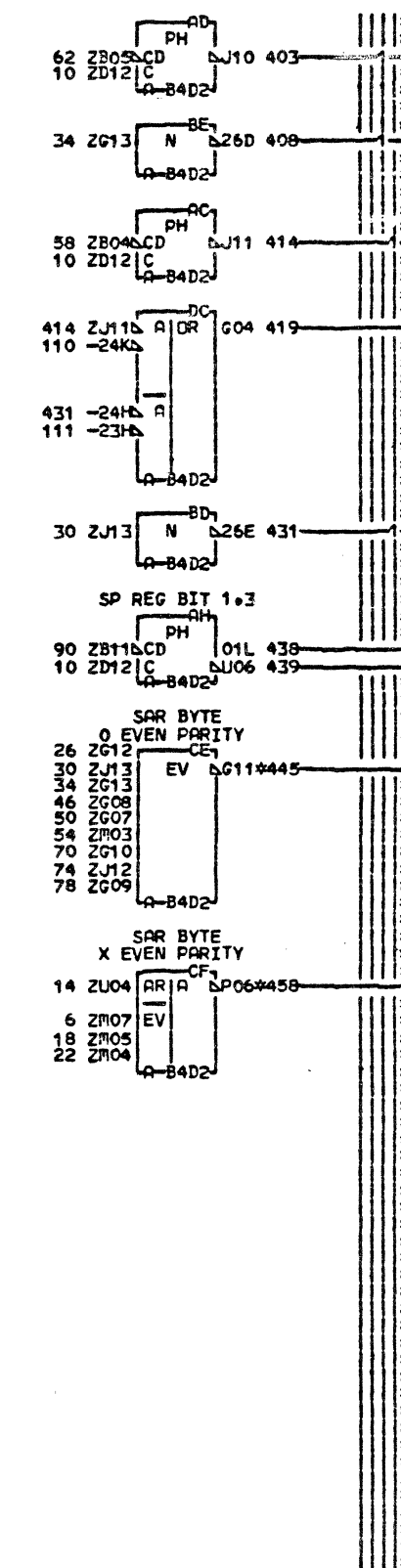
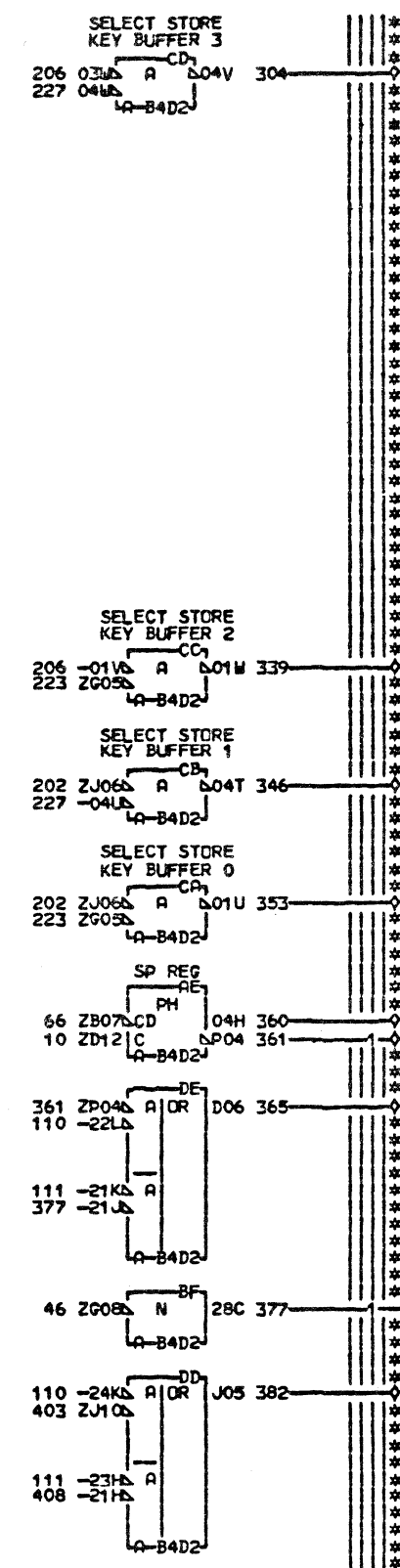
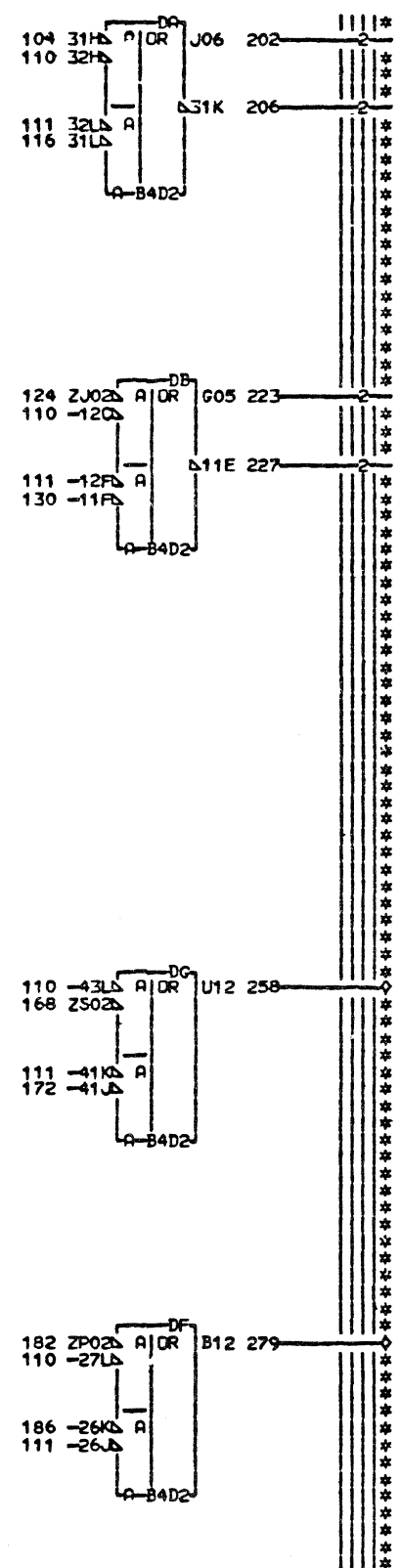
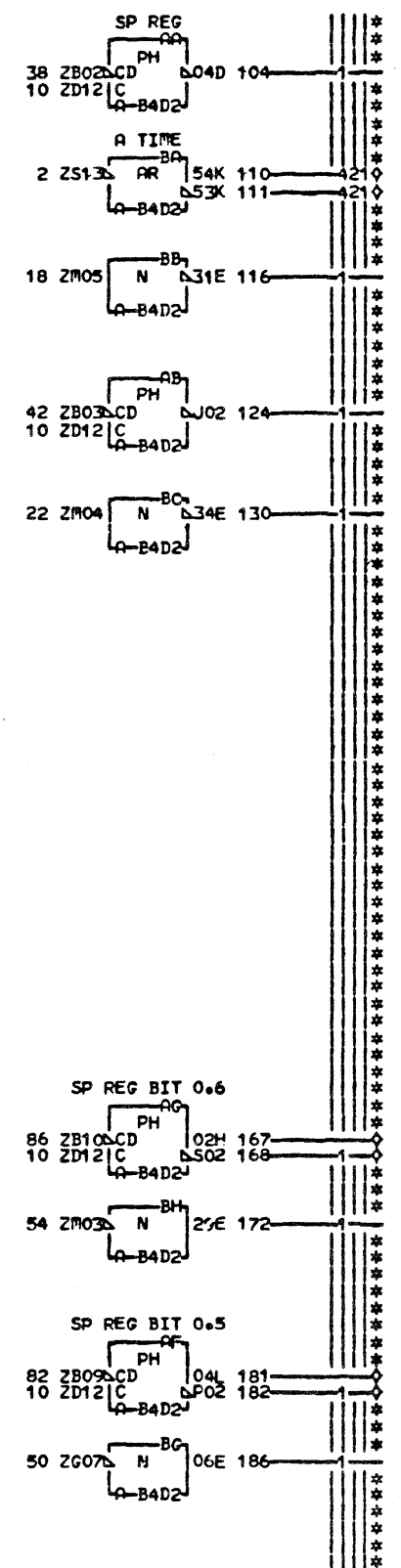
EDGE CONN. 304 A-B3V4D13

2 RESISTOR
 A-B3C4G03
 125 A-B3V4B09
 132 A-B3V4B06
 143 A-B3V4D09
 149 A-B3V4D11
 156 A-B3V4B10
 163 A-B3V4D07
 172 A-B3V4D10

CU017
000

GATED START AND CLOCK
 STEP AND MEM CONTROLS
 E.C. HISTORY — E. MACH. 3705
 344270
 DATE LAST EC 06-02-81 344828
 FRAME 01
 IBM CORP. SCD CU017
 P.No. 1769227 000

- A TIME CC001FC2* 2-11
 + SAR X.P REDRIVEN CM003DG2- 6-1
 + SET OUTPUT 73 CQ004DE2- 10-3
 + BYTE X YIE UP DF002GF4- 14-1
 + SAR BIT X.6 DF971EG2- 18-1
 + SAR BIT X.7 DF971EL2- 22-1
 + SAR BIT 0.P DG971EC2- 26-1
 + SAR BIT 0.0 DG971EG2- 30-2
 + SAR BIT 0.1 DG971EL2- 34-2
 - Z BUS BIT 0.0 DG974EB6- 38-1
 - Z BUS BIT 0.1 DG974EH6- 42-1
 + SAR BIT 0.2 DH011EC2- 46-1
 + SAR BIT 0.3 DH011EH2- 50-1
 + SAR BIT 0.4 DH011EK2- 54-1
 - Z BUS BIT 0.2 DH014GB6- 58-1
 - Z BUS BIT 0.3 DH014GF6- 62-1
 - Z BUS BIT 0.4 DH014GK6- 66-1
 + SAR BIT 0.5 DJ011EC2- 70-1
 + SAR BIT 0.6 DJ011EH2- 74-1
 + SAR BIT 0.7 DJ011EK2- 78-1
 - Z BUS BIT 0.5 DJ014GB6- 82-1
 - Z BUS BIT 0.6 DJ014GF6- 86-1
 - Z BUS BIT 1.3 DL004GF6- 90-1



000 CV001
 360 + SP REG BIT 0.4 CV011-BJ2
 361 - SP REG BIT 0.4 CV011-BJ6
 181 + SP REG BIT 0.5 CV011-BK2
 182 - SP REG BIT 0.5 CV011-BK6
 167 + SP REG BIT 0.6 CV011-BL2
 168 - SP REG BIT 0.6 CV011-BL6
 438 + SP REG BIT 1.3 BM2
 439 - SP REG BIT 1.3 BM6
 110 + A TIME CV011 LCV061
 111 - A TIME CV011 LCV061
 419 + STORE KEY ADDRESS XX1XXXX LCV021 LCV031
 382 + STORE KEY ADDRESS XXX1XXX LCV021 LCV031
 365 + STORE KEY ADDRESS XXXX1XX LCV021 LCV031
 279 + STORE KEY ADDRESS XXXXX1X LCV021 LCV031
 258 + STORE KEY ADDRESS XXXXX1 LCV021 LCV031
 353 - SELECT STORE KEY BUFFER 0 LCV021
 346 - SELECT STORE KEY BUFFER 1 LCV021
 339 - SELECT STORE KEY BUFFER 2 LCV031
 304 - SELECT STORE KEY BUFFER 3 LCV031
 445 - SAR BYTE 0 EVEN PARITY CK003-FG2
 458 - SAR BYTE X EVEN PARITY CK003-HE4

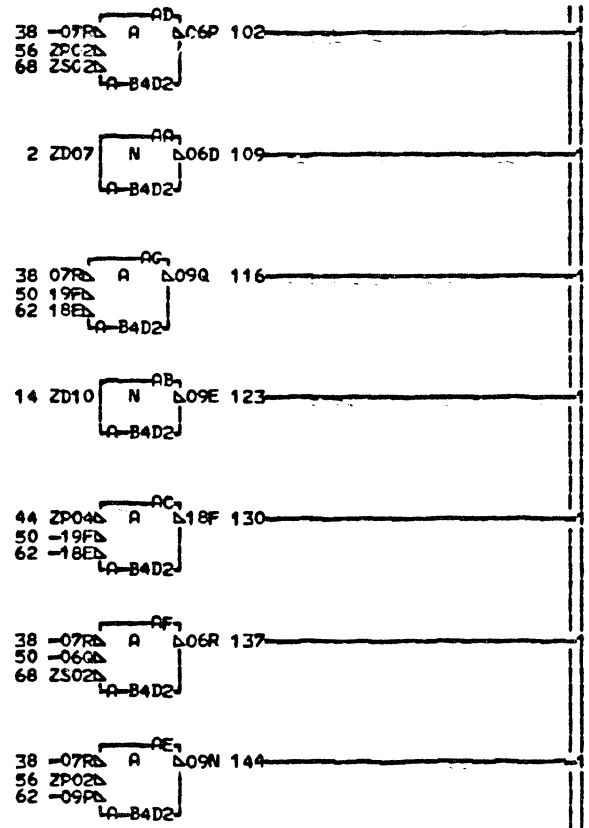
EDGE CONN.
 2 RESISTOR
 A-B4D2S13
 445 A-B4P1E13
 01A-B3P6E04
 458 A-B4P1E11
 01A-B3P6E02

LOC. TYPE
 A-B4D2 6798

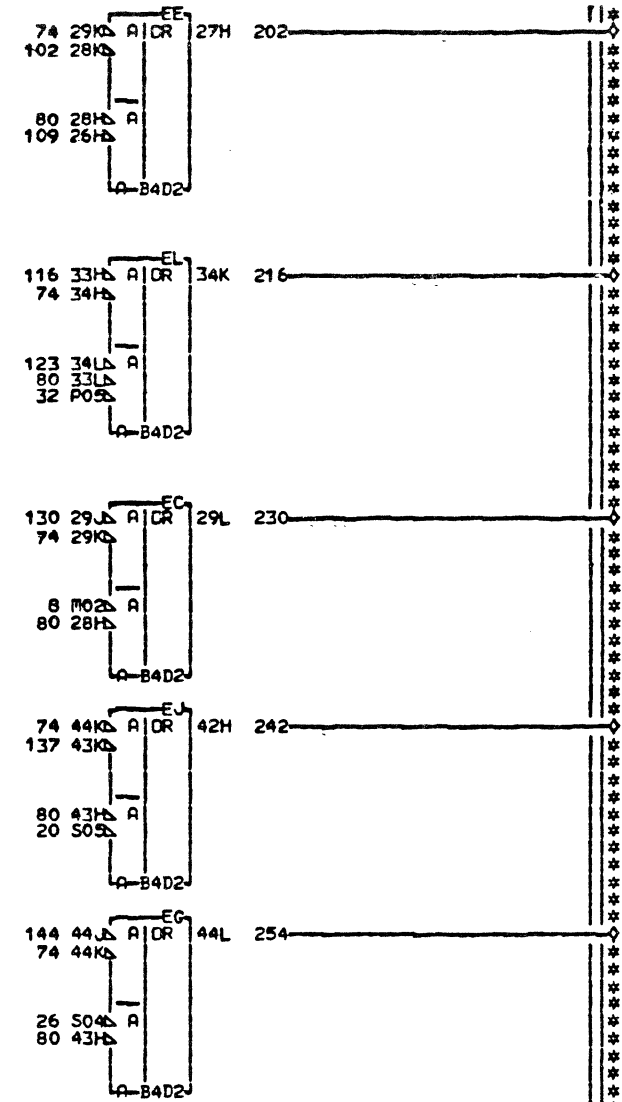
CCU STORAGE PROTECT		FRAME 01	
SP REG AND STORE KEY ADDR GEN		IBM CORP. SCD	CV001
-E.C.-HISTORY-		P.N. 1769228	000
DATE	LAST EC		
10-14-80	344270		

CV001
 000

- SPARE GO FOR PROTECTION KEY—AU001GH— 2—1
 ALWAYS PLUS—CP001FG6— 8—1
 + ALLOW INST—CP001FJ2— 14—1
 - GO CHANNEL 1—CP001FK6— 20—1
 - GO CHANNEL 2—CP001FM6— 26—1
 - PROG LEV 5 NEXT—CP002GK6— 32—1
 + SP REG BIT 0.4—CV001BJ2— 38—4
 - SP REG BIT 0.4—CV001BJ6— 44—1
 + SP REG BIT 0.5—CV001BK2— 50—3
 - SP REG BIT 0.5—CV001BK6— 56—2
 + SP REG BIT 0.6—CV001BL2— 62—3
 - SP REG BIT 0.6—CV001BL6— 68—2
 + A TIME—CV001CA2— 74—5
 - A TIME—CV001CA6— 80—5



LOC. TYPE
A-B4D2 6798



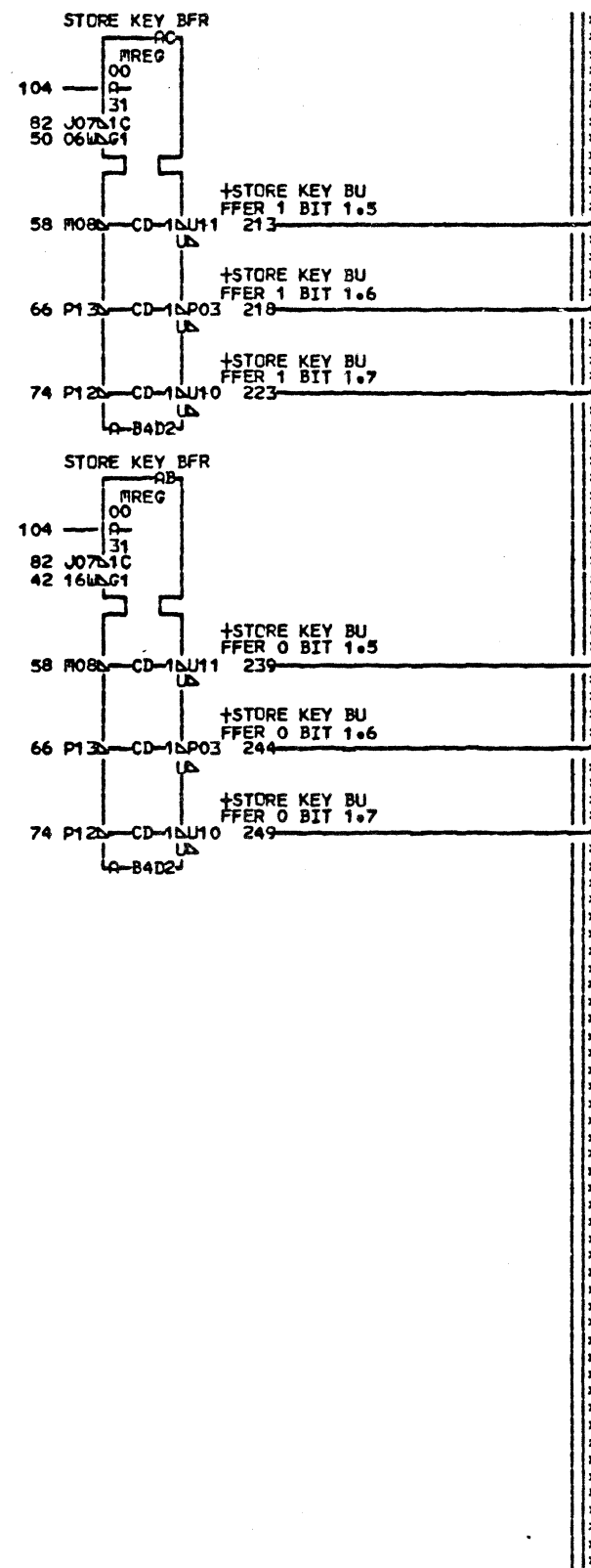
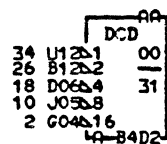
000 CV011

230 + PROT KEY ADDRESS 100— CV041-EC2
 202 + PROT KEY ADDRESS 011— CV041-EE2
 254 + PROT KEY ADDRESS 010— CV041-EG2
 242 + PROT KEY ADDRESS 001— CV041-EJ2
 216 + PROT KEY ADDRESS 000— CV041-EL2

CV011
000

CCU STORAGE PROTECT		PROTECT KEY ADDRESS GENERATION	
E.C.—HISTORY—E3MACH.3705		344270	
DATE	LAST EC	FRAME	01
06-02-81	344828	IBM CORP.SCD	CV011
		P.No. 1769229	000

+ STORE KEY ADDRESS XX1XXX—CV001DC2— 2—
 + STORE KEY ADDRESS XXX1XXX—CV001DD2— 10—
 + STORE KEY ADDRESS XXXX1XX—CV001DE2— 18—
 + STORE KEY ADDRESS XXXXX1X—CV001DF2— 26—
 + STORE KEY ADDRESS XXXXX1—CV001DG2— 34—
 - SELECT STORE KEY BUFFER 0—CV001FA6— 42—
 - SELECT STORE KEY BUFFER 1—CV001FB6— 50—
 + SP REG BIT 1.5—CV061BL2— 58—2
 + SP REG BIT 1.6—CV061BM2— 66—2
 + SP REG BIT 1.7—CV061BN2— 74—2
 - WRITE STORE KEY BUFFER—CV061FK6— 82—2



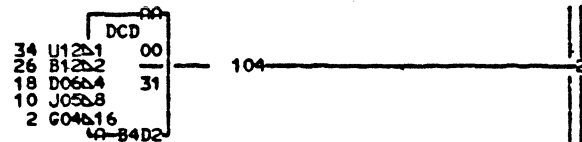
000 CV021
 239 + STORE KEY BUFFER 0 BIT 1.5—DB2
 LCV051
 244 + STORE KEY BUFFER 0 BIT 1.6—DB4
 LCV051
 249 + STORE KEY BUFFER 0 BIT 1.7—DB6
 LCV051
 213 + STORE KEY BUFFER 1 BIT 1.5—DG2
 LCV051
 218 + STORE KEY BUFFER 1 BIT 1.6—DG4
 LCV051
 223 + STORE KEY BUFFER 1 BIT 1.7—DG6
 LCV051

LOC. TYPE
A-B4D2 6798

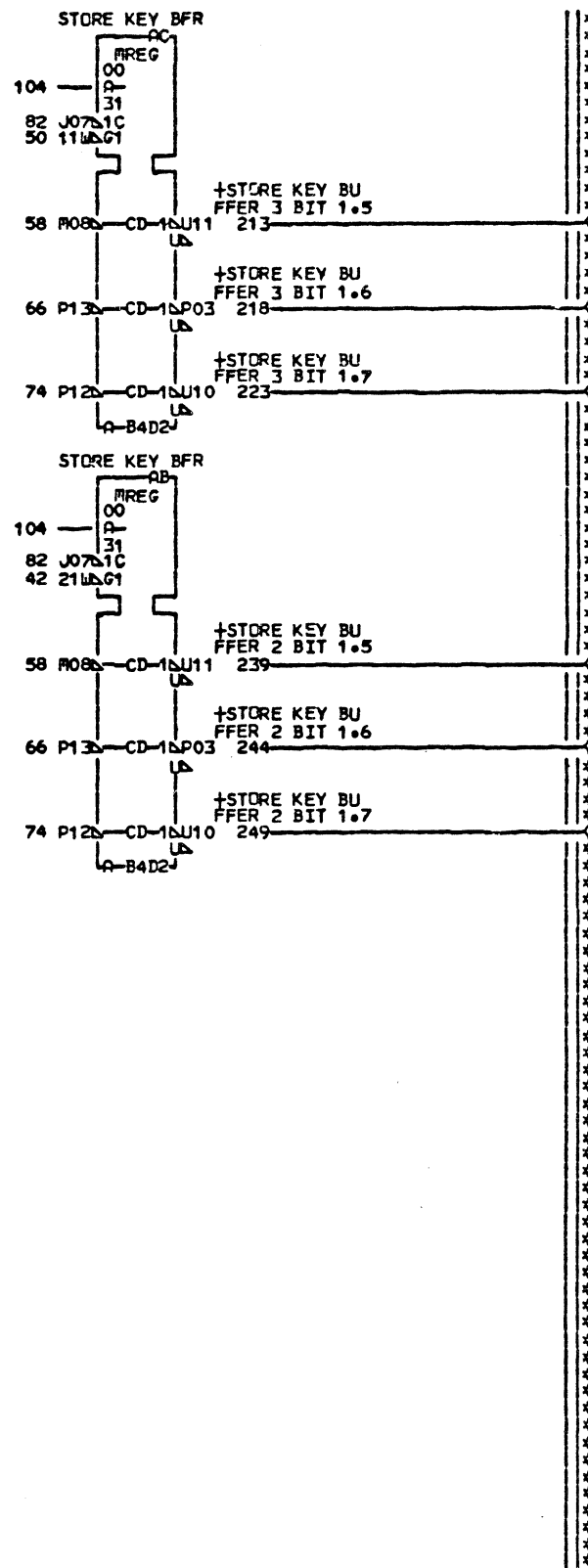
CV021
000

CCU STORAGE PROTECT		STORE KEY BUFFER	
E.C.—HISTORY—E		MACH.3705	
DATE	LAST EC	FRAME	01
10-14-80	344270	IBM CORP.SCD	CV021
		P.No. 1769230	000

+ STORE KEY ADDRESS XX1XXX—CV001DC2— 2—1
 + STORE KEY ADDRESS XXX1XXX—CV001DD2— 10—1
 + STORE KEY ADDRESS XXXX1XX—CV001DE2— 18—1
 + STORE KEY ADDRESS XXXXX1X—CV001DF2— 26—1
 + STORE KEY ADDRESS XXXXXX1—CV001DG2— 34—1
 - SELECT STORE KEY BUFFER 2—CV001FC6— 42—1
 - SELECT STORE KEY BUFFER 3—CV001FD6— 50—1
 + SP REG BIT 1.5—CV061FL2— 58—2
 + SP REG BIT 1.6—CV061FM2— 66—2
 + SP REG BIT 1.7—CV061FN2— 74—2
 - WRITE STORE KEY BUFFER—CV061FK6— 82—2



LDC. TYPE
A-B4D2 6798



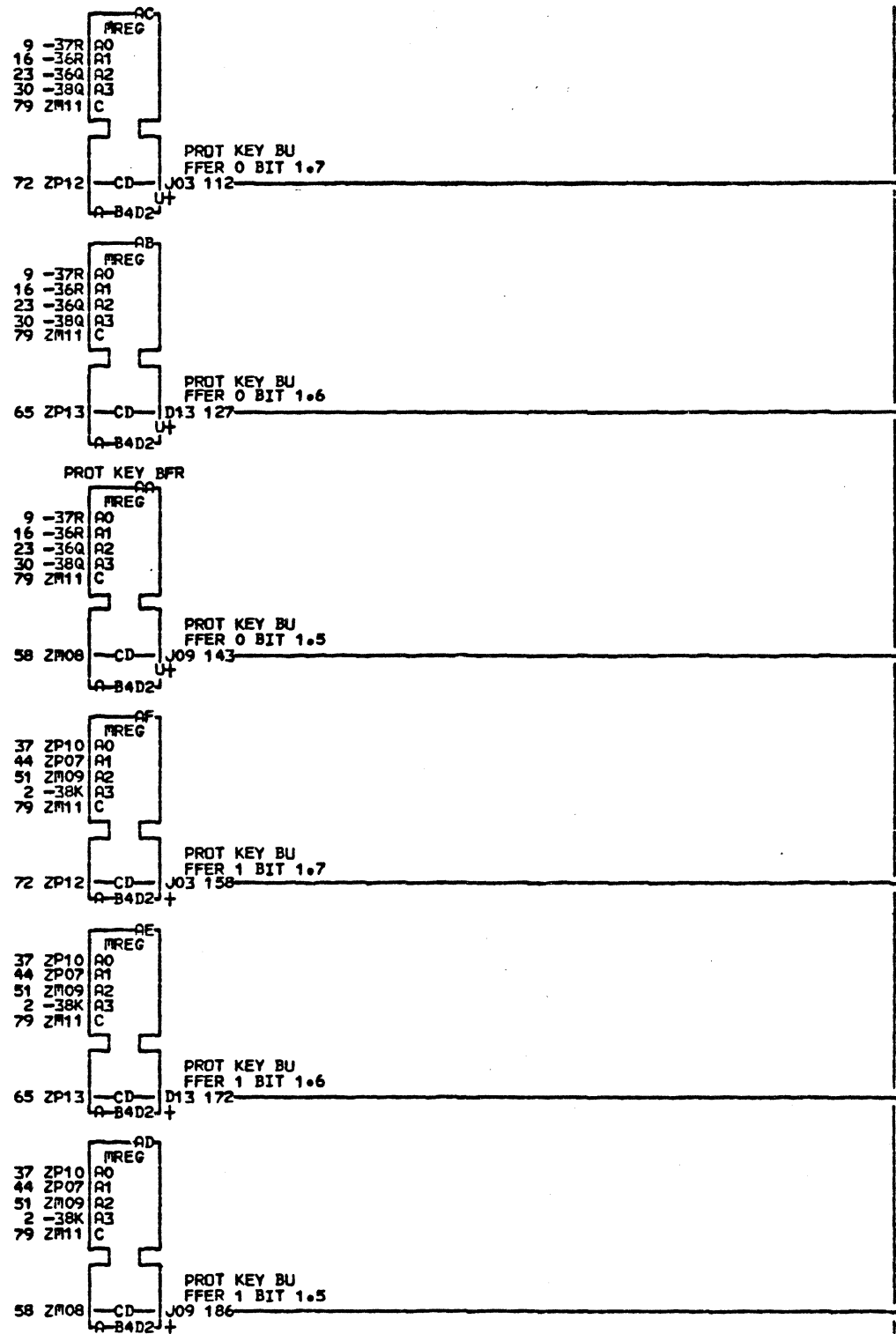
000 CV031

239 + STORE KEY BUFFER 2 BIT 1.5—DB2
 LCV051
 244 + STORE KEY BUFFER 2 BIT 1.6—DB4
 LCV051
 249 + STORE KEY BUFFER 2 BIT 1.7—DB6
 LCV051
 213 + STORE KEY BUFFER 3 BIT 1.5—DG2
 LCV051
 218 + STORE KEY BUFFER 3 BIT 1.6—DG4
 LCV051
 223 + STORE KEY BUFFER 3 BIT 1.7—DG6
 LCV051

CV031
000

CCU STORAGE PROTECT		STORE KEY BUFFERS 2 AND 3	
E.C.—HISTORY—E		MACH.3705	
FRAME	01	IBM CORP.SCD	CV031
DATE	LAST EC	P.No.	1769231
10-14-80	344270		000

+ PROT KEY ADDRESS 100 — CV011EC2 — 2-3
 + PROT KEY ADDRESS 011 — CV011EE2 — 9-3
 + PROT KEY ADDRESS 010 — CV011EG2 — 16-3
 + PROT KEY ADDRESS 001 — CV011EJ2 — 23-3
 + PROT KEY ADDRESS 000 — CV011EL2 — 30-3
 - FLOAT — CV041004 — 37-3
 - FLOAT — CV041005 — 44-3
 - FLOAT — CV041006 — 51-3
 + SP REG BIT 1.5 — CV061BL2 — 58-2
 + SP REG BIT 1.6 — CV061BM2 — 65-2
 + SP REG BIT 1.7 — CV061BN2 — 72-2
 + WRITE PROT KEY BUFFER — CV061FL2 — 79-6



LOC. TYPE
 A-34D2 6798

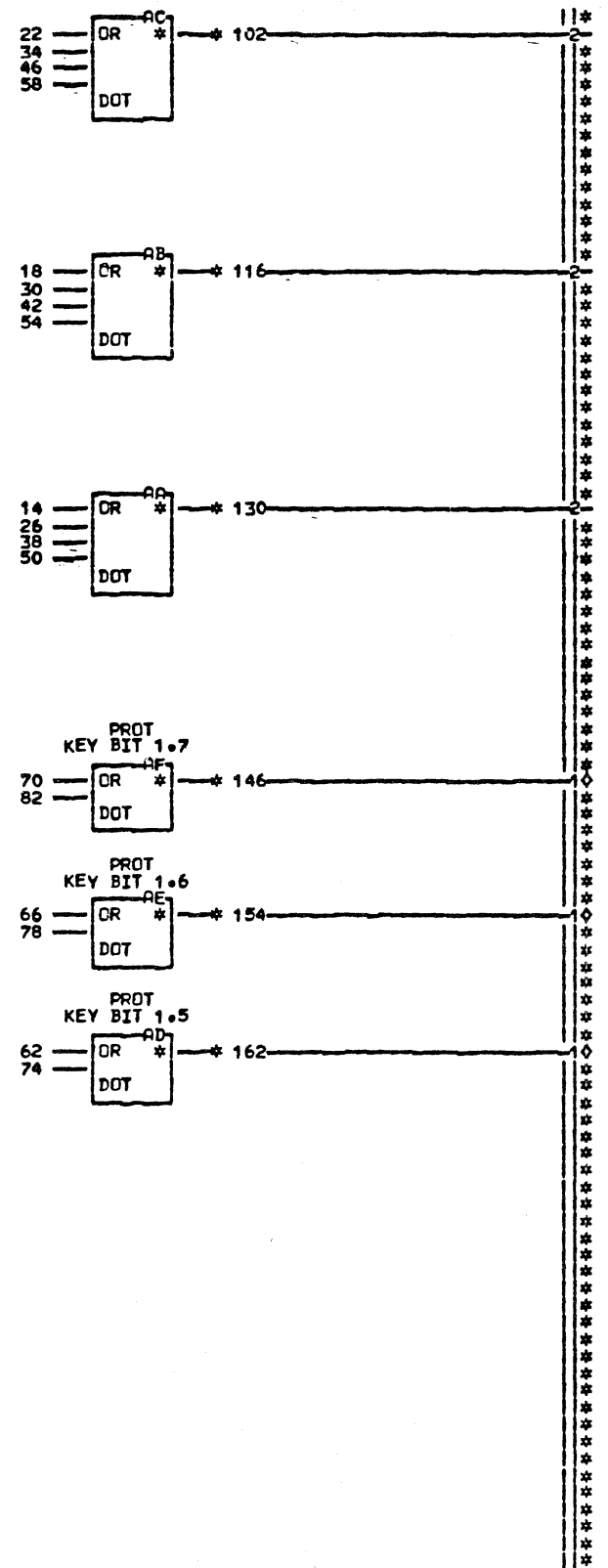
000 CV041

143 + PROT KEY BUFFER 0 BIT 1.5 — DB6
 LCV051
 127 + PROT KEY BUFFER 0 BIT 1.6 — DD6
 LCV051
 112 + PROT KEY BUFFER 0 BIT 1.7 — DF6
 LCV051
 186 + PROT KEY BUFFER 1 BIT 1.5 — DH6
 LCV051
 172 + PROT KEY BUFFER 1 BIT 1.6 — DK6
 LCV051
 158 + PROT KEY BUFFER 1 BIT 1.7 — DM6
 LCV051

CV041
 000

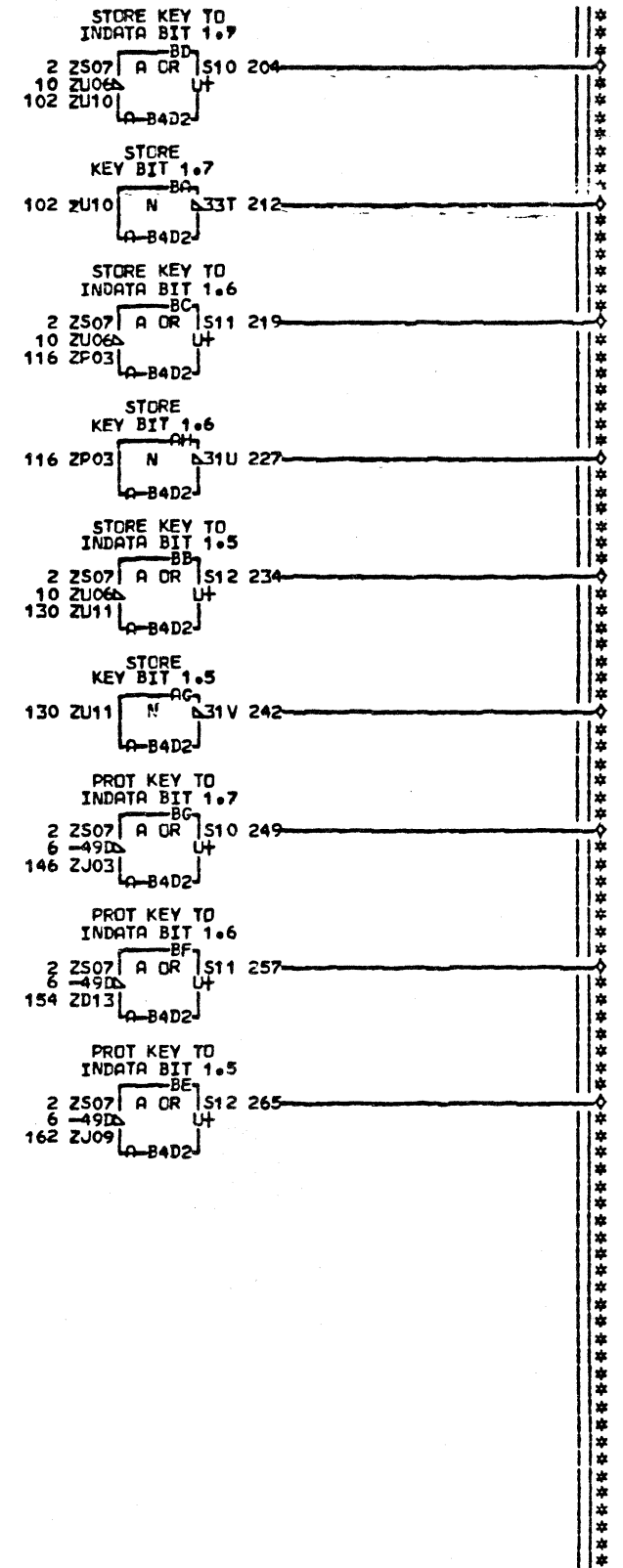
CCU STORAGE PROTECT		PROTECT KEY BUFFER	
-E.C.-HISTORY-		-E-MACH-3705	
FRAME	01	IBM CORP.SCD	CV041
DATE	LAST EC	P.No.	000
10-14-80	344270	1769232	

+ GATE INPUT 73 ———— CV004DA2 — 2-11
 + SP REG BIT 1.3 ———— CV001BA2 — 6-3
 - SP REG BIT 1.3 ———— CV001BA6 — 10-3
 + STORE KEY BUFFER 0 BIT 1.5 — CV021DB2 — 14-
 + STORE KEY BUFFER 0 BIT 1.6 — CV021DB4 — 18-
 + STORE KEY BUFFER 0 BIT 1.7 — CV021DB6 — 22-
 + STORE KEY BUFFER 1 BIT 1.5 — CV021DG2 — 26-
 + STORE KEY BUFFER 1 BIT 1.6 — CV021DG4 — 30-
 + STORE KEY BUFFER 1 BIT 1.7 — CV021DG6 — 34-
 + STORE KEY BUFFER 2 BIT 1.5 — CV031DB2 — 38-
 + STORE KEY BUFFER 2 BIT 1.6 — CV031DB4 — 42-
 + STORE KEY BUFFER 2 BIT 1.7 — CV031DB6 — 46-
 + STORE KEY BUFFER 3 BIT 1.5 — CV031DG2 — 50-
 + STORE KEY BUFFER 3 BIT 1.6 — CV031DG4 — 54-
 + STORE KEY BUFFER 3 BIT 1.7 — CV031DG6 — 58-
 + PROT KEY BUFFER 0 BIT 1.5 — CV041DB6 — 62-
 + PROT KEY BUFFER 0 BIT 1.6 — CV041DD6 — 66-
 + PROT KEY BUFFER 0 BIT 1.7 — CV041DF6 — 70-
 + PROT KEY BUFFER 1 BIT 1.5 — CV041DH6 — 74-
 + PROT KEY BUFFER 1 BIT 1.6 — CV041DK6 — 78-
 + PROT KEY BUFFER 1 BIT 1.7 — CV041DM6 — 82-



EDGE CONN. A-B4D2D13
 102 RESISTOR 162 RESISTOR
 A-B4D2U10 A-B4D2J09
 116 RESISTOR
 A-B4D2P03
 130 RESISTOR
 A-B4D2M1
 146 RESISTOR
 A-B4D2J03
 154 RESISTOR

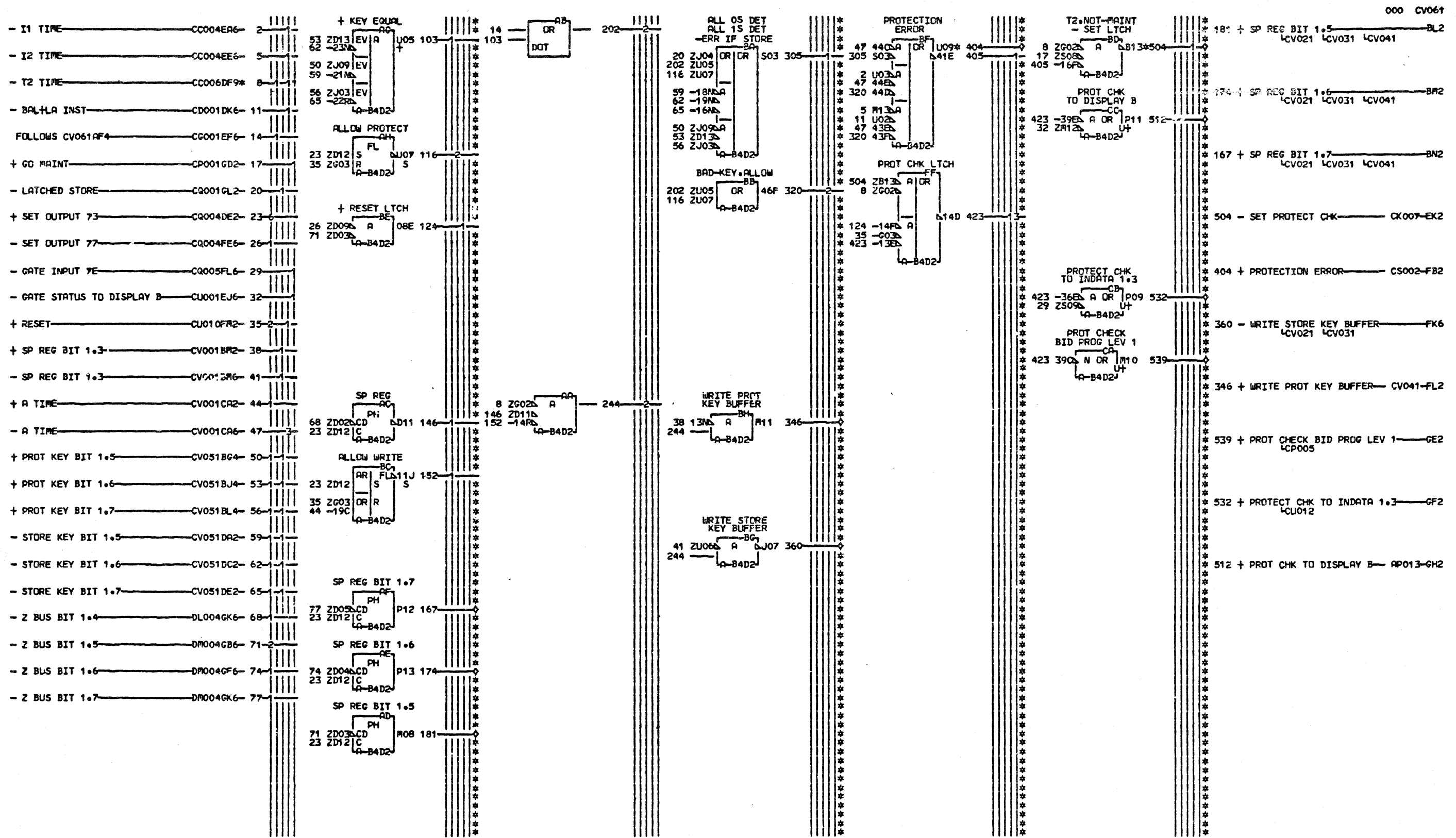
LOC. TYPE
 A-B4D2 6798



000 CV051
 162 + PROT KEY BIT 1.5 ———— CV061-BG4
 154 + PROT KEY BIT 1.6 ———— CV061-BJ4
 146 + PROT KEY BIT 1.7 ———— CV061-BL4
 242 - STORE KEY BIT 1.5 ———— CV061-DA2
 227 - STORE KEY BIT 1.6 ———— CV061-DC2
 212 - STORE KEY BIT 1.7 ———— CV061-DE2
 234 + STORE KEY TO INDATA BIT 1.5 — FA2
 LCU012
 219 + STORE KEY TO INDATA BIT 1.6 — FC2
 LCU013
 204 + STORE KEY TO INDATA BIT 1.7 — FE2
 LCU013
 265 + PROT KEY TO INDATA BIT 1.5 — FG2
 LCU012
 257 + PROT KEY TO INDATA BIT 1.6 — FJ2
 LCU013
 249 + PROT KEY TO INDATA BIT 1.7 — FL2
 LCU013

CV051
 000

CCU STORAGE PROTECT		MACH. 3705	
INDATA GATING		E.C. HISTORY	
344270		344270	
DATE	LAST EC	IBN CDRP.SCD	CV051
06-02-81	344828	P.No. 1769233	000



000 CV061

EDGE CONN.
8 RESISTOR
A-B4D2G02
404 A-B4Q1B13
01A-B3Q6B04
504 A-B4Q1A11
01A-B3Q6A02

LOC. TYPE
A-B4D2 6798

CCU STORAGE PROTECT PROTECTION ERROR DETECTION	
E.C. HISTORY	MACH. 3703
FRAME 01	
DATE LAST EC	IBM CORP. SCD CV061
10-14-60 344270	P.N. 1769234 000

CV061
000