SY28-0719-1
File No. S370-36

Systems

OS/VS2
System Logic Library
Volume 7

Directory
Data Areas
Diagnostic Aids

Includes Selectable Units:

Scheduler Improvements	VS2.03.804
Supervisor Performance #1	VS2.03.805
Supervisor Performance #2	VS2.03.807
IBM 3800 Printing Subsystem	VS2.03.810
TSO/VTAM	VS2.03.813
Service Data Improvements	VS2.03.817
MSS Enhancements	5752-824
System Security Support	5752-832
Dumping Improvements	5752-833
Hardware Recovery Enhancements	5752-855

IBM

Second Edition (August, 1977)

This is a major revision of, and obsoletes, SY28-0719-0 incorporating changes released in the following Selectable Unit Newsletters, and System Library Supplements:

Scheduler Improvements	VS2.03.804 SN28-2685 (dated May 28, 1976)
Supervisor Performance #1	VS2.03.805 SN28-2690 (dated May 28, 1976)
Supervisor Performance #2	VS2.03.807 SN28-2696 (dated May 28, 1976)
IBM 3800 Print Subsystem	VS2.03.810 SN28-2699 (dated May 28, 1976)
TSO/VTAM	VS2.03.813 SN28-2667 (dated May 28, 1976)
Service Data Improvements	VS2.03.817 SN28-2763 (dated July 30, 1976)
MSS Enhancements	5752-824 SY28-0797 (dated February 14, 1977)
System Security Support	5752-832 SY28-0857 (dated May 27, 1977)
Dumping Improvements	5752-833 SY28-0833 (dated March 30, 1977)
Hardware Recovery	5752-855 SY28-0901 (dated May 31, 1977)

This edition applies to Release 3.7 of OS/VS2 and to all subsequent releases of OS/VS2 until otherwise indicated in new editions or Technical Newsletters. Changes are continually made to the information herein; before using this publication in connection with the operation of IBM systems, consult the latest **IBM System/370 Bibliography,** GC20-0001, for the editions that are applicable and current.

Requests for copies of IBM publications should be made to your IBM representative or to the IBM branch office serving your locality.

A form for readers' comments is provided at the back of this publication. If the form has been removed, comments may be addressed to IBM Corporation, Publications Development, Department D58, Building 706-2, PO Box 390, Poughkeepsie, N.Y. 12602. Comments become the property of IBM.

System Logic Library comprises seven volumes. Following is the content and order number for each volume.

OS/VS2 System Logic Library,

Volume 1 contents: SY28-0713
- MVS logic introduction
- Abbreviation list
- Index for all volumes

Volume 2 contents: SY28-0714
- Method of Operation diagrams for
- Communications Task
- Command Processing
- Region Control Task (RCT)
- Started Task Control (STC)
- LOGON Scheduling

Volume 3 contents: SY28-0715
- Method of Operation diagrams for
- System Resources Manager (SRM)
- System Activity Measurement Activity (MF/1)
- JOB Scheduling
  - —Subsystem Interface
  - —Master Subsystem
  - —Initiator/Terminator
  - —SWA Create Interface
  - —Converter/Interpreter
  - —SWA Manager
  - —Allocation/Unallocation
  - —System Management Facilities (SMF)
  - —System Log
  - —Checkpoint/Restart

Volume 4 contents: SY28-0716
- Method of Operation diagrams for
- Timer Supervision
- Supervisor Control
- Task Management
- Program Management
- Recovery/Termination Management (RTM)

Volume 5 contents: SY28-0717
- Method of Operation diagrams for
- Real Storage Management (RSM)
- Virtual Storage Management (VSM)
- Auxiliary Storage Management (ASM)

Volume 6 contents: SY28-0718
- Program Organization

Volume 7 contents: SY28-0719
- Directory
- Data Areas
- Diagnostic Aids

Please note that if you use only one order number, you will only receive that volume. To receive all seven volumes, you must either use all seven form numbers or, simply the following number: SBOF-8210. If you use SBOF-8210, you will receive all seven volumes.

The publication is intended for persons who are debugging or modifying the system. For general information about the use of the MVS system, refer to the publication *Introduction to OS/VS Release 2,* GC28-0661.

How This Publication is Organized

This publication contains six chapters. Following, is a synopsis of the information in each section:

- *Introduction and Master Index* — an overview of each of the functions this publication documents, an abbreviation list of all acronyms used in the publication, and a complete index for all seven volumes.
- *Method of Operation* — a functional approach to each of the subcomponents, using both diagrams and text. Each subcomponent begins with an introduction; all the diagrams and text applying to that subcomponent follow.
- *Program Organization* — a description of module-to-module flow for each subcomponent; a description of each module's function, including entry and exit. The module-to-module flow is ordered by subcomponent. The module descriptions are in alphabetic order without regard to subcomponent.
- *Directory* — a cross-reference from names in the various subcomponents to their place in the source code and in the publication.
- *Data Areas* — a description of the major data areas used by the subcomponents (only those, however, that are not described in *OS/VS Data Areas,* SYB8-0606, which is on microfiche); a data area usage table, showing whether a module reads or updates a data area; a control block overview diagram for each subcomponent, showing the various pointer schemes for the control blocks applicable to each subcomponent; a table detailing data area acronyms, mapping macro instructions, common names, and symbol usage table.

- *Diagnostic Aids* — the messages issued, including the modules that issue, detect, and contain the message; register usage; return codes; wait state codes; and miscellaneous aids.

Corequisite Reading

The following publications are corequisites:

- *OS/VS2 JES2 Logic,* SY28-0622
- *OS/VS Data Areas,* SYB8-0606 (This document is on microfiche.)
- *OS/VS2 System Initialization Logic,* SY28-0623

Notes:

- You must have installed the following Selectable Units in order to use this publication:
  —Scheduler Improvements (SU4)
  —Supervisor Performance #1 (SU5)
  —Supervisor Performance #2 (SU7)
  —Service Data Improvements (SU17)
- The following additional Selectable Units have been incorporated in this publication:
  —TSO/VTAM (SU13)
  —JES3 3850 Mass Storage System (SU18)
  —MSS Enhancements (SU24)
  —System Security Support (SU32)
  —Dumping Improvements (SU33)
  —Attached Processor System (SU47)
  —Hardware Recovery Enhancements (SU55)
  These Selectable Units have been identified in this publication by name or SU number. Dumping Improvements (SU33) has been highlighted by shading the information.
- The date for this publication is August 15, 1977. Only supplements and TNLs with dates later than August 15, 1977, apply to this publication.
- SY28-0713-1 through SY28-0719-1 is a major revision of the OS/VS2 MVS System Logic Library with all outstanding SU TNLs and SL Supplements incorporated. This major revision obsoletes the following publications:
  SY28-0713-0 through SY28-0719-0
  SY28-0761-0 through SY28-0767-0
  Those users who do not have the required SUs for using this publication can order and obtain copies of SY28-0713-0 through SY28-0719-0 (release 3.7 with no SUs) by using order numbers ST68-0713-0 through ST68-0719-0.

Contents

Figures

Summary of Amendments for SY28-0713-1 through SY28-0719-1

Changes have been made throughout this publication to reflect service updates and the following SUs:

—TSO/VTAM (SU13)
—JES3 3850 Mass Storage System (SU18)
—MSS Enhancements (SU24)
—System Security Support (SU32)
—Dumping Improvements (SU33)
—Attached Processor System (SU47)
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  SY28-0713-0 through SY28-0719-0
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  Those users who do not have the required SUs for using this publication can order and obtain copies of SY28-0713-0 through SY28-0719-0 (release 3.7 with no SUs) by using order numbers ST68-0713-0 through ST68-0719-0.

Section 4: Directory

The Directory shows the relationships between:

- Load module names.
- Object (or assembly) module names (CSECT name, external name, or reference).
- Entry point names (the major entry point to a load module).
- Alias names (alternate entry points to load modules).

The first column, labeled *Names*, is in alphameric order; it shows all names noted above. Depending on the type of name, which is shown by the entry in the second column, different information is given:

- If the name is a load module, then all object modules contained in the load module are listed. With each of these object module names is listed any entry point name or alias name contained in that particular object module. If a load module has only one object module and no entry points are listed, then the object module name is also the entry point name.
- If the name is an object module, then the load module in which it is found is listed. Any entry points names or alias names contained in the object module are also listed.
- If the name is an alias or an entry point, the load module and object module in which they appear are shown.

The directory is on microfiche for all subsequent updates to Release 3.7. There will be no hard copy version of these updated pages.

Section 5: Data Areas

This section describes the data areas that are used during the operation of scheduler and supervisor programs. It contains four parts:

- Control block overviews for each subcomponent.
- A table that relates the acronym for a data area to its mapping macro and to its common name.
- Data area descriptions arranged alphabetically by acronym. These descriptions show the content of major data areas used by the scheduler and supervisor. (This part describes only data areas that are not in *OS/VS2 Data Areas,* SYB8-0606, which is on microfiche.)
- A data area usage table which is a cross reference between data area names and scheduler and supervisor modules.

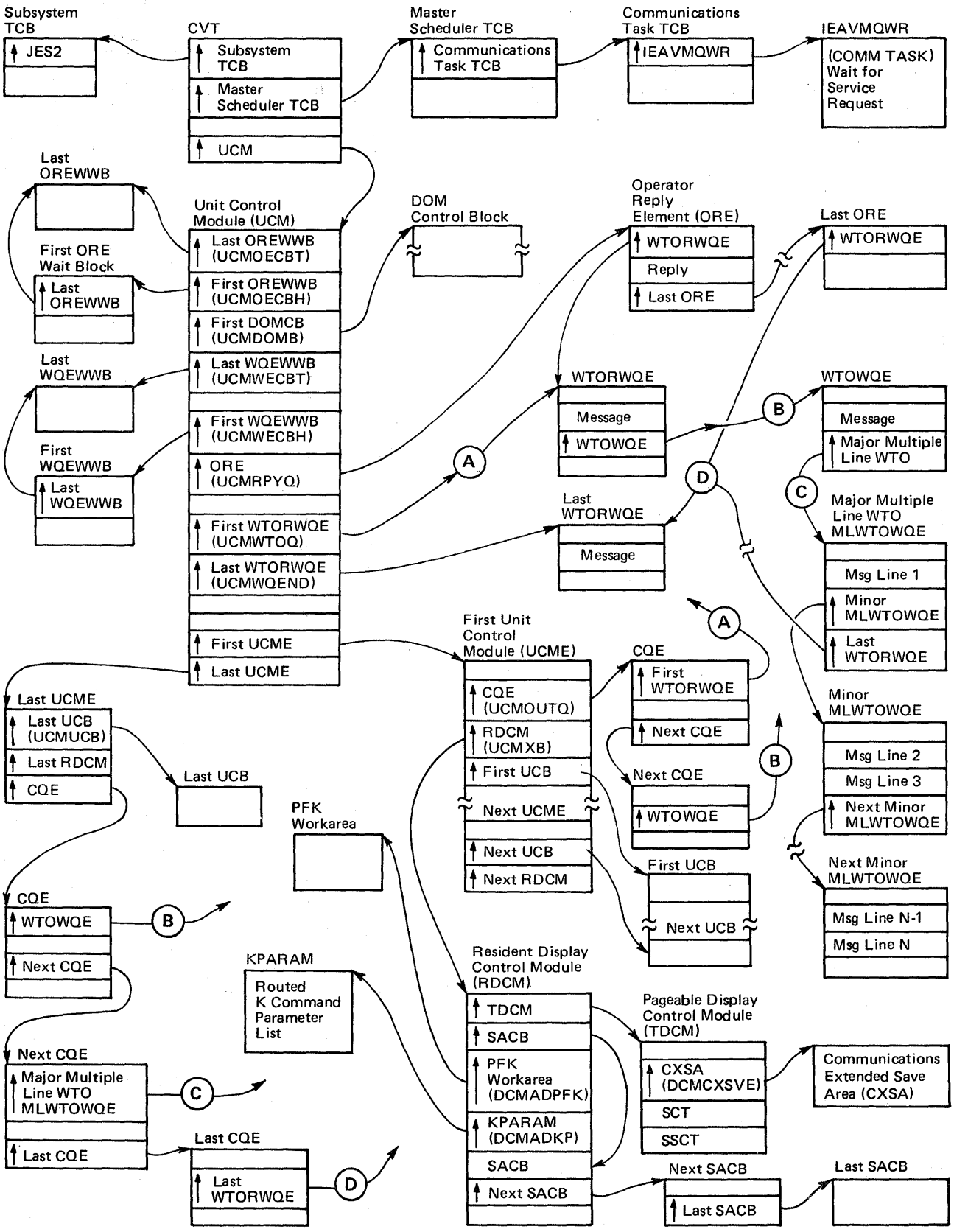


Figure 5-1. Communication Task (Without TSO) Control Block Overview (Part 1 of 3)

Control Block Definitions

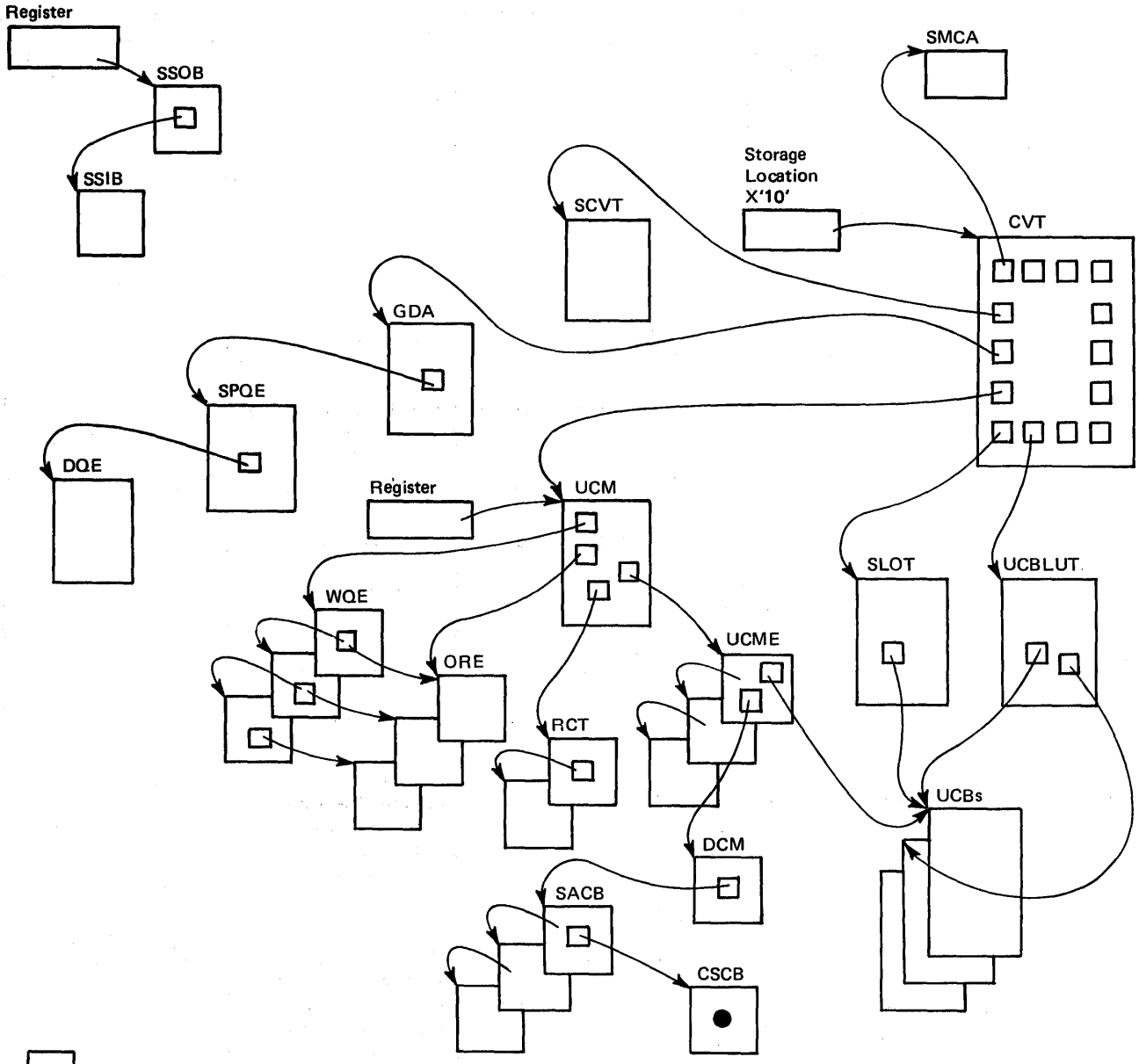
Name	Purpose	When Created	When Deleted	Macro Name
CQE	Console Queue Element One or more for each active console that is to receive a message. Each CQE contains a pointer to one major WQE having the contents of the message destined for that particular console. CQEs are generated in groups of six with the sixth CQE pointing to the next group of CQEs rather than to a major WQE. Each group of CQEs or chain of CQE groups are assigned to one console.	IEAVMWSV as needed.	Same as for WQE's. See WQE types.	IHACTM (CQE)
DOMC	Delete Operator Message Control Block One for each DOM macro instruction.	IEAVXDOM as needed.	IEAVMDOM after an attempt has been made to delete the WQEs, OREs, and graphic messages identified in the DOMC.	IHADOMC
MMB	Monitor Message Block Contains message text queued for TSO terminals having an MQE.	IEAVMWSV as needed.	IEAVMWSV after messages has been sent to the TSO terminals.	IEAMMB
MQE	Monitor Queue Element One MQE is created for each TSO terminal that has issued the operator commands that places the terminal in operator console monitor mode.	IEAVMNTR as a result of the terminal user entering the MONITOR SESS, STATUS or JOBNAMES command while in TSO OPERATOR mode.	IEAVMNTR as a result of the terminal user entering the STOPMN SESS, STATUS or JOBNAMES command or TSO END command.	IEAMQE
ORE	Operator Reply Element One for each operator message reply expected.	IEAVVWTO at the same time the WQE for a WTOR is created.	IEAVVRP2 after the reply has been received by the routine or program that issued the WTOR; or IEAVMDOM if DOM macro instruction was issued against a WTOR; or IEAVMED2 during task or memory termination.	IHAORE
ORE-WWB	Write to Operator Wait Block See WWB.			
UCM	Unit Control Module Contains pointers to the control block chains and routines that support the communication task.	System Generation.	Permanent	IEECUCM

Figure 5-1. Communication Task (Without TSO) Control Block Overview (Part 2 of 3)

Control Block Definitions (continued)

Name	Purpose	When Created	When Deleted	Macro Name
UCME	Unit Control Module Entry One per system generated console device including composite consoles. Identifies the attributes of each console as identified during system generation. The UCME's are sequentually adjacent, and therefore, do not need pointers from one to another.	System Generation	Permanent	IEECUCM
WQE-WWB	Write to Operator Wait Block See WWB.			
WQE Major for MLWTO	Write Queue Element One for each multiple line message regardless of the number of terminals receiving that message.	IEAVMWTO as needed.	IEAVMDSV after every line of the message has been sent to the consoles.	IHAWQE (MAJOR)
WQE Minor for MLWTO	Write Queue Element One for each additional two lines of a multiple line WTO message regardless of the number of terminals receiving that message.	IEAVMWTO as needed.	IEAVMDSV after both lines in this minor WQE have been sent to the console.	IHAWQE (MINOR)
WQE for WTO	Write Queue Element One for each message regardless of the number of terminals receiving that message.	IEAVVWTO as needed.	IEAVMDSV after the message has been sent to the consoles.	IHAWQE
WQE for WTOR	Write Queue Element One for each message regardless of the number of terminals receiving that message.	IEAVVWTO as needed.	IEAVMDSV after an operator has replied or the DOM macro has been issued by the routine that issued the WTOR.	IHAWQE
WWB	Write to Operator Wait Block Waited on by SVC 35 when either a WQE or ORE is unavailable. When the system limit has been reached and one more WQE or ORE has been requested, a WWB is placed on the appropriate WQE or ORE chain. A WAIT macro instruction is then issued against the ECB contained in the WWB.	IEAVVWTO or IEAVMWTO goes above the system limit.	IEAVVWTO or IEAVMWTO when the required WQE or ORE has been successfully obtained.	IHACTM (WWB)

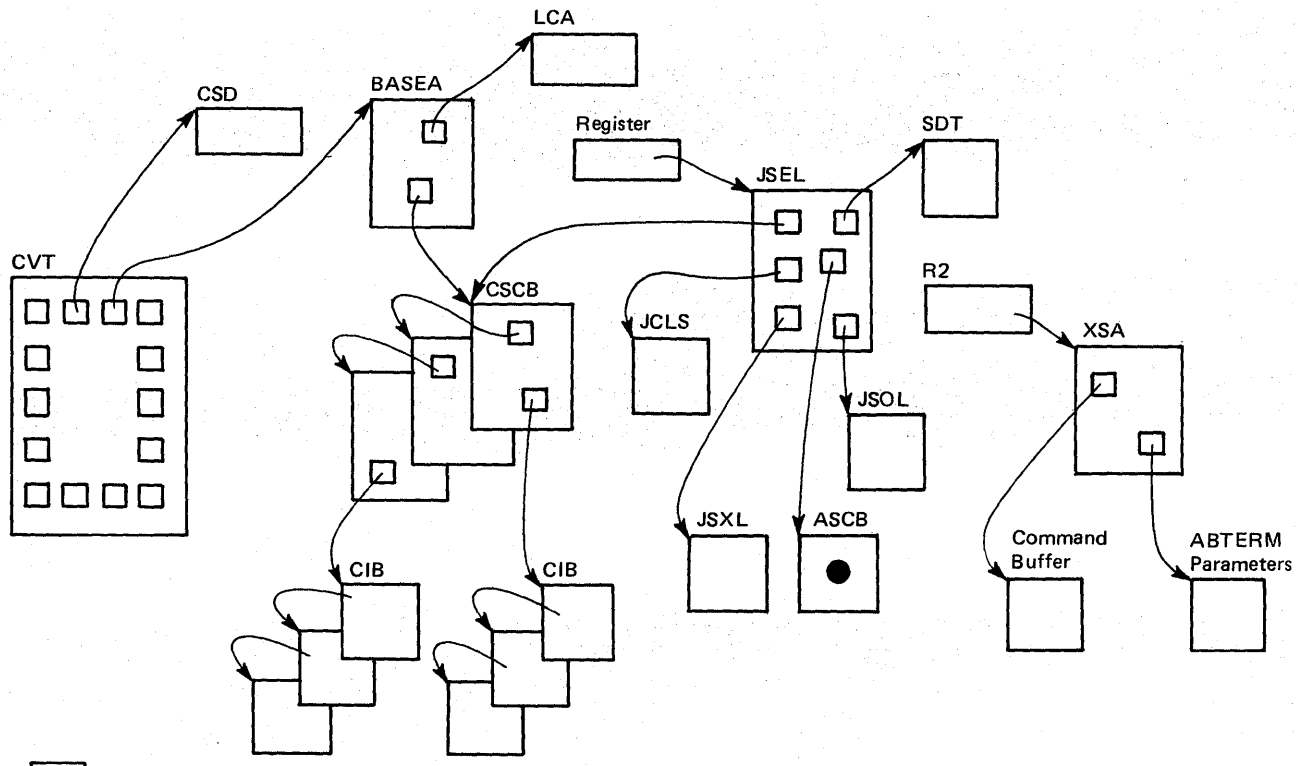
Figure 5-1. Communication Task (Without TSO) Control Block Overview (Part 3 of 3)



Acronym	Control Block Name
CSCB	Command Scheduling Control Block
CVT	Communication Vector Table
DCM	Display Control Module
DQE	Descriptor Queue Element
GDA	Global Data Area
ORE	Operator Reply Element
RCT	Routing Control Table
SACB	Screen Area Control Block
SCVT	Secondary Communication Vector Table
SLOT	Scheduler Look-up Table

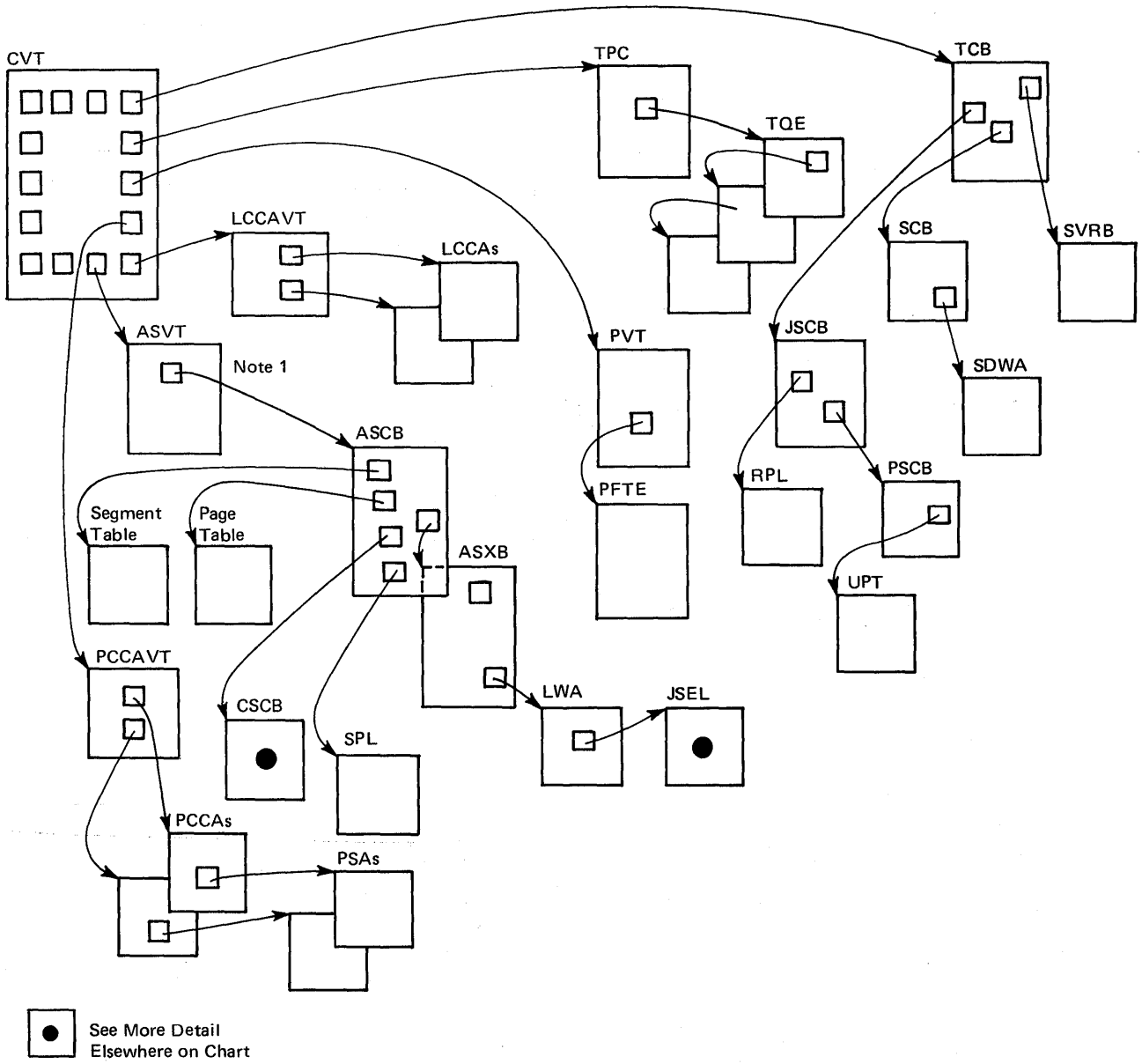
Acronym	Control Block Name
SMCA	System Management Control Area
SPQE	Subppol Queue Element
SSIB	Subsystem Interface Block
SSOB	Subsystem Options Block
UCB	Unit Control Block
UCBLUT	UCB Look-up Table
UCM	Unit Control Module
UCME	Unit Control Module Entry
WQE	Write Queue Element

Figure 5-2. Command Processing Control Block Overview (Part 1 of 3)



Acronym	Control Block Name	Acronym	Control Block Name
ASCB	Address Space Control Block	JSEL	Job Scheduling Entrance List
BASEA	Master Scheduler Resident Data Area (MSRDA)	JSOL	Job Scheduling Options List
CIB	Command Input Buffer	JSXL	Job Scheduling Exit List
CSCB	Command Scheduling Control Block	LCA	Log Control Area
CSD	Common System Data	SDT	Start Descriptor Table
CVT	Communication Vector Table	XSA	Extended Save Area
JCLS	Job Control Language Internal Text		

Figure 5-2. Command Processing Control Block Overview (Part 2 of 3)



Note:

1. The ASVT contains a pointer to an ASCB for each existing memory space.

Acronym	Control Block Name	Acronym	Control Block Name
ASCB	Address Space Control Block	PFTE	Page Fix Table Entry
ASVT	Address Space Vector Table	PSCB	Protected Step Control Block
ASXB	Address Space Extension Block	PSA	Prefix Save Area
CSCB	Command Scheduling Control Block	PVT	Page Vector Table
CVT	Communication Vector Table	RPL	Request Parameter List
JSCB	Job Scheduling Control Block	SCB	STAE Control Block
JSEL	Job Scheduling Entrance List	SDWA	System Diagnostic Work Area
LCCA	Logical Configuration Communication Area	SPL	Service Priority List
LCCAVT	Logical Configuration Communication Area Vector Table	SVRB	Supervisor Request Block
		TCB	Task Control Block
LWA	Logon Work Area	TPC	Timer Work Area
PCCA	Physical Configuration Communication Area	TQE	Timer Queue Element
PCCAVT	Physical Configuration Communication Area Vector Table	UPT	User Profile Table

Figure 5-2. Command Processing Control Block Overview (Part 3 of 3)

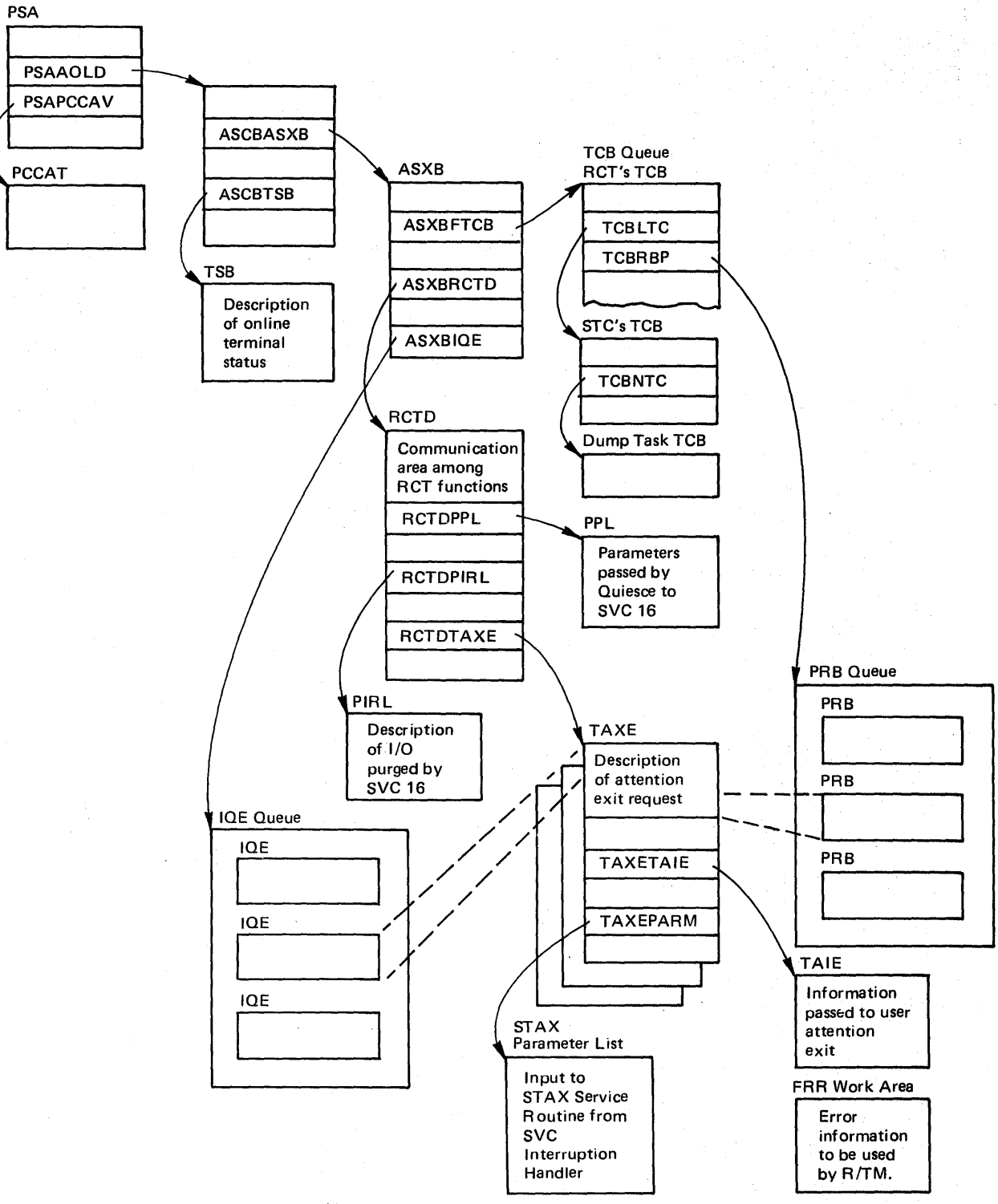


Figure 5-3. Region Control Task Control Block Overview

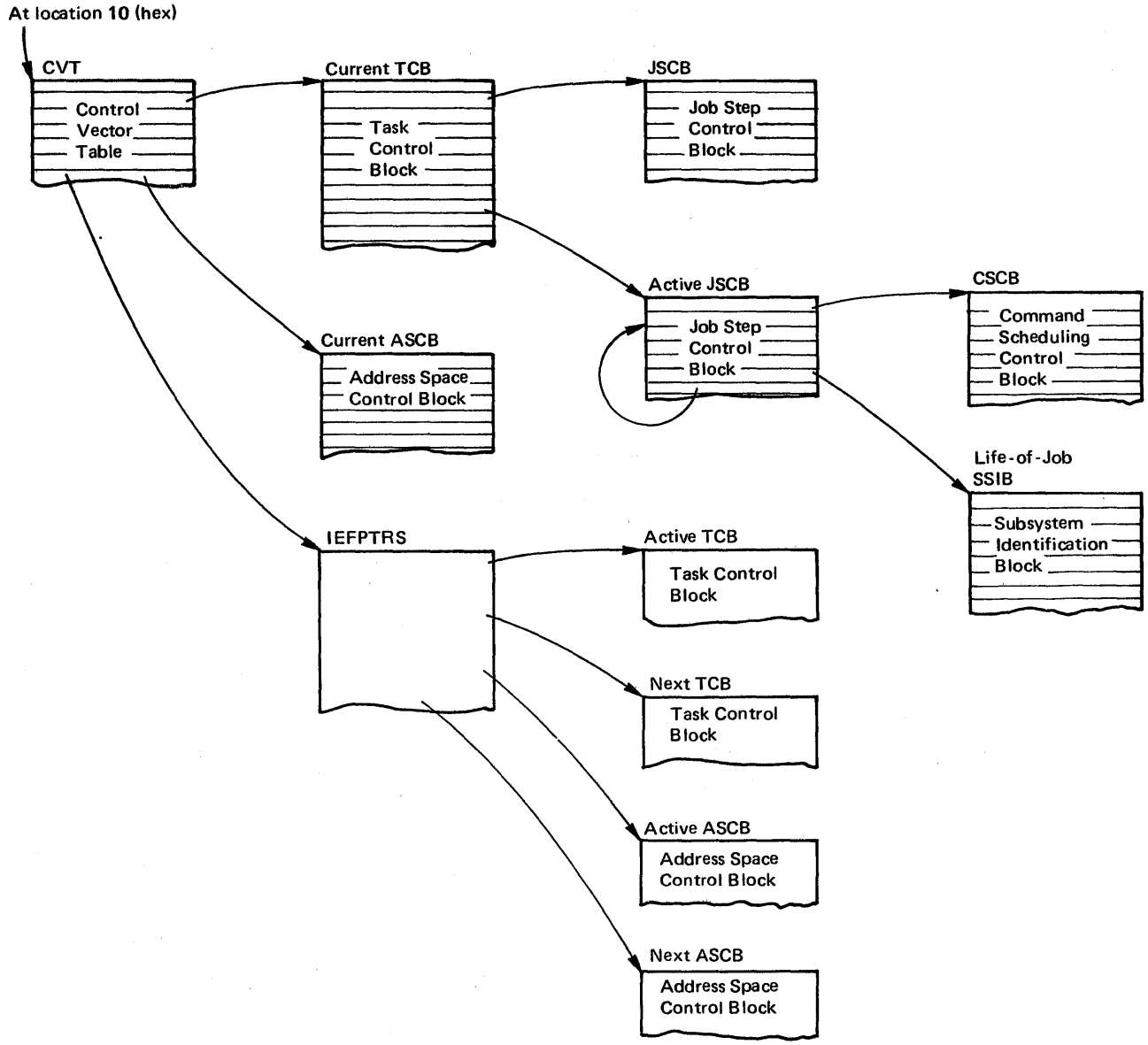


Figure 5-4. Started Task Control (STC) Control Block Overview (Part 1 of 2)

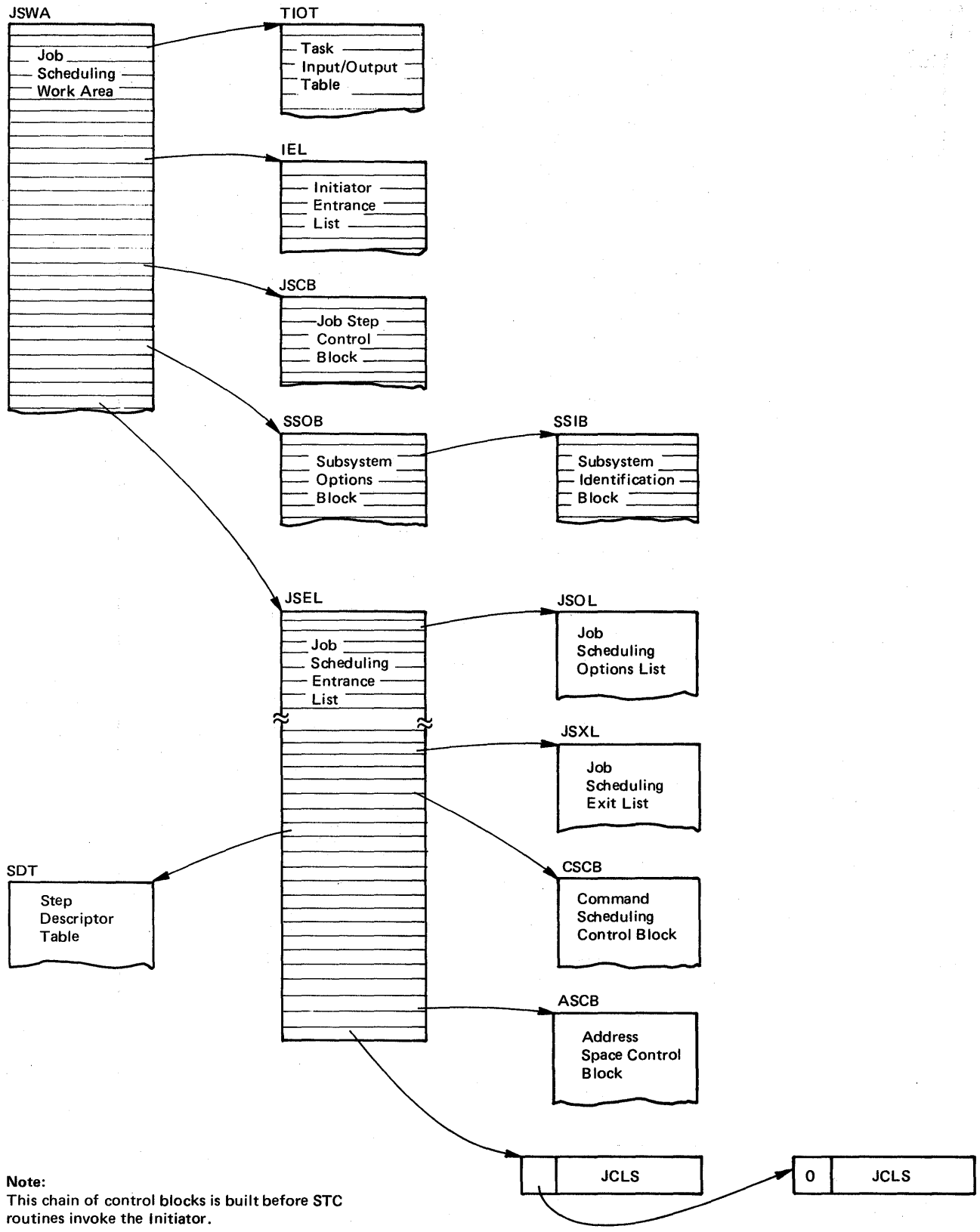


Figure 5-4. Started Task Control (STC) Control Block Overview (Part 2 of 2)

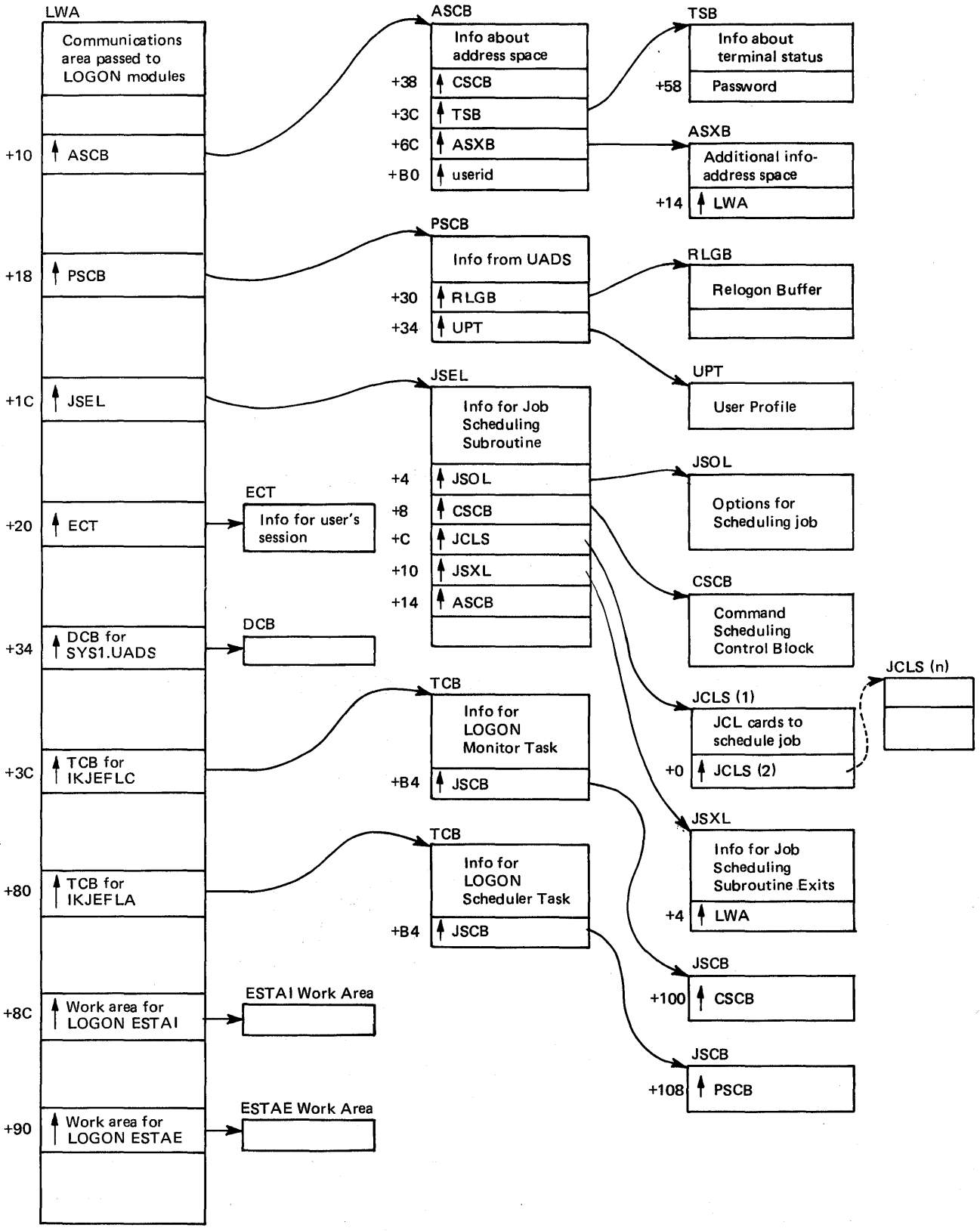


Figure 5-5. LOGON Scheduling Control Block Overview

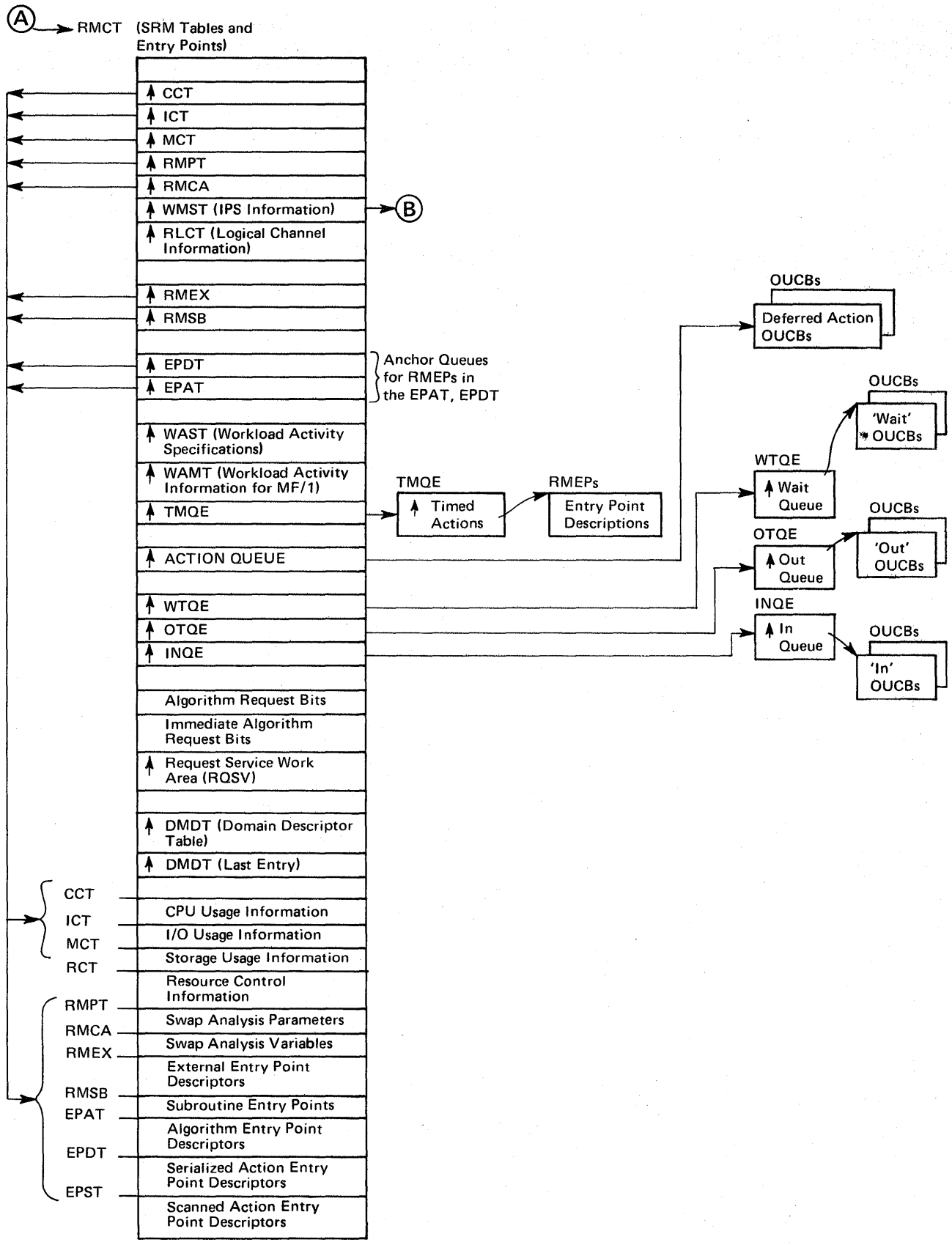


Figure 5-6. System Resources Manager (SRM) Control Block Overview (Part 1 of 2)

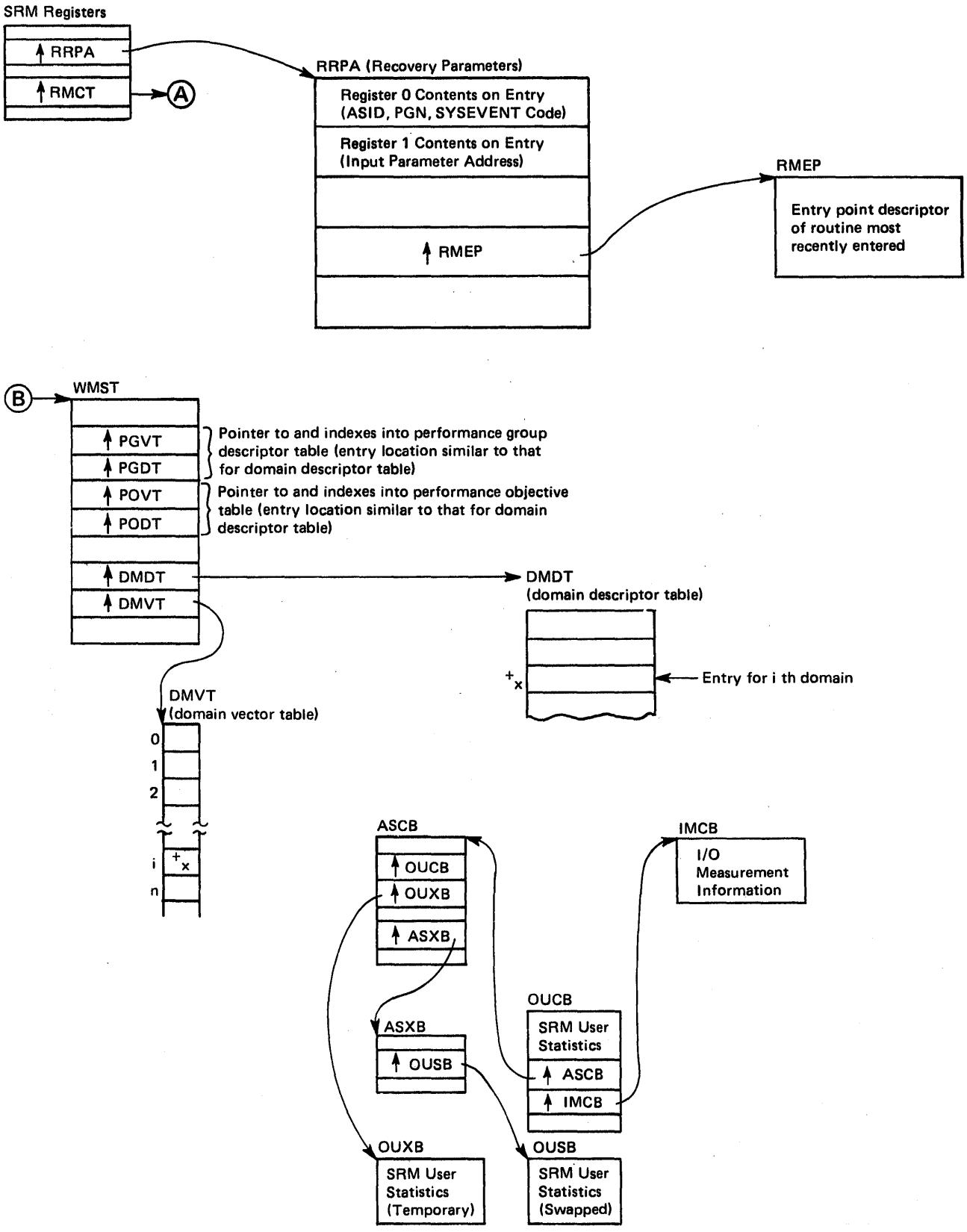


Figure 5-6. System Resources Manager (SRM) Control Block Overview (Part 2 of 2)

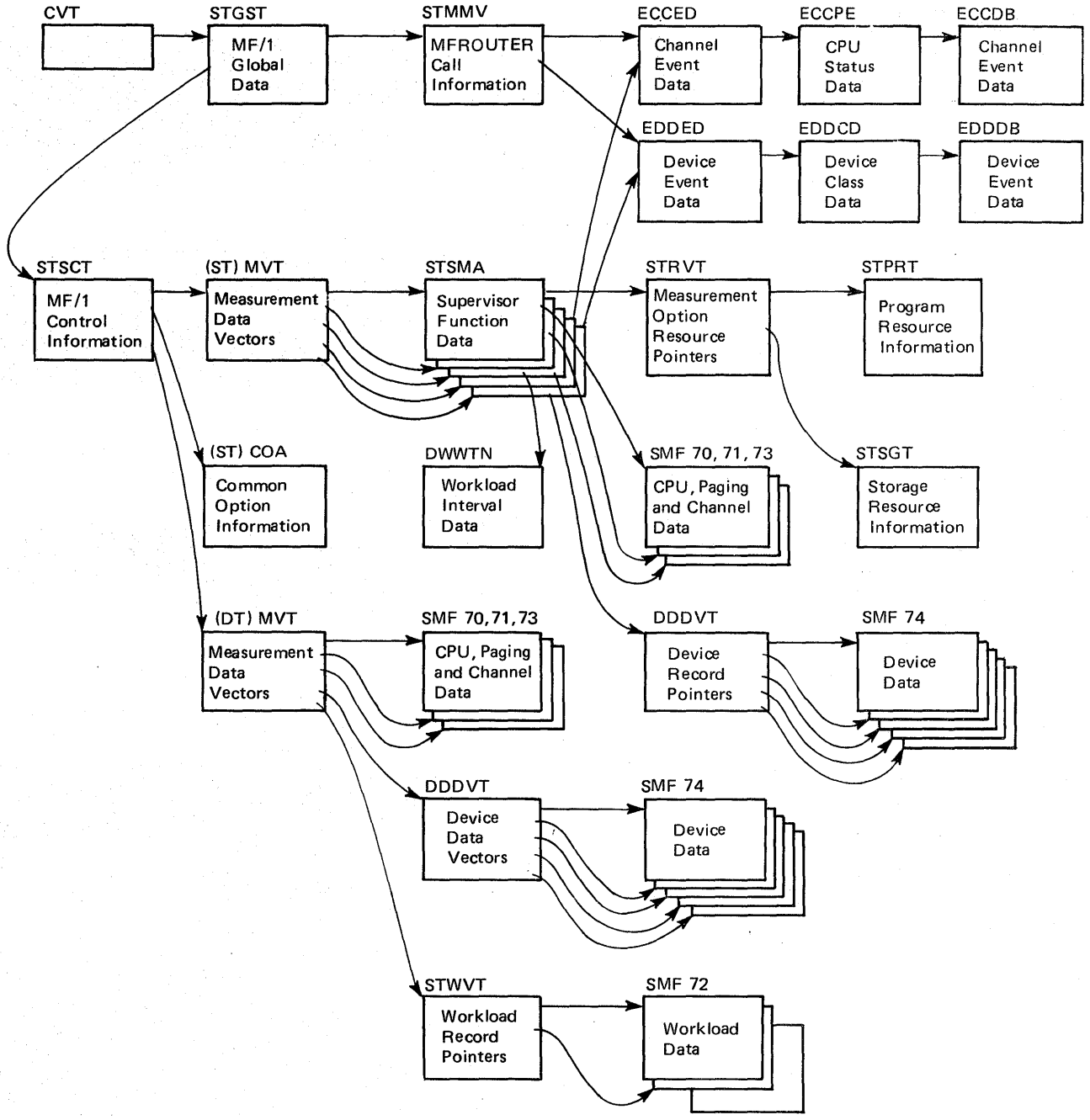


Figure 5-7. System Activity Measurement Facility (MF/1) Control Block Overview (Part 1 of 2)

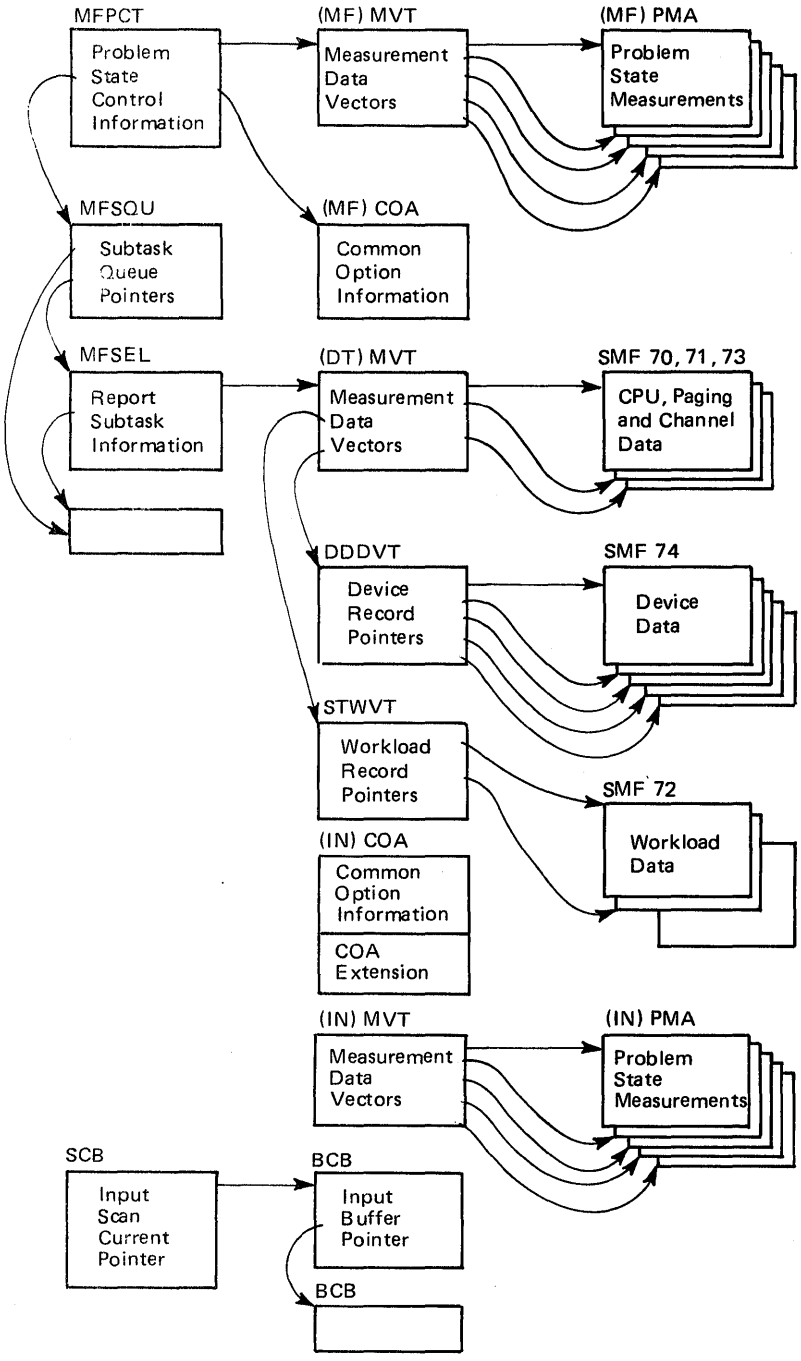
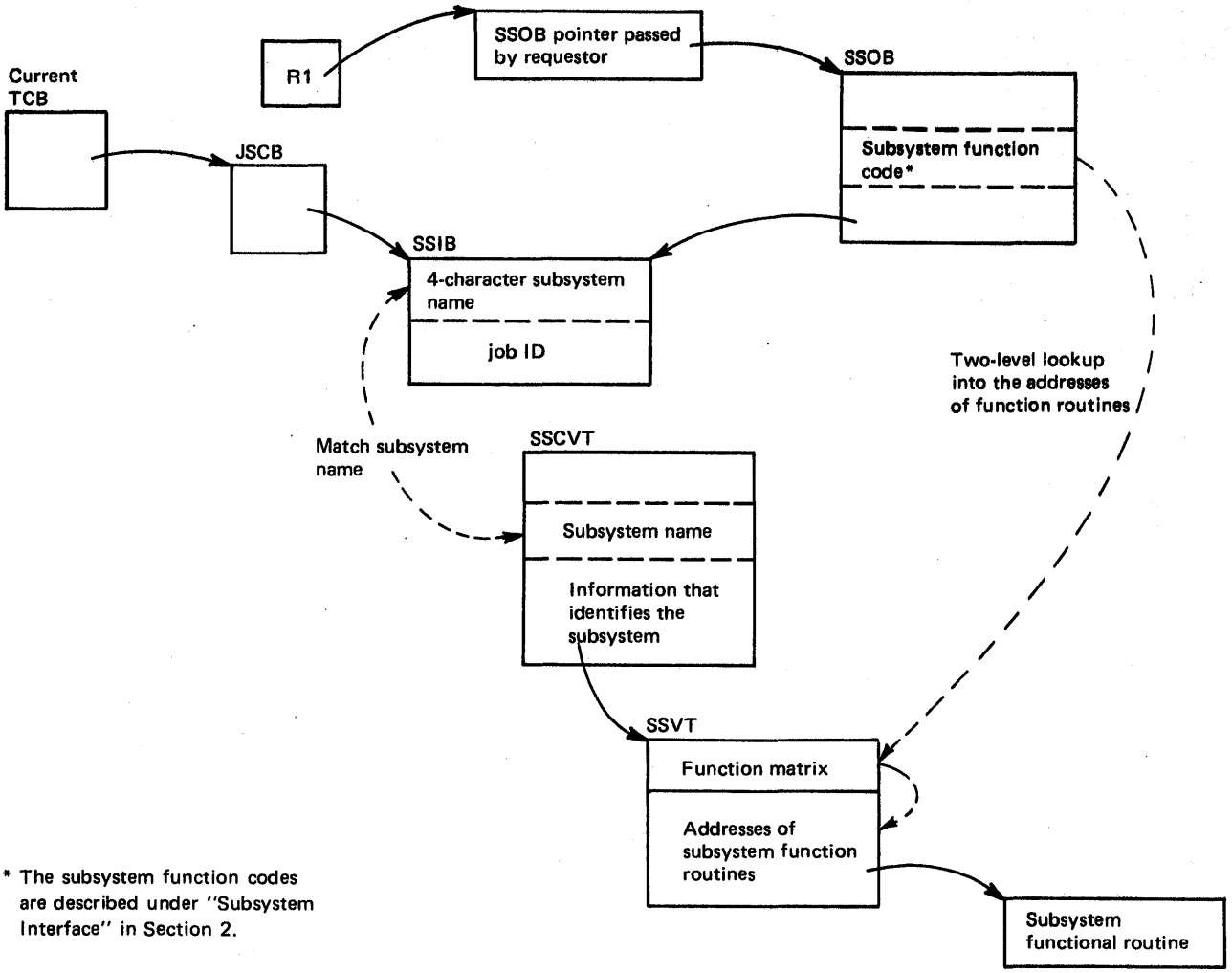


Figure 5-7. System Activity Measurement Facility (MF/1) Control Block Overview (Part 2 of 2)



* The subsystem function codes are described under "Subsystem Interface" in Section 2.

Note: This diagram shows the control block relationships during the processing of a request for a subsystem function.

Figure 5-8. Subsystem Interface Control Block Overview

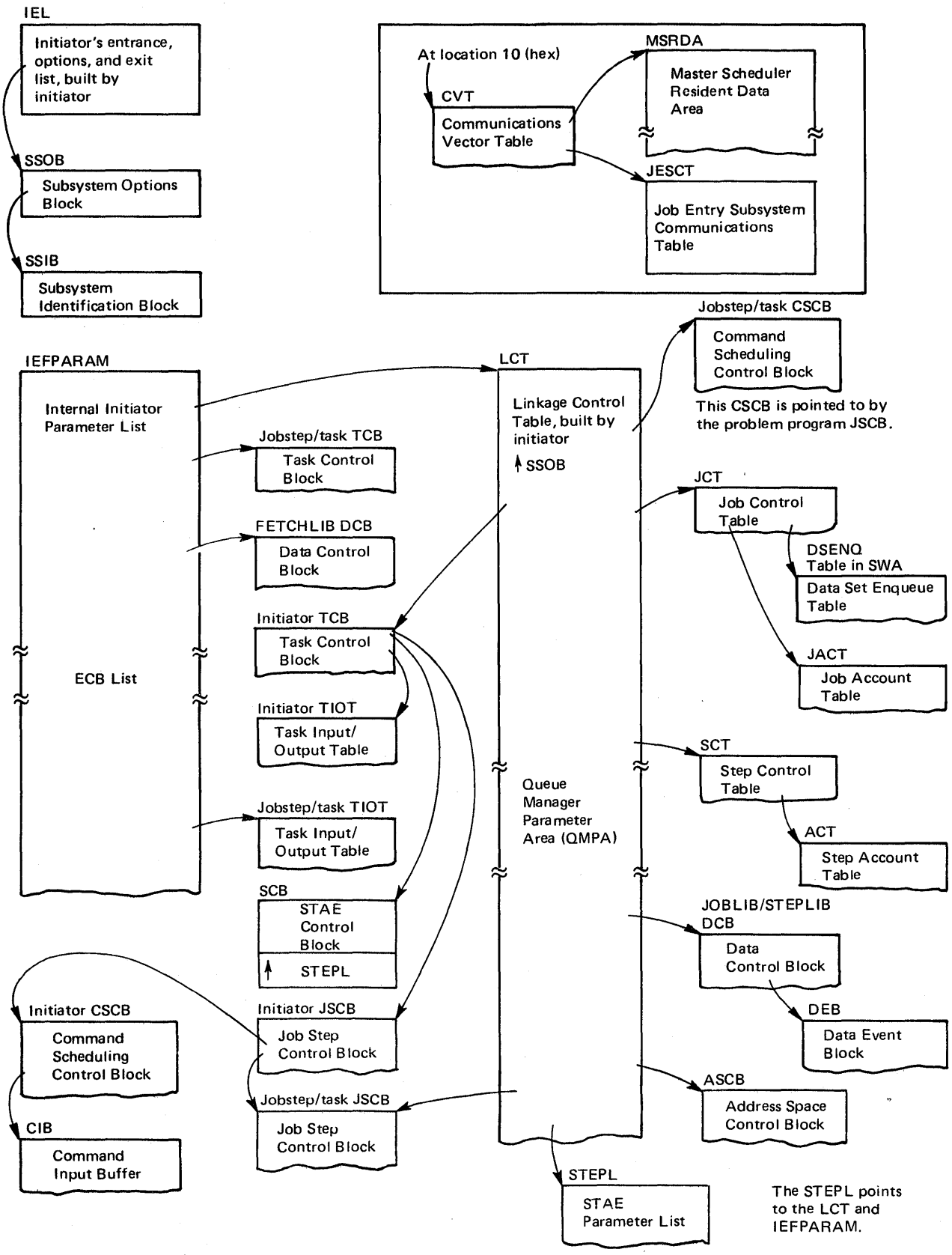


Figure 5-9. Iniator/Terminator Control Block Overview

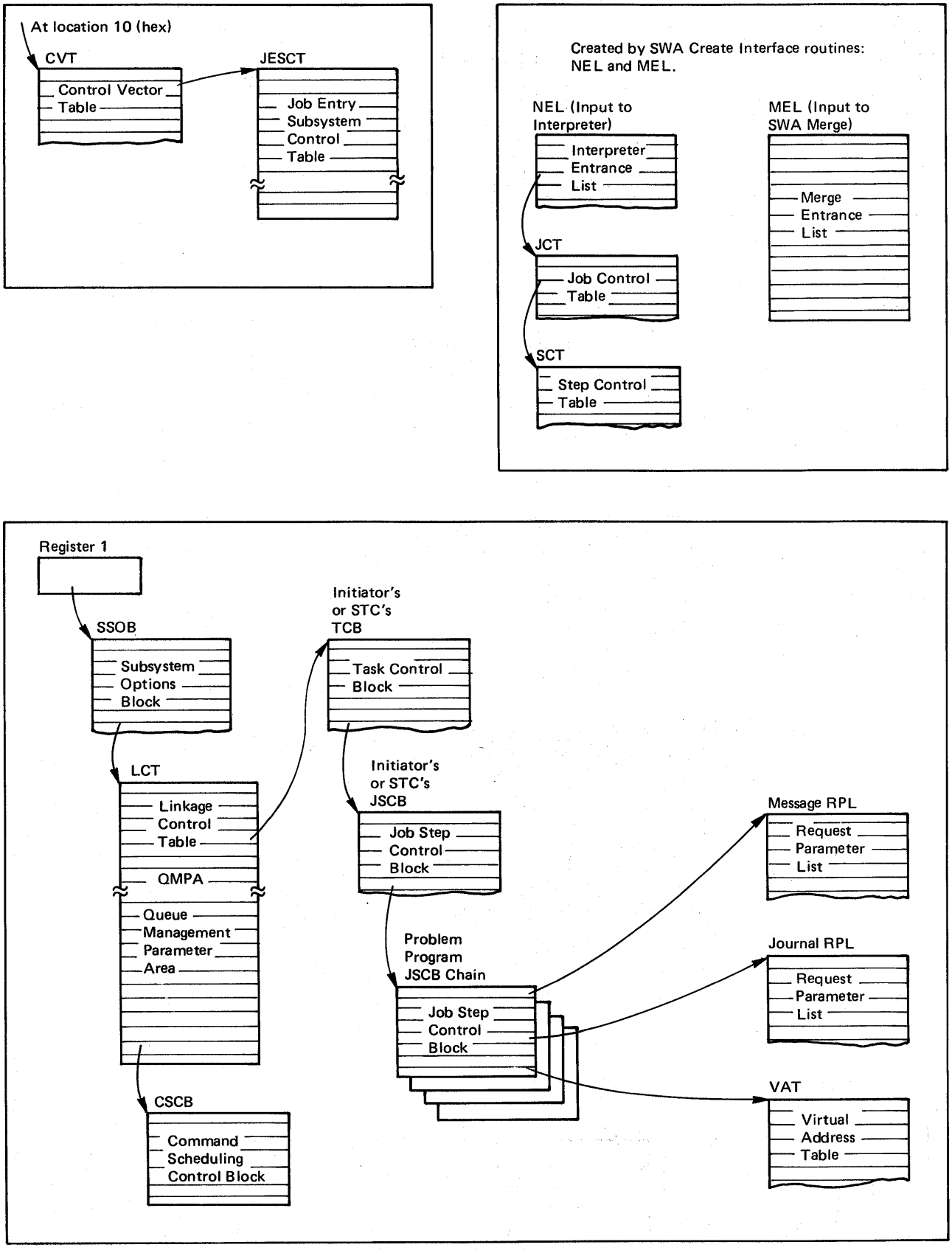


Figure 5-10. SWA Create Interface Control Block Overview

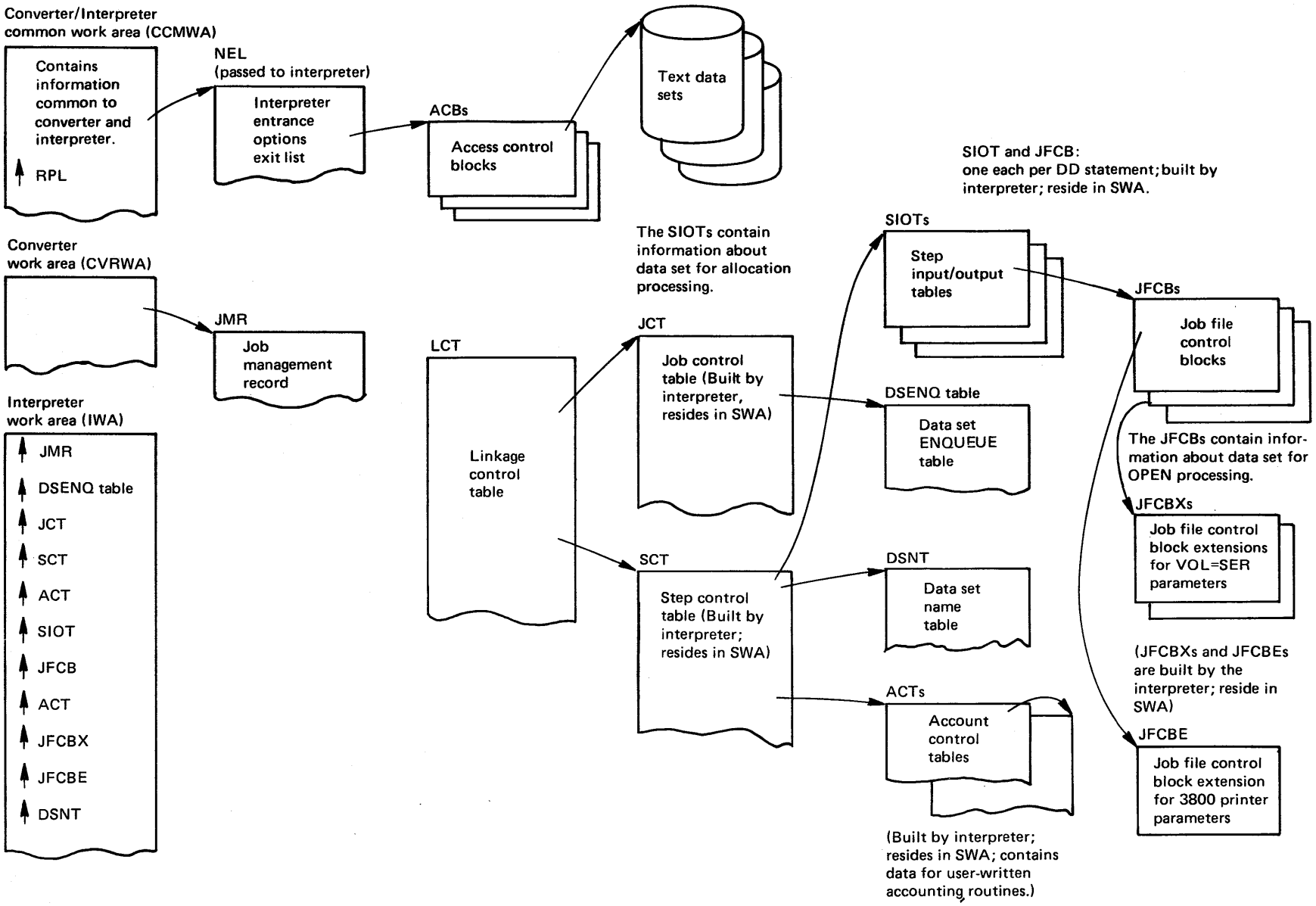


Figure 5-11. Converter/Interpreter Control Block Overview

```
//JOBNAME   JOB
//STEP1     EXEC
//DD1       DD      SYSOUT=A
//DD2       DD      VOLUME=REF=name
//STEP2     EXEC
//DD3       DD      DSNAME=name
//STEP3     EXEC
//DD4       DD      SYSOUT=B,BURST=Y
//DD5       DD      VOLUME=(,,,17)
```

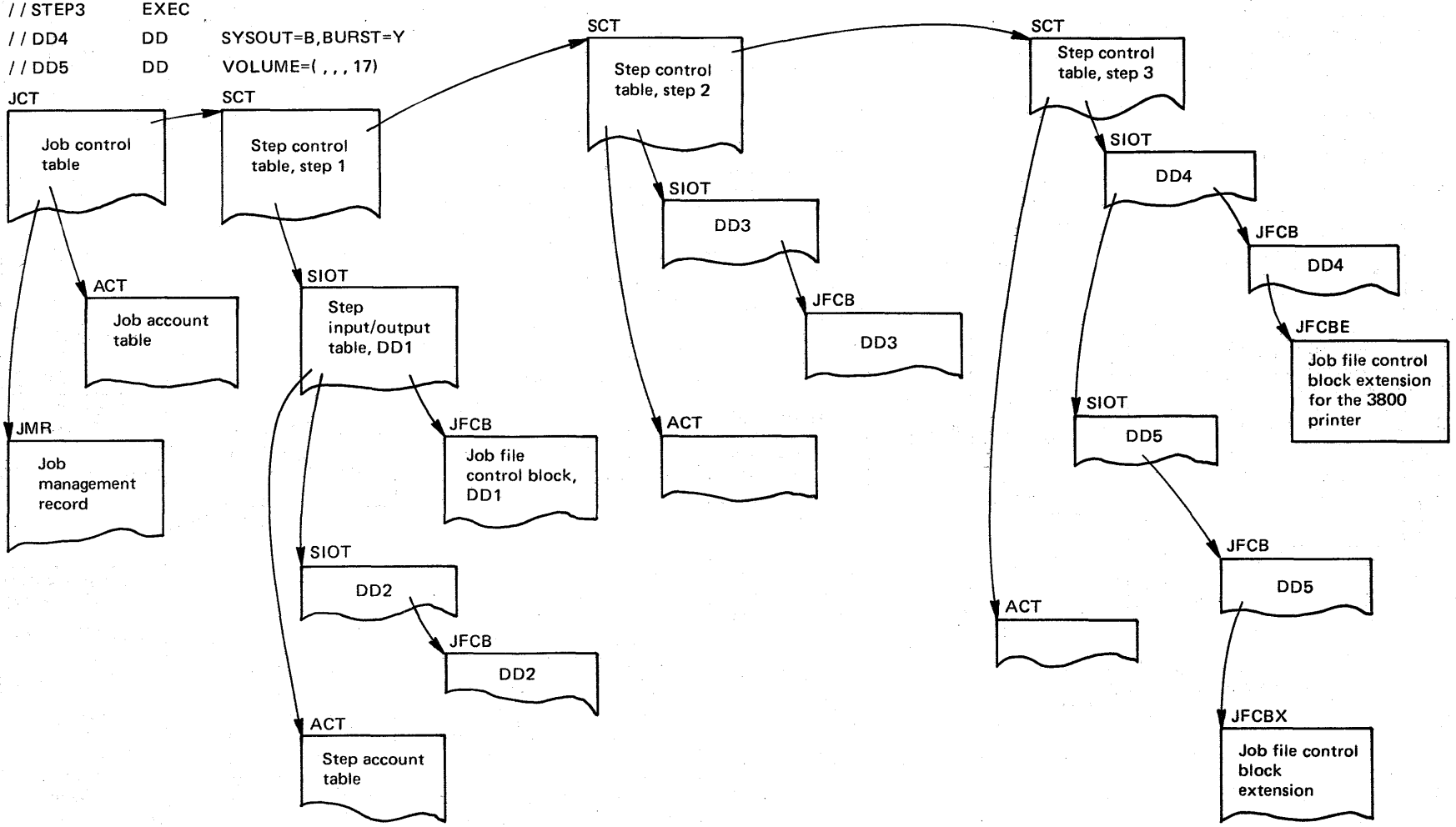


Figure 5-12. Typical Output of the Converter/Interpreter

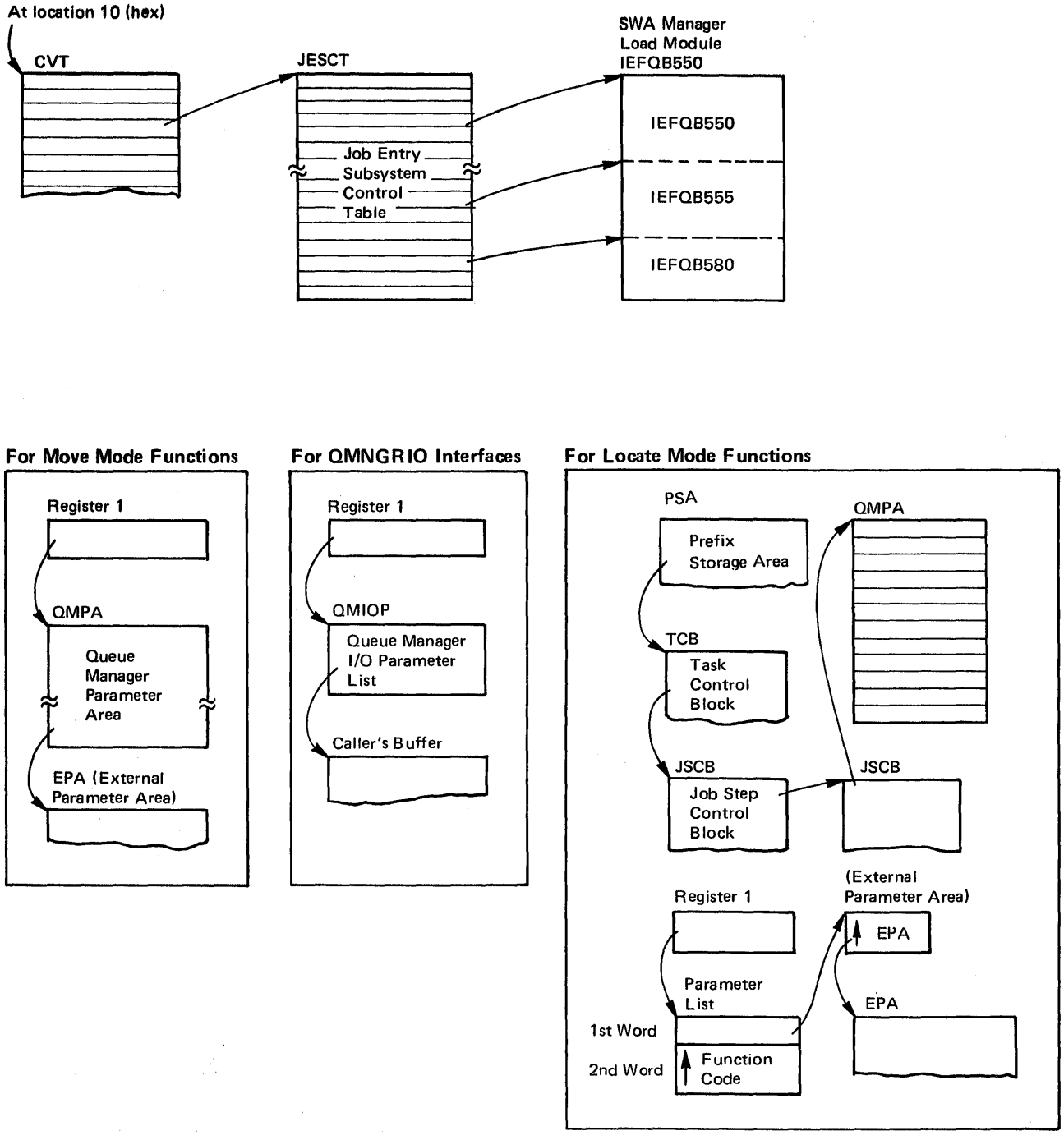


Figure 5-13. SWA Manager Control Block Overview

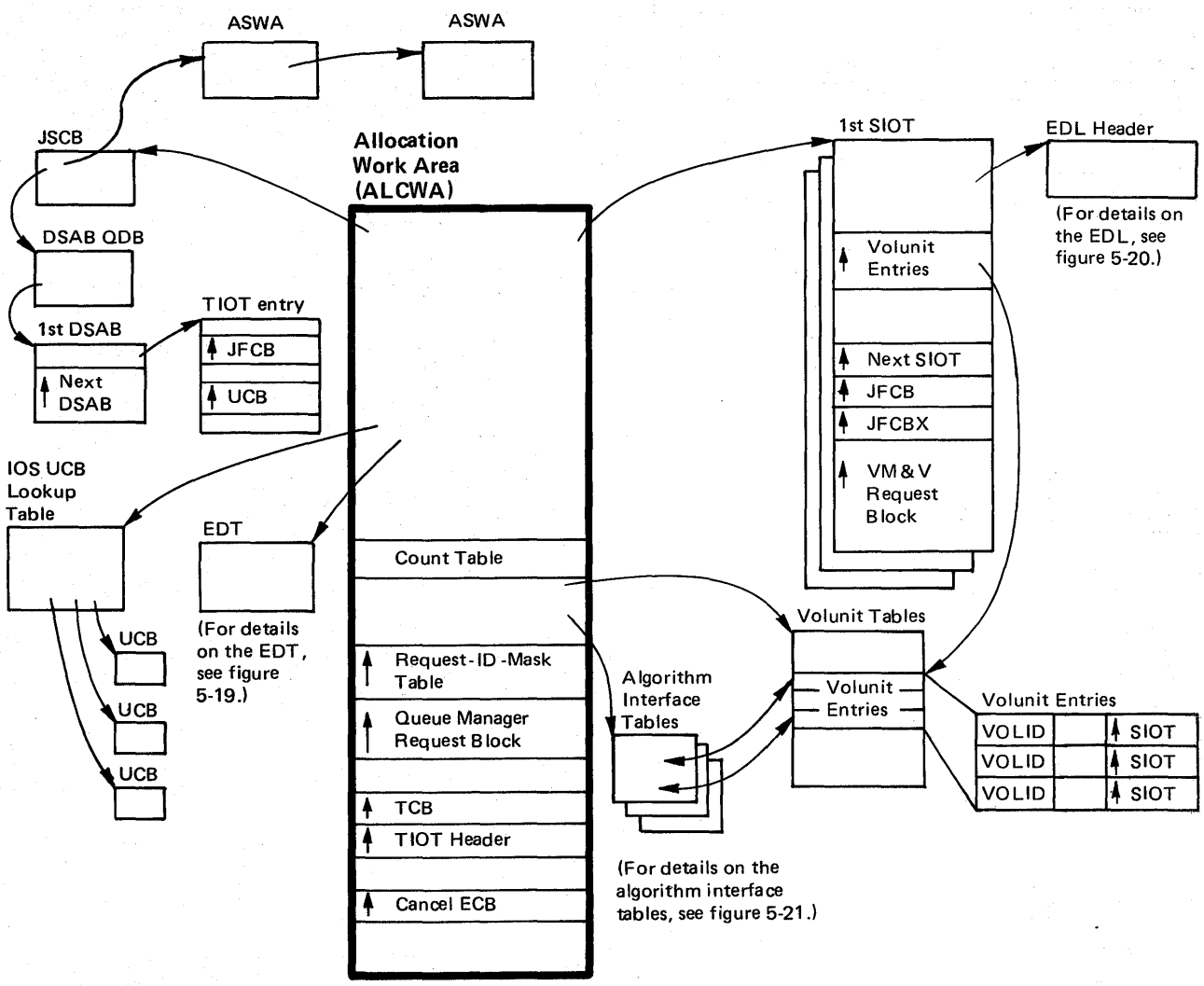
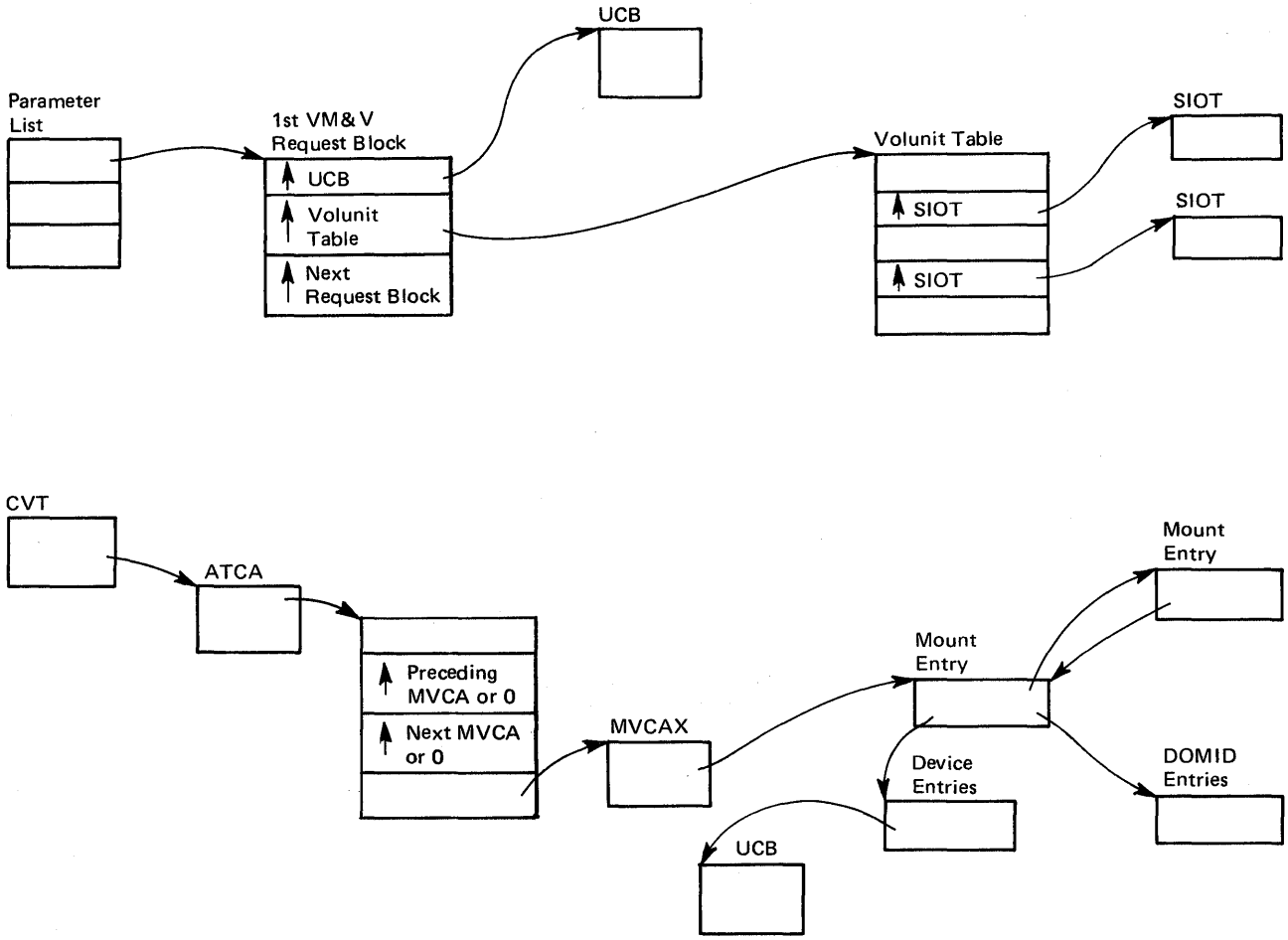
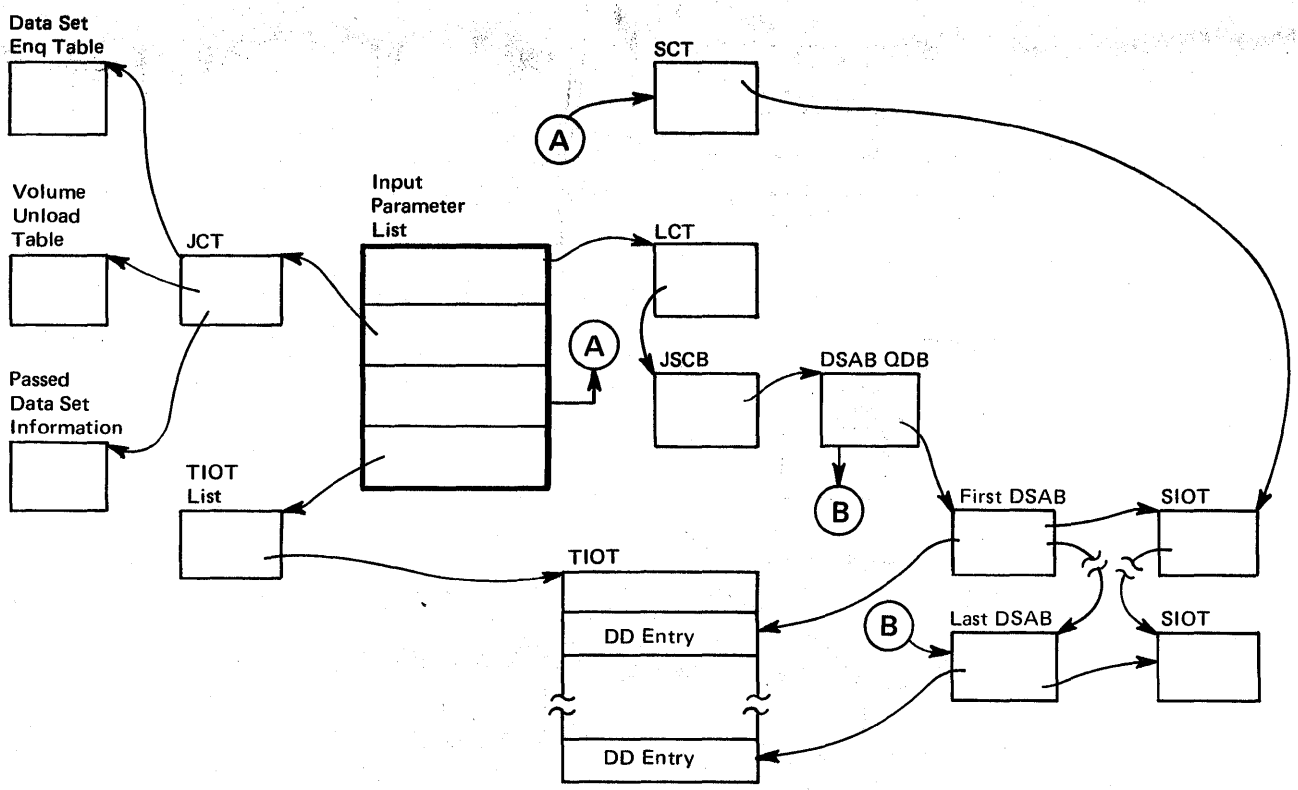


Figure 5-14. Common Allocation Control Block Overview



Note: SIOTs originally point to VM & V request blocks.

Figure 5-15. Volume Mount and Verify (VM&V) Control Block Overview



Batch Unallocation

Common Unallocation

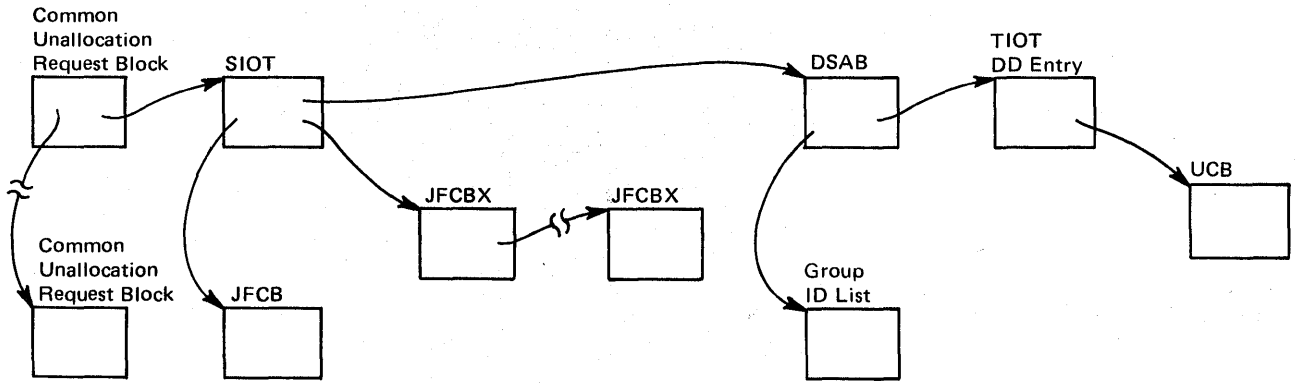


Figure 5-16. Batch Unallocation and Common Unallocation Control Block Overview

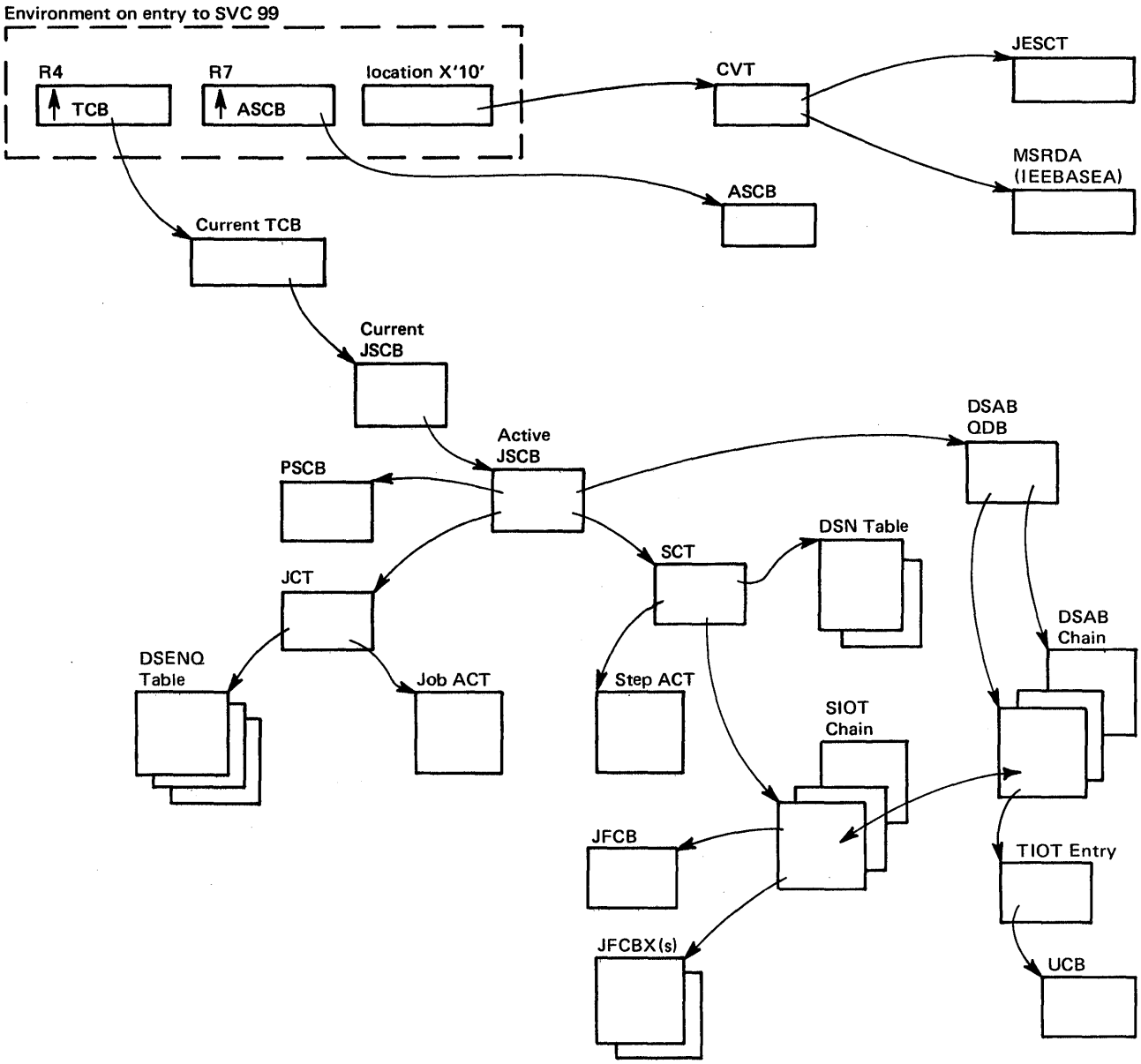
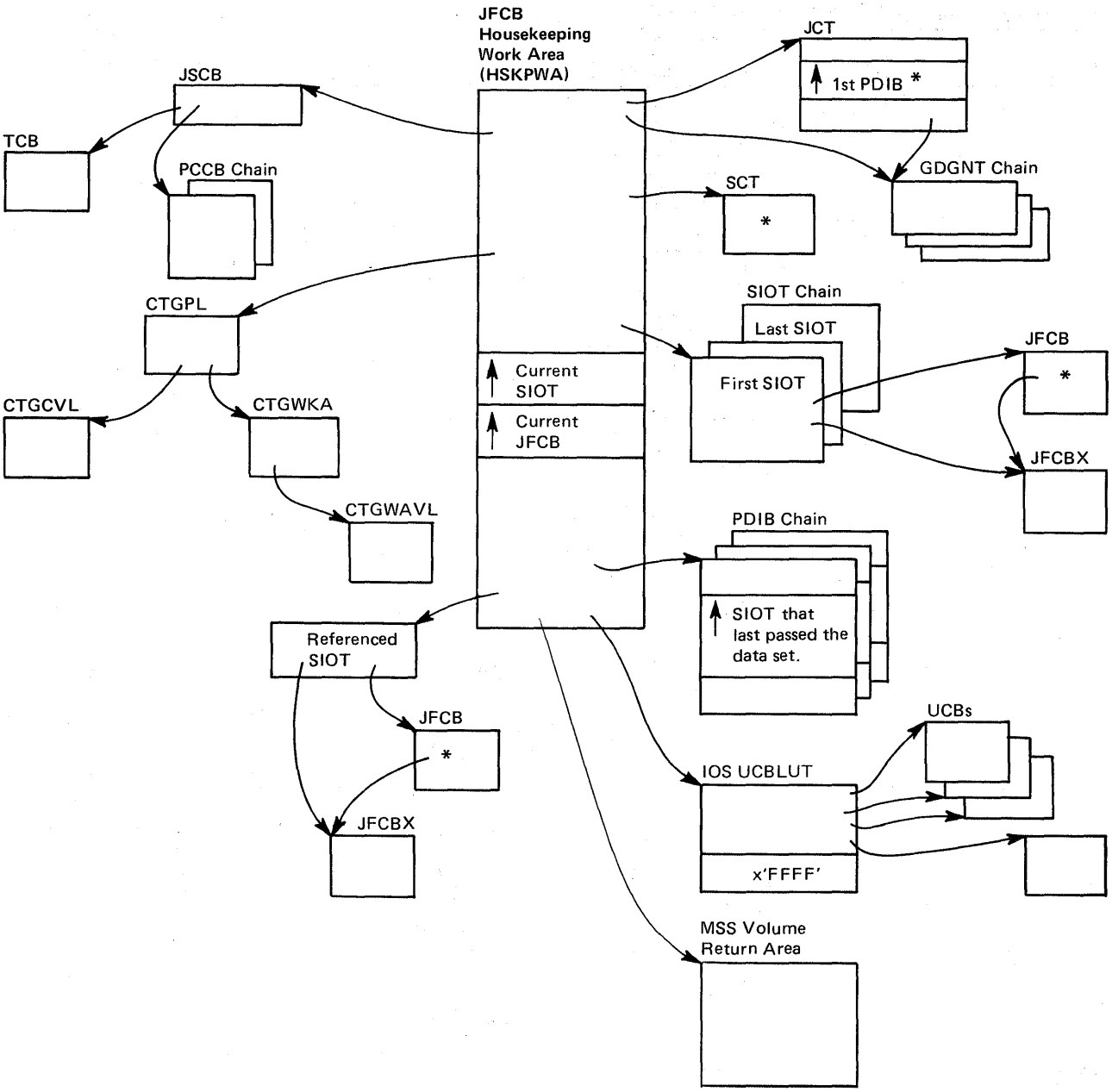


Figure 5-17. Dynamic Allocation Control Block Overview



*SWA virtual address

Figure 5-18. JFCB Housekeeping Control Block Overview

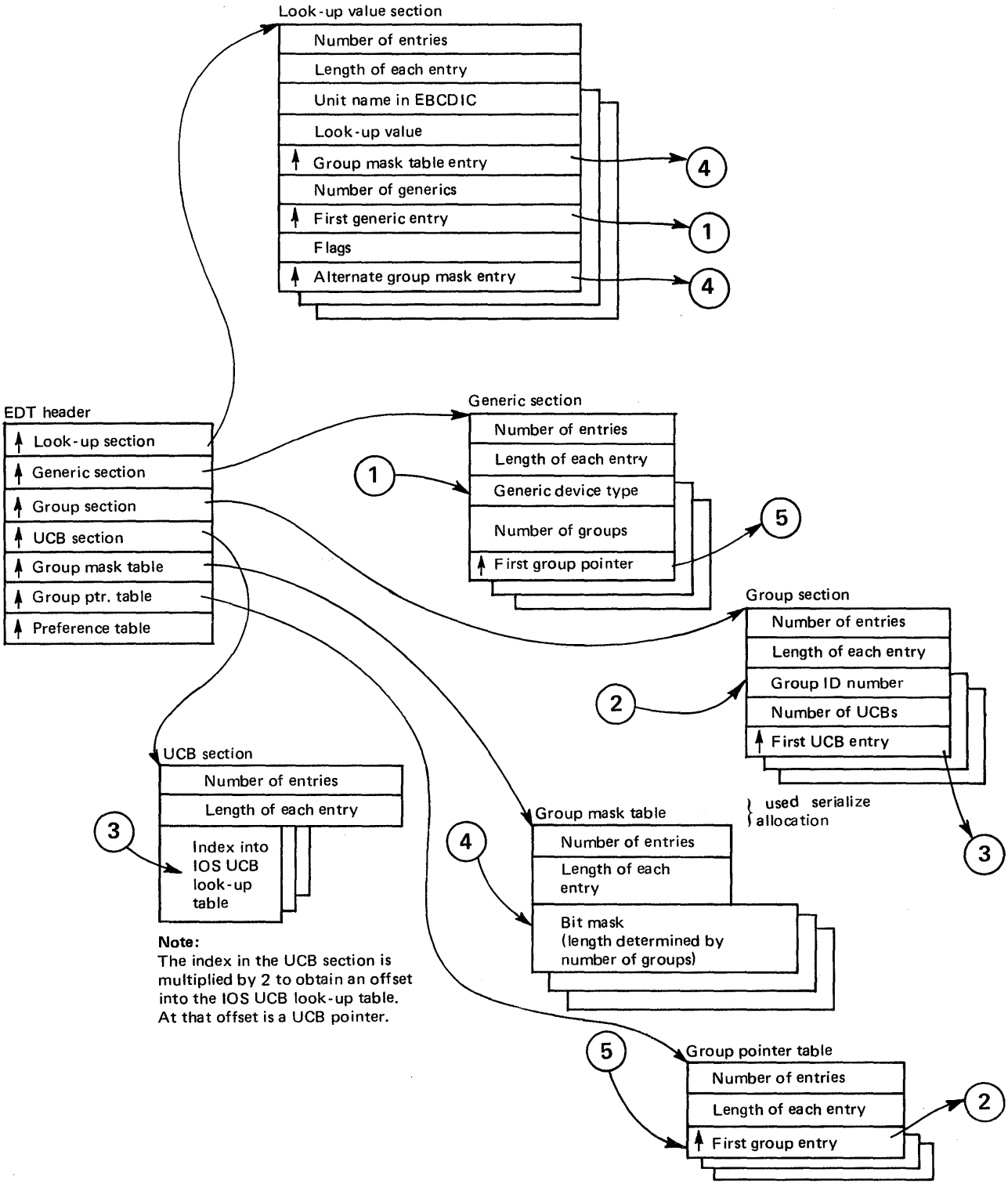


Figure 5-19. Eligible Devices Table (EDT). The EDT is built at SYSGEN time and link-edited into IEFW21SD.

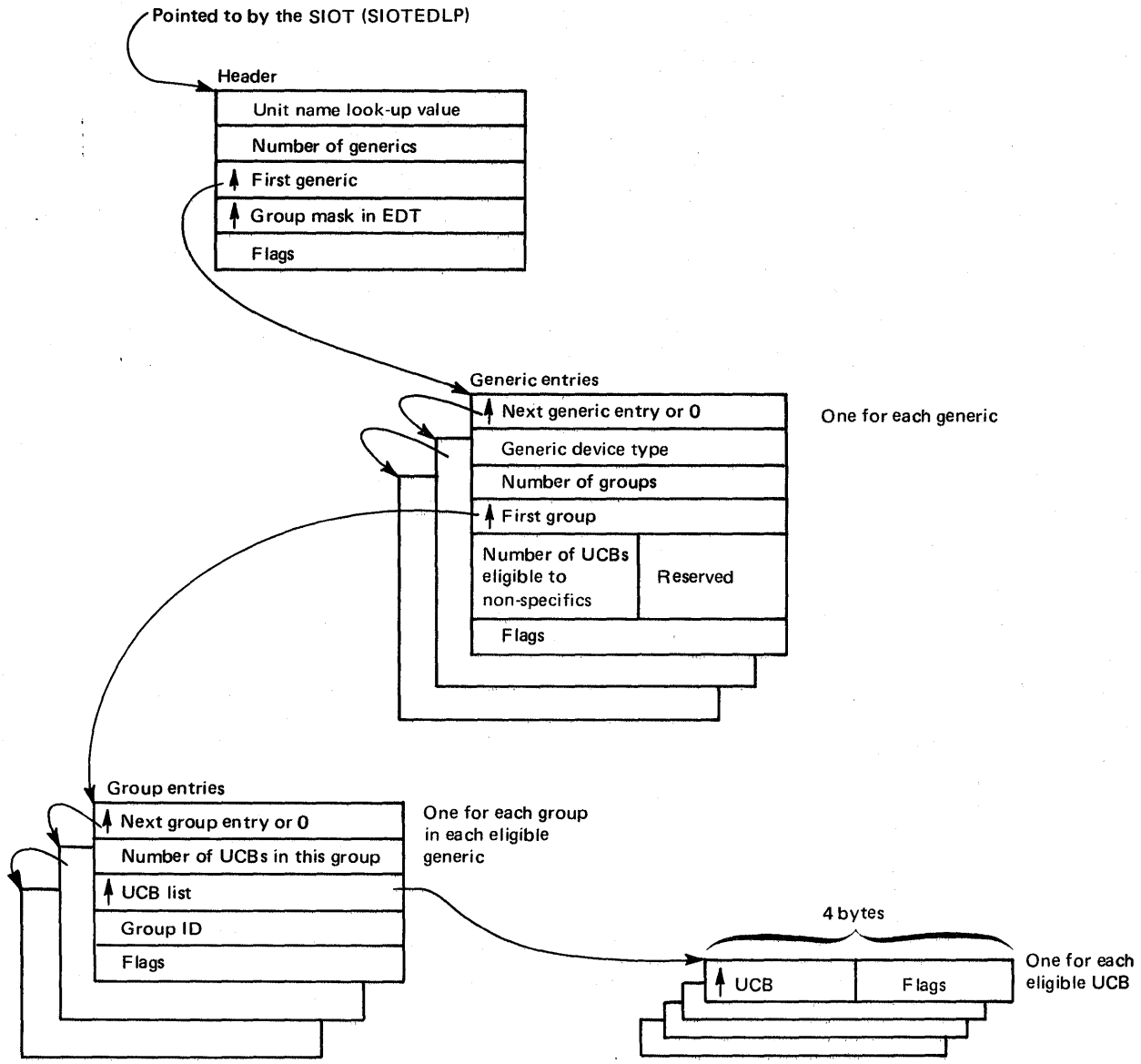


Figure 5-20. Eligible Device List (EDL)

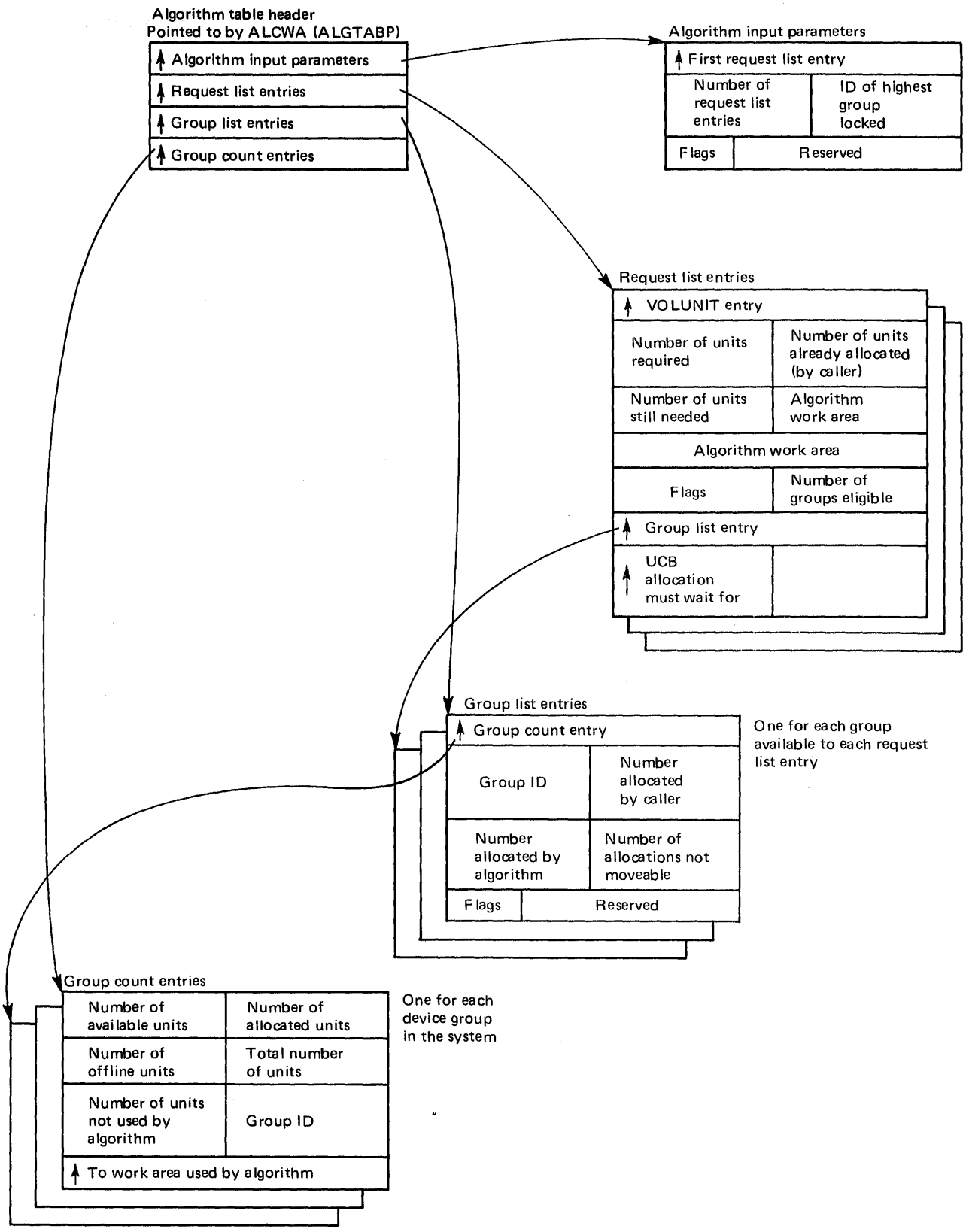
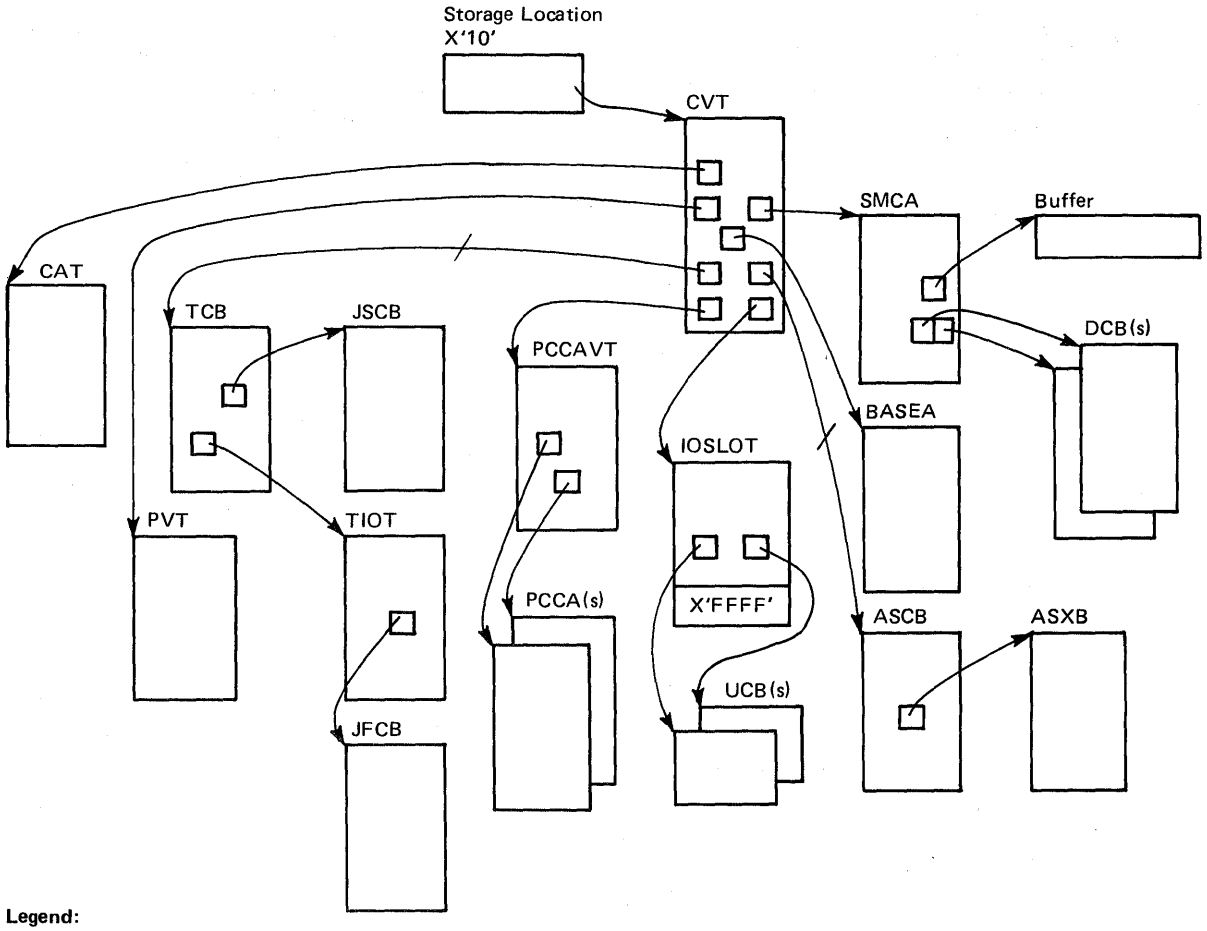


Figure 5-21. Algorithm Interface Tables

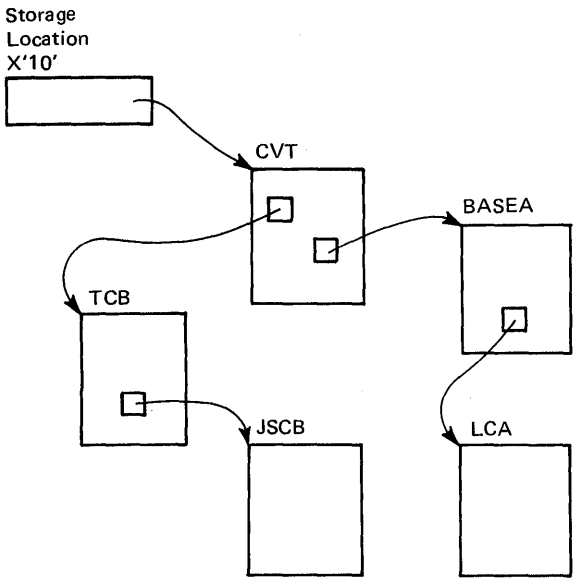


Legend:

→ = Indirect Pointer

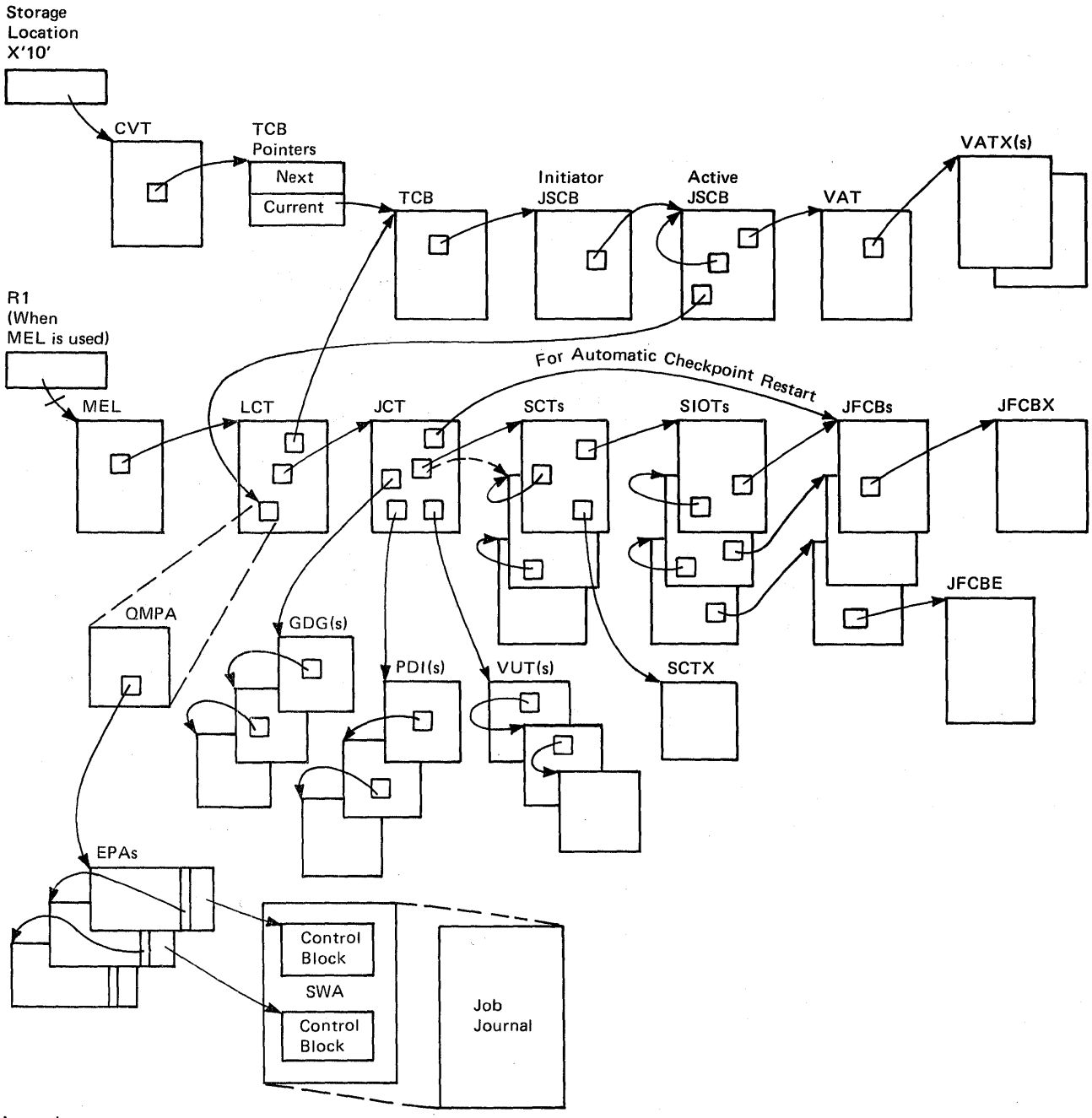
Acronym	Control Block Name	Acronym	Control Block Name
ASCB	Address Space Control Block	PCCA	Physical Configuration Communication Area
ASXB	Address Space Extension Block	PCCAVT	Physical Configuration Communication Area Vector Table
CAT	Channel Availability Table	PVT	Page Vector Table
CVT	Communication Vector Table	SMCA	System Management Control Area
DCB	Data Control Block	TCB	Task Control Block
IOSLOT	IOS Look Up Table	TIOT	Task Input/Output Table
JFCB	Job File Control Block	UCB	Unit Control Block
JSCB	Job Scheduling Control Block		

Figure 5-22. System Management Facilities (SMF) Control Block Overview



Acronym	Control Block Name
BASEA	Master Scheduler Resident Data Area
CVT	Communication Vector Table
JSCB	Job Scheduling Control Block
LCA	Log Control Area
TCB	Task Control Block

Figure 5-23. System Log Task Control Block Overview

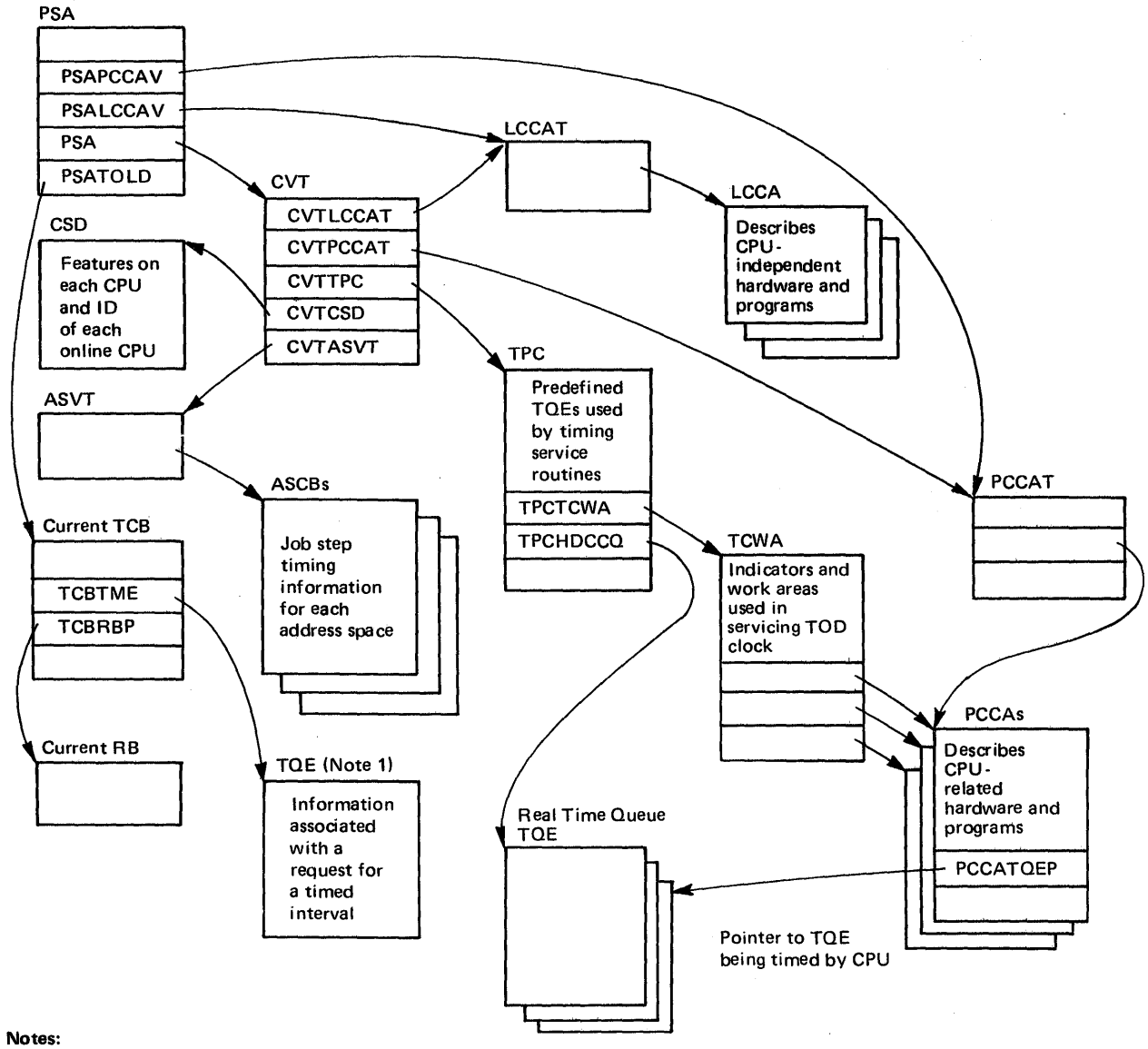


Legend:

→ = Indirect Pointer

Acronym	Control Block Name	Acronym	Control Block Name
CVT	Communications Vector Table	PDI	Passed Data Set Information
EPA	External Parameter Area	QMPA	Queue Manager Parameter Area
GDG	Generation Data Group	SCT	Step Control Table
JCT	Job Control Table	SCTX	Step Control Table Extension
JFCB	Job File Control Block	SIOT	Step Input/Output Table
JFCBE	Job File Control Block Extension for 3800	SWA	Scheduler Work Area
JFCBX	Job File Control Block Extension	TCB	Task Control Block
JSCB	Job Scheduling Control Block	VAT	Virtual Address Table
LCT	Linkage Control Table	VATX	Virtual Address Table Extension
MEL	Merge Entrance List	VUT	Volume Unload Table

Figure 5-24. Checkpoint/Restart Control Block Overview



Notes:
1. If real or wait type, this TQE is also on the real time queue.

Figure 5-25. Timer Supervision Control Block Overview

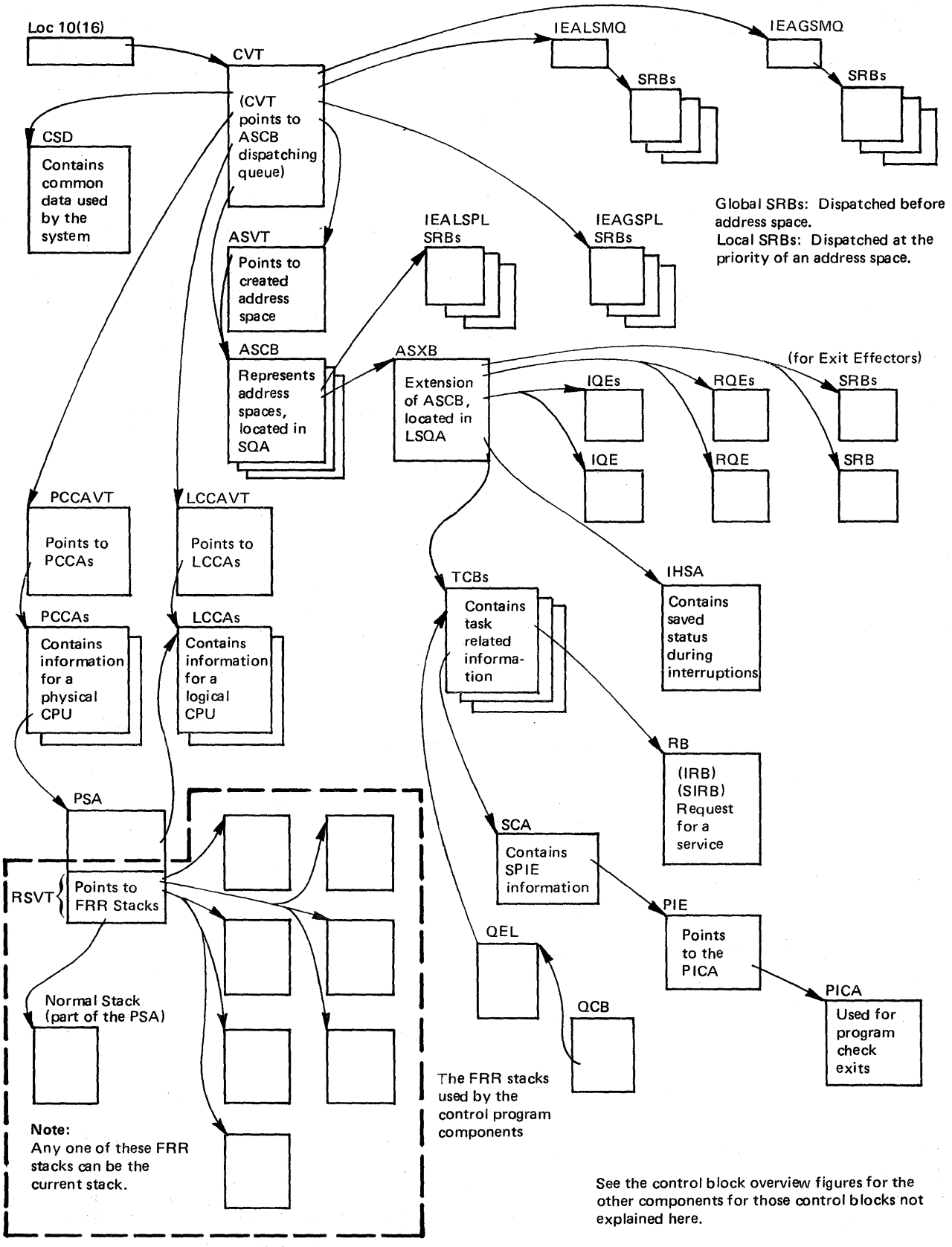


Figure 5-26. Supervisor Control Block Overview

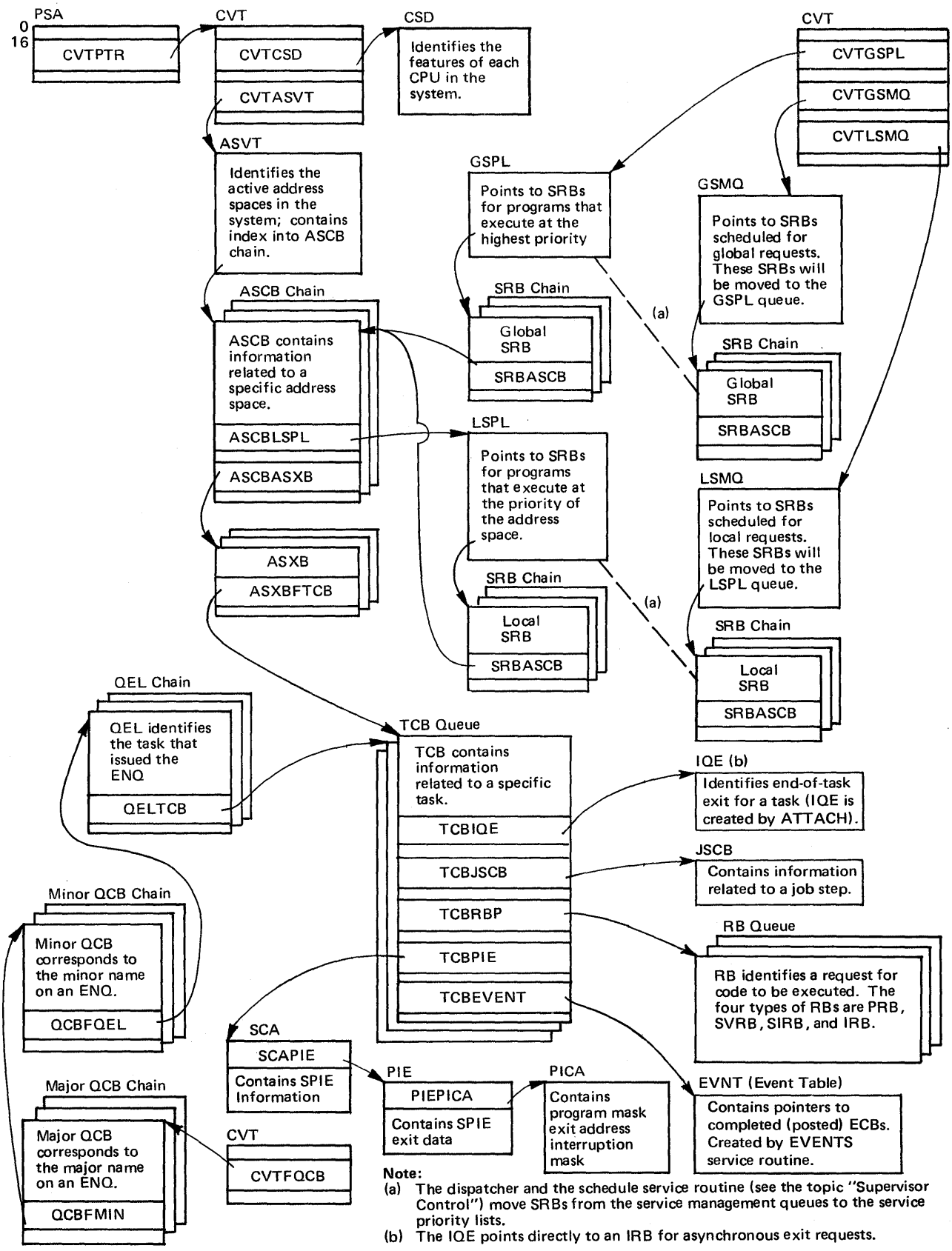
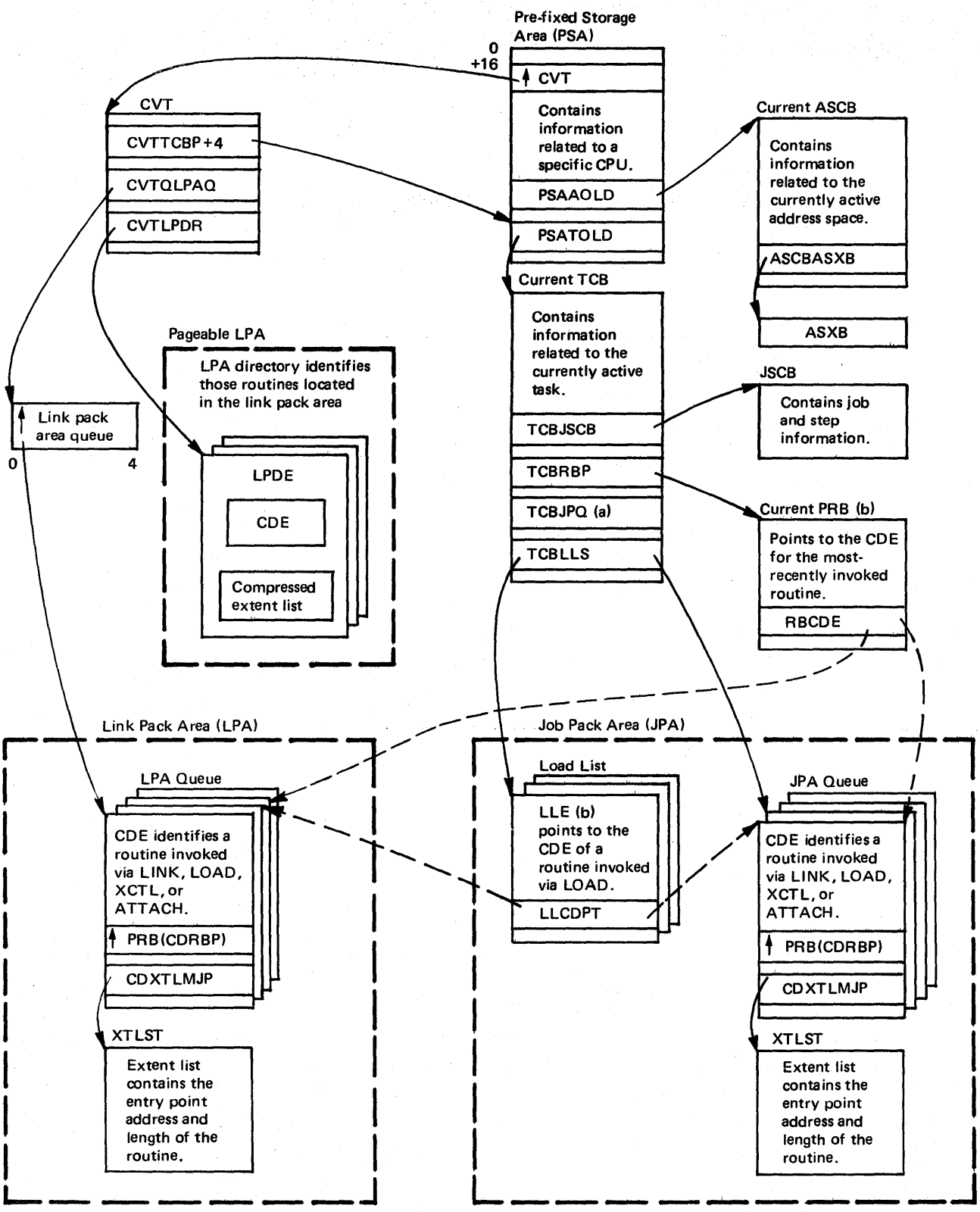


Figure 5-27. Task Management Control Block Overview



Notes:
(a) Only the job step TCB points to the JPA queue.
(b) A PRB or a LLE may point to a CDE located in either the LPA queue or the JPA queue.

Figure 5-28. Program Management Control Block Overview

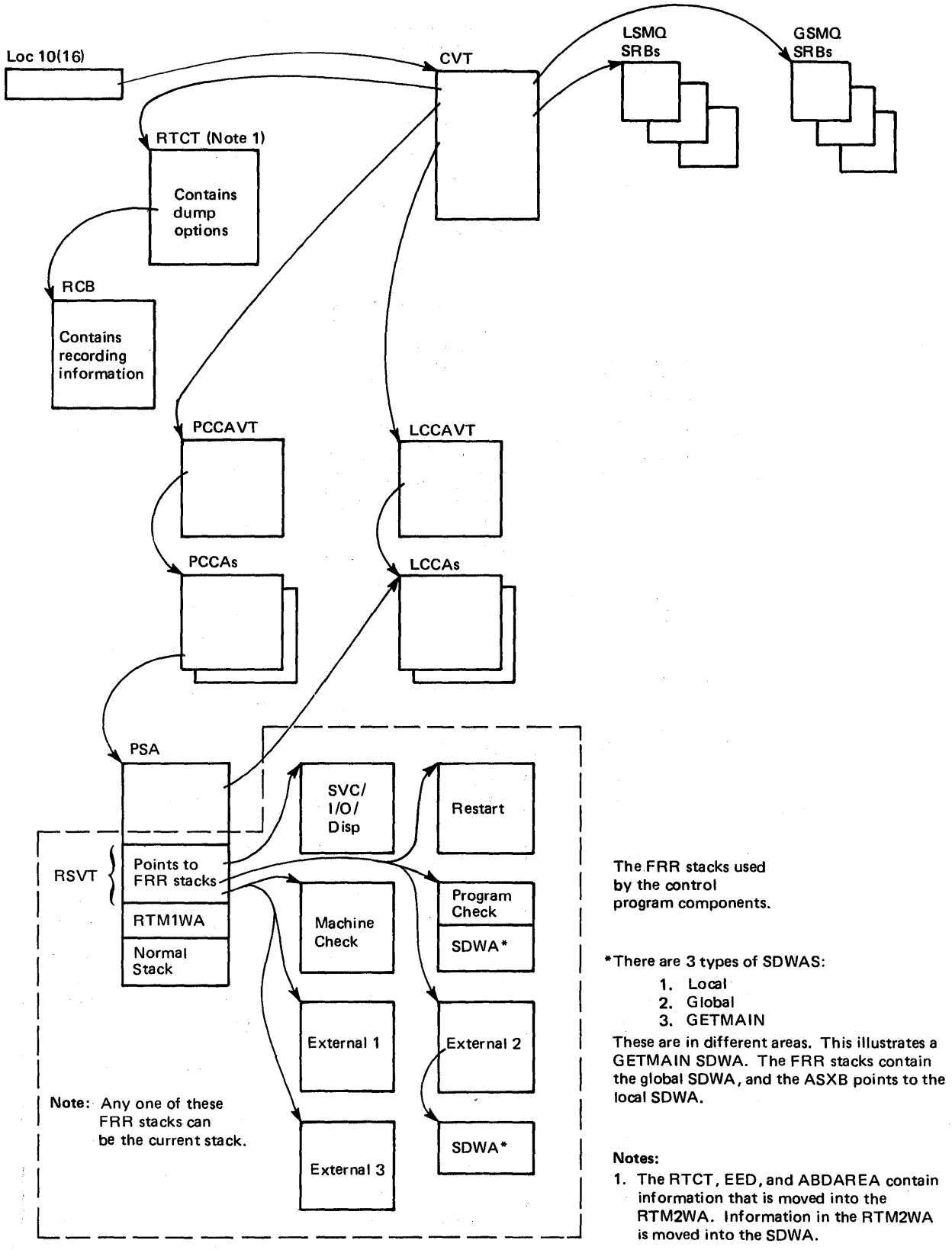
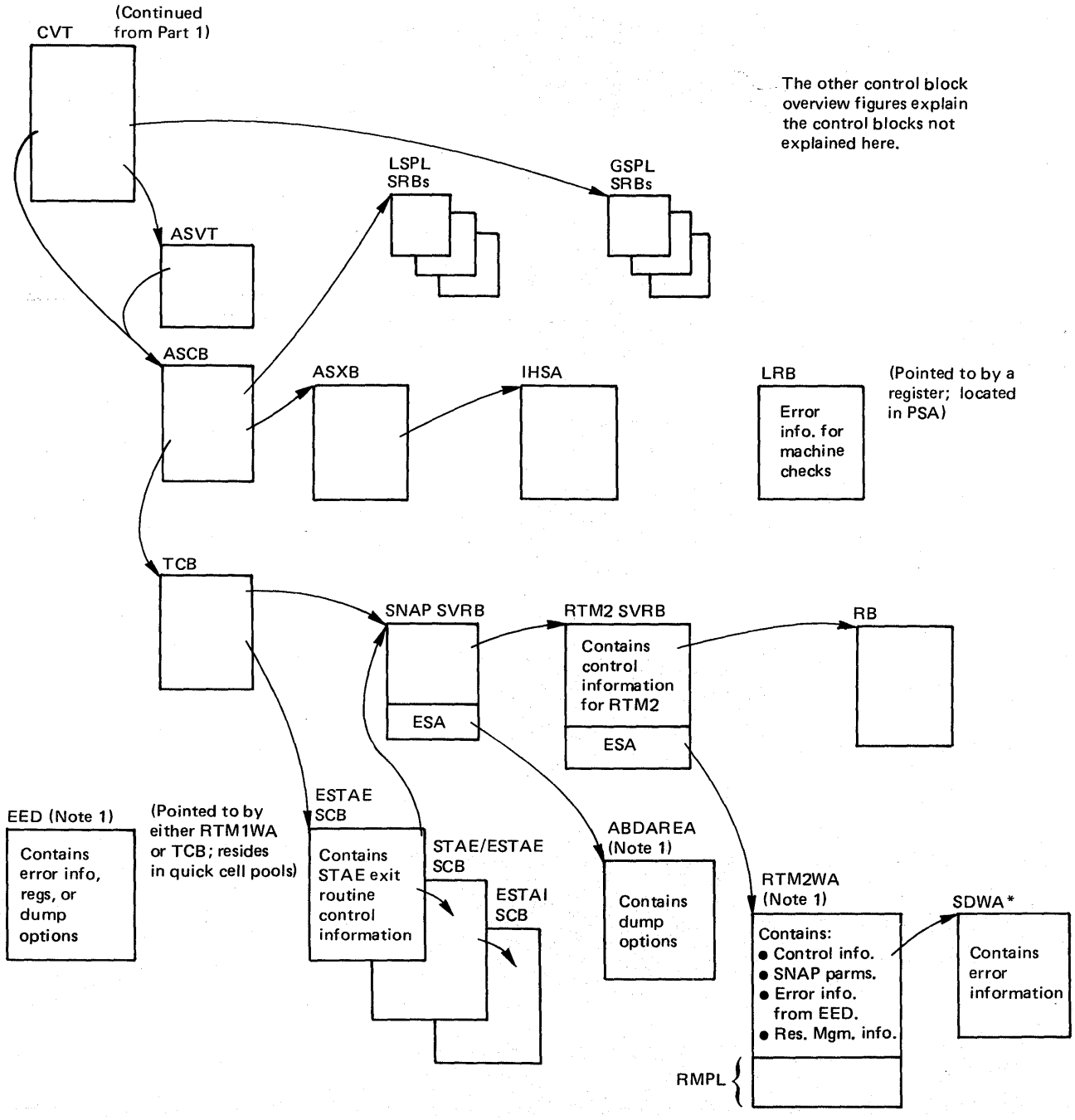


Figure 5-29. Recovery/Termination Management Control Block Overview (Part 1 of 2)



*There are 3 types of SDWAs:
1. Local
2. Global
3. GETMAIN

These are in different areas. This illustrates a GETMAIN SDWA. The FRR stacks contain the global SDWA, and the ASXB points to the local SDWA.

Notes:

1. The RTCT, EED, and ABDAREA contain information that is moved into the RTM2WA. Information in the RTM2WA is moved into the SDWA.

Figure 5-29. Recovery/Termination Management Control Block Overview (Part 2 of 2)

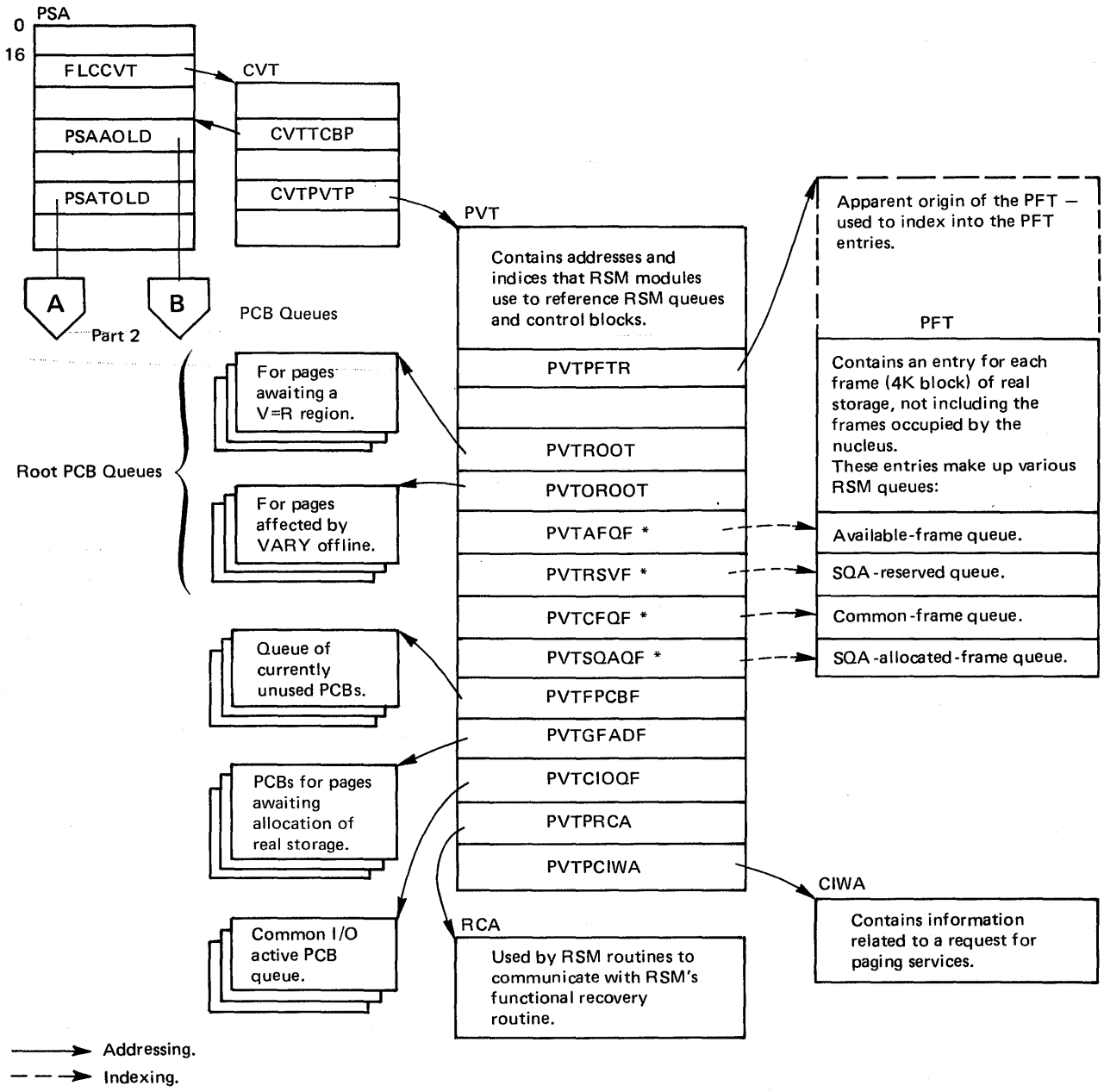


Figure 5-30. Real Storage Management Control Block Overview (Part 1 of 2)

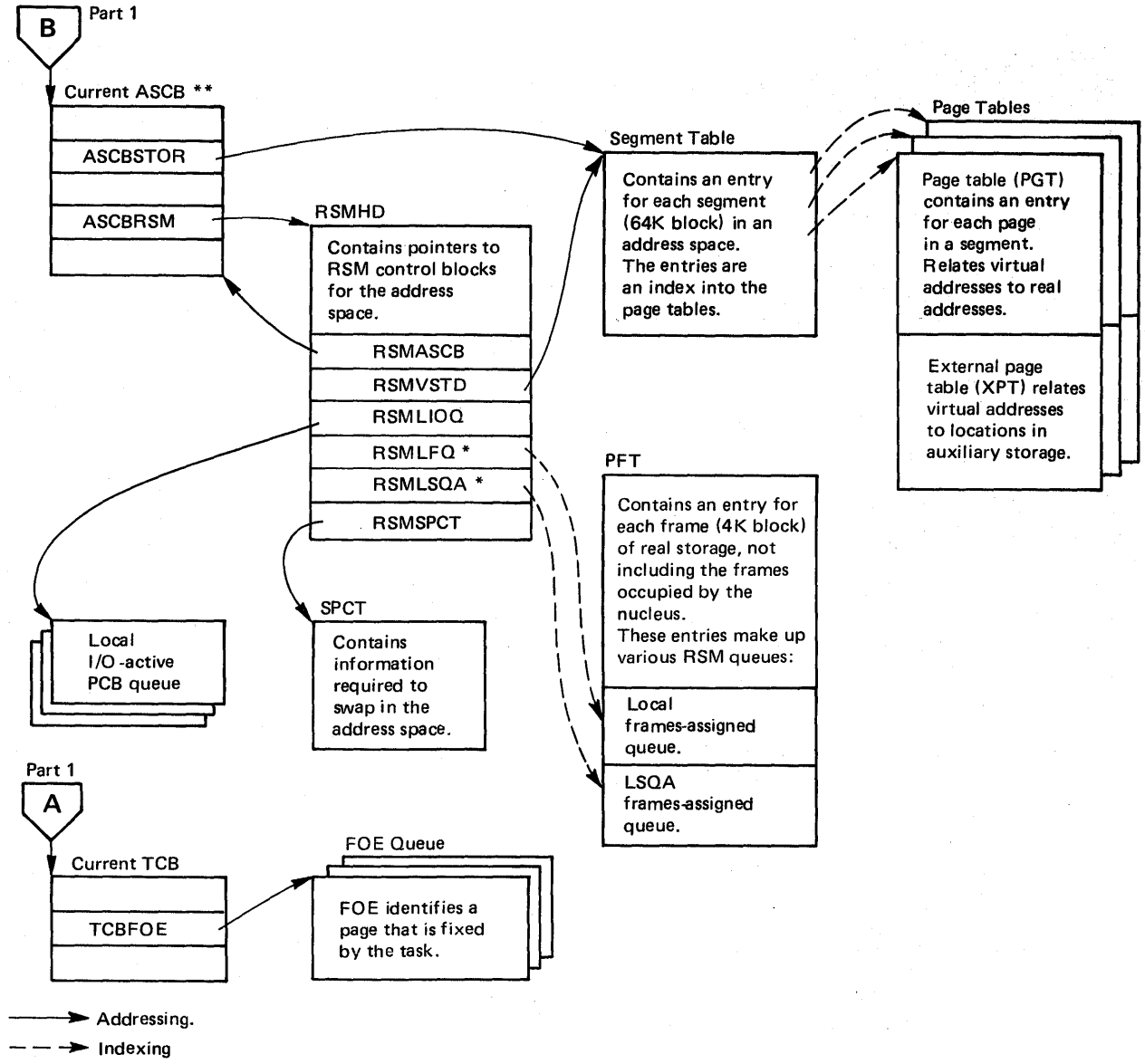


Figure 5-30. Real Storage Management Control Block Overview (Part 2 of 2)

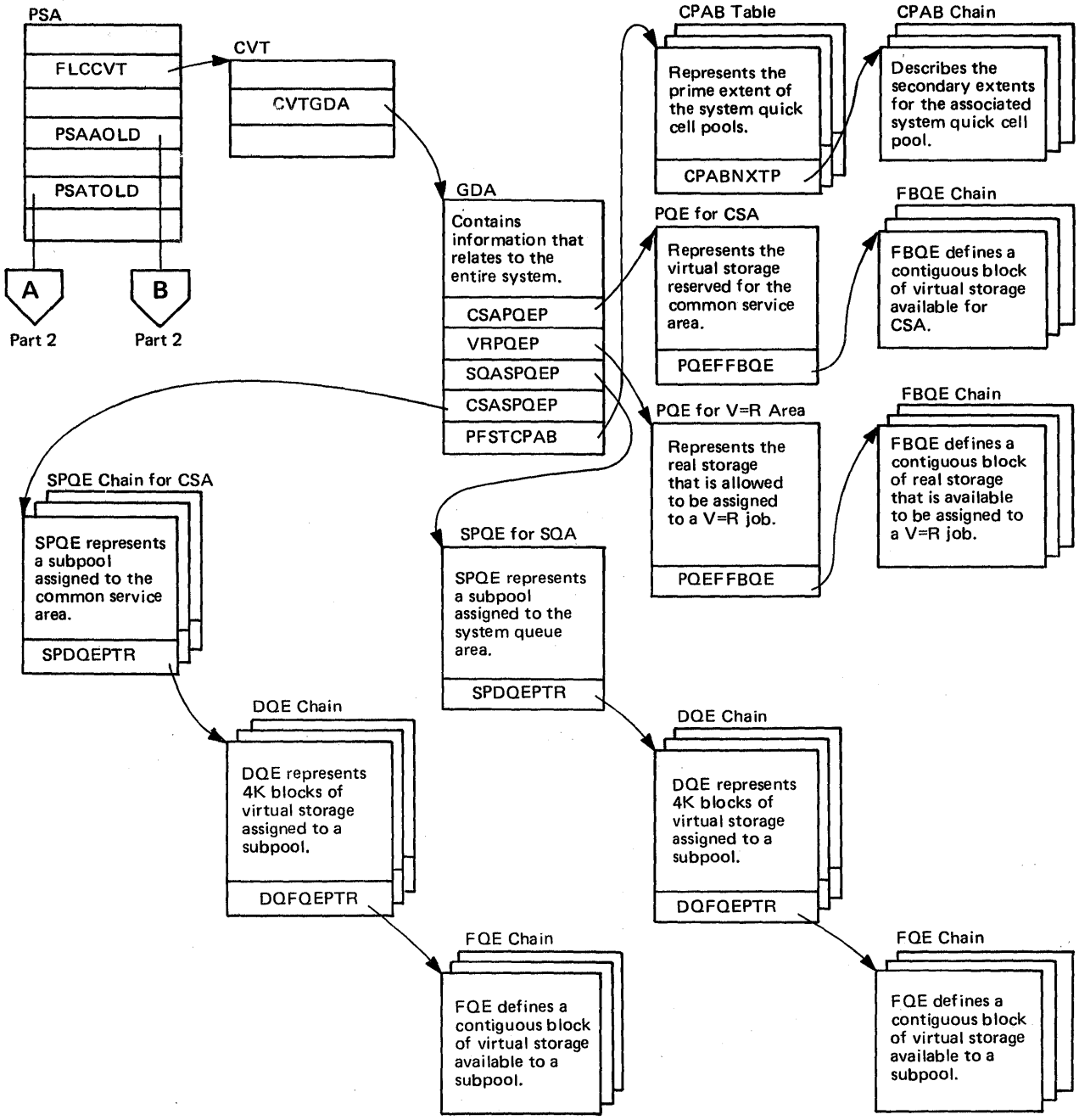


Figure 5-31. Virtual Storage Management Control Block Overview (Part 1 of 2)

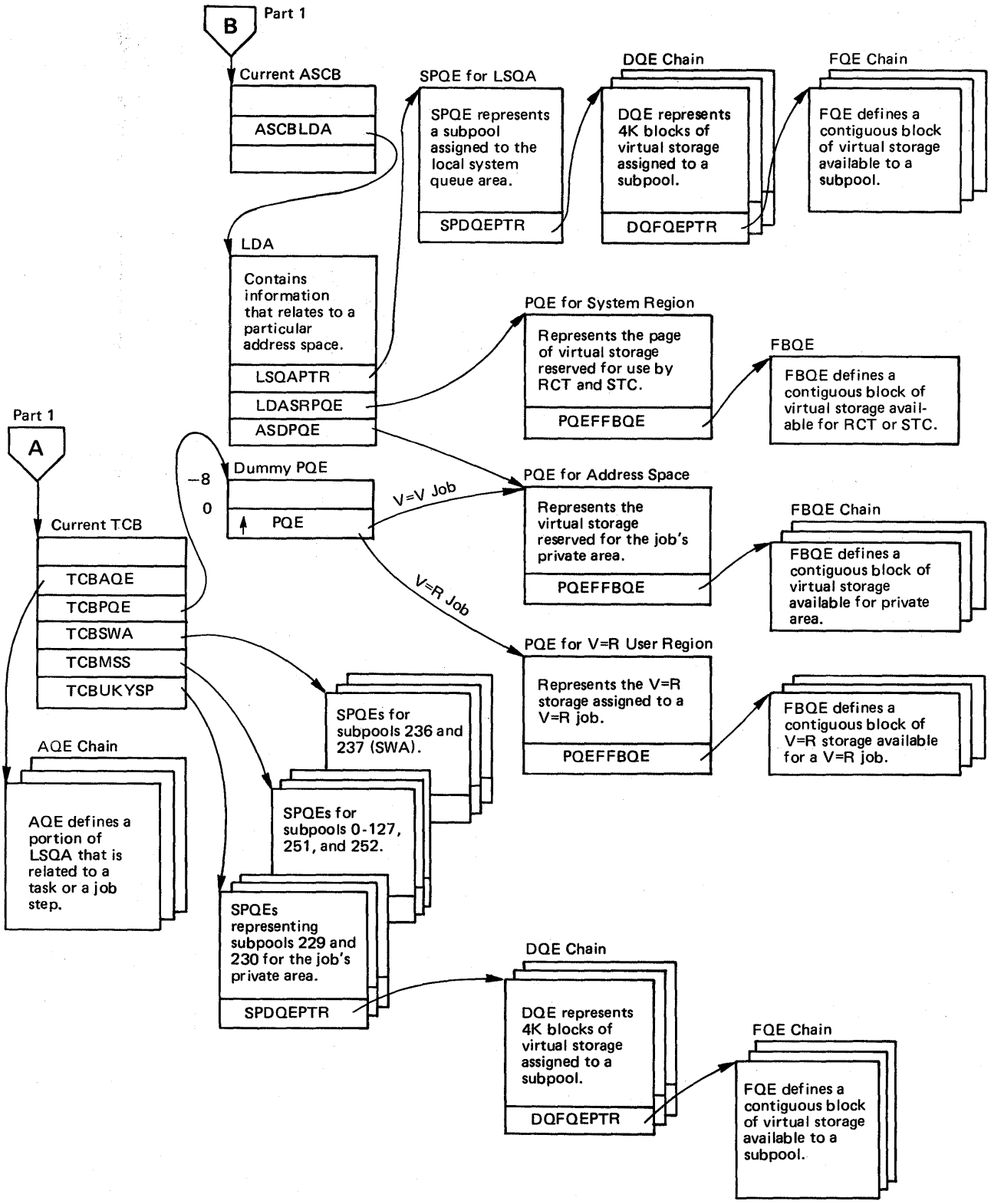


Figure 5-31. Virtual Storage Management Control Block Overview (Part 2 of 2)

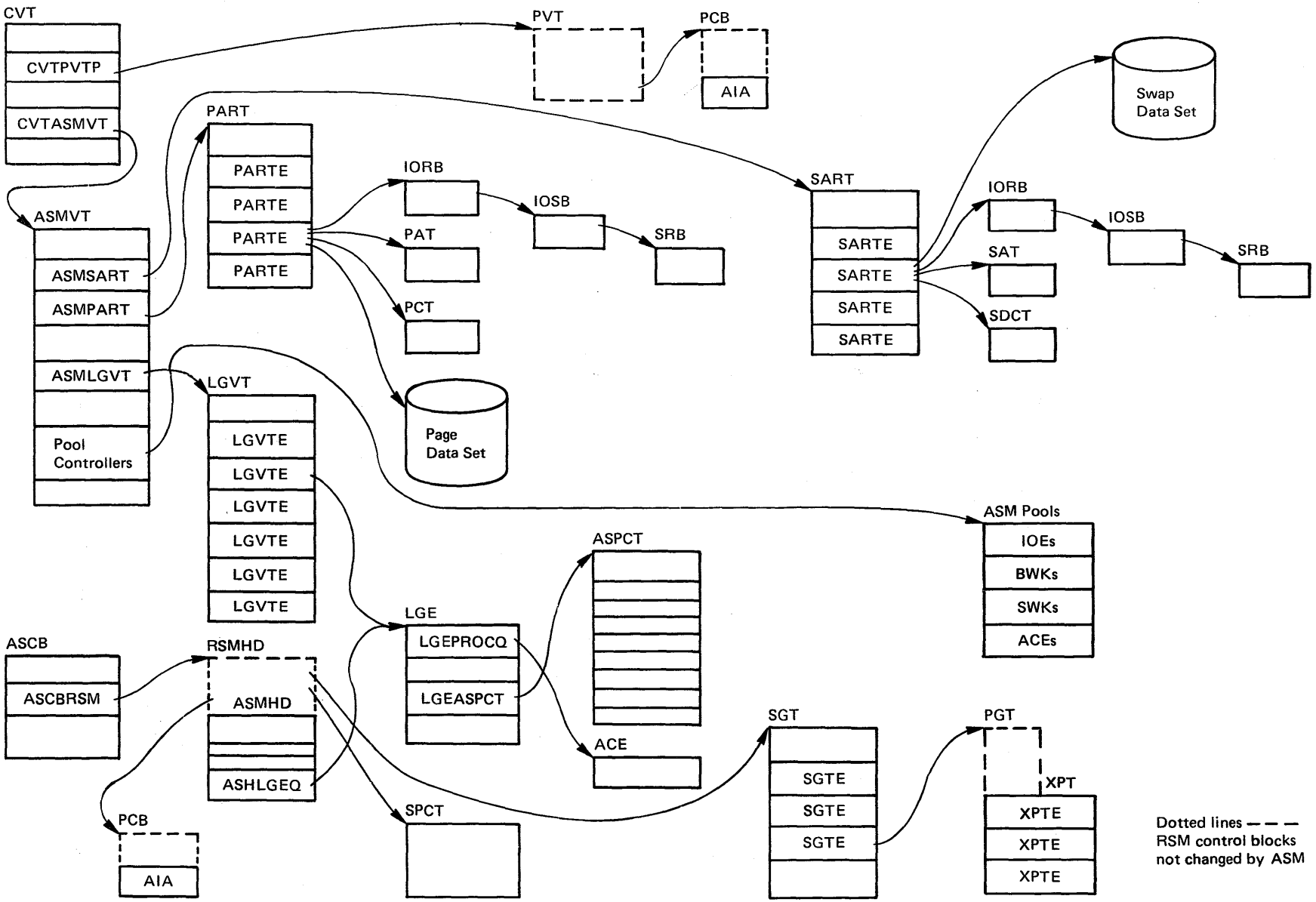


Figure 5-32. Auxiliary Storage Management Control Block Overview

Acronym/Mapping Macro/Common Macro

Acronym	Mapping Macro	Common Macro
ABDA	IHAABDA	Abdump Work Area
ABDPL	IHAABDPL	Snap Parameter List
ABP	IEZABP	ABP Communication Vector Table
ACA	ILRACA	ASM Control Area
ACB	IFGACB	Access Method Control Block
ACDEB	ISTACDEB	VTAM Data Extent Block
ACE	ILRACE	ASM Control Element
ACEV	ISTACE	VTAM Active Connection Element
AIA	ILRAIA	ASM I/O Request Area
AIB	TAIBD	Application Interface Block
AIT	IEFZB426	Algorithm Interface Tables
ALCA	IEFZB432	Allocation Communication Area
ALCC	IEFZB442	Allocate Catalog Controls
ALCWA	IEFZB425	Allocation Work Area
ALLOCDEF	IEFZB445	Device Allocation Defaults
ALLOCWA	IKJZT430	Work Area for Allocate
ALTIOSTB	IEFZB449	TIOT Status B Overlay
AMB	IDAAMB	Access Method Block
AMBL	IDAAMBL	Access Method Block List
AMBXN	IDAAMBXN	AMD Extension
AMCBS	AMCBS	Access Method Control Block Structure
AMDDATA	AMDDATA	Mapping of PRDMP Input Records
AMDSB	IDAAMDSB	Access Method Data Statistics Block
AMWA	IEFJAMWA	Master Subsystem Access Method Work Area
APCRR	ISTAPCRR	VTAM Component Recovery Record for Process Scheduling SVCs
AQE	IHAAQE	Allocated Queue Element
AQMRB	IEFZB427	Allocation Queue Manager Parameter/Communication Area
ARDB	IDAARDB	Address Range Definition Block
ASCB	IHAASCB	Address Space Control Block
ASCRR	ISTASCRR	VTAM Component Recovery Record for Storage Management
ASMHD	ILRASMHD	ASM Header
ASMVT	ILRASMVT	ASM Vector Table
ASPCT	ILRASPCT	Auxiliary Storage Page Correspondence Table
ASVT	IHAASVT	Address Space Vector Table
ASWA	IEFZB453	Allocation STAE Work Area
ASXB	IHAASXB	Address Space Extension Block
ATA	ILRATA	ASM Tracking Area
ATCVT	ISTATCVT	VTAM Communication Vector Table
ATTCH	IEZATTCH	ATTACH Parameter List DSECT
AVT	TAVTD	TCAM Address Vector Block
AVTV	ISTAVT	VTAM Address Vector Table
AWA	IEFVMAWA	Interpeter Work Area
BASEA	IEEBASEA	Master Scheduler Resident Data Area
BCDIR	IKJZT302	Broadcast Notices Directory Record
BCMSG	IKJZT303	Broadcast Notices Message Record
BDU	ISTBDU	VTAM Basic Data Unit
BEB	IECDBEB	Beginning-End Block
BFHDR	ISTBFHDR	VTAM Buffer Header
BFPFX	ISTBFPFX	VTAM Buffer Prefix
BIB	IDABIB	Base Information Block
BLDO	ISTBLDO	VTAM BAL Version of LDO (Logical Device Order)
BLPRM	IDABLPRM	Resource Pool Parameter List
BPCB	ISTBPCB	VTAM Buffer Pool Control Block
BPDTY	ISTBPDTY	VTAM Buffer Pool Directory
BPENT	ISTBPENT	VTAM Buffer Pool Entry
BRKELEM	BRKELEM	Break Element

Acronym	Mapping Macro	Common Macro
BSPH	IDABSPH	Buffer Subpool Header
BTUV	ISTBTU	VTAM Basic Transmission Unit
BUFC	IDABUFC	Buffer Control Block
BUILDTAB	ICGDSMA5	Build Communications Area
CA	IKJEBECA	TSO EDIT Communications Area
CAESTPA	IEFZB447	Common Allocation Estae Exit Parameter Area
CAFM	IEFZB428	Common Allocation Function Map
CAT	IECDCAT	Channel Availability Table
CAXWA	IGGCAXWA	Catalog Auxiliary Work Area
CBDA	IEDCBDA	TCAM Common Buffer Data Area
CCA	IGGCCA	Catalog Communications Area
CCNCB	ISTCCNCB	VTAM Cluster Controller Node Control Block
CCT	IRACCT	SRM CPU Management Control Table
CCW	TCCWD	TCAM Channel Command Word
CCWV	ISTCCW	VTAM Channel Command Word
CDA	IGFCDA	Channel Data Area
CDE	IHACDE	Contents Directory Entry
CHKWA	IEEVCHWA	Checkpoint Work Area
CIB	IEZCIB	Command Input Buffer
CICB	IFGJCICB	JES Compatibility Interface Control Block
CIWA	IEACIWA	Common Internal Work Area
CIX	IHACIX	CI SVC Exit List
CKP	TCKPD	Checkpoint Work Area
CLWRK	IDACLWRK	VSAM CLOSE and TCLOSE ACB Work Area
CMB	IEDCMB	TCAM Master QCB for Common Buffer Transmission
CNSTA	ISTCNSTA	VTAM Connection Services Component Recovery Record
COM	IEZCOM	Communications Parameter List
COMMON	COMMON	AMDPRDMP Common Area Macro
COMTAB	ICGDSMAC	Common Communications Area
COMWA	IEFCOMWA	Converter/Interpreter Common Work Area
CONFT	ISTCONFT	VTAM Configuration Table
CONTAB	IKJEFFCT	Internal Control Table for TSO SUBMIT Command
CPA	IDACPA	Channel Program Area
CPAB	IHACPAB	Cell Pool Anchor Block
CPB	TCPBD	TCAM Channel Program Block - 3330
CPPL	IKJCPPL	Command Processor Parameter List
CQE	IHACTM	Console Queue Element
CRA	ISTCRA	VTAM Component Recovery Area
CRPL	ISTCRPL	VTAM Copied Request Parameter List
CSCB	IEECHAIN	Command Scheduling Control Block
CSCBV	ISTCSCB	VTAM Command Scheduler Control Block
CSCRR	ISTCSCRR	VTAM Cluster Solicitor Component Recovery Record
CSD	IHACSD	Common System Data Area
CSL	IDACSL	Core Save List
CSOA	IKJCSOA	Command Scan Output Area
CSP	ISTCSP	VTAM Connection Services Parameter List
CSPL	IKJCSPL	Command Scan Parameter List
CSW	ISTCSW	VTAM Channel Status word
CTGCV	IEZCTGCV	VSAM Catalog Name/Volume Area
CTGFL	IEZCTGFL	VSAM Catalog Field Parameter List
CTGFV	IEZCTGFV	Catalog Field Vector Table
CTGPL	IEZCTGPL	VSAM Catalog Parameter List
CTGVL	IEZCTGVL	VSAM Catalog Volume List
CTGWA	IEZCTGWA	VSAM Catalog Scheduler Work Area
CUNESTPA	IEFZB441	Common Unallocation Estae Exit Parameter Area
CUNI	IEFZB439	Common Unallocation Interface
CVMAP	IEECVMAP	MP and K Command Parameter List
CVRWA	IEFCVRWA	Converter Work Area
CVT	CVT	Communications Vector Table
CWB	IEZCWB	Command Work Buffer
CXSA	IHACTM	SVC 72 Extended Save Area
DACB	IKJDACB	DAIR Attribute Control Block

Acronym	Mapping Macro	Common Macro
DAFM	IEFZB4D7	Dynamic Allocation Function Map
DAKEYDIC	IEFZB4D4	Dynamic Allocation Key Dictionary
DAKEYTAB	IEFZB4D3	Dynamic Allocation Key Table
DAPB0C	IKJDAP0C	DAIR Entry Code 0C Parm List
DAPB00	IKJDAP00	Dair Entry Code 00 Parm List
DAPB04	IKJDAP04	DAIR Entry Code 04 Parm List
DAPB08	IKJDAP08	DAIR Entry Code 08 Parm List
DAPB1C	IKJDAP1C	DAIR Entry Code 1C Parm List
DAPB10	IKJDAP10	DAIR Entry Code 10 Parm List
DAPB14	IKJDAP14	DAIR Entry Code 14 Parm List
DAPB18	IKJDAP18	DAIR Entry Code 18 Parm List
DAPB2C	IKJDAP2C	DAIR Entry Code 2C Parm List
DAPB24	IKJDAP24	DAIR Entry Code 24 Parm List
DAPB28	IKJDAP28	DAIR Entry Code 28 Parm List
DAPB30	IKJDAP30	DAIR Entry Code 30 Parm List
DAPB34	IKJDAP34	DAIR Entry Code 34 Parm List
DAPL	IKJDAPL	DAIR Parameter List
DATA	TDATAD	TCAM Disk Data Record Area
DCB1	DCBD	Data Control Block (EXCP, SAM, BPAM)
DCB2	DCBD	Data Control Block (ISAM)
DCB3	DCBD	Data Control Block (BDAM)
DCB4	DCBD	Data Control Block (BTAM)
DCB5	DCBD	Data Control Block (TCAM)
DCB6	DCBD	Data Control Block (GAM)
DCCRR	ISTDCCRR	VTAM Control Layer Component Recovery Record
DCE	ISTDCE	VTAM DEB Chain ELEMENT
DCLCP	ISTDCLCP	VTAM Control Layer Logical Channel Program Block
DDDVT	IRBDDDVT	MF/1 Device Vector Table
DDRCOM	IHADDR	Dynamic Device Reconfiguration Communication Table
DEB	IEZDEB	Data Extent Block
DEBAP	TDEBAPD	TCAM Application Program DEB
DECB	IHADECB	Data Event Control Block
DEVCH	ISTDEVCH	VTAM Device Characteristics Table
DEVTAB	IFDEVTAB	OLTEP Device Table
DFPARMS	IKJEFFDF	Parmlist to IKJEFF18 (DAIRFAIL)
DFPB	IKJDFPB	Default Parameter Block
DFPL	IKJDFPL	Default Parameter List
DISP	TDISPD	TCAM Dispatcher DSECT
DIVTH	ISTDIVTH	VTAM Dial-In Verification Table Header
DIWA	IDADIWA	Data Insert Work Area
DNCB	ISTDNCB	VTAM Destination Node Control Block
DNIB	ISTDNIB	VTAM Node Identification Block
DOMC	IHADOMC	Delete Operator Message Control Block
DOMPL	IHACTM	Parameter List For DOM - SVC 87
DPROC	ISTDPROC	VTAM Process Option Definition Block
DQE	IHADQE	Descriptor Queue Element
DRQ	TDRQD	Data Ready Queue
DSAB	IHADSAB	Data Set Association Block
DSABMASK	IEFZB4D8	Data Set Association Block Mask
DSABQDB	IEFZB4D5	DSAB Queue Descriptor Block
DSCB1	IECSDSL1	Format 1--Identifier Data Set Control Block
DSCB2	IECSDSL1	Format 2--Index Data Set Control Block
DSCB3	IECSDSL1	Format 3--Extension Data Set Control Block
DSCB4	IECSDSL1	Format 4--VTOC Data Set Control Block
DSCB5	IECSDSL1	Format 5--Available Space Data Set Control Block
DSCB6	IECSDSL1	Format 6 DSCB
DSENQT	IEFZB902	Data Set Enqueue Table
DSL	IDADSL	DEB Save List
DSNT	IEFDSNT	Data Set Name Table
DSPCT	IDAVBPH	Data Set Page Correspondence Table Header
DSPCTMAP	IDAVBPM	Data Set Page Correspondence Table Map Entry
DSRFM	IEFZB4D6	Data Set Reservation/Release Routine Function Map

Acronym	Mapping Macro	Common Macro
DTE	ISTDTE	VTAM Dial-In Type Table Entry
DUIDL	IKJEFUDL	User Data List
DVA	IHADVA	DEVTYP Output
DVCHR	ISTDVCHR	VTAM Device Characteristics Table
DVCIDT	TDVCIDTD	Device ID Table
DVCT	IHADVCT	Device Characteristics Table
DVE	ISTDVE	VTAM Dial-in Verification Table Entry
DVT	ISTDVT	VTAM Destination Vector Table
DVTAB	ILRDVTAB	ASM Hardcoded Device Table
DWWIN	IRBDWWIN	MF/1 Workload Interval Data Table
DYNESTPA	IEFZB4D9	Dynamic Allocation Estae Exit Parameter Area
DYNTCFRR	IEFZB451	Dynamic Allocation TCTIOT FRR Parameter Area
DYPAB	ISTDYPAB	VTAM Dynamic PAB
ECB	IHAECB	Event Control Block
ECCDB	IRBECCDB	MF/1 Channel Data Block
ECCEDT	IRBECCED	MF/1 Channel Event Data Table
ECCPE	IRBECCPE	MF/1 CPU Entry Table
ECT	IKJECT	Environment Control Table
EDB	IDAEDB	Extent Definition Block
EDDCDT	IRBEDDCD	MF/1 Device Class Data Table
EDDDB	IRBEDDDB	MF/1 Device Data Block
EDDEDT	IRBEDDED	MF/1 Device Event Data Table
EDL	IEFZB422	Eligible Device List
EDT	IEFZB421	Eligible Devices Table
EMM	IEEZB820	STC STAE Exit Parameter List
EPAL	IEFZB505	External Parameter Area, SWA Manager Locate Mode
EPAM	IEFZB506	External Parameter Area, SWA Manager, Move Mode
EPAT	IRAEPAT	System Resources Manager Algorithm Entry Point Descriptor Table
EPATH	ILREPATH	Recovery Audit Trail Area
EPDT	IRAEPDT	System Resources Manager Serialized Action Entry Point Descriptor Table
EPST	IRAEPST	System Resources Manager Scanned Action Entry Point Descriptor Table
EREPL	IEFZB9RD	Converter/Interpreter ESTAE Exit Parameter List
ERPIB	IGFERPIB	Error Recovery Procedure Information Block
ERRWORK	IHASDERR	FRR/ESTAE Work Area for SVC Dump
ESA	RTM2ESA	Extended Save Area
ESL	IDAESL	Enqueue Save List
ESTA	IHAESTA	Extended Stae Parameter List
ETIORB	IEFZB430	DSAB/TIOT Entry Build Routine Request Block
EVNT	IHAEVNT	Event Table
EWA	EWAMAP	Common ERP Work Area
EWD	EWDMAP	DASD ERP Work Area
EWT	EWTMAP	Tape ERP Work Area
EWU	EWUMAP	Unit Record ERP Work Area
EXITL	IKJEFFIE	Parameter List to TSO SUBMIT Installation Exit
EXLSTA	IFGEXLST	User ACB Exit List
EXLSTD	IHAEXLST	EXITLIST
EXTWA	IECEXTWA	Extend Work Area
FBQE	IHAFBQE	Free Block Queue Element
FCAUD	IHAFCAUD	Audit Trail, OPEN/CLOSE Executors
FCBIM	IHAFCBIM	FCBIMAGE IN EXITLIST
FDB	ISTFDB	VTAM Feedback Data Block
FETWK	IHAFETWK	FETCH Work Area
FFB2	IKJEFFB2	Mapping Macro of SVC 100 Attach Interface
FFIB	IKJEFFIB	Mapping Macro of SVC 100 Interface
FMCB	ISTFMCB	VTAM Function Management Control Block
FOE	IHAFOE	Fixed Ownership Element
FPWA	IKJEFPWA	Parse Work Area
FQE	IHAFQE	Free Queue Element
FRRS	IHAFRRS	FRR Stack
FSB	ISTFSB	VTAM Feedback Status Block

Acronym	Mapping Macro	Common Macro
FTPT	IHACTM	Parm List for FRR/ESTAE (COMTASK)
GDA	IHAGDA	Global Data Area
GDGNT	IEFZB429	Generation Data Group Name Table
GFPARMS	IKJEFFGF	Parameter List to TSO General Failure Service Routine
GTFBCB	GTFBCB	GTF Buffer Control Block
GTFBLOK	GTFBLOK	GTF Block Description
GTFPCT	GTFPCT	GTF Primary Control Block
GTPB	IKJGTPB	GETLINE
GWT	IEFZB600	GETPART Work Table
HCNCB	ISTHCNCB	VTAM Remote Cluster Node Control Block
HDR	IHAHDR	R/TM Mapping of the AR-149 Header
HEB	IDAHEB	Header Element Block
HISTORY	IKJEFFHT	Internal History Table for TSO SUBMIT Command
HSKESTPA	IEFZB444	JFCB Housekeeping Estae Exit Parameter Area
HSKPWA	IEFZB437	JFCB Housekeeping Work Area
ICB	IHAICB	Interrupt Control Block
ICB2	IEZSSC	Mass Storage System Communicator Control Block for VS2
ICE	ISTICE	VTAM Inactive Connection Element
ICFWORK	ICFWORK	PWF Communications Table
ICNCB	ISTICNCB	VTAM Intelligent Controller Node Control Block
ICT	IRAICT	SRM I/O Management Control Table
ICWA	IDAICWA	INDEX Create Work Area
ICX	ISTICX	VTAM ICE Extension
IECALLWA	IECALLWA	ALLWA Allocate Work Area
IECPRLWA	IECPRLWA	Partial Release Work Area
IECSCRWA	IECSCRWA	Scratch Work Area
IEL	IEZIEL	Initiator Entrance, Options Exit List
IFDCOM	IFDPF1	OLTEP Common Area
IHSA	IHAIHSA	Interrupt Handler Save Area
IICB	IDAIICB	ISAM Interface Control Block
IKJEFLWC	IKJEFLWC	Parameter List For IKJEFLGM
IKJEFUAD	IKJEFUAD	User Attribute Data Set
IMCB	IRAIMCB	SRM User I/O Management Control Block
IMWA	IDAIMWA	Index Modification Work Area
INF	IHAINF	Type 1 Message Table Entry
INITDATA	INITDATA	GTF Initialization Data Area
IOB	IEZIOB	Input/Output Block
IOBLOCKS	IOBLOCKS	TCAM I/O Control Blocks
IOE	ILRIOE	PART I/O Request Element
IOMB	IDAIOMB	I/O Management Block
IOMBXN	IDAAMBXN	IOMB Extension
IOPL	IKJIOPL	Input/Output Parameter List
IOQ	IECDIOQ	I/O Queue Element
IORB	ILRIORB	ASM I/O Request Block
IOSB	IECDIOSB	I/O Supervisor Block
IPIB	IECDIPIB	I/O Supervisor Purge Interface Block
IPLDATA	IEAPXNIP	IPLDATA
IQE	IHAIQE	Interrupt Queue Element
IRT	IECDIRT	I/O Supervisor Recovery Table
ITCRR	ISTITCRR	VTAM Initialization/Termination Component Recovery Record
IWA	IEFVMIWA	Interpreter Work Area
IXSPL	IDAIXSPL	Index Search Parm List
JCLS	IEFJCLS	Job Control Language Set
JCT	IEFAJCTB	Job Control Table
JESCT	IEFJESCT	JES Control Table
JFCB	IEFJFCBN	Job File Control Block
JFCBX	IEFJFCBX	Job File Control Block Extension
JMR	IEFJMR	Job Management Record
JNLPARM	IEFZB507	Journal Write Parameter List
JSCB	IEZJSCB	Job Step Control Block
JSEL	IEFVJSEL	Job Scheduling Entrance List
JSOL	IEFVJSOL	Job Scheduling Options List

Acronym	Mapping Macro	Common Macro
JSUESTPA	IEFZB440	Job/Step Unallocation Estae Exit Parameter Area
JSWA	IEFVJSWA	Job Scheduling Work Area
JSXL	IEFVJSXL	Job Scheduling Exit List
LCA	IEELCA	Log Control Area
LCB	TLCBD	T/P Line Control Block
LCCA	IHALCCA	Logical Configuration Communication Area
LCCAVT	IHALCCAT	Logical Configuration Communication Area Vector Table
LCCW	ISTLCCW	VTAM Logical Channel Command Word
LCH	IECDLCH	Logical Channel Queue Table
LCPB	ISTLCPB	VTAM Logical Channel Program Block
LCT	IEFALLCT	Linkage Control Table
LDA	IHALDA	Local Data Area
LDNCB	ISTLDNCB	VTAM Local Device Node Control Block
LDO	ISTLDO	VTAM Logical Device Order
LDPRM	ISTLDPRM	VTAM Attach/Load Parameter List
LGB	TLGBD	Line Group Block
LGCB	IDAVBPL	Logical Group Control Block
LGE	ILRLGE	Logical Group Entry
LGPFX	ISTLGPFX	VTAM Logon Data Prefix
LGVT	ILRLGVT	Logical Group Vector Table
LLE	IHALLE	Load List Element
LOK	ISTLOK	VTAM Lockword Format
LPDE	IHALPDE	Link Pack Directory Entry
LPMB	IDALPMB	Logical To Physical Mapping Block
LRB	IHALRB	LOGREC Buffer
LSCB	ISTLSCB	VTAM Logical Schedulable Control Block
LSD	IKJLSD	List Source Descriptor
LUNCB	ISTLUNCB	VTAM Logical Unit Node Control Block
LWA	IKJEFLWA	Logon Work Area
MB	IHAMB	Message Buffer DSECT
MCA	IEBMCA	IEBCOPY Communications Area
MCAWSA	MCAWSA	Monitor Call Application Work Save Area
MCCD	MCCD	Monitor Call Class Directory
MCCE	MCCE	Monitor Call Control Element
MCCLE	MCCLE	Monitor Call Class Element
MCEE	MCEE	Monitor Call Event Element
MCHEAD	MCHEAD	Monitor Call Routing Tables Head
MCQE	MCQE	Monitor Call Queue Element
MCRWSA	MCRWSA	Monitor Call Router Work Save Area
MCT	IRAMCT	SRM Storage Management Control Table
MFCOA	IRBMFCOA	MF/1 Common Options Area
MFMVT	IRBMFMVT	MF/1 Measurement Vector Table
MFPCT	IRBMFPCT	MF/1 Program Control Table
MFPMA	IRBMFPMA	MF/1 Program Measurement Area
MFSEL	IRBMFSEL	MF/1 Subtask Elements Table
MFSQU	IRBMFSQU	MF/1 Subtask Queue Anchor
MLCA	ISTMLCA	VTAM Main Line Communication Area
MMB	IEAMMB	Monitor Message Block
MNT	ISTMNT	VTAM Major Node Table
MPL	IEZMPL	Monitor Parameter List
MPST	ISTMPST	VTAM Memory Process Scheduling Table
MQE	IEAMQE	Monitor Queue Element
MQL	ISTMQL	VTAM TPIO Request Queueing Block
MSG	IGFMSG	Message Content Block
MSGTABLE	IKJEFFMT	TSO Message Table Parmlist
MSSCCWB	IEZCWB	Mass Storage System Communicator Command Work Buffer
MSSCDRE	ICBRQUE	Mass Storage System Communicator Delayed Response Element
MSSCIOSB	ICB2IOBX	Mass Storage System Communicator I/O Supervisor Block Extension
MSSCMPL	IEZMSGRQ	Mass Storage System Communicator Message Parameter List
MSSCRBE1	IEZCOVCE	Mass Storage System Communicator SVC 126 Request Block Extension

Acronym	Mapping Macro	Common Macro
MSSCRBE2	IEZDEFVE	Mass Storage System Communicator SVC 126 Request Block Extension
MSSCRBE3	IEZMCRTE	Mass Storage System Communicator SVC 126 Request Block Extension
MSSCRB01	ICBACREL	Mass Storage System Communicator SVC 126 Request Block
MSSCRB02	ICBASDAS	Mass Storage System Communicator SVC 126 Request Block
MSSCRB03	ICBCOVC	Mass Storage System Communicator SVC 126 Request Block
MSSCRB04	ICBCOVC	Mass Storage System Communicator SVC 126 Request Block
MSSCRB05	ICBCOTB	Mass Storage System Communicator SVC 126 Request Block
MSSCRB06	ICBDEFV	Mass Storage System Communicator SVC 126 Request Block
MSSCRB07	ICBTUNE	Mass Storage System Communicator SVC 126 Request Block
MSSCRB08	ICBMCRT	Mass Storage System Communicator SVC 126 Request Block
MSSCRB09	ICBMNTDE	Mass Storage System Communicator SVC 126 Request Block
MSSCRB10	ICBMNTDE	Mass Storage System Communicator SVC 126 Request Block
MSSCRB11	ICBPAIR	Mass Storage System Communicator SVC 126 Request Block
MSSCRB12	ICBPAIR	Mass Storage System Communicator SVC 126 Request Block
MSSCRB13	ICBSUSP	Mass Storage System Communicator SVC 126 Request Block
MSSCRB14	ICBTRACE	Mass Storage System Communicator SVC 126 Request Block
MSSCRB15	ICBVARY	Mass Storage System Communicator SVC 126 Request Block
MSSCRB16	ICBVARY	Mass Storage System Communicator SVC 126 Request Block
MSSCRB17	IEZINIT	Mass Storage System Communicator SVC 126 Request Block
MSSCRB18	IEZMESG	Mass Storage System Communicator SVC 126 Request Block
MSSCRB19	IEZMGP	Mass Storage System Communicator SVC 126 Request Block
MSSCRB20	IEZMVR	Mass Storage System Communicator SVC 126 Request Block
MSSCRB21	IEZRVR	Mass Storage System Communicator SVC 126 Request Block
MSSCRB22	IEZRVVI	Mass Storage System Communicator SVC 126 Request Block
MSSCRB23	IEZSGP	Mass Storage System Communicator SVC 126 Request Block
MSSCVCE	IEZRPLV	Mass Storage System Communicator Volume Control Element
MSVC	IEZVVICB	Mass Storage System Communicator Volume Control Block
MSVIBCDV	IEZBCDV	Mass Storage Volume Inventory Base, Copy, and Duplicate Volume Record Common Part
MSVIBV	IEZBASEV	Mass Storage Volume Inventory Base Volume Record
MSVIC	IEZINDEX	Mass Storage Volume Inventory Cartridge Record
MSVICPU	IEZCPUID	Mass Storage Volume Inventory CPU Record
MSVICV	IEZCOPYV	Mass Storage Volume Inventory Copy Volume Record
MSVIDV	IEZDUPV	Mass Storage Volume Inventory Duplicate Volume Record
MSVIG	IEZGROUP	Mass Storage Volume Inventory Group Record
MSVIGE	IEZGVSNE	Mass Storage Volume Inventory Group Extension Record
MSVIN	IEZNGVR	Mass Storage Volume Inventory Nongroup Record
MVCA	IEFZB433	Mount and Verify Communication Area
MVV	IEHMVV	IEHMOVE Communications Area
NCB	ISTNCB	VTAM Node Control Block
NCSPL	ISTNCSPL	VTAM Network Configuration Services Control Block
NEL	IEFNEL	Interpreter Entrance List
NIB	ISTNIB	VTAM Node Identification Block
NIPMNTPL	IEAPMNIP	NIP Mount Parameter List
NIPOPNPL	IEAPMNIP	NIP Open Parameter List
NIPPAHDR	IEAPPNIP	NIP Parameter Area Header
NIPPAREA	IEAPPNIP	NIP Parameter Area
NIPPTE	IEAPPNIP	NIP Parameter Address Table Entry
NIPSCHDL	IEAPMNIP	NIP Schedule Parameter List
NIPSPE	IEAPMNIP	NIP System Parameter Queue Entry
NIR	ISTNIR	VTAM Node Information Record
NMLPB	ISTNMLPB	VTAM Network Manager Logical Channel Program Block
NSRU	ISTNSRU	VTAM Network Services Request/Response Unit
NVT	IHANVT	NIP Vector Table
NWTOHDR	IEAPMNIP	NIP Write-to-Operator Message Header
NWTORLST	IEAPMNIP	NIP WTOR Parameter List
OCA	ISTOCA	VTAM OPEN/CLOSE Work Area
OCCRR	ISTOCCRR	VTAM OPEN/CLOSE Component Recovery Record
OCEWA	IECDSECS	O/C/EOV Work Area
OCW	ISTOCW	VTAM OPEN/CLOSE Work Element

Acronym	Mapping Macro	Common Macro
OLTCB	OLTCB	On-Line Test Control Block
OPCAVT	TOPCAVTD	Operator Control Address Vector Table
OPCE	TOPCED	Operator Control Element
OPWRK	IDAOPWRK	VSAM OPEN ACB Work Area
ORE	IHAORE	Operator Reply Element
OUCB	IRAOUCB	SRM User Control Block
Swappable Block		
OUXB	IHAOUXB	System Resources Manager User Extension Block
PAB	ISTPAB	VTAM Process Anchor Block
PAPL	IKJPPL	Parse Parameter List
PARAM	IEFZB630	Initiator Parameter List
PARMA	IKJPARMA	Parse Descriptor Element
PARML	IKJEFFIE	Parameter List to TSO FIB Installation Exit
PARMLIST	IKJEFFPT	Internal Parameter List for TSO CANCEL and STATUS Commands
PARMTAB	IEAPPNIP	NIP Parameter Address Table
PART	ILRPART	Paging Activity Reference Table
PAT	ILRPAT	Page Allocation Table
PCB	IHAPCB	Page Control Block
PCBR	IHAPCBR	Page Control Block Root
PCCA	IHAPCCA	Physical Configuration Communication Area
PCCAVT	IHAPCCAT	Physical Configuration Communication Area Vector Table
PCCB	IEFPCCB	Private Catalog Control Block
PCCNTRLS	IEFZB450	Private Catalog Control Block Routine Controls
PCCW	ILRPCCW	Page Channel Command Workarea
PCT	ILRPCT	Performance Characteristics Table
PDI	IEFZB435	Passed Data Set Information
PDS	IHAPDS	Partitioned Data Set Directory Entry
PEB	TPEBD	Process Element Block
PECB	TPECBD	Process Element Control Block
PEWA	TPEWAD	TCAM Process Entry Work Area
PFCRR	ISTPFCRR	VTAM Component Recovery Record Prefix
PFTE	IHAPFTE	Page Frame Table Entry
PFX	ISTPFX	VTAM Prefix for Queue Elements
PGPB	IKJPGPB	PUTGET Parameter Block
PGTE	IHAPGTE	Page Table Entry
PHROA	ISTPHROA	VTAM Parameter Handler Output Area
PICA	IHAPICA	Program Interrupt Control Area
PIE	IHAPIE	Program Interrupt Element
PIRL	IECDPIRL	Purged I/O Restore List
PIU	ISTPIU	VTAM Path Information Unit
PLCPB	ISTPLCPB	VTAM Purge Request Logical Channel Program Block
PLH	IDAPLH	Place Holder Header and Entry
PPL	IECDPPL	Purge Parameter List
PPT	IEFZB610	Program Properties Table Entry
PQE	IHAPQE	Partition Queue Element
PRF	TPRFD	TCAM Buffer Prefix
PROCD	ISTPROCD	VTAM Process Option Definition Block
PSA	IHAPSA	Prefixed Save Area
PSCB	IKJPSCB	Protected Step Control Block
PSCRR	ISTPSCRR	VTAM Port Solicitor Component Recovery Record
PSL	IDAPSL	Page Save List
PST	ISTPST	VTAM Process Scheduling Table
PTPB	IKJPTPB	Putline Parameter Block
PTRS	IEFPTRS	TCB and ASCB Pointers
PVT	IHAPVT	Paging Vector Table
PWA	IGFPWA	Processor Work Area
QAB	ISTQAB	VTAM Queue Anchor Block
QCB	IHAQCB	Queue Control Block (MAJOR/MINOR)
QCBE	TQCBED	TCAM Queue Control Block Extension
QDB	IHAQDB	Queue Descriptor Block
QEL	IHAQEL	Queue Element
QIO	IHAQIO	QMNGRIO Work Area

Acronym	Mapping Macro	Common Macro
QMIOP	IEFQMIOP	QMNGRIO Parameter List
QMPA	IEFQMNGR	Queue Manager Parameter Area
QSR	ILRQSRCD	Quick Start Record
QVOD	IHAQVOD	Queue Verifier Output Data
QVPL	IHAQVPL	Queue Verifier Parameter List
RB	IHARB	Request Blocks
RCA	IHARCA	Recovery Control Area
RCB	RTMRCB	Recording Control Buffer
RCTD	IEARCTD	Region Control Task Data Area
RDCM	IEERDCM	Map Resident Display Control Modules
RDT	ISTRDT	VTAM Resource Definition Table
RECB	TRECBD	TCAM Resource Element Control Block
RESPL	RESPL	Resident Module Parameter List
RH	ISTRH	VTAM Request Header
RIA	ISTRIA	VTAM Recovery Interface Area
RLGB	IKJRLGB	Relogon Buffer
RMCA	IRARMCA	SRM Control Area
RMCT	IRARMCT	SRM Control Table
RMEP	IRARMEP	SRM Entry Point Descriptor
RMEX	IRARMEX	SRM External Entry Point Descriptor Table
RMPL	IHARMPL	Resource Manager Parameter List
RMPT	IRARMPT	SRM Parameter Table
RMS	IGFRMS	RMS Initialization Parameter List
RMSB	IRARMSB	SRM Subroutine Vector Table
RNCA	ISTRNCA	VTAM RN Segment Build Communication Area
RPH	ISTRPH	VTAM Request Parameter List Header
RPL	IFGRPL	Request Parameter List
RPLE	IDARPLE	Request Parameter List Extension
RQE	IECDRQE	Request Queue Element
RRPA	IRARRPA	SRM Recovery Parameter Area
RRPL	IECDSECS	Recovery OPEN/CLOSE/EOV/DADSM Parameter List
RSMHD	IHARSMHD	Real Storage Management Header
RSTWA	IEEVRSWA	Restart Work Area
RTCT	IHARTCT	Recovery Termination Control Table
RTM2WA	IHARTM2A	RTM2 Work Area
RT1W	IHART1W	RT1W Work Area
RVT	IHARVT	Recovery Management Vector Table
RWA	IGFRWA	Recovery Work Area
R1BC	IKJZT301	Broadcast Data Set Record 1
SART	ILRSART	Swap Activity Reference Table
SAT	ILRSAT	Swap Allocation Table
SCA	IHASCA	SPIE Control Area
SCB	IHASCB	STAE Control Block
SCE	IHASCE	SLIP Control Element
SCCW	ILRSCCW	Swap Channel Command Workarea
SCRA	IHASCRA	Supervisor Control Recovery Area
SCT	IEFASCTB	Step Control Table
SCVA	IHASCVA	SLIP Control Element Variable Area
SCVT	IHASCVT	Secondary Communication Vector Table
SDCT	ILRSDCT	Swap Device Characteristics Table
SDT	IEESDT	START Descriptor Table
SDUMP	IHASDUMP	SVC Dump Parameter List
SDVT	ISTSDVT	VTAM Skeletal Destination Vector Table
SDWA	IHASDWA	System Diagnostic Work Area
SDWRK	IHASDWRK	SVC Dump Work Area
SGTE	IHASGTE	Segment Table Entry
SHDR	IHASHDR	SLIP Header
SIOT	IEFASIOT	System Input/Output Table
SLE	SLE	Save List Elements
SMCA	IEESMCA	System Management Facilities Control Area
SMDLR	IHASMDLR	Logical Record for Summary SVC Dump
SMWK	IHASMWK	Summary Dump Work Area

Acronym	Mapping Macro	Common Macro
SNAP	IHASNP	Snap Parameter List
SNT	ISTSNT	VTAM Specific Node Table
SPCT	IHASPCT	Swap Communication Table
SPL	IHASPL	Service Priority List
SPP	IHASPP	SETPRT Parameter List
SPQE	IHASPQE	Subpool Queue Element
SRB	IHASRB	Service Request Block
SRU	ISTSRU	VTAM Standard Request/Response Unit
SSCR	IHJSSCR	Subsystem Checkpoint Record
SSCVT	IEFJSCVT	Subsystem Communications Vector Table
SSIB	IEFJSSIB	Subsystem Identification Block
SSL	IDASSL	Swap Save List
SSOB	IEFJSSOB	Subsystem Options Block
SSRB	IHASSRB	Save Area for SRB
SSVT	IEFJSSVT	Subsystem Vector Table
STAES	STAES	STAE Parameter Table
STCB	TSTCBD	Subtask Control Block
STCPARM	IEEZB800	STC Internal Parameter Area
STEPL	IEFZB622	Initiator STAE Exit Parameter List
STGST	IRBSTGST	MF/1 Global Storage Table
STMMV	IRBSTMMV	MFROUTER Measurement Vector Table
STOWPARM	IHASTOW	STOW Parameter List
STPB	IKJSTPB	Stack Parameter Block
STPL	IKJSTPL	STACK Parameter List
STPRT	IRBSTPRT	MF/1 Program Resource Table
STRVT	IRBSTRVT	MF/1 Resource Vector Table
STSCT	IRBSTSCT	MF/1 Supervisor State Control Table
STSGT	IRBSTSGT	MF/1 Storage Resource Table
STSMA	IRBSTSMA	MF/1 Supervisor State Measurements Area
STWVT	IRBSTWVT	MF/1 Workload Vector Table
SVCTABLE	IHASVC	SVC Table Entry
SWAE	IEEZB801	STC Parameter List
SWAIC	IEFZB436	SWA Manager Interface Controls
S99PARMS	IEFZB4D0	Dynamic Allocation (SVC 99) Parameter List
TABL	IHJDSTAB	Data Set Table Entry
TAIE	IKJTAIE	Terminal Attention Interruption Element
TAXE	IKJTAXE	Terminal Attention Exit Element
TCB	IKJTCB	Task Control Block
TCCW	IECDTCCW	Translation Control Block
TCOMTAB	TCOMTAB	Test Communication Table
TCT	IEFTCT	SMF Timing Control Table
TCXD	TTCXD	TCAM CVT Extension
TDCM	IEETDCM	Pageable DCMs
TDEB	TDEBD	T/P Data Extent Block
TECB	TTECB	TCAM Test Event Control Block
TEXTUNIT	IEFZB4D1	Dynamic Allocation Text Unit
TH	ISTTH	VTAM Transmission Header
THB	IGFTHB	Threshold Block
TIE	ISTTIE	VTAM TOLTEP Interface Element
TIOB	TIOBD	T/P, I/O Block
TIOCBUF	IKJTIOCB	TIOC Buffer Prefix
TIOCRPT	IKJTIOCP	TIOC Reference Pointer Table
TIOT	IEFTIOT1	Task Input/Output Table
TMPWA	IKJTMPWA	Terminal Monitor Program Work Area
TMRB	IEFZB424	TIOT Manager Request Block
TNT	TTNTD	TCAM Terminal Name Table
TPARTBLE	ILRTPARB	Temporary Page Address Reference Table
TPC	IEAVVTPC	Timer Supervision Work Area
TPCB	TPCBD	TCAM Process Control Block
TPL	IKJTPL	TEST Parameter List
TQCB	TQCBD	TCAM Queue Control Block
TQE	IHATQE	Timer Queue Element

Acronym	Mapping Macro	Common Macro
TRHDR	IEAPXNIP	System Trace Header
TRM	TTRMD	TCAM Terminal Table Entry
TSB	IKJTSB	TSB Terminal Status Block
TSCB	TSCBD	TCAM Station Control Block
TSTCWORK	TSTCWORK	TEST Work Area
TTCB	TTCBD	TCAM Task Control Block
TTE	NONE	Trace Table Entry
TXTFT	IEFTXTFT	Internal Text Format
UCB	IEFUCBOB	Unit Control Block
UCBTYP	NONE	Unit Control Block Type Bytes
UCDX	IEEUCDX	Data Management and I/O Supervisor Control Blocks
UCM	IEECUCM	Unit Control Module
UECB	ISTUECB	VTAM User Exit Control Block
UNALCC	IEFZB443	Unallocate Catalog Controls
UNITTAB	IECDSECS	Unit Table Work Area (O/C/EOV)
UPCON	IEBUPCON	IEBUPDTE Communications Area
UPT	IKJUPT	User Profile Table
USDIR	IKJZT304	Broadcast Mail Directory Record
USERLAB	IECDSECS	User Label Work Area
USERTOT	IECDSECS	User Totaling Facility Save and Work Area
USMSG	IKJZT305	Broadcast Mail Message Record
UTILWORK	IKJEBEUW	EDIT Access Method Work Area
VAMBLT	IDAVAT	Valid AMBL Table
VAT	IEFZB611	Virtual Address Table
VBPPL	IDDVBPPL	VBP Parameter List
VCB	IHAVCB	Virtual I/O Control Block
VDSCB	IDDVDSCB	Virtual Data Set Control Block
VGTT	IDARTMAC	VSAM Global Termination Table
VIOT	IDAVIOT	Valid IOMB Table
VMT	IDAVMT	Volumes Mounted Table
VMVESTPA	IEFZB452	Volume Mount and Verify Estae Exit and FRR Parameter Area
VMVRB	IEFZB431	Volume Mount and Verify Request Block
VRWPQEL	IHAWPQEL	V=R Wait Post Queue Element
VSL	IHAVSL	Virtual Sub-area List
VSRT	IDAVSRT	VSAM Shared Resources Table
VTRACK	IDDTRACK	Virtual Track Buffer (also known as VIO buffer and window)
VUNT	IEFZB423	Volunit Table Entry
VUT	IEFZB438	Volume Unload Table
VYCRR	ISTVYCRR	VTAM VARY Component Recovery Record
WAMT	IRAWAMT	Workload Activity Measurement Table
WAX	IDAWAX	Work Area for AIX
WICB	IDDWICB	VIO Control Block
WKE	ISTWKE	VTAM Work Element Chain Field
WMST	IRAWMST	Workload Manager Specifications Table
WORKAREA	WORKAREA	OS/VS Catalog Management WORKAREA
WPL	IEZWPL	WTO/WTOR/MLWTO/WTP Parameter List Definition
WQE	IHAWQE	Write-To-Operator Queue Element
WSAVTC	IHAWSAVT	CPU Work/Save Area Vector Table
WSAVTG	IHAWSAVT	Global Work/Save Area Vector Table
WSAVTL	IHAWSAVT	Local Work/Save Area Vector Table
WWB	IHACTM	Write To Operator Wait Block
XDBA	IECDXDBA	IOS EXCP Debugging Area
XPTE	IHAXPTE	External Page Table Entry
XSA	IEEXSA	Extended Save Area
XTLST	IHAXTLST	Extent List
XV	IHACTM	SVC 35 Extended Save Area
YSTAK	IHAYSTAK	FRR Stack Attributes
ZB502	IEFZB502	SWA Block Prefix
ZB831	IKJZB831	Parmlist for IKJCB831
ZCRR	ISTZCRR	VTAM TPIOS Component Recovery Record
ZFSAV	ISTZFSAV	VTAM TPIOS Fixed Save Area for TPZLOCK
ZFSVT	ISTZFSVT	VTAM TPIOS Fixed Services Vector Table

Acronym	Mapping Macro	Common Macro
ZIBUF	ISTZIBUF	VTAM TPIOS Inbound Buffer
ZLBUF	ISTZLBUF	VTAM TPIOS Local Buffer
ZLBVT	ISTZLBVT	VTAM LCCW/BTU Translation Vector Table
ZLFVT	ISTZLFVT	VTAM Local 3270 Fixed Services Vector Table
ZOBUF	ISTZOBUF	VTAM TPIOS Outbound Buffer
ZPSVT	ISTZPSVT	VTAM TPIOS Pageable Services Vector Table
ZRCVT	ISTZRCVT	VTAM Request Completion Vector Table
ZSAVE	ISTZSAVE	VTAM TPIOS Save Area Format
Z19SV	ISTZ19SV	VTAM TPIOS 19-Word Save Area for TPZLOCK

Data Area Usage Table

The data area usage table is a cross-reference between data area names and scheduler and supervisor module names. All the data area and module names are listed in alphameric order on the left. Depending on whether the table entry is a module or a data area, the following information is given.

- For a data area, the table lists all the modules that use it.
- For a module, the table lists all the data areas the module uses.

The abbreviations for access are as follows:

R - Read only.
W - Write only.
RW -Read and write.
M - The data area is used in a macro instruction call as a positional or keyword parameter; the module may update the data area.
P - The data area is passed as a parameter on a PL/S statement; the module may update the data area.
C - The data area is referenced with a compare instruction, (equivalent to R).
D - Definition.
E - The data area name is used as an equate.
F - Absolute.

The data area usage table is on microfiche for all subsequent updates to Release 3.7. There will be no hard copy version of these updated pages.

Symbol Usage Table

This table lists various data field symbolic names, giving for each one, the acronym of the data area in which it appears, and the object modules which update the field. The data area acronyms are listed with their common and mapping macro names in another table in this section.

The symbol usage table is on microfiche for all subsequent updates to release 3.7. There will be no hard copy version of these updated pages.

Section 6: Diagnostic Aids

This section contains information that can be used to diagnose problems in scheduler and supervisor programs:

- ABEND codes -- names the object modules related to each code.
- Message and wait state codes -- names the object modules related to each message or wait state code:
  - Modules that detect the condition requiring the message or wait state code.
  - Modules that issue the message or wait state code.
  - Modules that contain the message text.
- Return code table -- lists the return codes set by each object module.
- Register usage table -- shows register contents at entry to and exit from each object module.
- Miscellaneous diagnostic aids -- contains various tables each associated with a particular subcomponent. The tables, arranged by subcomponent, contain information such as ABEND codes, reason codes, and post codes.

ABEND Codes List

The following ABEND codes are issued by components of the scheduler and supervisor. The table lists the object module that issues each code. For explanations, see *OS/VS Message Library: System Codes*.

Code	Module Detecting	Module Issuing
028	IEAVAMSI	IEAVAMSI
	IEAVFXLD	IEAVFXLD
	IEAVPIOI	IEAVPIOI
	IEAVPSI	IEAVPSI
	IEAVRCF	IEAVRCF
	IEAVRCV	IEAVRCV
	IEAVSWIN	IEAVSWIN
	ILRIOC00	IEAVEPC
047	IEAVESVC	IEAVESVC
	IGC109	IGC109
	IGC116	IGC116
	IGC122	IGC122
072	IEAVEDS0	IEAVEDS0
073	IEAVELK	IEAVELK
074	IEAVELKR	IEAVELKR
076	IEAVEMCR	IEAVEMCR
077	IEAVMNTR	IEAVMNTR
078	IEAVAR00	IEAVAR00
	IEAVAR02	IEAVAR02
	IEAVAR03	IEAVAR03
079	IEAVAR04	IEAVAR04
07A	IEAVAR00	IEAVAR00
	IEAVAR02	IEAVAR02
07B	IEAVEDR	IEAVEDR
	IEAVERI	IEAVERI
	IEAVERP	IEAVERP
07C	IEAVESPR	IEAVESPR
07E	IEEVDEV	IEEVDEV
081	IEAVEQR	IEAVEQR
	IEAVSQA	IEAVSQA
082	IEAVPRT0	IEAVGPRR
083	ILRSLSQA	ILRSLSQA
	ILRSRT	ILRSRT
084	ILRCMP	ILRCMP
	ILRPTM	ILRPTM
	ILRSWPDR	ILRSWPDR
085	ILRSAV	ILRSAV
086	ILRVSAMI	ILRVSAMI
087	ILRSAV	ILRSAV
	ILRACT	ILRACT
	ILRRLG	ILRRLG
	ILRVSAMI	ILRVSAMI
	ILRTMRLG	ILRTMRLG
0B0	IEFQB550	IEFQB550
	IEFQB555	IEFQB555
	IEFQB580	IEFQB580
0B1	IEFJCNTL	IEFJCNTL
	IEFJJCLS	IEFJJCLS
	IEFJACTL	IEFJACTL
	IEFJWTOM	IEFJWTOM
0B2	IEFJJCLS	IEFJJOBS
0B3	IEFJACTL	IEFJACTL
0B4	IEFJJNCTL	IEFJJNCTL
0B5	IEFJJNCTL	IEFJJNCTL
0B7	IEEMPDM	IEEMPDM
	IEEMPS03	IEEMPS03
	IEEMPVST	IEEMPVST
	IEEVPTH	IEEVPTH
0B8	IEFJSWT	IEESB605
	IEESB601	IEESB605
0B9	IEESB605	IEESB605
0BA	IEFSD162	IEFSD162
	IEFSD166	IEFSD166
	IEFSD605	IEFSD605
0BB	IEFSD263	IEFSD263
0Cx (where x=1-F)	IEAVEPC	IEAVEPC
	IEFAB4FC	IEFAB4FC

Code	Module Detecting	Module Issuing
0D2	IEAVEPC	IEAVEPC
0D3	IEAVEPC	IEAVEPC
0F8	IEAVESVC	IEAVESVC
0F9	IEAVESVC	IEAVESVC
0FA	IEAVESVC	IEAVESVC
0FB	IEAVEPC	IEAVEPC
101	IEAVGM00	IEAVGM00
	IEAVSY50	IEAVSY50
102	IEAVGM00	IEAVGM00
	IEAVSY50	IEAVSY50
104	IEAVGM00	IEAVGM00
106	IEAVLK01	IEAVLK00
10A	IEAVGM00	IEAVGM00
10B	IEAVRT01	IEAVRT01
10D	IEAVTRTC	IEAVTRTC
10E	IEAVTB00	IEAVTB00
122	IEE3703D	IEE3703D
128	IEAVTB00	IEAVTB00
12A	IEAVEAT0	IEAVEAT0
12C	IEAVECH0	IEAVECH0
12E	IEAVRT00	IEAVRT00
12F	IEAVRT00	IEAVRT00
130	IEAVENQ1	IEAVENQ1
133	IEAVAD00	IEAVAD00
	IEAVTSDX	IEAVTSDX
138	IEAVENQ1	IEAVENQ1
13C	IGC00060	IGC00060
13E	IEAVEED0	IEAVEED0
14F	IEAVSETS	IEAVSETS
153	IEEMB827	IEEMB827
157	IEAVXDOM	IEAVXDOM
15F	IRARMINT	IRARMINT
16B	IEAVMODE	IEAVMODE
16D	IGC109	IGC109
	IGC116	IGC116
	IGC122	IGC122
171	IEAVFREE	IEAVPSI
	IEAVFXLD	IEAVPSI
	IEAVOUT	IEAVPSI
	IEAVPSI	IEAVPSI
	IEAVRELS	IEAVPSI
177	IEAVTEST	IEAVTEST
178	IEAVGM00	IEAVGM00
17A	IEAVEVT0	IEAVEVT0
17B	IEAVEPDQ	IEAVEPDQ
17D	IEAVEVT0	IEAVEVT0
1FC	IEAVESVR	IEAVESVR
1FD	IRBMFEVT	IRBMFEVT
201	IEAVSY50	IEAVSY50
202	IEAVSY50	IEAVSY50
206	IEAVLK03	IEAVLK03
20B	IEAVRT01	IEAVRT01
20D	IEAVTRTE	IEAVTRTE
20E	IEAVTB00	IEAVTB00
222	IEE3703D	IEE3703D
228	IEAVTB00	IEAVTB00
22A	IEAVEAT0	IEAVEAT0
22C	IEAVECH0	IEAVECH0
22E	IEAVRT00	IEAVRT00
22F	IEAVRT00	IEAVRT00
230	IEAVENQ1	IEAVENQ1
233	IEAVAD00	IEAVAD00
	IEAVTSDT	IEAVTSDT
	IEAVTSDX	IEAVTSDX
238	IEAVENQ1	IEAVENQ1
23E	IEAVEED0	IEAVEED0
25F	IRARMERR	IRARMINT
260	IEAVAX00	IEAVAX00

Code	Module Detecting	Module Issuing
26D	IRBMFDWP	IRBMFDWP
	IRBMFIWK	IRBMFIWK
271	IEAVPSI	IEAVPSI
2F3	IEFIB605	IEFIB605
2FC	IEAVEIOR	IEAVEIOR
301	IEAVSY50	IEAVSY50
304	IEAVGPRR	IEAVGPRR
305	IEAVGM00	IEAVGM00
306	IEAVLK00	IEAVLK00
	IEAVLK01	IEAVLK00
30A	IEAVGM00	IEAVGM00
30E	IEAVTB00	IEAVTB00
322	IEATLEXT	IEATLEXT
328	IEAVTB00	IEAVTB00
338	IEAVENQ1	IEAVENQ1
33E	IEAVEED0	IEAVEED0
35F	IEFSD263	IEFSD263
378	IEAVGM00	IEAVGM00
37A	IEAVEVT0	IEAVEVT0
37D	IEAVEVT0	IEAVEVT0
3FC	IEAVEE1R	IEAVEE1R
	IEAVEE2R	IEAVEE2R
	IEAVEE3R	IEAVEE3R
402	IEAVSY50	IEAVSY50
406	IEAVLK00	IEAVLK00
	IEAVLK01	IEAVLK00
40A	IEAVGM00	IEAVGM00
42A	IEAVEAT0	IEAVEAT0
430	IEAVENQ1	IEAVENQ1
438	IEAVENQ1	IEAVENQ1
43E	IEAVEED0	IEAVEED0
478	IEAVGM00	IEAVGM00
47A	IEAVEVT0	IEAVEVT0
47D	IEAVEVT0	IEAVEVT0
4FC	IEAVEPCR	IEAVEPCR
502	IEAVSY50	IEAVSY50
504	IEAVGM00	IEAVGM00
505	IEAVGM00	IEAVGM00
522	IEATLEXT	IEATLEXT
52A	IEAVEAT0	IEAVEAT0
530	IEAVENQ1	IEAVENQ1
53E	IEAVEED0	IEAVEED0
57D	IEAVEVT0	IEAVEVT0
5FC	IEAVERER	IEAVERER
604	IEAVGM00	IEAVGM00
605	IEAVGM00	IEAVGM00
622	IKJEFLJ	IEFSD263
(See reason #1 in VS2 System Codes.)		
	IKJEFLG	IKJEFLG
(See reason #3 in VS2 System Codes.)		
630	IEAVENQ1	IEAVENQ1
638	IEAVENQ1	IEAVENQ1
67D	IEAVEVT0	IEAVEVT0
6FC	IEAVEPC	IEAVEPC
700	IGC109	IGC109
	IGC116	IGC116
	IGC122	IGC122
(See also ABEND codes in Diagnostic Aids chapter of OS/VS2 I/O Supervisor Logic.)		
702	IEAVSV50	IEAVSY50
704	IEAVGM00	IEAVGM00
705	IEAVGM00	IEAVGM00
706	IEAVLK01	IEAVLK00
70A	IEAVGM00	IEAVGM00
72A	IEAVEAT0	IEAVEAT0

Code	Module Detecting	Module Issuing
730	IEAVENQ1	IEAVENQ1
738	IEAVENQ1	IEAVENQ1
778	IEAVGM00	IEAVGM00
77D	IEAVEVT0	IEAVEVT0
804	IEAVGM00	IEAVGM00
806	IEAVLK00	IEAVLK00
	IEAVLK01	IEAVLK00
80A	IEAVGM00	IEAVGM00
822	IEFSD263	IEFSD263
82A	IEAVEAT0	IEAVEAT0
838	IEAVENQ1	IEAVENQ1
878	IEAVGM00	IEAVGM00
87D	IEAVEVT0	IEAVEVT0
905	IEAVGM00	IEAVGM00
906	IEAVLK00	IEAVLK00
90A	IEAVGM00	IEAVGM00
922	IEFIB621	N/A
92A	IEAVEAT0	IEAVEAT0
978	IEAVGM00	IEAVGM00
A03	IEAVTSKT	IEAVTSKT
A05	IEAVGM00	IEAVGM00
A06	IEAVLK00	IEAVLK00
A0A	IEAVGM00	IEAVGM00
A23	IEAVMFRR	IEAVMFRR
A78	IEAVGM00	IEAVGM00
B04	IEAVGM00	IEAVGM00
B05	IEAVGM00	IEAVGM00
B0A	IEAVGM00	IEAVGM00
B23	IEAVSTAA	IEAVSTAA
B78	IEAVGM00	IEAVGM00
C0D	IEAVAMSI	IEAVAMSI
	IEAVCSEG	IEAVCSEG
	IEAVDLAS	IEAVDLAS
	IEAVDSEG	IEAVDSEG
	IEAVEQR	IEAVEQR
	IEAVESC0	IEAVESC0
	IEAVFREE	IEAVFREE
	IEAVFXLD	IEAVFXLD
	IEAVGFA	IEAVGFA
	IEAVINV	IEAVINV
	IEAVIOCP	IEAVIOCP
	IEAVOUT	IEAVOUT
	IEAVPIOI	IEAVPIOI
	IEAVPIOP	IEAVPIOP
	IEAVPIX	IEAVPIX
	IEAVPSI	IEAVPSI
	IEAVRCF	IEAVRCF
	IEAVRELS	IEAVRELS
	IEAVRFR	IEAVRFR
	IEAVSOUT	IEAVSOUT
	IEAVSQA	IEAVSQA
	IEAVSWIN	IEAVSWIN
	IEAVTERM	IEAVTERM
	ILRSLSQA	ILRSLSQA
	ILRGOS	ILRGOS
	ILRTERMR	ILRTERMR
	ILRSRBC	ILRSRBC
D05	IEAVGM00	IEAVGM00
D0A	IEAVGM00	IEAVGM00
D0D	IEAVTRTE	IEAVTRTE
D23	IEAVVWTO	IEAVVWTO
D78	IEAVGM00	IEAVGM00
E03	IEAVTSKT	IEAVTSKT
E23	IEAVVRP2	IEAVVRP2

Messages and Wait State Codes

The following messages and wait state codes are issued by components of the scheduler and supervisor. For an explanation of these codes, see *OS/VS Message Library: VS2 System Codes,* GC38-1008 For wait state codes issued during system initialization, refer to *OS/VS2 System Initialization Logic,* SY28-0623

Note: The wait state codes appear at the beginning of the following list of messages and codes.

Message ID	Module Detect	Module Issuing	Module Containing
X'014'	IEAVEPC	IGFPTERM	
X'01A'	IEEVDUMY	IGFPTERM	
X'01B'	IEAVTSLP	IEESTPRS	
X'01C'	IEAVESPR	IGFPTERM	
X'024'	IGFPTREC	IGFPTREC	
X'02E'	ILRMSG00	ILRMSG00	
X'03C'	ILRMSG00	ILRMSG00	
X'050'	IEAVTACR	IGFPTERM	
X'051'	IEAVTCR1	IGFPTERM	
X'052'	IEAVTCR1	IGFPTERM	
X'101'	IEAVGM00	IEAVGM00	
X'102'	IEAVGM00	IEAVGM00	
X'A01'	IGFPMCIH	IGFPMCIH	
X'A23'	IGFPMCIH	IGFPMCIH	
X'A24'	IGFPMCIH	IGFPMCIH	
X'A25'	IGFPMCIH	IGFPMCIH	
X'A26'	IGFPMCIH	IGFPMCIH	
X'CCC'	IEEMPS03	IEESTPRS	
IEA030I	IEAVTABD	IEAVTABD	IEAVTABD
IEA107I	IEAVNP05	IEAVNP05	IEAVNP05
IEA108I	IEAVNP05	IEAVNP05	IEAVNP05
IEA109I	IEAVNP05	IEAVNP05	IEAVNP05
IEA152I	IEAVNPA1	IEAVNPM2	IEAVNPA1
IEA153I	IEAVNPA1	IEAVNPM2	IEAVNPA1
IEA208I	IEAVNP05	IEAVNP05	IEAVNP05
	IEAVNP13	IEAVNPM2	IEAVNP13
IEA300I	IEAVNP05	IEAVNP05	IEAVNP05
	IEAVNP13	IEAVNPM2	IEAVNP13
IEA301I	IEAVNP05	IEAVNP05	IEAVNP05
	IEAVNP13	IEAVNPM2	IEAVNP13
IEA322A	IEAVNPA1	IEAVNPM2	IEAVNPA1
IEA326I	IEAVNP05	IEAVNP05	IEAVNP05
IEA330A	IEAVNPA1	IEAVNPM2	IEAVNPA1
IEA332A	IEAVNPA1	IEAVNPM2	IEAVNPA1
IEA340I	IEAVNP05	IEAVNP05	IEAVNP05
IEA350I	IEAVNP05	IEAVNP05	IEAVNP05
IEA351I	IEAVNP05	IEAVNP05	IEAVNP05
IEA352I	IEAVNP05	IEAVNP05	IEAVNP05
IEA353I	IEAVNP05	IEAVNP05	IEAVNP05
IEA354I	IEAVNP05	IEAVNP05	IEAVNP05
IEA356I	IEAVNP05	IEAVNP05	IEAVNP05
IEA357I	IEAVNP05	IEAVNP05	IEAVNP05
IEA363I	IEAVNP05	IEAVNP05	IEAVNP05
IEA404A	IEAVMQWR	IEAVMQWR	IEAVMQWR
IEA405E	IEAVMQWR	IEAVMQWR	IEAVMQWR
IEA406I	IEAVMQWR	IEAVMQWR	IEAVMQWR
IEA410I	IECVRSTI	IECVRSTI	
IEA700I	IEAVGM00	IEAVTPMT	IEAVTPMT
IEA703I	IEAVLK00	IEAVLK00	IEAVLK00
IEA801I	IEAVENQ1	IEAVENQ1	IEAVENQ1
IEA803I	IEAVENQ1	IEAVENQ1	IEAVENQ1
IEA807I	IEAVLK00	IEAVLK00	IEAVLK00
IEA856W	IEAVTACR	IEAVTACR	IEAVTACR
IEA857W	IEAVTCR1	IGFPTERM	IEAVTCR1
IEA862I	IEAVAR00	IEAVAR00	IEAVAR00
IEA863I	IEAVNP13	IEAVNPM2	IEAVNP13
IEA864I	IEAVNP13	IEAVNPM2	IEAVNP13
IEA865I	IEAVNP05	IEAVNP05	IEAVNP05
IEA868I	IEAVNP13	IEAVNPM2	IEAVNP13
IEA875I	IEAVTSDI	IEAVNPM2	IEAVTSDI
IEA876I	IEAVTSDI	IEAVNPM2	IEAVTSDI
IEA877A	IEAVTSDI	IEAVNPM2	IEAVTSDI
IEA878I	IEAVTSDI	IEAVNPM2	IEAVTSDI
IEA879A	IEAVTSDI	IEAVNPM2	IEAVTSDI
IEA880I	IEAVTSDI	IEAVNPM2	IEAVTSDI
IEA881I	IEAVTSDI	IEAVNPM2	IEAVTSDI
IEA882A	IEAVTSDI	IEAVNPM2	IEAVTSDI
IEA883I	IEAVTSDI	IEAVNPM2	IEAVTSDI
IEA884I	IEAVTABI	IEAVNPM2	IEAVTABI
IEA885I	IEAVTABI	IEAVMPM2	IEAVTABI
IEA886A	IEAVRTOD	IEAVRTOD	IEAVRTOD
IEA887A	IEAVRTOD	IEAVRTOD	IEAVRTOD
IEA888A	IEAVRTOD	IEAVRTOD	IEAVRTOD
IEA889A	IEAVRTOD	IEAVRTOD	IEAVRTOD
IEA890I	IEAVEMCR	IEAVEMCR	IEAVEMCR
	IEAVEMIN		
IEA891I	IEAVNP09	IEAVNP09	IEAVNP09
IEA892I	IEAVNP09	IEAVNP09	IEAVNP09
IEA897I	IEAVNPA6	IEAVNPM2	IEAVNPA6
IEA898I	IEAVRTI1		
	IEAVRTOD	IEAVRTOD	IEAVRTOD
IEA899I	IEE6503D	IEE6503D	IEE6503D
IEA903A	IEE6603D	IEE6603D	IEE6603D
IEA904I	IEAVNPA1	IEAVNPM2	IEAVNPA1
IEA905I	IEAVNPA1	IEAVNPM2	IEAVNPA1
IEA906A	IEAVNP08	IEAVNP08	IEAVNP08
	IEAVNP13	IEAVNPM2	IEAVNP03
IEA907I	IEAVNP08	IEAVNP08	IEAVNP08
IEA908I	IEAVNP08	IEAVNP08	IEAVNP08
IEA909A	IEAVNP08	IEAVNP08	IEAVNP08
IEA911I	IEAVAD00	IEAVAD00	IEAVAD00
IEA912I	IEAVTABD	IEAVTABD	IEAVTABD
IEA946W	IEAVGM00	IGFPTERM	IEAVGM00
IEA959I	IEAVLK03	IEAVLK03	IEAVLK03
IEA960I	IEAVENQ1	IEAVENQ1	IEAVENQ1
IEA961I	IEAVENQ1	IEAVENQ1	IEAVENQ1
IEA962A	IEAVMQR0	IEAVMQR0	IEAVMQR0
IEA963A	IEAVMQR0	IEAVMQR0	IEAVMQR0
IEA964I	IEAVSWCH	IEAVSWCH	IEAVSWCH
	IGC0007B	IGC0007B	IGC0007B
IEA988I	IEAVGFA	IEAVPREF	IEAVPREF
IEA992I	IEAVTSLP	IEAVTSLP	IEAVTSLP
IEA993I	IEAVTABD	IEAVTABD	IEAVTABD
IEA994A	IEAVTSDH	IEAVTSDH	IEAVTSDH
IEA994E	IEAVTSDH	IEAVTSDC	IEAVTSDC
IEA999W	IEAVEPC	IEAVEPC	IEAVEPC
IEE019I	IEE1603D	IEE0503D	IEE0503D
IEE023I	IEE1603D	IEE0503D	IEE0503D
IEE025I	IEE3603D	IEE3603D	IEE3603D
IEE026I	IEE1603D	IEE0503D	IEE0503D
	IEE5703D	IEE0503D	IEE0503D
IEE032I	IEE1603D	IEE0503D	IEE0503D
IEE033I	IEE1603D	IEE0503D	IEE0503D
IEE035I	IEE3603D	IEE3603D	IEE3603D
IEE037I	IEEMB803	IEEMB807	IEEMB807
	IEEMB806	IEEMB807	IEEMB807
IEE041I	IEEMB803	IEEMB807	IEEMB807
IEE043I	IEEMB803	IEEMB807	IEEMB807
IEE050A	IEEMB825	IEEMB825	IEEMB826
IEE070I	IEEMPDM	IEEMPDM	IEEMPDM
IEE071I	IEEMPDM	IEEMPDM	IEEMPDM
IEE073I	IEEMPDM	IEEMPDM	IEEMPDM
IEE078I	IEE9403D	IEE0503D	IEE0503D
IEE084I	IEE1403D	IEE0503D	IEE0503D
	IEE3103D	IEF3103D	IEE3103D
	IEE3603D	IEE3603D	IEE3603D
IEE094D	IEECB866	IEECB866	IEECB866
IEE102I	IEECB800	IEECB801	IEECB801
IEE110I	IEE2903D	IEE2903D	IEE2903D
IEE111I	IEE2903D	IEE2903D	IEE2903D
IEE121I	IEESB605	IEEVSMSG	IEEVSMSG
IEE122I	IEESB605	IEEVSMSG	IEEVSMSG
IEE124I	IEESB605	IEEVSMSG	IEEVSMSG
IEE130I	IEEC2740	IEEC2740	IEEC2740
IEE132I	IEESB605	IEEVSMSG	IEEVSMSG
IEE134I	IEESB605	IEEVSMSG	IEEVSMSG

Message ID	Module Detect	Module Issuing	Module Containing
IEE135I	IEESB605	IEEVSMSG	IEEVSMSG
IEE136I	IEE3503D	IEE3503D	IEE3503D
IEE141A	IEAVSWCH	IEAVSWCH	IEAVSWCH
IEE142I	IEAVSWCH	IEAVSWCH	IEAVSWCH
IEE143I	IEAVSWCH	IEAVSWCH	IEAVSWCH
IEE147I	IEEMB804	IEEMB804	IEEMB804
IEE150I	IEECVETA	IEECVETD	IEECVETD
	IEE6903D	IEE5603D	IEE5603D
IEE151I	IEECVET4	IEECVETE	IEECVETE
	IEECVET6	IEECVETE	IEECVETE
	IEECVET8	IEECVETE	IEECVETE
	IEE6703D	IEE5603D	IEE5603D
	IEE6703D	IEE5903D	IEE5903D
IEE152I	IEECVETP	IEECVETP	IEECVETP
	IEECVETU	IEECVETU	IEECVETU
IEE153I	IEECVET4	IEECVETE	IEECVETE
IEE154I	IEECVETF	IEECVETD	IEECVETD
IEE156I	IEECVETA	IEECVETD	IEECVETD
	IEE6303D	IEE5603D	IEE5603D
	IEE6303D	IEE5903D	IEE5903D
	IEE6403D	IEE5603D	IEE5603D
	IEE6403D	OEE5903D	IEE5903D
	IEE6703D	IEE5603D	IEE5603D
	IEE6703D	IEE5903D	IEE5903D
	IEE6803D	IEE5603D	IEE5603D
	IEE6803D	IEE5903D	IEE5903D
	IEE6903D	IEE5603D	IEE5603D
	IEE7503D	IEE5603D	IEE5603D
	IEE7503D	IEE5903D	IEE5903D
	IEE7703D	IEE5603D	IEE5603D
	IEE7803D	IEE5603D	IEE5603D
	IEE7803D	IEE5903D	IEE5903D
IEE157I	IEECVET6	IEECVETD	IEECVETD
	IEECVET8	IEECVETD	IEECVETD
IEE158I	IEECVET9	IEECVETD	IEECVETD
	IEECVFTB	IEECVETD	IEECVETD
	IEE6703D	IEE5603D	IEE5603D
	IEE6703D	IEE5903D	IEE5903D
IEE159E	IEECVETJ	IEECVETH	IEECVETH
		IEECVETP	IEECVETP
		IEECVETR	IEECVETR
		IEECVETU	IEECVETU
	IEECVFTL	IEECVETH	IEECVETH
		IEECVETP	IEECVETP
		IEECVETR	IEECVETR
		IEECVETU	IEECVETU
	IEECVFT2	IEECVETH	IEECVETH
		IEECVETP	IEECVETP
		IEECVETR	IEECVETR
		IEECVETU	IEECVETU
IEE160I	IEECVET1	IEECVETD	IEECVETD
	IEECVET2	IEECVETD	IEECVETD
	IEECVET3	IEECVETD	IEECVETD
	IEECVET9	IEECVETD	IEECVETD
	IEECVFTL	IEECVETD	IEECVETD
	IEECVFT2	IEECVETD	IEECVETD
IEE161I	IEECVETA	IEECVETD	IEECVETD
IEE162I	IEE10110	IEE10110	IEE10110
		IEE11110	IEE11110
		IEE12110	IEE12110
IEE163I	IEECVETA	IEECVETD	IEECVETD
	IEECVFTR	IEECVETD	IEECVETD
IEE164I	IEECVETF	IEECVETE	IEECVETE
IEE167E	IEECVETR	IEECVETR	IEECVETR
IEE170E	IEECVETC	IEECVETE	IEECVETE
IEE171E	IEECVETC	IEECVETE	IEECVETE
IEE250I	IEEXEDNA	IEEXEDNA	IEEXEDNA
IEE298I	IEE0403D	IEE0503D	IEE0503D
IEE299I	IEE4203D	IEE4203D	IEE4203D
	IEE5703D	IEE0503D	IEE0503D
IEE300I	IEE4203D	IEE4203D	IEE4203D
IEE301I	IEE3703D	IEE0503D	IEE0503D
IEE302I	IEEVPTH	IEEVPTH	IEEVPTH
	IEE3103D	IEE3103D	IEE3103D
	IEE4603D	IEE4603D	IEE4603D
IEE303I	IEEVPTH	IEEVPTH	IEEVPTH
	IEE3103D	IEE3103D	IEE3103D
	IEE4603D	IEE4603D	IEE4603D
IEE304I	IEEMB810	IEE0503D	IEE0503D
IEE305I	IEEMB815	IEE2103D	IEE2103D
	IEE0403D	IEE0503D	IEE0503D
	IEE0603D	IEE0503D	IEE0503D
	IEE0803D	IEE0503D	IEE0503D
	IEE1403D	IEE0503D	IEE0503D
	IEE2903D	IEE0503D	IEE0503D
	IEE3503D	IEE0503D	IEE0503D
	IEE3703D	IEE0503D	IEE0503D
	IEE5503D	IEE0503D	IEE0503D
	IEE7103D	IEE0503D	IEE0503D
	IEE7503D	IEE0503D	IEE0503D
IEE306I	IEECB866	IEE0503D	IEE0503D
	IEEMB810	IEE0503D	IEE0503D
	IEE0603D	IEE0503D	IEE0503D
	IEE4403D	IEE0503D	IEE0503D
	IEE4703D	IEE0503D	IEE0503D
	IEE5703D	IEE0503D	IEE0503D
	IEE7203D	IEE0503D	IEE0503D
IEE307I	IEECB866	IEE0503D	IEE0503D
	IEEMB815	IEE0503D	IEE0503D
	IEEMPDM	IEEMPDM	IEEMPDM
	IEE0603D	IEE0503D	IEE0503D
	IEE1603D	IEE0503D	IEE0503D
	IEE3203D	IEE0503D	IEE0503D
	IEE3303D	IEE0503D	IEE0503D
	IEE3603D	IEE3603D	IEE3603D
	IEE4303D	IEE0503D	IEE0503D
IEE308I	IEECB866	IEE0503D	IEE0503D
	IEECB907	IEE0503D	IEE0503D
	IEEMB810	IEE0503D	IEE0503D
	IEEMB811	IEE0503D	IEE0503D
	IEEMB815	IEE0503D	IEE0503D
	IEEVMNT1	IEEVSMSG	IEEVSMSG
	IEE0603D	IEE0503D	IEE0503D
	IEE0703D	IEE0503D	IEE0503D
	IEE3203D	IEE0503D	IEE0503D
	IEE3303D	IEE0503D	IEE0503D
	IEE3603D	IEE3603D	IEE3603D
	IEE3703D	IEE0503D	IEE0503D
IEE309I	IEEMB810	IEE0503D	IEE0503D
	IEEMB811	IEE0503D	IEE0503D
	IEEMB815	IEE0503D	IEE0503D
	IEEMPDM	IEEMPDM	IEEMPDM
	IEEVMNT1	IEEVSMSG	IEEVSMSG
	IEE0603D	IEE0503D	IEE0503D
	IEE1403D	IEE0503D	IEE0503D
	IEE3203D	IEE0503D	IEE0503D
	IEE3303D	IEE0503D	IEE0503D
	IEE4403D	IEE0503D	IEE0503D
	IEE4703D	IEE0503D	IEE0503D
	IEE5703D	IEE0503D	IEE0503D
	IEE7203D	IEE0503D	IEE0503D
IEE310I	IEECB905	IEE0503D	IEE0503D
	IEEMB815	IEE0503D	IEE0503D
	IEE0603D	IEE0503D	IEE0503D
IEE311I	IEEMPDM	IEEMPDM	IEEMPDM
	IEEVMNT1	IEEVSMSG	IEEVSMSG

Message ID	Module Detect	Module Issuing	Module Containing
	IEE0603D	IEE0503D	IEE0503D
	IEE0703D	IEE0503D	IEE0503D
	IEE0803D	IEE0503D	IEE0503D
	IEE1403D	IEE0503D	IEE0503D
	IEE1603D	IEE0503D	IEE0503D
	IEE3203D	IEE0503D	IEE0503D
	IEE3503D	IEE0503D	IEE0503D
	IEE3703D	IEE0503D	IEE0503D
	IEE5503D	IEE0503D	IEE0503D
	IEE7103D	IEE0503D	IEE0503D
IEE312I	IEEMB815	IEE2103D	IEE0503D
	IEEVMNT2	IEEVSMSG	IEEVSMSG
	IEE0503D	IEE0503D	IEE0503D
	IEE0603D	IEE0503D	IEE0503D
	IEE3103D	IEE0503D	IEE0503D
	IEE3203D	IEE0503D	IEE0503D
	IEE3603D	IEE3603D	IEE3603D
	IEE4203D	IEE0503D	IEE0503D
	IEE4403D	IEE0503D	IEE0503D
	IEE5703D	IEE0503D	IEE0503D
	IEE7203D	IEE0503D	IEE0503D
IEE313I	IEECB904	IEECB904	IEECB904
	IEE3103D	IEE3103D	IEE3103D
	IEE3603D	IEE3603D	IEE3603D
	IEE4203D	IEE4203D	IEE4203D
IEE314I	IEEMB813	IEE0503D	IEE0503D
IEE324I	IEE3703D	IEE0503D	IEE0503D
IEE328I	IEECB905	IEE0503D	IEE0503D
	IEE0803D	IEE0503D	IEE0503D
	IEE3603D	IEE3603D	IEE3603D
	IEE4903D	IEE4903D	IEE4903D
	IEE5603D	IEE0503D	IEE0503D
	IEE7103D	IEE0503D	IEE0503D
	IEE7203D	IEE0503D	IEE0503D
IEE329I	IEE3103D	IEE3103D	IEE3103D
	IEE3603D	IEE3603D	IEE3603D
	IEE4203D	IEE4203D	IEE4203D
	IEE4603D	IEE4603D	IEE4603D
IEE334I	IEE40110	IEE90110	IEE90110
IEE335I	IEEVMNT1	IEEVSMSG	IEEVSMSG
IEE338I	IEE5703D	IEE0503D	IEE0503D
IEE339I	IEE4203D	IEE4203D	IEE4203D
	IEE5703D	IEE0503D	IEE0503D
IEE341I	IEECB866	IEE0503D	IEE0503D
	IEEMB810	IEE0503D	IEE0503D
	IEE0703D	IEE0503D	IEE0503D
	IEE3703D	IEE0503D	IEE0503D
IEE342I	IEECVETE	IEECVETE	IEECVETE
	IEE0703D	IEE0503D	IEE0503D
IEE345I	IEECB866	IEE0503D	IEE0503D
	IEEMPS03	IEEMPS03	IEEMPS03
	IEEMPVST	IEEMPVST	IEEMPVST
	IEEVCPU	IEECLEAN	IEECLEAN
	IEEVPTH	IEEVPTH	IEEVPTH
	IEE0403D	IEE0503D	IEE0503D
	IEE3303D	IEE0503D	IEE0503D
	IEE3503D	IEE0503D	IEE0503D
	IEE4203D	IEE4203D	IEE4203D
	IEE4303D	IEE0503D	IEE0503D
	IEE4403D	IEE0503D	IEE0503D
	IEE4703D	IEE0503D	IEE0503D
	IEE5703D	IEE0503D	IEE0503D
	IEE6303D	IEE5603D	IEE5603D
	IEE6303D	IEE5903D	IEE5903D
	IEE7503D	IEE5603D	IEE5603D
	IEE7503D	IEE5903D	IEE5903D
	IEE7703D	IEE5603D	IEE5603D
IEE349I	IEE4103D	IEE4103D	IEE4103D

Message ID	Module Detect	Module Issuing	Module Containing
	IEE4903D	IEE4903D	IEE4903D
IEE351I	IEEMB820	IEEMB820	IEEMB824
	IEEMB821	IEEMB820	IEEMB824
	IEEMB822	IEEMB820	IEEMB824
	IEEMB825	IEEMB820	IEEMB824
	IEEMB829	IEEMB820	IEEMB824
IEE352A	IEEMB821	IEEMB821	IEEMB824
IEE353A	IEEMB821	IEEMB821	IEEMB824
IEE354I	IEEMB821	IEEMB821	IEEMB824
IEE355I	IEEMB821	IEEMB821	IEEMB824
IEE356A	IEEMB821	IEEMB821	IEEMB824
IEE357A	IEEMB821	IEEMB821	IEEMB824
IEE358I	IEEMB829	IEEMB822	IEEMB824
IEE359I	IEEMB821	IEEMB821	IEEMB824
IEE360I	IEEMB829	IEEMB829	IEEMB828
IEE361I	IEEMB829	IEEMB829	IEEMB828
IEE362A	IEEMB829	IEEMB829	IEEMB828
IEE363I	IEEMB822	IEEMB822	IEEMB824
IEE364I	IEEMB829	IEEMB829	IEEMB828
IEE365I	IEEMB821	IEEMB821	IEEMB824
	IEEMB822	IEEMB822	IEEMB824
	IEEMB829	IEEMB822	IEEMB824
IEE375I	IEE7203D	IEE0503D	IEE0503D
IEE376I	IEEVPTH	IEEVPTH	IEEVPTH
IEE378I	IEEVPTH	IEEVPTH	IEEVPTH
IEE379I	IEEVPTH	IEEVPTH	IEEVPTH
IEE382I	IEE1603D	IEE0503D	IEE0503D
IEE400I	IEAVMDOM	IEAVMDOM	IEAVMDOM
IEE479W	IEEMB860	IEEMB860	IEEMB860
	IEEVIPL	IEEVIPL	IEEVIPL
	IEEVWAIT	IEEVWAIT	IEEVWAIT
IEE480I	IEECB860	IEECB860	IEECB860
	IEEMB820	IEEMB820	IEEMB824
	IEEMB822	IEEMB822	IEEMB824
	IEEMB825	IEEMB825	IEEMB826
	IEE5103D	IEE5103D	IEE5103D
IEE481I	IEEVWAIT	IEEVWAIT	IEEVWAIT
IEE482I	IEEVWAIT	IEEVWAIT	IEEVWAIT
IEE500E	IEEVCPU	IEECLEAN	IEECLEAN
IEE502I	IEEVCPU	IEECLEAN	IEECLEAN
IEE503I	IEEVCPU	IEECLEAN	IEECLEAN
IEE504I	IEEVCPU	IEECLEAN	IEECLEAN
IEE505I	IEEVCPU	IEECLEAN	IEECLEAN
IEE506E	IEEVCPU	IEECLEAN	IEECLEAN
IEE510I	IEEMPVST	IEEMPVST	IEEMPVST
IEE512I	IEEVCPU	IEECLEAN	IEECLEAN
IEE513I	IEEVWAIT	IEEVWAIT	IEEVWAIT
	IEE5103D	IEE5103D	IEE5103D
IEE515E	IEEMPVST	IEEMPVST	IEEMPVST
IEE517I	IEEMPVST	IEEMPVST	IEEMPVST
IEE519E	IEEMPVST	IEEMPVST	IEEMPVST
IEE520I	IEEMPVST	IEEMPVST	IEEMPVST
IEE523I	IEEVCPU	IEECLEAN	IEECLEAN
IEE524I	IEEMPVST	IEEMPVST	IEEMPVST
IEE527E	IEEVCPU	IEECLEAN	IEECLEAN
IEE528I	IEEMPVST	IEEMPVST	IEEMPVST
IEE531I	IEEMB803	IEEMB807	IEEMB807
IEE532I	IEE1603D	IDD1603D	IEE1603D
IEE533I	IEEMB803	IEEMB807	IEEMB807
IEE534I	IEEMB803	IEEMB807	IEEMB807
IEE535I	IEECB866	IEE0503D	IEE0503D
	IEEMB815	IEE0503D	IEE0503D
	IEE0603D	IEE0503D	IEE0503D
	IEE0803D	IEE0503D	IEE0503D
IEE536I	IEEMB811	IEEMB814	IEEMB814
IEE537I	IEEMB811	IEEMB814	IEEMB814
IEE538I	IEEMB811	IEEMB814	IEEMB814
IEE539I	IEEMB811	IEEMB814	IEEMB814

Message ID	Module Detect	Module Issuing	Module Containing
IEE540I	IEEMB811	IEEMB814	IEEMB814
IEE541E	IEEVCPU	IEECLEAN	IEECLEAN
IEE600I	IEAVVRP1	IEAVVRP1	IEAVVRP1
IEE699I	IEAVVRP1	IEAVVRP1	IEAVVRP1
IEE700I	IEAVVRP1	IEAVVRP1	IEAVVRP1
IEE701I	IEAVVRP1	IEAVVRP1	IEAVVRP1
IEE702I	IEAVVRP1	IEAVVRP1	IEAVVRP1
IEE703I	IEAVVRP1	IEAVVRP1	IEAVVRP1
IEE704I	IEAVVRP1	IEAVVRP1	IEAVVRP1
IEE706I	IEE70110	IEE90110	IEE90110
IEE707I	IEE0403D	IEE0503D	IEE0503D
IEE708I	IEECB866	IEE0503D	IEE0503D
	IEECB910	IEE0503D	IEE0503D
	IEEMB815	IEE2103D	IEE0503D
	IEEMB860	IEE0503D	IEE0503D
IEE709I	IEE4903D	IEE4903D	IEE4903D
IEE710I	IEE4903D	IEE4903D	IEE4903D
IEE711I	IEECB866	IEE0503D	IEE0503D
IEE712I	IEEMB815	IEE0503D	IEE0503D
IEE713I	IEEMPDM	IEEMPDM	IEEMPDM
	IEEMPVST	IEEMPVST	IEEMPVST
	IEEVCPU	IEECLEAN	IEECLEAN
	IEEVPTH	IEEVPTH	IEEVPTH
IEE714I	IEEVPTH	IEEVPTH	IEEVPTH
IEE715I	IEEMPVST	IEEMPVST	IEEMPVST
	IEEVCPU	IEECLEAN	IEECLEAN
	IEEVPTH	IEEVPTH	IEEVPTH
IEE717D	IEEVCPU	IEEVCPU	IEEVCPU
IEE718D	IEEVCPU	IEEVCPU	IEEVCPU
IEE718I	IEEVCPU	IEECLEAN	IEECLEAN
IEE719I	IEEVCPU	IEECLEAN	IEECLEAN
IEE720I	IEECVFTB	IEECVFTD	IEECVFTD
IEE721I	IEECVFTA	IEECVFTD	IEECVFTD
IEE722I	IEECVFTA	IEECVFTD	IEECVFTD
IEE723I	IEECVFTA	IEECVFTD	IEECVFTD
IEE724I	IEE40110	IEE40110	IEE40110
IEE725I	IEECB905	IEECB907	IEECB907
IEE726D	IEECB905	IEECB908	IEECB908
IEE727I	IEECB905	IEECB908	IEECB908
IEE728D	IEECB905	IEECB908	IEECB908
IEE729D	IEECB905	IEECB908	IEECB908
IEE731I	IEECB905	IEECB908	IEECB908
	IEECB907	IEECB908	IEECB908
IEE732D	IEECB905	IEECB908	IEECB908
	IEECB907	IEECB908	IEECB908
IEE733I	IEECB905	IEECB908	IEECB908
	IEECB907	IEECB908	IEECB908
IEE734I	IEEMB813	IEEMB813	IEEMB813
IEE735I	IEECB907	IEECB907	IEECB907
IEE736D	IEECB905	IEECB908	IEECB908
IEE745I	IEEVCPU	IEECLEAN	IEECLEAN
IEE746I	IEEVCPU	IEECLEAN	IEECLEAN
IEE747I	IEEVCPU	IEECLEAN	IEECLEAN
IEE751E	IEEMPS03	IEEMPS03	IEEMPS03
IEE752I	IEEMPS03	IEEMPS03	IEEMPS03
IEE753I	IEEMPS03	IEEMPS03	IEEMPS03
IEE754I	IEEVCPU	IEECLEAN	IEECLEAN
IEE755I	IEECLEAN	IEECLEAN	IEECLEAN
IEE756I	IEEVCPU	IEECLEAN	IEECLEAN
	IEECB905	IEECB908	IEECB908
	IEECB907	IEECB908	IEECB908
IEE757I	IEEVCPU	IEECLEAN	IEECLEAN
IEE758I	IEEVCPU	IEECLEAN	IEECLEAN
IEE759I	IEEVCPU	IEECLEAN	IEECLEAN
IEE760I	IEEMPDM	IEEMPDM	IEEMPDM
	IEEMPVST	IEEMPVST	IEEMPVST
	IEEVCPU	IEECLEAN	IEECLEAN
	IEEVPTH	IEEVPTH	IEEVPTH

Message ID	Module Detect	Module Issuing	Module Containing
IEE761I	IEEVCPU	IEECLEAN	IEECLEAN
IEE762I	IEEVCPU	IEECLEAN	IEECLEAN
IEE763I	IEEVCPU	IEECLEAN	IEECLEAN
IEE764I	IEEMPVST	IEEMPVST	IEEMPVST
IEE765D	IEEMPVST	IEEMPVST	IEEMPVST
IEE766I	IEEMPVST	IEEMPVST	IEEMPVST
IEE767I	IEEMPDM	IEEMPDM	IEEMPDM
IEE768I	IEEMPDM	IEEMPDM	IEEMPDM
IEE769I	IEEMB806	IEEMB806	IEEMB806
IEE770I	IEEMB807	IEEMB807	IEEMB807
IEE771I	IEEVCPU	IEECLEAN	IEECLEAN
IEE772I	IEEVCPU	IEECLEAN	IEECLEAN
IEE773I	IEEVCPU	IEECLEAN	IEECLEAN
IEE774I	IEEVCPU	IEECLEAN	IEECLEAN
IEE775I	IEEMB803	IEEMB807	IEEMB807
IEE777I	IEE7103D	IEE0503D	IEE0503D
IEE779I	IEECB904	IEECB904	IEECB904
IEE780I	IEEVPTH	IEEVPTH	IEEVPTH
IEE782I	ILRPGEXP	ILRPGEXP	ILRPGEXP
IEE783I	ILRPGEXP	ILRPGEXP	ILRPGEXP
IEE784I	ILRPGEXP	ILRPGEXP	ILRPGEXP
IEE785I	ILRPGEXP	ILRPGEXP	ILRPGEXP
IEE786I	ILRPGEXP	ILRPGEXP	ILRPGEXP
IEE787I	ILRPGEXP	ILRPGEXP	ILRPGEXP
IEE788I	ILRPGEXP	ILRPGEXP	ILRPGEXP
IEE789I	ILRPGEXP	ILRPGEXP	ILRPGEXP
IEE790I	ILRPGEXP	ILRPGEXP	ILRPGEXP
IEE791I	ILRPGEXP	ILRPGEXP	ILRPGEXP
IEE792I	ILRPGEXP	ILRPGEXP	ILRPGEXP
IEE824A	IEAVSTAA	IEAVSTAA	IEAVSTAA
IEE824I	IEEPRWI2	IEEVSMSG	IEEVSMSG
	IEESB665	IEEVSMSG	IEEVSMSG
IEE838I	IEE3703D	IEE0503D	IEE0503D
IEE841I	IEE3703D	IEE0503D	IEE0503D
IEE856I	IEECB910	IEECB910	IEECB911
IEE857I	IEECB910	IEECB910	IEECB911
IEE908I	IEE3203D	IEE0503D	IEE0503D
IEE920I	IEECB800	IEECB801	IEECB800
IEE921I	IEE7503D	IEE5603D	IEE5603D
	IEE7503D	IEE5903D	IEE5903D
	IEE7703D	IEE5603D	IEE5603D
	IEE7703D	IEE5903D	IEE5903D
IEE922I	IEE6903D	IEE6903D	IEE6903D
IEE924I	IEE6803D	IEE5603D	IEE5603D
	IEE6803D	IEE5903D	IEE5903D
	IEE6903D	IEE5603D	IEE5603D
	IEE6903D	IEE5903D	IEE5903D
IEE925I	IEE6303D	IEE5603D	IEE5603D
	IEE6303D	IEE5903D	IEE5903D
	IEE6703D	IEE5603D	IEE5603D
	IEE6703D	IEE5903D	IEE5903D
	IEE6803D	IEE5603D	IEE5603D
	IEE6803D	IEE5903D	IEE5903D
	IEE7503D	IEE5603D	IEE5603D
	IEE7503D	IEE5903D	IEE5903D
IEE926I	IEE6303D	IEE5603D	IEE5603D
	IEE6303D	IEE5903D	IEE5903D
	IEE6703D	IEE5603D	IEE5603D
	IEE6703D	IEE5903D	IEE5903D
	IEE6803D	IEE5603D	IEE5603D
	IEE7503D	IEE5603D	IEE5603D
	IEE7503D	IEE5903D	IEE5903D
	IEE7703D	IEE5603D	IEE5603D
	IEE7703D	IEE5903D	IEE5903D
IEE927I	IEE6703D	IEE5603D	IEE5603D
	IEE6703D	IEE5903D	IEE5903D
	IEE7503D	IEE5603D	IEE5603D
	IEE7503D	IEE5903D	IEE5903D

Message ID	Module Detect	Module Issuing	Module Containing
	IEE7803D	IEE5603D	IEE5603D
	IEE7803D	IEE5903D	IEE5903D
IEE928I	IEE6803D	IEE5603D	IEE5603D
	IEE6803D	IEE5903D	IEE5903D
	IEE6803D	IEE6803D	IEE6803D
	IEE6903D	IEE6903D	IEE6903D
IEE929I	IEE6903D	IEE6903D	IEE6903D
IEE930I	IEE6403D	IEE6403D	IEE6403D
IEE931I	IEE6303D	IEE5603D	IEE5603D
	IEE6303D	IEE5903D	IEE5903D
	IEE6403D	IEE5603D	IEE5603D
	IEE6403D	IEE5903D	IEE5903D
	IEE6903D	IEE5603D	IEE5603D
IEE932I	IEAVMWTO	IEAVMWTO	IEAVMWTO
IEE934I	IEECVFT1	IEECVFT1	IEECVFT1
IEF085I	IEFSD263	IEFSD263	IEFIB650
IEF086I	IEFXB601	IEFXB601	IEFXB603
IEF087I	IEFXB601	IEFXB601	IEFXB603
IEF089I	IEFXB500	IEFXB500	IEFXB603
IEF090E	IEFSD161	IEFSD161	IEFIB650
IEF091I	IEFSD161	IEFSD161	IEFIB650
IEF092I	IEFSD263	IEFSD263	IEFIB650
IEF099I	IEFSD102	IEFSD102	IEFIB650
IEF125I	IEFBB401	IEFBB401	IEFBB4M1
IEF126I	IEFBB410	IEFAB4FD	IEFBB4M4
IEF127I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF128I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF129I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF130I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF131I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF132I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF133I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF134I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF135I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF136I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF140I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF141I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF142I	IEFBB410	IEFAB4FD	IEFBB4M4
IEF143I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF145I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
IEF165I	IEFVHM	IEFVHM	IEFVHM
IEF166I	IEFVHM	IEFVHM	IEFVHM
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF167I	IEFRPREP	IEFRPREP	IEFXB603
IEF168I	IEFRPREP	IEFRPREP	IEFXB603
IEF169I	IEFXB601	IEFXB601	IEFXB603
IEF170I	IEEJB840	IEEJB840	IEEJB840
IEF172E	IEFSD161	IEFSD161	IEFIB650
IEF173I	IEFSD101	IEFSD101	IEFIB650
	IEFSD161	IEFSD161	IEFIB650
IEF174I	IEFIB645	IEFIB645	IEFIB645
IEF180I	IEFBB401	IEFAB4FD	IEFBB4M2
IEF181I	IEFBB401	IEFAB4FD	IEFBB4M2
IEF186I	IEFSD263	IEFSD263	IEFIB650
IEF187I	IEFIB621	IEFIB621	IEFIB650
IEF188I	IEFSD101	IEFSD101	IEFIB650
	IEFSD161	IEFSD162	IEFIB650
IEF192I	IEFAB424	IEFAB4FD	IEFBB4M3
IEF193I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF194I	IEFAB423	IEFAB4FD	IEFBB4M3
IEF195I	IEFBB404	IEFAB4FD	IEFBB4M3
IEF196I	IEFJWTOM	IEFJWTOM	IEFJWTOM
IEF197I	IEFAB4E4	IEFAB4E4	IEFAB4M5
	IEFAB4DD	IEFAB4DD	IEFAB4M5
IEF201I	IEFBB410	IEFAB4FD	IEFBB4M4
IEF202I	IEFBB402	IEFBB401	IEFBB4M2
IEF209I	IEFXB609	IEFXB609	IEFXB603
IEF210I	IEFAB464	IEFAB4FD	IEFBB4M3
	IEFAB470	IEFAB4FD	IEFBB4M3
IEF211I	IEFAB459	IEFAB4FD	IEFBB4M3
IEF212I	IEFAB469	IEFAB4FD	IEFBB4M3
IEF213I	IEFAB469	IEFAB4FD	IEFBB4M3
IEF217I	IEFAB458	IEFAB4FD	IEFBB4M3
IEF218I	IEFAB458	IEFAB4FD	IEFBB4M3
IEF219I	IEFAB461	IEFAB4FD	IEFBB4M3
IEF221I	IEFAB453	IEFAB4FD	IEFBB4M3
IEF225D	IEFRPREP	IEFRPREP	IEFRPREP
IEF233A	IEFAB495	IEFAB495	IEFAB4M4
IEF233D	IEFAB495	IEFAB495	IEFAB4M4
IEF234E	IEFAB494	IEFAB494	IEFAB4M4
IEF235D	IEFAB421	IEFAB421	IEFAB4M5
IEF236I	IEFAB4EE	IEFAB4FD	IEFAB4M7
IEF237I	IEFAB4EE	IEFAB4FD	IEFAB4M7
IEF238D	IEFAB488	IEFAB488	IEFAB4M9
IEF240I	IEFAB4FC	IEFBB401	IEFBB4M2
IEF242I	IEFAB4EE	IEFAB4FD	IEFAB4M7
IEF244I	IEFAB487	IEFAB487	IEFAB4M9
IEF245I	IEFBB404	IEFAB4FD	IEFBB4M3
IEF246I	IEFAB436	IEFAB4FD	IEFBB4M3
IEF247I	IEFAB48A	IEFAB48A	IEFAB4M9
IEF251I	IEFBB401	IEFBB401	IEFBB4M1
IEF253I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF254I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF256I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF257I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF258I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF260I	IEFAB431	IEFAB4FD	IEFBB4M3

Message ID	Module Detect	Module Issuing	Module Containing
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF261I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF262I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF263I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF264I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF266I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF267I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF272I	IEFBB410	IEFAB4FD	IEFBB4M4
IEF273I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF281I	IEFAB421	IEFAB421	IEFAB4M5
IEF282I	IEFAB421	IEFAB421	IEFAB4M5
IEF283I	IEFAB4A2	IEFAB4FD	IEFAB4M6
IEF285I	IEFAB4A2	IEFAB4FD	IEFAB4M6
IEF286I	IEFAB461	IEFAB4FD	IEFBB4M3
IEF287I	IEFAB4A2	IEFAB4FD	IEFAB4M6
IEF318I	IEFAB423	IEFAB4FD	IEFBB4M3
IEF361I	IEFAB4F5	IEFAB4FD	IEFBB4M2
IEF362I	IEFAB4F4	IEFAB4FD	IEFBB4M2
IEF363I	IEFAB469	IEFAB4FD	IEFBB4M2
IEF364I	IEFAB469	IEFAB4FD	IEFBB4M2
IEF365I	IEFAB457	IEFAB4FD	IEFBB4M3
IEF366I	IEFAB461	IEFAB4FD	IEFBB4M3
	IEFAB456	IEFAB4FD	IEFBB4M3
IEF367I	IEFAB458	IEFAB4FD	IEFBB4M2
IEF369D	IEFAB496	IEFAB496	IEFBB4M3
IEF371I	IEFAB425	IEFAB4FD	IEFBB4M3
IEF372I	IEFAB457	IEFAB4FD	IEFBB4M3
IEF373I	IEFTB722	IEFTB722	IEFTB720
IEF374I	IEFTB722	IEFTB722	IEFTB720
IEF375I	IEFTB722	IEFTB722	IEFTB720
IEF376I	IEFTB722	IEFTB722	IEFTB720
IEF402I	IEFIRECM	IEFIRECM	IEFIRECM
IEF403I	IEFBB401	IEFBB401	IEFBB4M1
IEF404I	IEFBB410	IEFBB410	IEFBB4M4
IEF417I	IEFVHA	IEFVHR	IEFVHR
IEF430I	IEFVHR	IEFVHR	IEFVHR
IEF433D	IEFAB488	IEFAB487	IEFAB4M9
IEF434D	IEFAB487	IEFAB487	IEFAB4M9
IEF450I	IEFBB410	IEFBB410	IEFBB4M4
IEF451I	IEFBB410	IEFBB410	IEFBB4M4
IEF452I	IEFBB401	IEFBB401	IEFBB4M1
	IEFVHF	IEFVHR	IEFVHR
	IEFVHN	IEFVHR	IEFVHR
IEF453I	IEFBB410	IEFBB410	IEFBB4M4
IEF455D	IEFAB495	IEFAB495	IEFAB4M4
IEF456I	IEFAB4A0	IEFAB4FD	IEFBB4M2
	IEFAB4F5	IEFAB4FD	IEFBB4M2
	IEFAB421	IEFAB4FD	IEFBB4M2
	IEFAB451	IEFAB4FD	IEFBB4M2
	IEFAB493	IEFAB4FD	IEFBB4M2
	IEFBB401	IEFAB4FD	IEFBB4M2
	IEFBB410	IEFAB4FD	IEFBB4M2
IEF458D	IEFAB4DC	IEFAB4DC	IEFBB4M5

Message ID	Module Detect	Module Issuing	Module Containing
IEF465I	IEFAB427	IEFAB4FD	IEFBB4M2
IEF466I	IEFAB492	IEFAB4FD	IEFBB4M3
IEF467I	IEFAB479	IEFAB4FD	IEFBB4M3
IEF468I	IEFBB410	IEFAB4FD	IEFBB4M5
IEF469I	IEFBB410	IEFAB4FD	IEFBB4M5
IEF470I	IEFBB410	IEFBB410	IEFBB4M4
IEF471E	IEFBB410	IEFBB410	IEFBB4M4
IEF472I	IEFBB410	IEFAB4FD	IEFBB4M4
IEF473I	IEFAB436	IEFAB4FD	IEFBB4M2
	IEFAB478	IEFAB4FD	IEFBB4M2
	IEFAB489	IEFAB4FD	IEFBB4M2
IEF474I	IEFBB404	IEFAB4FD	IEFBB4M3
IEF475I	IEFAB441	IEFAB4FD	IEFBB4M3
	IEFAB442	IEFAB4FD	IEFBB4M3
	IEFAB479	IEFAB4FD	IEFBB4M3
IEF476I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF477I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF478I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF479I	IEFAB431	IEFAB4FD	IEFBB4M3
	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492	IEFAB4FD	IEFBB4M3
IEF480I	IEFAB427	IEFAB4FD	IEFBB4M3
IEF481I	IEFAB479	IEFAB4FD	IEFBB4M3
IEF482I	IEFAB479	IEFAB4FD	IEFBB4M3
IEF483I	IEFAB479	IEFAB4FD	IEFBB4M3
IEF484I	IEFAB479	IEFAB4FD	IEFBB4M3
IEF485I	IEFAB421	IEFAB4FD	IEFBB4M2
	IEFAB479	IEFAB4FD	IEFBB4M3
IEF488I	IEFAB487	IEFAB487	IEFAB4M9
IEF489I	IEFAB48A	IEFAB48A	IEFAB4M9
IEF490I	IEFAB488	IEFAB488	IEFAB4M9
IEF491I	IEFAB466	IEFAB4FD	IEFBB4M2
IEF492I	IEFAB452	IEFAB4FD	IEFBB4M3
IEF493I	IEFAB452	IEFAB4FD	IEFBB4M3
IEF502I	IEFAB473	IEFAB473	IEFAB4M5
IEF503I	IEFAB473	IEFAB473	IEFAB4M5
IEF506I	IEFAB490	IEFAB4FD	IEFAB4M7
IEF510I	IEFAB473	IEFAB473	IEFAB4M5
IEF601I	IEFVFA	IEFVGM	IEFVGM1
	IEFVHA	IEFVGM	IEFVGM1
	IEFVHCB	IEFVGM	IEFVGM1
IEF603I	IEFVEA	IEFVGM	IEFVGM1
IEF605I	IEFVHCB	IEFVGM	IEFVGM1
	IEFVHM	IEFVGM	IEFVGM1
IEF606I	IEFVDA	IEFVGM	IEFVGM1
IEF607I	IEFVHCB	IEFVGM	IEFVGM1
IEF609I	IEFVEA	IEFVGM	IEFVGM2
IEF610I	IEFVHA	IEFVGM	IEFVGM2
	IEFVHCB	IEFVGM	IEFVGM2
IEF611I	IEFVHEB	IEFVGM	IEFVGM2
	IEVFHCB	IEFVGM	IEFVGM2
	IEVVHH	IEFVGM	IEFVGM2
IEF612I	IEFVFA	IEFVGM	IEFVGM2
IEF613I	IEVFEA	IEFVGM	IEFVGM2
IEF614I	IEFVEA	IEFVGM	IEFVGM2
IEF615I	IEFVEA	IEFVGM	IEFVGM2
IEF616I	IEFVFA	IEFVGM	IEFVGM3
IEF617I	IEFVDA	IEFVGM	IEFVGM3
	IEFVHCB	IEFVGM	IEFVGM3
IEF618I	IEFVFA	IEFVGM	IEFVGM3
IEF621I	IEFVHC	IEFVGM	IEFVGM3
IEF622I	IEFVFA	IEFVGM	IEFVGM3

Message ID	Module Detect	Module Issuing	Module Containing
IEF623I	IEFVFA	IEFVGM	IEFVGM3
IEF624I	IEFVDA	IEFVGM	IEFVGM4
	IEFVFA	IEFVGM	IEFVGM4
IEF625I	IEFVFA	IEFVGM	IEFVGM4
IEF626I	IEFVFA	IEFVGM	IEFVGM4
IEF627I	IEFVFA	IEFVGM	IEFVGM4
IEF628I	IEFVFA	IEFVGM	IEFVGM4
IEF629I	IEFVFA	IEFVGM	IEFVGM4
IEF630I	IEFVDA	IEFVGM	IEFVGM4
	IEFVFA	IEFVGM	IEFVGM4
IEF631I	IEFVDA	IEFVGM	IEFVGM4
IEF632I	IEFVDA	IEFVGM	IEFVGM5
	IEFVEA	IEFVGM	IEFVGM5
	IEFVFA	IEFVGM	IEFVGM5
	IEFVJA	IEFVGM	IEFVGM5
IEF633I	IEFVJA	IEFVGM	IEFVGM5
IEF634I	IEFVJA	IEFVGM	IEFVGM5
IEF635I	IEFVJA	IEFVGM	IEFVGM5
IEF636I	IEFVDA	IEFVGM	IEFVGM5
IEF637I	IEFVEA	IEFVGM	IEFVGM5
	IEFVJA	IEFVGM	IEFVGM5
IEF638I	IEFVGS	IEFVGM	IEFVGM5
	IEFVGT	IEFVGM	IEFVGM5
IEF639I	IEFVGS	IEFVGM	IEFVGM5
	IEFVGT	IEFVGM	IEFVGM5
IEF640I	IEFVDA	IEFVGM	IEFVGM6
	IEFVFA	IEFVGM	IEFVGM6
	IEFVGK	IEFVGM	IEFVGM6
	IEFVGS	IEFVGM	IEFVGM6
	IEFVGT	IEFVGM	IEFVGM6
IEF641I	IEFVGK	IEFVGM	IEFVGM6
IEF642I	IEFVDA	IEFVGM	IEFVGM6
	IEFVEA	IEFVGM	IEFVGM6
	IEFVFB	IEFVGM	IEFVGM6
	IEFVGT	IEFVGM	IEFVGM6
	IEFVHCB	IEFVGM	IEFVGM6
	IEFVJA	IEFVGM	IEFVGM6
IEF643I	IEFVDA	IEFVGM	IEFVGM6
	IEFVGT	IEFVGM	IEFVGM6
IEF644I	IEFVGT	IEFVGM	IEFVGM6
IEF645I	IEFVDA	IEFVGM	IEFVGM6
	IEFVEA	IEFVGM	IEFVGM6
	IEFVGS	IEFVGM	IEFVGM6
IEF646I	IEFVDA	IEFVGM	IEFVGM6
	IEFVEA	IEFVGM	IEFVGM6
	IEFVFA	IEFVGM	IEFVGM6
	IEFVJA	IEFVGM	IEFVGM6
IEF647I	IEFVDA	IEFVGM	IEFVGM6
	IEFVFB	IEFVGM	IEFVGM6
	IEFVGT	IEFVGM	IEFVGM6
IEF648I	IEFVDA	IEFVGM	IEFVGM7
IEF649I	IEFVDA	IEFVGM	IEFVGM7
IEF650I	IEFVFA	IEFVGM	IEFVGM7
IEF651I	IEFVFA	IEFVGM	IEFVGM7
IEF652I	IEFVFA	IEFVGM	IEFVGM7
IEF653I	IEFVFB	IEFVGM	IEFVFB
IEF654I	IEFVDA	IEFVGM	IEFVGM7
IEF655I	IEFVDA	IEFVGM	IEFVGM7
IEF657I	IEFVHA	IEFVGM	IEFVGM70
IEF658I	IEFVHCB	IEFVGM	IEFVGM70
IEF659I	IEFVHCB	IEFVGM	IEFVGM70
IEF660I	IEFVHCB	IEFVGM	IEFVGM70
IEF661I	IEFVHGM	IEFVGM	IEFVGM70
IEF662I	IEFVINA	IEFVGM	IEFVGM70
IEF663I	IEFVINA	IEFVGM	IEFVGM70
IEF664I	IEFVINA	IEFVGM	IEFVGM71
IEF665I	IEFVINA	IEFVGM	IEFVGM71
IEF668I	IEFVHCB	IEFVGM	IEFVGM71
IEF669I	IEFVDA	IEFVGM	IEFVGM71
IEF670I	IEFVEA	IEFVGM	IEFVGM71
IEF671I	IEFVDA	IEFVGM	IEFVGM71
IEF672I	IEFVDA	IEFVGM	IEFVGM71
IEF673I	IEFVEA	IEFVGM	IEFVGM71
	IEFVJA	IEFVGM	IEFVGM71
IEF674I	IEFVEA	IEFVGM	IEFVGM1
IEF675I	IEFVEA	IEFVGM	IEFVGM1
IEF676I	IEFVJA	IEFVGM	IEFVGM3
IEF677I	IEFVGM	IEFVHR	IEFVHR
IEF678I	IEFVGM	IEFVGM	IEFVGM3
	IEFVHA	IEFVGM	IEFVGM3
	IEFVHCB	IEFVGM	IEFVGM3
	IEFVHE	IEFVGM	IEFVGM3
IEF679I	IEFVHA	IEFVHR	IEFVHR
	IEFVHCB	IEFVHR	IEFVHR
	IEFVHE	IEFVHR	IEFVHR
	IEFVINA	IEFVHR	IEFVHR
IEF680I	IEFVGM	IEFVHR	IEFVHR
IEF681I	IEFVDA	IEFVGM	IEFVGM70
IEF682I	IEFVDA	IEFVGM	IEFVGM71
IEF683I	IEFNB9CR	IEFNB9CR	IEFNB9CR
	IEFNB9IR	IEFNB9IR	IEFNB9IR
IEF684I	IEFVDA	IEFVGM	IEFVGM71
IEF685I	IEFVDA	IEFVGM	IEFVGM71
IEF687I	IEFAB441	IEFAB4FD	IEFBB4M3
IEF689I	IEFSD162	IEFSD162	IEFI3650
IEF690I	IEFAB42I	IEFAB42I	IEFAB4ME
IEF700I	IEFAB486	IEFAB4FD	IEFBB4M2
	IEFAB489	IEFAB4FD	IEFBB4M2
	IEFAB491	IEFAB4FD	IEFBB4M2
IEF701I	IEFAB477	IEFAB4FD	IEFBB4M2
	IEFAB490	IEFAB4FD	IEFBB4M2
IEF702I	IEFAB485	IEFAB4FD	IEFBB4M3
	IEFAB486	IEFAB4FD	IEFBB4M3
IEF703I	IEFAB427	IEFAB4FD	IEFBB4M3
IEF704I	IEFAB469	IEFAB4FD	IEFBB4M3
IEF710I	IEFAB495	IEFAB495	IEFAB4M4
IEF711I	IEFAB494	IEFAB494	IEFAB4M4
IEF712I	IEFAB49B	IEFAB49B	IEFAB4M4
IEF713I	IEFAB495	IEFAB4FD	IEFBB4M2
IEF714I	IEFAB495	IEFAB4FD	IEFBB4M2
IEF715I	IEFAB495	IEFAB4FD	IEFBB4M2
IEF716I	IEFAB495	IEFAB4FD	IEFBB4M2
	IEFAB49B		
IEF717I	IEFAB495	IEFAB4FD	IEFBB4M2
IEF718I	IEFAB495	IEFAB4FD	IEFBB4M2
IEF719I	IEFAB434	IEFAB4FD	IEFBB4M3
		IEFAB492	
IEF720I	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB492		
IEF721I	IEFAB490	IEFAB4FD	IEFBB4M3
IEF722I	IEFIB600	IEFIB600	IEFIB650
IEF724I	IEFAB4A3	IEFBB410	IEFBB4M5
IEF725I	IEFAB457	IEFAB4FD	IEFBB4M3
IEF726I	IEFAB424	IEFAB4FD	IEFBB4M3
IEF740I	IEFAB434	IEFAB4FD	IEFBB4M3
IEF741I	IEFAB434	IEFAB4FD	IEFBB4M3
	IEFAB459		
IEF742I	IEFBB410	IEFBB410	IEFBB4M4
IEF743I	IEFIRECM	IEFIRECM	IEFIRECM
IEF861I	IEFSD102	IEFSD102	IEFIB650
	IEFAB4DC	IEFAB4DC	IEFAB4M5
IEF863I	IEFSD102	IEFSD102	IEFIB650
	IEFAB4DC	IEFAB4DC	IEFAB4M5
IKJ600I	IKJEFLE	IKJEFLGM	IKJEFLGN
	IKJEFLL		
IKJ601I	IKJEFLGB	IKJEFLGB	IKJEFLGB

Message ID	Module Detect	Module Issuing	Module Containing
	IKJEFLS	IKJEFLS	IKJEFLS
IKJ602I	IKJEFLE	IKJEFLGM	IKJEFLGN
IKJ603I	IKJEFLEA	IKJEFLGM	IKJEFLGN
	IKJEFLGB		
	IKJEFLI		
IKJ605I	IKJEFLEA	IKJEFLGM	IKJEFLGN
IKJ606I	IKJEFLE	IKJEFLGM	IKJEFLGN
IKJ608I	IKJEFLC	IKJEFLGM	IKJEFLGN
	IKJEFLE		
	IKJEFLEA		
	IKJEFLI		
	IKJEFLL		
IKJ609I	IKJEFLA	IKJEFLA	IKJEFLA
IKJ54050I	IEAVAR04	IEAVAR00	IEAVAR00
	IEAVAR00	IEAVAR00	IEAVAR00
	IEAVAR05	IEAVAR05	IEAVAR05
IKJ56400A	IKJEFLEA	IKJEFLGM	IKJEFLGN
IKJ56401I	IKJEFLEA	IKJEFLGM	IKJEFLGN
IKJ56402I	IKJEFLEA	IKJEFLGM	IKJEFLGN
IKJ56403I	IKJEFLE	IKJEFLGM	IKJEFLGN
IKJ56404I	IKJEFLE	IKJEFLGM	IKJEFLGN
IKJ56405I	IKJEFLE	IKJEFLGM	IKJEFLGN
IKJ56406I	IKJEFLGB	IKJEFLGB	IKJEFLGB
IKJ56407I	IKJEFLEA	IKJEFLGM	IKJEFLGN
IKJ56408I	IKJEFLEA	IKJEFLGM	IKJEGLGN
IKJ56409I	IKJEFLEA	IKJEFLGM	IKJEFLGN
IKJ56410I	IKJEFLEA	IKJEFLGM	IKJEFLGN
IKJ56411I	IKJEFLEA	IKJEFLGM	IKJEFLGN
IKJ56412I	IKJEFLE	IKJEFLGM	IKJEFLGN
IKJ56420I	IKJEFLE	IKJEFLGM	IKJEFLGN
IKJ56421I	IKJEFLE	IKJEFLGM	IKJEFLGN
IKJ56422I	IKJEFLE	IKJEFLGM	IKJEFLGN
IKJ56423I	IKJEFLE	IKJEFLGM	IKJEFLGN
IKJ56424I	IKJEFLE	IKJEFLGM	IKJEFLGN
IKJ56425I	IKJEFLE	IKJEFLGM	IKJEFLGN
	IKJEFLI		
IKJ56428I	IKJEFLEA	IKJEFLGM	IKJEFLGN
IKJ56429A	IKJEFLE	IKJEFLGM	IKJEFLGN
IKJ56450I	IKJEFLH	IKJEFLH	IKJEFLH
IKJ56451I	IKJEFLI	IKJEFLGM	IKJEFLGN
	IKJEFLGB	IKJEFLGB	IKJEFLGB
IKJ56452I	IKJEFLA	IKJEFLA	IKJEFLA
	IKJEFLE	IKJEFLGM	IKJEFLGN
	IKJEFLGB	IKJEFLGB	IKJEFLGB
IKJ56453I	IKJEFLC	IKJEFLGM	IKJEFLGN
IKJ56454I	IKJEFLA	IKJEFLA	IKJEFLA
	IKJEFLC	IKJEFLGM	IKJEFLGN
	IKJEFLE	IKJEFLGM	IKJEFLGN
	IKJEFLEA	IKJEFLGM	IKJEFLGN
	IKJEFLI	IKJEFLGM	IKJEFLGN
IKJ56455I	IKJEFLE	IKJEFLGM	IKJEFLGN
IKJ56456I	IKJEFLE	IKJEFLGM	IKJEFLGN
IKJ56457I	IKJEFLL	IKJEFLGM	IKJEFLGN
IKJ56470I	IKJEFLL	IKJEFLGM	IKJEFLGN
ILR001I	ILRTMI00	ILRTMI00	ILRTMI00
ILR002I	ILRTMI00	ILRTMI00	ILRTMI00
	ILRTMI01	ILRTMI01	ILRTMI01
ILR003A	ILRTMI00	ILRTMI00	ILRTMI00
	ILRTMI01	ILRTMI01	ILRTMI01
ILR004I	ILRTMI00	ILRTMI00	ILRTMI00
	ILRTMI01	ILRTMI01	ILRTMI01
ILR005I	ILRPTM	ILRMSG00	ILRMSG00
	ILRSRT01		
ILR006I	ILRPTM	ILRMSG00	ILRMSG00
	ILRSRT01		
ILR007I	ILRPTM	ILRMSG00	ILRMSG00
	ILRSRT01		
	ILRMSG00		
ILR008W	ILRMSG00	ILRMSG00	ILRMSG00
ILR009I	ILRMSG00	ILRMSG00	ILRMSG00
ILR010I	ILRPTM	ILRMSG00	ILRMSG00
	ILRSRT01		
	ILRMSG00		
ILR020I	ILRTMI00	ILRTMI00	ILRTMI00
ILR021I	ILRTMI01	ILRTMI01	ILRTMI01
ILR022A	ILRTMI00	ILRTMI00	ILRTMI00
	ILRTMI01	ILRTMI01	ILRTMI01
IRA100I	IRARMEVT	IRARMSRV	IRARMMSG
IRA101I	IRARMEVT	IRARMSRV	IRARMMSG
IRA102I	IRARMEVT	IRARMSRV	IRARMMSG
IRA200I	IRARMSTM	IRARMSRV	IRARMMSG
IRA201I	IRARMSTM	IRARMSRV	IRARMMSG
IRA202I	IRARMSTM	IRARMSRV	IRARMMSG
IRA300I	IRARMIPS	IEEMB812	IRARMIPS
IRA301I	IRARMIPS	IEEMB812	IRARMIPS
IRB100I	IRBMFDTA	IRBMFMPR	IRBMFLMV
IRB101I	IRBMFMFC	IRBMFMPR	IRBMFLMV
IRB102I	IRBMFMFC	IRBMFMPR	IRBMFLMV
IRB103I	IRBMFINP	IRBMFMPR	IRBMFLMV
IRB200I	IRBMFMFC	IRBMFMPR	IRBMFLMV
IRB201I	IRBMFMLN	IRBMFMPR	IRBMFLMV
IRB202I	IRBMFMFC	IRBMFMPR	IRBMFLMV
IRB300I	IRBMFINP	IRBMFMPR	IRBMFLMV
IRB301I	IRBMFINP	IRBMFMPR	IRBMFLMV
IRB302I	IRBMFINP	IRBMFMPR	IRBMFLMV
IRB303I	IRBMFINP	IRBMFMPR	IRBMFLMV
IRB304I	IRBMFINP	IRBMFMPR	IRBMFLMV
IRB305I	IRBMFINP	IRBMFMPR	IRBMFLMV
IRB306D	IRBMFINP	IRBMFMPR	IRBMFLMV
IRB308A	IRBMFINP	IRBMFMPR	IRBMFLMV
IRB309I	IRBMFINP	IRBMFMPR	IRBMFLMV
IRB400I	IRBMFRGM	IRBMFMPR	IRBMFLMV
IRB401I	IRBMFRGM	IRBMFMPR	IRBMFLMV
IRB402I	IRBMFDTA	IRBMFMPR	IRBMFLMV
	IRBMFSAR	IRBMFMPR	IRBMFLMV

Return Code Table

This table is an alphabetic list of scheduler, supervisor, MF/1, SRM, and ASM object modules and the return codes they set. Where more than one meaning is given for a single return code, the applicable meaning is sometimes indicated in the reason code set by the module. (See the lists of reason codes in 'Miscellaneous Diagnostic Aids,' Section 6.)

Object Module	Location of Code	Return Code	Meaning
IEAFTEED	register 15	0	EED formatted successfully.
		4	Either the access service or format service routine terminated with a return code of 4.
IEAFTIHS	register 15	0	IHSA and RT1W formatted successfully.
		4	Either the access service or format service routine terminated with a return code of 4.
IEAFTFRR	register 15	0	FRRS and RT1W formatted successfully.
		4	Either the access service or format service routine terminated with a return code of 4.
IEAFTRT2	register 15	0	RTM2WA and summary formatted successfully.
		4	Either the access service or format service routine terminated with a return code of 4.
IEAFTESA	register 15	0	ESA bit flag summary formatted successfully.
		4	The access service routine terminated with a return code of 4.
IEAFTSDW	register 15	0	SDWA registers formatted successfully
		4	Format service routine terminated with a return code of 4.
IEAFTSCB	register 15	0	SCB and summary formatted successfully.
		4	Either the access service or format service routine terminated with a return code of 4.
IEAFTRTC	register 15	0	RTCT formatted successfully.
		4	Either the access service or format service routine terminated with a return code of 4.
IEAVADOA	register 15	0	Dump of SQA and LSQA.
		4	Dump failed – insufficient space.
IEAVADOB	register 15	0	Dump of registers, LPA, and JPA completed normally.
		8	Dump failed – insufficient space.
IEAVADOC	register 15	0	Trace dump completed normally.
		4	GETMAIN failed.
		8	UPR unexpected.
		12	GETMAIN for save area failed.
		16	GTF failed.
IEAVADOD	register 15	0	Dump of user's subpools completed normally.
		8	Dump failed – insufficient space.
IEAVADOE	register 15	0	Dump of SWA and/or SP229/SP230 completed normally.
		4	Dump failed – insufficient space.
IEAVADOF	register 15	0	Dump of Storage areas and headers completed normally.
		4	Dump failed – insufficient space.
IEAVAD00	register 15	0	Dump complete
		4	Partial dump taken.
		8	Unable to dump.
IEAVAD01			
IEAVAD01	register 15	0	Snap dump successful.
		4	Snap dump failed: DCB not open or inconsistent with JCL or insufficient space to take a dump.
		8	Snap dump failed: TCB not valid or read failed for JFCB or JFCBE.
		12	Snap dump failed: DCB type incorrect.
SNPRCUR	register 15	0	Valid snap parameter list.
		4	UPR for DCB.
		8	UPR for TCB.
IEAVAD02	register 15	0	Dump of PSW completed normally.
		4	Dump failed -- insufficient space.
IEAVAD03	register 15	0	Dump of control blocks completed normally.
		4	Dump failed -- insufficient space.
IEAVAD05	register 15	0	Dump of control blocks completed normally.
		4	Dump failed -- insufficient space.
IEAVAD06	register 15	0	Dump of QCB and QEL completed normally.
		4	Dump failed -- insufficient space.
IEAVAD07	register 15	0	Dump of save areas completed normally.
		4	Dump failed -- insufficient space.
IEAVAD08	register 15	0	GTF or TCAM dump formatting routine was involved.
		8	Space for save area not available.

Object Module	Location of Code	Return Code	Meaning
IEAVAD09	register 15	0	Dump of Nucleus and PSA completed normally.
		4	Dump failed – insufficient space.
IEAVAD10	register 15	0	PSW or registers displayed.
		4	Dump failed – insufficient space.
IEAVAD31	register 15	0	Dump formatting routine completed normally.
		8	Unexpected UPR.
IEAVAD51	register 15	0	Dump formatting routine completed normally.
		8	Unexpected UPR.
IEAVAD71	register 15	0	Dump formatting routine completed normally.
		8	Space for save area not available.
IEAVAMSI	register 15	0	The input VCBs have been successfully processed.
		4	An error was detected in a VCB or in an input parameter.
		8	Last VCB processed is not valid in storage.
		12	Last VCB processed did not contain operation flag.
IEAVAR00			
IEAVAER0	SDWARCDE	0	Continue with termination.
		4	Retry.
IEAVAR02			
IEAVAFR2	SDWARCDE	0	Continue with termination.
IEAVAR03			
IEAVAFR3	SDWARCDE	0	Continue with termination.
IEAVAR04			
IEAVAFR4	SDWARCDE	0	Continue with termination.
		4	Retry.

Object Module	Location of Code	Return Code	Meaning
IEAVAR05			
IEAVAFR5	SDWARCDE	0	Continue with termination.
		4	Retry.
IEAVAR06			
IEAVAFR6	SDWARCDE	0	Continue with termination.
IEAVAR07			
IEAVAR07	register 15	0	Successful execution.
IEAVAFR7	SDWARCDE	0	Continue with termination.
IEAVAX00			
IGC0009F	register 15	0	Successful execution of STAX routine.
		4	Defer has already been requested.
		8	Invalid parameter; STAX request is ignored.
IEAVBLDP	register 15	0	Successful formatting of the extent or pool.
		8	Invalid CPID or unformatted pool.
		12	Invalid subpool.
		16	Invalid cell size.
		20	An incompatible request is in process concurrently against the specified pool.
IEAVCARR			
IEAVCARR			
IEAVTTRR			
IEAVFARR	SDWARCDE	0	Continue with termination.
		4	Retry.
	SDWARCRD	1	Recording is desired.
IEAVCKEY	register 15	0	Storage key for area has successfully been changed.
IEAVCKRR	SDWARCDE	0	Continue with termination.
		4	Retry.
IEAVCSEG	register 15	0	Successful creation.
		4	Creation request not satisfied.
		8	Segment is already valid - ID is in register 0.
IEAVCSGB	register 15	0	Successful creation.
		4	Creation request not satisfied.
		8	Segment is already valid - ID is in register 0.
IEAVDELP	register 15	0	Successful deletion.
		4	Error during FREEMAIN of a deleted extent.
		8	An attempt was made to delete a pool created by NIP.
		12	An attempt was made to delete an unformatted pool or to delete an extent of a pool that has no extents.
		16	Invalid or null CPID passed.
		20	A mutually exclusive operation is taking place concurrently.
IEAVEAC0	register 15	0	Change in ASCB dispatching queue was successful.
		4	Specified ASCB not on queue.
IEAVEAT0			
IGC0004B	register 15	0	ATTACH completed normally.
		4	ATTACH was issued from a STAE exit routine.
		8	Insufficient storage for STAE control block.
		12	Invalid address of exit routine or parameter list on STAI operand of ATTACH macro instruction.
		14	System task specified JSTCB = YES, but was not itself a job step task.
		18	New task cannot be created -- invalid combination (job-step tasks and non-job-step tasks would be subtasks of the same task).
IGC044R2	register 15	0	TCB queues of current address space are usable.
		4	TCB queues are not usable -- invalid ASCB or ASXB.
		8	TCB queues were not checked.
		12	TCB dispatching queue is empty after processing by queue verifier.

Object Module	Location of Code	Return Code	Meaning
IEAVECH0			
IGC044R1	register 15	0	Do not schedule retry.
IGC044R2	register 15	0	TCB queues of current address space are usable.
		4	TCB queues are not usable -- invalid ASCB or ASXB.
		8	TCB queues were not checked.
		12	TCB dispatching queue is empty after processing by queue verifier.
IEAVEDR	register 15	0	Signal to CPU (SIGP) successfully initiated.
		4	Specified CPU is not online.
		8	Signal failed.
		12	Specified CPU is not operating.
		16	Specified CPU is a uniprocessor -- signal failed.
IEAVEED0			
IGC062	register 15	0	No 33E ABEND code.
		4	An incomplete subtask abnormally terminated with code 33E.
IEAVELK	register 13	0	Obtaining a lock was successful.
		4	The lock is already held by the caller.
		8	Lock was not obtained -- it is held by another CPU.
IEAVEMIN	Cross-memory-post ECB	0	Success.
		4	Failure.
IEAVEMRQ	register 15	0	Successful.
		4	No assignment by system resources manager.
		8	System overloaded.
		12	Unexpected ABEND.
IEAVENQ1	register 15	0	See ENQ/RESERVE Processing and DEQ Processing descriptions.
IEAVEQR	register 15	0	Real storage allocated or freed in behalf of a V = R region.
		4	Allocation delayed; wait on ECB.
		8	Real storage is available, but corresponding virtual space is not.
		16	Allocation was not possible or specified V = R region did not exist (for freeing).
IEAVERI	register 15	0	Signal to CPU (SIGP) was successfully initiated.
		4	Specified CPU is not online.
		8	Signal failed -- register 1 contains status bits.
		12	Specified CPU is not operating.
		16	Specified CPU is a uniprocessor -- signal failed.
		20	Specified CPU was taken offline during the spin routine.
IEAVERP	register 15	0	Signal to CPU was successfully initiated.
		4	Specified CPU is not online.
		8	Signal failed -- register 0 contains status bits.
		12	Specified CPU is not operating.
		16	Specified CPU is a uniprocessor -- signal failed.
IEAVEVT0	register 15		Irrelevant.
IEAVFP			
IEAVFP1	register 15	0	Successful location of page.
		4	Input virtual storage address resides in invalid segment.
		8	An internal RSM error was detected.
IEAVFP2	register 15	0	Successful location of page.
		4	Virtual storage address resides in invalid segment.
		8	An internal RSM error was detected.
IEAVFRCL	register 15	0	The cell has been returned to its pool.
		4	The cell was not allocated from this pool.
		8	The cell does not belong to any extent of this pool.
		12	The pool contains no extents or is unformatted.
		16	The CPID references an undefined pool.
IEAVFREE	CIWRETC	0	Successful PGFREE operation.
		4	PGFREE not successful.
		16	Input parameter error in a VSL entry address.

Object Module	Location of Code	Return Code	Meaning
IEAVFXLD	CIWRETC	0	Request satisfied immediately.
		4	Invalid page address in a VSL entry or internal RSM error.
		8	Request being processed; final completion of the request will occur when paging I/O is complete.
		16	Input parameter error in VSL entry.
		20	SQA or LSQA space not available for required GETMAIN operation.
IEAVGCAS			
IEAVGCAS	register 15	0	Successful create processing.
		4	Unsuccessful create processing; locking hierarchy violation.
IEAQSPET	register 15	0	Successful clean-up.
		4	Unsuccessful clean-up.
IEAVGFAS	register 15	0	Successful clean-up.
		4	Unsuccessful clean-up; locking hierarchy violation.
IEAVGFA	register 15	0	All pages allocated and available for immediate use.
		4	Request in process; asynchronous completion.
		8	Stage I Swap-In rejected due to lack of available frames.
IEAVGFRR	register 15	0	Continue termination.
		4	Retry.
IEAVGM00			
GMBRANCH			
FMBRANCH			
RMBRANCH			
CRBRANCH			
GLBRANCH			
IGC004			
IGC005			
IGC010			
IGC120			
MRELEASR	register 15	0	Successful GETMAIN or FREEMAIN.
		4	Unsuccessful GETMAIN or FREEMAIN.
		8	For a GETMAIN for SQA or LSQA, a real frame is not available; for a FREEMAIN of a local page that is fixed, the TCBEOTFM bit is set to one.
IEAVGPRR			
IEAVGPRR	SDWARCDE	0	Continue with termination.
		4	Retry.
	SDWARCRD	1	Recording is desired.
IEAVGTCL	register 15	0	A cell has been allocated.
		4	No cells are available.
		8	The pool pointers to available cells have been destroyed.
		12	The pool has never been formatted.
		16	The CPID specified has never been defined to the system with a IEAVBLDP operation.
IEAVID00			
IGC041	register 15	0	Successful completion.
		4	Entry point name and address already exist.
		8	Entry point name duplicates the name of a load module currently available.
		12	Entry point address is not within an elegible load module.
		16	Caller is not operating with a PRB.
		20	An IDENTIFY macro instruction was previously issued using the same entry point name by a different address.
		24	Invalid parameter list.
		32	Invalid extent list or module address.
		40	Unexpected system error.
FRRSVC41	register 15	0	Continue with termination of routine.
		4	Retry at error exit in IEAVID00.

Object Module	Location of Code	Return Code	Meaning
IEAVIOCP			
IEAVCPBR	register 15	0	All PCBs encountered were processed without having to schedule IEAVIOCP.
		4	IEAVIOCP had to be scheduled to process one or more PCBs found on either the input string or on the PCB I/O active queues.
IEAVITAS	register 15	0	Successful processing.
		4	Function not performed.
IEAVLK00			
IGC009	register 15	0	DELETE routine found module.
		4	DELETE routine could not find module.
IEAVLK02			
IEAPPGMX	register 15	0	Clean-up for EXIT, DELETE, ABEND, or EDT succeeded.
		4	Clean-up for EXIT, DELETE, ABEND, or EDT failed.
IEAPPGMA	register 15	0	Clean-up for EXIT, DELETE, ABEND, or EDT succeeded.
		4	Clean-up for EXIT, DELETE, ABEND, or EDT failed.
IEAVLK03			
FRRPGMMG	register 15	0	Continue with termination.
FRRPGMX	register 15	4	Request retry processing.
IEAVMASV			
IEAVTPUT	termination ECB		Posted to indicate that there are no more active monitoring terminals or consoles.
IEAVMFRR	register 15	0	Continue with termination.
		4	Attempt a retry of the failing module.
IEAVMWTO	SVRB (XVWQEID)	0	No message was put out.
	SVRB (XVRETCOD)	4	Number of lines in the write parameter list (WPL) was zero.
		8	The message identification passed in register 0 did not match any of the WQE message identifications currently on the WQE chain. This could result from : 1) register 0 not being zero for the first multiple line WTO(MLWTO) service request. 2) The multiple line message was going to a console that encountered an I/O error. The multiple line message was deleted when the console functions were switched from the failing console to another console. Multiple line messages are not switched. 3) The user lost the message identification passed to him in register 1 after the execution of the WTO or WTOR macro instruction.
		12	A new multiple line message (MLWTO) consists only of an end message.
		16	Routing code 11 (WTP) was the only routine code specified.
		20	The multiple line message (MLWTO) was sent only to hardcopy.
IEAVOUT	CIWRETC	0	Successful page-out.
		4	Invalid address in VSL.
		12	Page-out not done for one or more pages.
		16	Input parameter error in VSL entry.
		20	Internal error detected.
IEAVPCB	register 15	0	Successful processing.
		4	Not enough PCBs to satisfy build request.
IEAVPIX	register 15	0	Current user cannot be redispatched; page-in is required or allocation is deferred.
		4	Current user may be redispatched.
		8	A local protection exception has been detected.
		12	An internal error has occurred.
IEAVPREF	register 15	0	No frame was found that is eligible to be stolen.
		non-zero	Real block number (RBN) of a frame to be stolen.
IEAVPRTO	register 15	0	The region was successfully allocated.
		4	Insufficient virtual space is available to back up the V = R request.
		8	Invalid request.
		12	Insufficient contiguous real pages are available to immediately satisfy the V = R region request.
		16	The available allocatable V = R space has been decreased so that the V = R request cannot be honored.

Object Module	Location of Code	Return Code	Meaning
		20	The private area is fragmented with system space such that the region request cannot be honored.
IEAVPSI			
IGC112	register 15	0	Request was satisfied successfully.
		4	Request was not honored.
IGC113			
IEAVPSIB			
IEAVPSII	register 15	0	Operation was completed successfully; ECB has been posted if passed as input.
		4	Operation was not completed; invalid address in VSL entry or register. ECB has been posted complete. ABEND is issued for the requestor's TCB.
		8	Operation is proceeding; ECB will be posted when paging I/O completes.
		16	Input parameter error. Either an invalid combination of operation and option codes was specified on entry, or the VSL, ECB, or TCB addresses supplied were invalid. No ECB is posted. ABEND is issued for the requestor's TCB.
NEXTVSL	register 15	0	Successful validation.
		4	Input VSL invalid.
		8	There are no further VSL entries.
IEAVRCF			
IEAVRCF	register 15	0	Requested action completed. The ECB is not posted.
		4	Requested action in process; it will complete asynchronously. The ECB will be posted when all frames are offline.
		8	Unsuccessful action; either recovery could not be done or one or more frames requested to go offline contain permanently fixed data.
		12	A frame specified by RBN and count exceeds the known storage configuration. No action has been taken and no status is provided for any frame.
		16	An online or offline request incorrectly overlaps a pending offline request, i.e. the overlap is not exact.
		20	Parameter error; no request is indicated on the input parameter.
IEAVRCFI	register 1	0	Frame was accepted by interception. Unchanged frame was rejected by interception because no request existed for it in a root PCB.
IEAVRELS			
IEAVRELS	CIWARETC	0	Successful release.
		4	All pages not released successfully.
		16	Input parameter error in a VSL entry.
IEAVRELV	register 15	0	All pages were released successfully.
		4	All pages not released successfully.
		8	Release for page addressed in register 3 has been deferred.
IEAVRELF	register 15	0	The input page has been successfully released.
		4	The page has not been successfully released.
		8	The input page was not on deferred status but has been put on deferred status.
IEAVRTI1			
IEAQPGTM	register 15	0	Always set on return from this entry.
IEAVRTVR	register 15	0	TQE is valid.
		4	TQE contains some invalid data.
		8	Some part of the TQE cannot be accessed.
IEAVRTOD			
IEAVRINT	register 15	0	Successful operation.
		Not 0	Unsuccessful.
IEAVRSSC	register 15	0	Successful operation.
		Not 0	Unsuccessful.
IEAVRNOT	register 15	0	Successful operation.
		4	Failure.
IEAVRTOO			
IGC0004F	register 15	0	Successful operation.
		8	Unsuccessful; no usable TOD clocks in the system. Error return address specified.

Object Module	Location of Code	Return Code	Meaning
IGC0004G	register 15	0	Successful operation.
		8	Unsuccessful because: • For task type requests, there exists no usable CPU timer in the system or the task has affinity to the executing CPU and the executing CPU has an unusable CPU timer. • For real or wait type requests, there exists no usable TOD clock in the system or no CPU in the system has a usable clock comparator and TOD clock.
IEAVRT01			
IGC0001A	register 15	0	Successful operation.
		8	Unsuccessful - no usable clock in the system and an error return address has been specified.
IEAVRT02			
IEAVRDIE	register 15	0	TQE was successfully enqueued.
		4	Unsuccessful - necessary clocks not available.
IEAVSETS			
IGC079	register 15	0	STATUS routine completed normally.
		4	Specified task is not a subtask of the caller's task.
IGC07902	register 15	0	STATUS routine completed normally.
IGC07903	register 15	0	STATUS routine completed [normal exit].
IEAVSOUT			
IEAVSOUT	register 15	0	Successful swap-out.
		4	Swap not valid for this address space.
		8	Unable to swap out this address space at this time because: • Changed bad page in user's LSQA. • GETCELL for SRB to schedule IEAVPIOP failed. • GETMAIN to obtain SQA for SPCT failed. • Unable to get enough PCBs from IEAVPCB to swap out the address space. • ASCBFMCT is in error.
IEAVSQA	register 15	0	Successful allocation.
		4	Allocation failed - No real storage available.
IEAVSTAA	register 15	0	Continue with termination.
		4	Retry at IEAVMQWR.
IEAVSTAO	register 15	0	STA or ESTA request was processed.
		4	ESTAE OV has been requested, but the last SCB is nonexistant, not owned by user's RB, or is not an ESTAE create is performed.
		8	A previous ESTAE create was issued with the BRANCH = YES option. The create was performed and previously-created SCB was eliminated. [Error exit following STAE or STAI request].
	register 15	4	Insufficient storage.
		8	STAE issued in a STAE exit or a cancel or overlay request was issued with no SCB on the queue.
		12	STAI was not issued by the ATTACH macro instruction or STAI was issued with no TCB operand.
		16	A cancel or overlay was requested but SCB is not a STAE SCB or is not owned by requestor's RB. or An unexpected error was encountered while processing the request. [Error exit following STAE, ESTAI, or ESTAR request.]
	register 15	12	Invalid cancel request.
		16	Unexpected error occurred.
		20	Insufficient storage.
IEAVSWCH	register 15	0	Operation completed normally.
		4	No action. The request does not result in a message to either console or the hardcopy log.
IEAVSY50	register 15		Irrelevant.
IEAVTABD	register 15	0	Snap processing was successful. or No dump options specified by CHNGDUMP, the requestor, or SYS1.PARMLIB. or No SYSABEND or SYSUDUMP DD was specified.

Object Module	Location of Code	Return Code	Meaning
		4	DCB not open or UPR on DCB.
		8	Invalid TCB, UPR, on TCB, or insufficient storage.
		12	Invalid TCB type.
IEAVTAS2			
	RTM2WA		
	RTM2RCDE	0	Continue with termination.
		4	Retry portion of recovery is requested.
IEAVTAS3			
	RTM2WA		
	RTM2RCDE	0	Percolation recovery has been set up. or Continue with termination (error exit).
		4	Retry recovery has been set up.
IEAVTERM	register 15	0	Successful termination services performed.
		4	Function not performed.
IEAVTEST			
IEAVTEST	register 15	0	Task authorized.
		4	Task not authorized.
		8	Invalid codes for authorization or function -- code not found in matrix.
IGC119	register 15	0	Task authorized.
		4	Task not authorized.
		8	Invalid codes for authorization or function -- code not found in matrix.
IEAVTFMT	register 15	0	Always returns a zero return code.
IEAVTFRD	register 15	8	Normal end of SUMDUMP read.
		12	End of SUMDUMP pseudo address space data reached trying to read next header.
		16	No SUMDUMP data contained in this dump.
		20	No CVT in dump.
		24	No GDA in dump.
		28	Unable to obtain sufficient storage for reconstruction buffer.
IEAVTMSI	register 15	0	R/T management initialization was successful.
		4	R/T management initialization failed.
IEAVTMTC	register 15	0	Address space termination controller is initialized.
		4	Initialization of address space termination controller failed.
IEAVTRER	register 15	0	Asynchronous recording request was successful.
		4	Request has been scheduled directly from caller's buffer -- no intermediate buffering.
		8	Record for LOGREC has been truncated to fit in available space.
		12	Record has been lost -- insufficient space.
		16	Record has been lost -- error in processing.
		20	Recording request facility is inactive.
IEAVTRET	register 15	0	Recording task successfully initialized.
		4	Initialization of recording task failed.
IEAVTRTE	register 15	0	Successful completion.
		4	Failure during subtask termination processing.
IEAVTRV	register 15	0	Successful translation.
		4	Unsuccessful translation.
IEAVTSBP	register 15	0	All SCBs properly purged and/or transferred.
		4	No TCB address specified. or Unexpected error occurred while processing request.
IEAVTSDR	register 15	0	SVC dump resource manager has completed.
IEAVTSDX	register 15	0	Dump scheduled.
IEAVTSKT	register 15	0	Successful completion.
		4	Failure during subtask termination processing.

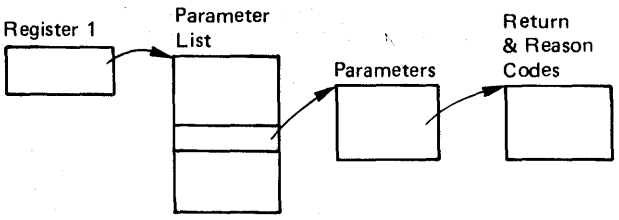
Object Module	Location of Code	Return Code	Meaning
IEAVTSSD	register 15	0	Summary dump complete.
IEEVTSLP	register 15	0	Normal exit.
		4	Parmlist has a zero for both work area pointers.
		8	Insufficient room for FRRs in the RTS environment.
		12	Insufficient room for FRRs in the RT2 environment.
		16	FRR recovery completed and SLIP processing canceled.
IEAVVWTO	register 1 (SVRB (XVWQEID) register 15		Message identification or zero if no message sent.
	SVRB (XVRETCOD)		See IEAVMWTO above.
IEAVXDOM	register 15	0	Normal exit.
		4	Error in register 0 input value.
		8	Error in a message identification.
IECDAFT1			
IECDADCB	register 15	0	DCB formatted successfully and there are IOBs (ICBs , LCBs) to be formatted.
		4	Either the access or format service routine terminated with a return code of 4, or there are not to be any IOB (ICB, LCB) formatted with this DCB.
IECDADEB	register 15	0	DEB formatted successfully.
		4	Either the access or format service routine terminated with a return code of 4.
IECDAIOB	register 15	0	IOB (ICB, LCB) formatted successfully.
		4	Either the access or format service routine terminated with a return code of 4.
IECDAFMT	register 15	0	Always.
IECIOFMT	register 15	0	Always.
IECIOFT1			
IECIOEXD	register 15	0	EXCPD (XDBA) formatted successfully.
		4	A return code of 4 from the format service routine.
IECIOUCB	register 15	0	UCB formatted successfully.
		4	Either the access or format service routine terminated with a return code of 4.
IEECB801	register 15	non-zero	Error in MLWTO or TPUT operation.
IEECB860	register 15	0	STAE created.
		non-zero	STAE creation failed.
IEECB900	register 15		Irrelevant.
IEECB901	register 15		Irrelevant.
IEECB904	register 15		Irrelevant.
IEECB906	register 15	0	Normal exit.
		4	No ESTAE environment.
IEECB907	register 15	0	Normal exit.
		4	WTO error.
IEECB908	register 15	0	Normal exit.
		4	Parameter list in error; no message written.
IEEMB803	register 15	0	Log initialized successfully.
		4	Return to ABEND/STAE processing for retry. Meaning depends on time of error: • After OPEN: log not initialized; log data set could not be opened. • After BLDCPOOL: log not initialized; storage not available. • After PUT: internal ABEND macro instruction is issued.
		non-zero	Log is not initialized: after issuing IEFSSREQ (JES2 interface) or SVC 99 (dynamic allocation interface).
IEEMB806	register 15	0	ESTAE environment created.
		4	ESTAE environment not created.
IEEMB812	register 15	0	The IPS has been changed.
		4	The IPS data in IEAIPSxx is invalid.
		8	IEAIPSxx list cannot be found in SYS1.PARMLIB.
		12	An I/O error occurred in reading IEAIPSxx.
		16	SYS1.PARMLIB cannot be opened.

Object Module	Location of Code	Return Code	Meaning
IEEMB825	register 15	0	No retry necessary.
IEEMB830	register 15	0	Record moved to SMF buffer.
		4	Truncated record written.
		8	Attempting to write record that is less than 18 bytes.
		16	SMF not recording.
IEESTPRS	register 15	4	Subroutine can't stop system because one CPU is disabled for machine check interruption.
		8	Subroutine can't stop system because one online CPU was in check stop state.
		12	Error code received from IPC routine.
		16	Recovery routine has gotten control. System is restored to subroutine entry state.
		20	A CPU has failed to restart in the FLIH.
IEEVDEV	register 15	0	For function code x'00': an operational path to the device is available. For function code x'01': other paths exist; removing CPU does not remove last path. For function code x'02': other paths exist; removing channel does not remove last path.
		4	For function code x'00': the path, channel, or CPU needed to bring the device online is unavailable. For function code x'01': no other paths exist; the CPU is the last path to the device. For function code x'02': no other paths exist; the channel is the last path to the device.
		8	No working storage available; function has not been performed.
		12	No operational paths to the device are available.
		16	Device remains offline.
IEEVPTH	register 15	0	Vary is successful (online request).
		8	Meaningless: If this appears, a 'processor failure' message is required.
		12	Same as for an offline request.

Object Module	Location of Code	Return Code	Meaning
IEE0303D	register 15	0	Chain manipulated successfully.
		4	CIB or CSCB pointers don't match.
		8	Error related to problem-program-issued situation.
IEE0703D	register 15	0	Successful processing.
		4	PROC name or TASK name length over 8 characters or invalid command operand or CSCB/CIB count limit reached.
IEE0803D	register 15	0	Successful completion of SVC 34 processing.
		8	ASID/ASCB failure in memory-creating LOGON command.
IEE1403D	register 15	4	Operand missing.
IEE3603D	register 15	0	Path to device is available.
		non-zero	Path to device is unavailable - device cannot be varied online.
IEE5103D	register 15	8	STAE processing successful.
IEE7103D	register 15	4	Error in obtaining storage in IEAVMNTR.
IEE9403D	register 15		Irrelevant.
IEFAB4A0	register 15	0	Successful completion.
		4	GETMAIN or ESTAE error (as indicated by the reason code).
		8	ESTAE error.
		12	ESTAE error.
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB4A2	register 15	0	Successful completion.
		4	GETMAIN error.
IEFAB4A3	register 15	0	Successful completion.
		4	GETMAIN error.
IEFAB4A6	register 15	0	Successful completion.
		4	GETMAIN error.
IEFAB4A8	register 15	0	Successful completion.
		4	GETMAIN error.
IEFAB4B0	register 15	0	Successful completion.
IEFAB4B2	register 15	0	Successful completion.
IEFAB4DD	SDWA, if it exists; otherwise, register 15	0	Continue with termination.
IEFAB4DE	SDWA, if it exists; otherwise, register 15	0	Continue with termination.
IEFAB4EA	SDWA, if it exists; otherwise, register 15	0	Continue with termination.
IEFAB4EC	register 15	0	Lock or unlock successfully completed.
		4	ESTAE failure.
		8	Mask of groups to be locked is zero because none of the UCBs in the input list could be found.
		12	GETMAIN failure.
IEFAB4ED	SDWA, if it exists; otherwise, register 15	0	Continue with termination.
IEFAB4EE	register 15	0	Successful completion.

Object Module	Location of Code	Return Code	Meaning
IEFAB4EF	pointed to by second parameter[1]	0	Success.
		4	Failure.
		8	Failure.
IEFAB4E0			Irrelevant.
IEFAB4E1	SDWA, if it exists; otherwise, register 15	0	Continue with termination.
IEFAB4E2	SDWA, if it exists; otherwise, register 15	0	Continue with termination.
IEFAB4E3	register 15	4	Allocation failed.
IEFAB4E4	SDWA, if it exists; otherwise, register 15	4	Retry requested.
IEFAB4E6	SDWA, if it exists; otherwise, register 15	0	Continue with termination.
IEFAB4E8	SDWA, if it exists; otherwise, register 15	0	Continue with termination.
IEFAB4FA	register 15	0	Successful completion.
		4	GETMAIN error.
		16	Cancel ECB posted.
IEFAB4FB	register 15	0	Successful.
IEFAB4FC	register 15	0	Successful completion.
		4	GETMAIN error; or not enough space in TIOT for new entry (as indicated by the reason code).
IEFAB4FD	register 15	0	Successful completion.
IEFAB4FE	register 15	0	Successful completion.
IEFAB4F0	register 15	0	Successful completion.
		4	The job cannot enqueue on the resource.
		8	The job is already enqueued on the resource.
IEFAB4F1	register 15	0	Volume can be used.
		4	Volume cannot be used.
IEFAB4F2	register 15	0	Successful completion.
		4	Recall this module when affinity requests are allocated.
IEFAB4F4	pointed to by second parameter[1]	0	Success.
		4	Failure.
IEFAB4F5	pointed to by second parameter[1]	0	Success.
		8	Failure.
IEFAB4F6			Irrelevant.
IEFAB4F8	register 15	0	Read successful.
		4	Read unsuccessful.
IEFAB4F9	register 15	0	Read successful.
		4	Read unsuccessful.

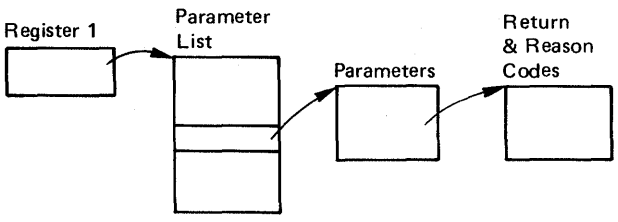
[1] The return code is located as follows:



Object Module	Location of Code	Return Code	Meaning
IEFAB4UV	register 15	0	Successful completion.
		4	Input unit name invalid.
		8	Input unit address not associated with input unit name.
		12	Device group split.
		16	GETMAIN error.
IEFAB421	register 15	0	Successful completion.
		4	No requests allocated.
		8	Some requests allocated.
IEFAB422	register 15		Irrelevant.
IEFAB423	register 15	0	Successful completion.
		4	GETMAIN error; or too many units required; or invalid unit affinity; or invalid volume sequence count; or error in called routines (as indicated by the reason code).
IEFAB424	register 15	0	Successful completion.
		4	GETMAIN error; or invalid unit parameter; or not enough units to satisfy request (as indicated by the reason code).
IEFAB425	register 15	0	Successful completion.
		4	Line is ineligible; or GETMAIN error; or no eligible lines (as indicated by the reason code). Note: See the called routine, IEFAB434, for additional return codes.
IEFAB426	register 15	0	Successful completion.
		4	Too many units required.
IEFAB427	register 15	0	Successful completion.
		4	Subsystem failed request; or called routine (IEFAB428) detected error (as indicated by the reason code).
IEFAB428	register 15	0	Successful completion.
		4	GETMAIN error; or called routine (IEFAB4FC) detected error (as indicated by the reason code).
IEFAB430	register 15	0	Successful completion.
		4	GETMAIN error; or two requests for same unit is invalid (as indicated by the reason code).
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB431	register 15	0	Successful completion.
		4	DADSM error; or error detected by called routine (IEFAB428) (as indicated by the reason code).
IEFAB432	register 15	0	Successful completion.
		20	Volume mounted on unit in unserialized group.
		32	Recoverable DADSM error.
		36	Nonrecoverable DADSM error; or volume mounted on ineligible device type (as indicated by the reason code).
		40	Volume mounted on unit in unserialized generic.
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB433	register 15	0	Successful completion.
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB434	register 15	0	Successful completion.
		12	Recoverable DADSM error.
		16	Nonrecoverable DADSM error.
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.

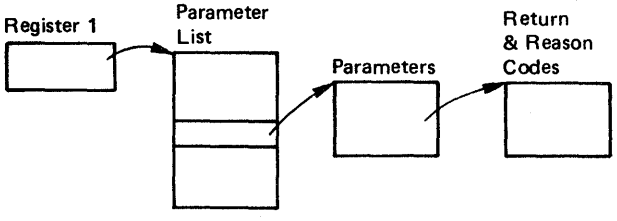
Object Module	Location of Code	Return Code	Meaning
IEFAB435	register 15	0	Successful completion.
		4	GETMAIN error.
		20	Volume mounted on unit in unserialized group.
		24	Volume mounted on unit in unserialized generic.
		28	Volume allocated on another unit.
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB436	register 15	0	Successful completion.
		4	GETMAIN error; or no space on storage volumes (as indicated by the reason code).
		36	Failing error from IEFAB434.
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB438	register 15		The DDR count is passed back in register 15 as a return code.
IEFAB440	register 15	0	Successful completion.
		4	GETMAIN error.
IEFAB441	register 15	0	Successful completion.
		4	Volume in use by system function; or volume mounted on incompatible device type; or volume allocated and waiting not allowed; or volume mounted on ineligible fixed device; or IEFAB434 detected an error; or IEFAB49C detected an error. The type of error is indicated by the reason code. (See description of IEFAB442 for other possible return codes.)
IEFAB442	register 15	0	Successful completion or non-failing error.
		4	Too many units required; or error from IEFAB428; or affinity request is a demand request and can't be changed to another unit (as indicated by the reason code).
IEFAB451	register 15	0	Success.
		4	Error.
IEFAB452	pointed to by third parameter[1]	0	Success.
		4	STEPCAT syntax or data-set-type error -- failure (as indicated by the reason code).
IEFAB453	pointed to by third parameter[1]	0	Success.
		4	Referenced DD not allocated -- failure.
IEFAB457	pointed to by third parameter[1]	0	Success.
		4	Volume reference to GDG--failure; or volume reference to DD in a step that was not executed -- failure (as indicated by the reason code); or MSS select failure.
IEFAB458	pointed to by third parameter[1]	0	Success.
		4	Failure.
		8	Failure.
IEFAB459	pointed to by third parameter[1]	0	Success.
		4	Failure.
IEFAB461	pointed to by third parameter[1]	0	Success.
		4	Failure.

[1] The return code is located as follows:



Object Module	Location of Code	Return Code	Meaning
IEFAB464	pointed to by third parameter[1]	0	Success.
		4	Failure.
IEFAB466	pointed to by third parameter[1]	0	Success.
		8	Failure.
IEFAB469	pointed to by third parameter[1]	0	Success.
		4	Failure.
		8	Failure.
IEFAB470	pointed to by third parameter[1]	0	Success.
		4	Failure.
IEFAB471	register 15	0	Successful completion.
		4	GETMAIN error. Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB472	register 15	0	Successful completion.
		4	GETMAIN error; or two demand requests in same multi-unit, multi-generic group want different generic device types; or error detected by IEFAB4FA (as indicated by the reason code).
IEFAB473	register 15	0	Successful completion.
		4	Error reading label.
		8	Tape with non-standard labels not handled.
		12	ANSI tape in non-ANSI system.
		16	Duplicate volume serial number.
IEFAB474	register 15	0	Successful completion.
IEFAB475	register 15	0	Successful completion; or non-failing error (as indicated by the reason code).
		4	Not enough eligible devices; or volume mounting not allowed; or GETMAIN error (as indicated by the reason code). Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB476	register 15	0	Successful completion.
		4	GETMAIN error. Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB477	register 15	0	Successful completion.
		4	GETMAIN error; or request not scratched (as indicated by the reason code). Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB478	register 15	0	Successful completion; or non-failing error (as indicated by the reason code).
		4	GETMAIN error; or System Resources Manager error (as indicated by the reason code). Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.

[1]The return code is located as follows:



Object Module	Location of Code	Return Code	Meaning
IEFAB479	register 15	0	Successful completion; or non-failing error (as indicated by the reason code).
		4	More than one unit required for request; or unit in use by system function; or allocation can't consider offline units; or volumes cannot be mounted; or requested device is console; or volume mounted is permanently resident or reserved; or allocation cannot wait for allocated units (as indicated by the reason code).
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB48A	register 15	0	Successful completion.
		4	Allocation environment changed.
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for hits module in Section 3, Program Organization) for other possible return codes.
IEFAB480	register 15	0	Successful completion.
		4	Preliminary solution cannot be found.
		8	Required request not satisfied.
		12	Error in input flags.
		16	GETMAIN error.
IEFAB481	register 15	0	Successful completion.
IEFAB485	register 15	0	Successful completion.
		4	Volume mounting not allowed; or GETMAIN error; or cannot use offline or allocated units (as indicated by the reason code).
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB486	register 15	0	Successful completion.
		4	GETMAIN error; or one generic cannot cover entire request (as indicated by the reason code).
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB487	register 15	0	Successful completion; or non-failing errors (as indicated by the reason code).
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB488	register 15	0	Successful completion.
		4	Job cancelled by operator; or GETMAIN error by called routine (as indicated by the reason code).
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB489	register 15	0	Successful completion.
		4	GETMAIN error; or System Resources Manager error; or failing error from called routines (as indicated by the reason code).
		20	Retry needed.
IEFAB49A	register 15	0	Successful completion.
IEFAB49B	register 15	0	Successful completion.
		4	Unable to read label.
		24	Unable to mount MSS volume.
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB49C	register 15		Note: No return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for possible return codes.
IEFAB490	register 15	0	Successful completion.
		4	Wrong DSORG/DISP; or too many concatenated DDs; or GETMAIN error (as indicated by the reason code).

Object Module	Location of Code	Return Code	Meaning
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB491	register 15	0	Successful completion or non-failing error (as indicated by the reason code).
		4	Allocation environment changed; or GETMAIN error; or operator cancelled (as indicated by the reason code).
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB492	register 15	0	Successful completion.
		4	Error occurred.
IEFAB493	register 15	0	Successful completion.
		44	Error issuing ESTAE macro.
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB494	register 15	0	Successful conpletion.
			Note: No other return codes are explicity set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB495	register 15	0	Successful completion.
		4	GETMAIN error.
		8	MSS volume not available.
		20	MSS volume not defined.
		24	Unable to mount MSS volume.
		28	MSS volume inaccessible.
		32	The specified virtual volume group (VVGRP) name does not exist.
		36	Neither space nor virtual volume group (VVGRP) specified for nonspecific MSS request.
IEFAB496	register 15	0	Successful completion.
		12	Operator replied 'NO' to mount message.
		16	CANCEL ECB posted.
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFAB498	register 15	0	Successful completion.
		8	MVCA with matching ASID found.
		12	MVCA with matching ASID not found.
IEFAB499	register 15	0	Successful completion.
		4	GETMAIN error.
IEFAB4UV	register 15	0	Successful completion.
		4	Input unit name invalid.
		8	Input unit address not associated with input unit name.
		12	Device group split.
		16	GETMAIN error.
IEFBB401	register 15	0	Successful completion.
		4	Step not successfully allocated.
IEFBB402	register 15	0	Step to be run.
		4	Job to be failed.
		8	Step to be bypassed.
IEFBB404	register 15	0	Successful completion.
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFBB410	register 15	0	Successful completion.
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.

Object Module	Location of Code	Return Code	Meaning
IEFBB412	register 15	0	Successful completion.
		4	Job to be failed.
IEFBB414	register 15	0	Successful completion.
			Note: No other return codes are explicitly set by this module; see the routines called by this module (listed in the module description for this module in Section 3, Program Organization) for other possible return codes.
IEFBB416	register 15	0	Successful completion.
		4	GETMAIN error.
IEFDB4A0	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFDB4A1	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFDB4D0	register 15	12	An error occurred in dynamic allocation. The DAIR function block contains the error reason code.
		16	No TIOT entries were available for use.
		20	The ddname requested is unavailable.
		24	The dsname requested is a member of a concatenated group.
		28	The ddname or dsname specified is not allocated.
		32	The requested data set was previously permanently allocated, or was allocated with a disposition of NEW and was not deleted. DISP = NEW cannot now be specified.
		36	Error in IKJEFCIR service routine.
		40	The return area provided for qualifiers was exhausted and more index blocks exist. A larger return area is needed.
		44	The previous allocation specified a disposition of DELETE for this non-permanently allocated data set.
		48	Reserved.
		52	Request denied by installation exit.
IEFDB4FB	register 15	0	Successful.
IEFDB4FD	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFDB4FE	register 15	0	Successful completion.
		4	Volume not mounted.
		8	DSCB not in VTOC.
		12	Permanent I/O error.
		16	Invalid work area pointer.
IEFDB4FF	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes in Section 6, Diagnostic Aids.
IEFDB4F8	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFDB4F9	register 15	0	Successful completion or GETMAIN error. (Success or error does not affect caller's processing.)
IEFDB400	register 15	0	Success.
		4	Environmental error; or resource unavailable; or I/O error.
		8	Request failed by user exit.
		12	Parameter list error. (Error reason code contains explanatory value.)
IEFDB401	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFDB402	SDWA, if it exists; otherwise, register 15	0	A zero is always returned; successful or unsuccessfull completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFDB403	SDWA, if it exists; otherwise,	0	Continue with termination.

Object Module	Location of Code	Return Code	Meaning
	register 15		
IEFDB410	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFDB411	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFDB412	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFDB413	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFDB414	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFDB417	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFDB418	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFDB450	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFDB460	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes s" in Section 6, Diagnostic Aids.
IEFDB470	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFDB480	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFDB481	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFDB490	register 15	0	A zero is always returned; successful or unsuccessful completion is indicated in the reason code -- see the list of dynamic allocation reason codes in "Allocation/Unallocation Reason Codes" in Section 6, Diagnostic Aids.
IEFIB600	register 15	0	Successful completion.
		4	JCL error exists.
		8	I/O error occurred.
		36	Error occured in restart.
IEFIB605	register 15	0	Successful completion.
		36	Error in journal merge or DSDR processing.
IEFIB660	register 15	0	Successful completion.
IEFICATL	register 15	0	Catalogs were opened successfully.
		non-zero	Catalogs were not opened successfully.
IEFICPUA	register 15	0	One or more required CPUs are on-line.
		4	None of the required CPUs are on-line.
IEFIRECM	register 15		Irrelevant.
IEFISEXR	register 15		Irrelevant.
IEFJRECM	register 15		Irrelevant.
IEFJSREQ	register 15	4	The requested sybsystem does not perform the function indicated by the function code in the SSOB.
		8	The requested subsystem is inactive -- there is no SSVT for it.
		12	The subsystem name in the SSIB is invalid -- there is no SSCVT with that subsystem name in it.

Object Module	Location of Code	Return Code	Meaning
		16	The request cannot be satisfied because: • The function code in the SSOB is larger than the number of functions supported by the subsystem. - or - • The SSOB pointer is zero. - or - • Both the SSIB pointers (one in the SSOB, the other in the JESCT) are zero.
		20	Either the SSIB or SSOB have an invalid length or format.
		20	Either the SSIB or SSOB have an invalid length of format.
IEFQB550	register 15	0	Success.
		non-zero	0B0 ABEND was issued.
IEFQB555	register 15	0	Success.
		non-zero	0B0 ABEND was issued.
IEFSD061	register 15	0	Success.
IEFTB72I	register 15	0	Successful completion.
		4	Cancel job.
IEFVHF	register 15	0	Interpreter processing successful.
		4	JCL error occurred.
		8	I/O error occurred.
IEFVHN	register 15	0	Converter processing successful.
		4	JCL error occurred.
		8	I/O error occurred.
IEFXB500	JNLPARM	64	Journal does not exist.
		128	Journal error.
IEFXB601	register 15	0	Successful SWA reconstruction.
		36	Error in accessing job journal or in SWA reconstruction.
IEFXB602	register 15	0	Successful processing of SWA manager function.
IEFXB604	register 15		Irrelevant.
IEFXB609	register 15	0	Successful completion.
		36	Unsuccessful open of checkpoint data set; or unexpected ABEND occurs.
IEFXVNSL	register 15	4	Always returned failure (user routine to read non-standard labels was not included in system).
IGX00013	register 15	0	Requested options were valid and SYNCH was done to IRBMFDTA to collect MF/1 measurements.
		4	One or more invalid options were detected or no measurement options were detected. MF/1 measurements were not initialized and SYNCH to IRBMFDTA was not done.
		8	The ENQ name SYSZREMF.ACTIVE was not available and not held by the calling task, or MF/1 initialization has already been performed; and SYNCH to IRBMFDTA was not taken.
IGX00014	register 15	0	Normal Return.
		4	MFDATA SVC routine has not been previously executed; no interval data returned.
IKJEFLEA	register 15	0	Parse or command was successful.
		4	Parse unable to prompt because the no-prompt bit in the UPT is on.
		8	The cancel ECB in the CSCB has been posted, indicating an attention interruption, a disconnected line, or a cancel.
		12	Invalid parameters have been detected by parse, command scan, or IKJEFLGM.
		16	An attempt to obtain storage (via a GETMAIN) has failed.
		20	The terminal user entered a LOGOFF command, or a LOGON RECONNECT has been entered and the user has been reinstated in his former address space.
IKJEFLG	register 15	0	The attention exit was successful.
		non-zero	An error occurred in the attention exit.

Object Module	Location of Code	Return Code	Meaning
IKJEFLGB	register 15		Upon return to ABEND processing:
		0	Retry is not to be scheduled.
		4	Retry has been scheduled.
IKJEFLGM	register 15	0	The message handler was successful.
		4	A PUTGET was issued, but the UPT indicates that no prompting is to be done.
		8	The ECB passed to IKJEFLGM has been posted, indicating an attention interruption or a disconnected line.
ILRACT	register 15	0	Function successful.
		8	Record could not be found on SYS1.STGINDEX.
		20	I/O error accessing SYS1.STGINDEX.
		28	Storage could not be obtained for either a workarea, I/O buffer space, or LSQA space for rebuilding the ASPCT.
ILRFMT00	register 15	0	Function successful.
		4	Not enough storage.
ILRFMTCV			
ILRFMTC	register 15	0	Function successful.
		4	Unable to format blocks.
ILRFMTH	register 15	0	Function successful.
		4	Unable to format blocks.
ILRFMTV	register 15	0	Function successful.
		4	Unable to format blocks.
		8	LGVT inaccessible.
ILRFMTPG	register 15	0	All control blocks format.
		4	At least one error in format.
		8	Unable to locate or format the PART.
ILRFMTSW	register 15	0	All control blocks format.
		4	At least one error in format.
		8	Unable to locate or format the SART.
ILRFRR01			
ILRVACE	register 15	0	Element passed all tests.
		4	Element contains bad data.
		8	Element is not an ACE.
ILRVACEQ	register 15	0	No errors were detected.
		4	Elements with bad data were removed.
		8	Damaged queue was reconstructed.
		24	Input parameters were invalid.
ILRVACQ2	register 15	same as ILRVACEQ	
ILRVAIA	register 15	0	Element passed all tests.
		4	Element contains bad data.
		8	Element is not an AIA.
ILRVAIAC	register 15	0	Element passed all tests.
		4	Element contains bad data.
		8	Element is not an AIA/ACE.
ILRVAIAQ	register 15	0	No errors were detected.
		4	Elements with bad data were removed.
		8	Damaged queue was reconstructed.
		24	Input parameters were invalid.
ILRVASGQ	register 15	0	No errors were detected.
		4	Elements with bad data were removed.
		8	Damaged queue was reconstructed.
		24	Input parameters were invalid.
ILRVIOE	register 15	0	Element passed all tests.
		4	Element contains bad data.
		8	Element is not an IOE.
ILRVIOEQ	register 15	same as ILRVACEQ	
ILRVIORB	register 15	0	Element passed all tests.
		8	Element is not an IORB-IOSB-SRB.
ILRVLGE	register 15	0	Element passed all tests.
		4	Element contains bad data.
		8	Element is not an LGE.

Object Module	Location of Code	Return Code	Meaning
ILRFRR01 (continued)			
ILRVLPRG	register 15	0	No errors were detected.
		4	Elements with bad data were removed.
		8	Damaged queue was reconstructed.
		24	Input parameters were invalid, or register 0 did not point to a valid LGE.
ILRVPCB	register 15	0	Element passed all tests.
		4	Element contains bad data.
		8	Element is not a PCB.
ILRVPCBQ	register 15	0	No errors were detected.
		4	Elements with bad data were removed.
		8	Damaged queue was reconstructed.
		24	Input parameters were invalid.
ILRVPCCW	register 15	0	Element passed all tests.
		4	Element contains bad data.
		8	Element is not a PCCW.
ILRVPCWQ	register 15	0	No errors were detected.
		4	Elements with bad data were removed.
		8	Damaged queue was reconstructed.
		24	Input parameters were invalid.
ILRVSCCW	register 15	0	Element passed all tests.
		4	Element contains bad data.
		8	Element is not an SCCW.
ILRVSCWQ	register 15	0	No errors were detected.
		4	Elements with bad data were removed.
		8	Damaged queue was reconstructed.
		24	Input parameters were invalid.
ILRVSPAQ	register 15	0	No errors were detected.
		4	Elements with bad data were removed.
		8	Damaged queue was reconstructed.
		24	Input parameters were invalid.
ILRVSWTQ	register 15	0	No errors were detected.
		4	Elements with bad data were removed.
		8	Damaged queue was reconstructed.
		24	Input parameters were invalid.
ILRGOS			
ILRGOS	register 15	0	Function successful.
		4	Invalid LGN.
		8	Storage locator 'S' symbol is invalid.
		20	Unable to store in SYS1.STGINDEX.
		28	Unable to obtain storage.
		32	Op code in ACA invalid.
		40	Invalid identifier type.
		44	Indeterminate error.
ILRFRELG	register 15	0	Freed the LGE.
		4	LGE was not found.
ILRGOS01			
ILRCGOSE	register 15	0	Continue with termination. Only used if no SDWA passed by RTM.
ILROPS00	Register 15	0	Function successful.
		8	Mount failed.
		12	Locate failed.
		16	SQA out of space.
		20	Nucleus buffer out of space.
ILRPAGIO	register 15	0	All AIAs were processed.
		4	Error found in an AIA. Register 1 contains address of error AIA (last one processed).
ILRPOS			
ILRPOS	register 15	0	Successful.
		4	Invalid LGN.
		28	Unable to obtain storage.

Object Module	Location of Code	Return Code	Meaning
ILRPOS (continued)			
ILRTRPAG	register 15	0	Successful.
		12	Invalid target LPID for a transfer page request.
		28	Unable to obtain storage.
ILRPREAD	register 15	0	Function successful.
		4	Conversion error or I/O error.
		8	Not enough storage to build control blocks.
ILRRLG	register 15	0	Function successful.
		20	SYS1.STGINDEX has not been opened.
		28	Workarea storage could not be obtained.
ILRSAV	register 15	0	Function successful.
		20	I/O error trying to write to SYS1.STGINDEX.
		28	Storage could not be obtained for a workarea or RPL.
		44	Freemain or I/O buffers failed.
ILRSRT	register 15	0	No work returning.
		4	Work returning.
		8	Data set full, no reads remaining.
		12	Data set full, reads remaining.
ILRSWAP	register 15	0	Function successful.
		4	Error chain being returned.
ILRTERMR			
ILRSLTRV	register 15	0	Adjusted ASMBKSLT and ASHBKSLT.
		4	Insufficient unreserved slots.
TERMRFRR	SDWARCDE	0	Continue termination
		4	Retry.
ILRTMI01	register 15	0	Percolate.
		4	Retry.
ILRVSAMI	register 15	0	Successful.
		8	Record could not be found.
		20	I/O error processing SYS1.STGINDEX.
		28	Storage could not be obtained.
		48	Partially retrieved ASPCT for release.
IRARMEVT			
SYSEVENT			Note: All the following return codes are contained in the field of the RRPA corresponding to the indicated register.
6	register 1		
	byte 0	0	Proceed with address space creation.
		128	Do not create the address space, since a resource shortage exists.
7	register 1		
	byte 3	0	Proceed with address space deletion.
		4	Issue WAIT before deleting address space.
12	register 1		
	byte 3	0	Continue with quiesce processing.
		8	Restore the address space to its original status.
13	register 1		
	byte 0	0	The USERRDY SYSEVENT has just been received.
		128	No USERRDY has been received since QSCEST.
	byte 2	1-10	Swap reason code (if byte 3 = 0).
			Irrelevant (if byte 3 ≠ 0).
	byte 3	0	Swap out the address space.
		8	Restore the address space to its former status.
28	register 15		
	byte 3	0	The allocation selection was successful.
		8	The allocation selection was not successful.
30	register 1		
	byte 2	0	The performance group number was valid.
		1	The performance group number is not valid, and the ASID belongs to a non-TSO user.
		2	The performance group number is not valid, and the ASID belongs to a TSO user.

Object Module	Location of Code	Return Code	Meaning
31	register 1		
	byte 2	0	The reset request was honored.
		4	The new performance group number is not valid.
		8	The ASID is not currently assigned.
37	register 15		
	byte 3	0	The SETDMN is successful.
		4	The domain is undefined.
		8	The minimum value exceeds the maximum value.
38	register 15		
	byte 3	0	No data loss occurred.
		4	Data was lost due to an accumulation control block error.
39	register 15		
	byte 3	0	No data loss occurred.
		4	Data was lost due to an accumulation control block error.
41	register 1		
	byte 3	0	The request to mark the address space as non-swappable was honored.
		4	The request was not for the current address space.
		8	The requestor was not authorized to make the request.
42	register 1		
	byte 3	0	The request to mark the address space as non-swappable was honored.
		4	The request was not for the current address space.
		8	The requestor was not authorized to make the request.
43	register 1		
	byte 3	0	The swap-out request was honored.
		4	The request was ignored, since the address space is non-swappable.
		8	The request was ignored, since the address space was already being swapped.
45	register 1	0	Data collection was successfully initialized.
		non zero	Data collection was not initialized.
	register 15		
	byte 3	0	The request was honored.
		8	The request to start data collection was rejected because of an incorrect buffer size.
		32	Data collection is already active.
46	register 15		
	byte 3	0	The request was honored.
		4	Data collection was stopped because of an IPS change.
		64	No data collection buffer has been established.
47	register 15		
	byte 3	0	The request was honored.
		64	No data collection buffer has been established.
IRARMINT			The return codes for this module are the same as those for IRARMEVT. Additionally, a '15F' ABEND will occur because of invalid invocation of this module. The codes passed with the ABEND are explained in "System Codes", form GC38-1008.
IRARMIOM	register 15		Irrelevant.
IRARMIPS	register 15	0	The IPS is valid.
		4	The IPS is invalid.

Object Module	Location of Code	Return Code	Meaning
IRARMSRV	register 15		
	byte 3	0	The requested function was performed.
		4	The requested function was not performed.
		12	No space was available for the requested function (entry IRARMI09 only).
IRARMWAR	register 15		
	byte 3		
	(field of RRPA)	0	The requested function was successfully performed.
		4	Data collection was successful, but a IPS change has occurred (entry IRARMWR3 only).
		8	An incorrect length data area was specified (entry IRARMWR1 only).
		40	Data collection was not active, a data buffer was not in existence, or the copy buffer was of an incorrect size (entry IRARMWR3 only).
IRBMFANL	register 15	0	Syntax string found.
		4	Syntax string not found.
IRBMFCNV	register 15	0	Normal processing completed.
		4	Possible precision loss.
		8	Significant digits lost; field filled with asterisks.
		12	Bad input parameters.
IRBMFDEA	register 15	0	Continue with termination return to R/TM.
	SDWARCDE	0	Continue with termination return to R/TM.
IRBMFEVT	SDWARCDE	4	Retry return to R/TM.
IRBMFFUR	SDWARCDE	0	Continue with termination return to R/TM.
IRBMFICP	register 15	0	Normal Return. Requested options were valid.
IRBMFIDV	register 15	44	Activate MFROUTER, enqueue TQE, and activate I/O data collection.
IRBMFIHA	register 15	44	Activate MFROUTER, enqueue TQE, and activate I/O data collection.
IRBMFIOI	register 15	0	Continue with termination return to R/TM.
	SDWARCDE	0	Continue with termination return to R/TM.
		4	Retry return to R/TM.
IRBMFIPG	register 15	0	Normal Return. Requested options were valid.
IRBMFIWK	register 15	0	Normal Return. Requested options were valid.
IRBMFMFC	register 15	0	Function successfully completed.
IRBMFMLN	register 15	0	Continue with termination return to R/TM.
	SDWARCDE	0	Continue with termination return to R/TM.
		4	Retry return to R/TM.
IRBMFRGM	register 15		Irrelevant.
IRBMFSAR	register 15	0	Continue with termination return to R/TM.
	SDWARCDE	0	Continue with termination return to R/TM.
		4	Retry return to R/TM.
IRBMFSDE	register 15	0	Continue with termination return to R/TM.
	SDWARCDE	0	Continue with termination return to R/TM.
IRBMFTMA	SDWARCDE	0	Continue with termination return to R/TM.
IRBMFTRM	register 15	0	Continue with termination return to R/TM.
	SDWARCDE	0	Continue with termination return to R/TM.

Register Usage Table

This table is an alphameric list of scheduler and supervisor object modules and their register contents upon entry and exit. Many modules use standard register usage or standard SVC register usage. These standards are as follows:

Standard Register Usage

Register	Contents at Entry	Contents at Exit
0	Irrelevant.	Irrelevant.
1	Address of a parameter list.	Address of a parameter list.
2-12	Irrelevant.	Irrelevant.
13	Address of register save area.	Address of register save area.
14	Return address.	Return address.
15	Address of module entry point.	Return code, if any.

Standard SVC Register Usage

Register	Contents at Entry	Contents at Exit
2	Irrelevant.	Unpredictable.
3	CVT address.	Unpredictable.
4	Caller's TCB address.	Unpredictable.
5	Caller's RB address.	Unpredictable.
6	Address of module entry point.	Unpredictable.
7	ASCB address.	Unpredictable.
8-13	Irrelevant.	Unpredictable.
14	Return address (exit prolog, (exit prolog, IEAVEXPR).	Unchanged.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAFTEED	0	Irrelevant.	Irrelevant.
	1	Address of print dump parameter list.	Unchanged.
	2-12	Irrelevant.	Irrelevant.
	13	Caller's save area address.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
The following modules have the same register usage as IEAFTEED. IEAFTESA (entry point) IEAFTFRR (entry point) IEAFTIHS IEAFTRTC (entry point) IEAFTRT2 IEAFTSCB IEAFTSDW (entry point)			
IEATLEXT	0-9,11-13,15	Irrelevant.	Irrelevant.
	10	Irrelevant.	Return address.
	14	Return address.	Irrelevant.
IEAVAD0A	0	Irrelevant.	Irrelevant.
	1	Address of SNAP work area.	Irrelevant
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry address.	Return code.
IEAVAD0B	Same as IEAVAD0A for all registers.		
IEAVAD0C	0	Irrelevant.	Irrelevant.
	1	Address of SNAP work area.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Return codes 0, 8 depending on exit.
IEAVAD0D	0	Irrelevant.	Irrelevant.
	1	Address of abdarea.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Save area address.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Address of IEAVAD0D.	Return code of 0 or 8, depending on exit.
IEAVAD0E	0	Irrelevant.	Irrelevant.
	1	Address of snap work area.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Entry point address.	Return code.
IEAVAD0F (same register usage as IEAVAD0E)			

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVAD00	0	Irrelevant.	Irrelevant.
	1	Address of snap or SVC dump parameter list.	Irrelevant.
	2	Irrelevant.	Irrelevant.
	3	CVT address.	Irrelevant.
	4	SVC 51 TCB address.	Irrelevant.
	5	SVC 51 SVRB address.	Irrelevant.
	6	Irrelevant.	Irrelevant.
	7	Current ASCB address.	Irrelevant.
	8-13	Irrelevant.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Entry point address.	Return code.
IEAVAD01	0	Irrelevant.	Irrelevant.
	1	Caller's parameter list address.	Irrelevant.
	2	Irrelevant.	Irrelevant.
	3	CVT address.	Unchanged.
	4	Current TCB address.	Unchanged.
	5	SNAP's SVRB address.	Unchanged.
	6-8	Irrelevant.	Irrelevant.
	9	Base for IEAVAD00.	Unchanged.
	10-12	Irrelevant.	Irrelevant.
	13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVAD02	0	Irrelevant	Irrelevant
	1	SNAP work area address.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Save area address.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Return codes of 0 or 8, depending on the exit.
IEAVAD03	0-15	Same as IEAVAD02 for all registers.	
IEAVAD05	0	Irrelevant.	Irrelevant.
	1	SNAP work area's address.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Caller's save area address.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code of 0 or 8, depending on the exit.
IEAVAD06	0	Irrelevant.	Unchanged.
	1	SNAP work area address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Caller's save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	0 or 8 depending on the exit.
IEAVAD07	0	Irrelevant.	Irrelevant.
	1	SNAP work area address.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Caller's save area address.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Entry point address.	Return code of 0 or 8.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVAD08	0	Irrelevant.	Irrelevant.
	1	SNAP work area address.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Caller's save area address.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Entry point address.	Irrelevant.
IEAVAD09 and IEAVAD10 have the same register usage as IEAVAD0E.			
IEAVAD11			
IEAVAD11	0	Irrelevant.	Irrelevant.
	1	Snap work area's address.	Irrelevant.
	2-15	Irrelevant	Irrelevant.
IEAVAD21	0-15	Same as IEAVAD11 except register 15 contains address of IEAVAD21.	Irrelevant.
IEAVAD81	0-15	Same as IEAVAD11 except R15 contains address of IEAVAD81. (ABLINEA in SNAP work area must contain data to be written.)	Irrelevant.
IEAVAD31			
IEAVAD31	0	Irrelevant.	Unchanged.
	1	SNAP work area address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Caller's save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	IEAVAD31 address.	0 or 8, depending on exit.
IEAVAD41		Same as for IEAVAD31 except register 15 contains address of IEAVAD41.	Same as for IEAVAD31.
IEAVAD51			
IEAVAD51	0	Irrelevant.	Irrelevant.
	1	Snap's work area address.	Irrelevant.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Address of IEAVAD51.	0 or 8, depending on exit.
IEAVAD61		Same as IEAVAD51 except register 15 contains address of IEAVAD51.	
IEAVAD71	0	Irrelevant.	Irrelevant.
	1	Address of SNAP work area.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Caller's save area address.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Address of IEAVAD71.	Irrelevant.
IEAVAMSI	0	Address of TCB.	Unchanged.
	1	Address of first or only VCB to be processed.	Unchanged.
	2	Irrelevant.	In error case, address of VCB in error; otherwise, unchanged.
	3-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVAR00			
IEAVAR00	0-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVAER0	0	12 (Dec) or irrelevant.	Unpredictable.
	1	If reg. 0 is 12, system and user completion codes; otherwise, address of SDWA.	If reg. 0 input was 12, unpredictable; otherwise, unchanged.
	2	If reg. 0 is 12, address of ESTAE Parameter List; otherwise, irrelevant.	Unpredictable.
	3-12	Irrelevant.	Unpredictable.
	13	If reg. 0 is 12, irrelevant; otherwise, save area address.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVAR01	0,1	Irrelevant.	Unpredictable.
	2	Address of ASXB.	Unchanged.
	3	Address of CVT.	Unchanged.
	4	Address of RCT's TCB.	Unchanged.
	5	Address of RCT's RB.	Unchanged.
	6	RCT Init/Term base register.	Unchanged.
	7	Address of ASCB.	Unchanged.
	8-12	Irrelevant.	Unpredictable.
	13	Address of RCT save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVAR02			
IEAVAR02	0,1	Irrelevant.	Unchanged.
	2	Address of ASXB.	Unchanged.
	3	Address of CVT.	Unchanged.
	4	Address of TCB.	Unchanged.
	5	Address of RB.	Unchanged.
	6	Irrelevant.	Unchanged.
	7	Address of ASCB.	Unchanged.
	8-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVAFR2	0	Address of 200-byte work area.	Unpredictable.
	1	Address of SDWA.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVAR03			
IEAVAR03	0,1	Irrelevant.	Unchanged.
	2	Address of ASXB.	Unchanged.
	3	Address of CVT.	Unchanged.
	4	Address of TCB.	Unchanged.
	5	Address of RB.	Unchanged.
	6	Irrelevant.	Unchanged.
	7	Address of ASCB.	Unchanged.
	8-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVAR3	0	Address of 200-byte work area.	Unpredictable.
	1	Address of SDWA.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVAR3A	0, 1	Irrelevant.	Unpredictable.
	2	Address of RCTD.	Address of ASXB.
	3	Irrelevant.	Address of CVT.
	4	Address of RCT's TCB.	Unchanged.
	5	Irrelevant.	Address of RCT's RB.
	6	Irrelevant.	Unpredictable.
	7	Irrelevant.	Address of ASCB.
	8,9	Irrelevant.	Unpredictable.
	10	Base register.	Unpredictable.
	11,12	Irrelevant.	Unpredictable.
	13	Irrelevant.	IEAVAR03 save area address.
	14	Irrelevant.	IEAVAR03 return address.
	15	Irrelevant.	IEAVAR03 entry point address.
IEAVAR04			
IEAVAR04	0,1	Irrelevant.	Unchanged.
	2	Address of ASXB.	Unchanged.
	3	Address of CVT.	Unchanged.
	4	Address of TCB.	Unchanged.
	5	Address of RB.	Unchanged.
	6	Irrelevant.	Unchanged.
	7	Address of ASCB.	Unchanged.
	8-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVAFR4	0	Irrelevant.	Unpredictable.
	1	Address of SDWA.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVAR05			
IEAVAR05	0	Address of IQE.	Unchanged.
	1	Address of TAXE.	If user attention exit is to be scheduled, address of user parameter list; if user attention exit is cancelled or suppressed, unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVAR05 (continued)			
IEAVAFR5	0	Irrelevant.	Unpredictable.
	1	Address of SDWA.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unpredictable.
	15	Irrelevant.	Unpredictable.
IEAVART5	0-13	Same as at time of error.	Unpredictable.
	14	Same as at time of error.	Return address.
	15	Same as at time of error.	Unpredictable.
IEAVAR06			
IEAVAR06	0-2	Irrelevant.	Unchanged.
	3	Address of CVT.	Unchanged.
	4	Address of TCB.	Unchanged.
	5	Address of RB.	Unchanged.
	6	Irrelevant.	Unchanged.
	7	Address of ASCB.	Unchanged.
	8-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVAFR6	0	Irrelevant.	Unpredictable.
	1	Address of SDWA.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVAR07			
IEAVAR07	0	Irrelevant.	Unchanged.
	1	Pointer to address of interface block.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Always zero.
IEAVAFR7	0	Irrelevant.	Unpredictable.
	1	Address of SDWA.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVAX00			
IGC0009F	0	Irrelevant.	Irrelevant.
	1	Address of parameter list or 0.	Address of previous attention exit or zero.
	2	Irrelevant.	Irrelevant.
	3	Address of CVT.	Irrelevant.
	4	Address of TCB.	Irrelevant.
	5	Address of SVRB.	Irrelevant.
	6	Entry point address.	Irrelevant.
	7	Address of ASCB.	Irrelevant.
	8-13	Irrelevant.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Irrelevant.	Return code.
STXFRR	0	Irrelevant.	Unpredictable.
	1	Address of SDWA.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unpredictable.
	15	Entry point address.	Unpredictable.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVBLDP	0	Cell pool identifier or 0, for dynamic pool creation.	Unchanged ERROR—Subpool number and length of extent.
	1	Address of storage to be formatted or 0, for recovery of an entire pool.	Unpredictable ERROR—Address of extent in error.
	2	Unpredictable.	Unpredictable.
	3	Address of CVT.	Unpredictable.
	4-12	Unpredictable.	Unpredictable.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVCARR			
IEAVCARR			
IEAVTTRR			
IEAVFARR	0	Address of work area.	Unpredictable.
	1	Address of SDWA.	Unchanged.
	2-13	Unpredictable.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVCKEY	0	Bit 0 = 0 indicates register type of request. Bit 0 = 1 indicates parameter list type of request. Bits 24-28 contains storage key and fetch protection.	Storage key and fetch protection of first virtual page changed.
	1	For register type requests, the address of the first virtual page to be changed. For parameter list type requests, the parameter list address.	Unchanged.
	2	For register type requests, the address of the last virtual page to be changed.	Unchanged.
	3-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry address.	Return code.
IEAVCSEG			
IEAVCSEG	0	Parameters.	Address of page table; if return code 8, segment ID of valid segment.
	1	Virtual Storage Address of area obtained by GETMAIN where control blocks are to be built.	Address of external page table, if one is created.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVCSGB	0	Parameters.	Address of page table; if return code 8, segment ID of valid segment.
	1	Virtual Storage Address of area obtained by GETMAIN where control blocks are to be built.	Address of external page table, if one is created.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4-12	Irrelevant.	Unchanged.
	13	Address of PVT save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVDELP	0	CPID of pool to be acted upon.	Unpredictable, unless a FREEMAIN error occurs; then, FREEMAIN error code.
	1	Option code.	Address of the first dequeued extent ERROR—Address of extent in error.
	2	Unpredictable.	Unpredictable.
	3	Address of CVT.	Unpredictable.
	4-12	Unpredictable.	Unpredictable.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVDLAS			
IEAVDLAS	0	Irrelevant.	Irrelevant.
	1	Address of ASCB.	Unpredictable.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVDLAS (continued)			
IEAVSRBP	0	Irrelevant.	Unpredictable.
	1	Address of purged SRB.	Unpredictable.
	2	Value of SRBPARM for purged SRB.	Unpredictable.
	3-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVDSEG	0	Parameters.	Unpredictable.
	1	Irrelevant.	Virtual Storage Address of destroyed page table or, in error case, zero.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVEAC0	0	Entry Code.	Unchanged.
	1	ASCB address or parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Return Code.
	15	Irrelevant.	Unchanged.
IEAVEAT0			
IGC0004B	0	Irrelevant.	Irrelevant.
	1	User parameter list address.	Address of new TCB or 0 for error.
	2-14	SVC registers.	SVC registers.
	15	Address of input parameter list.	Return code or reason code.
IEAVECH0	0	Change value for dispatching priority.	Irrelevant.
	1	Address of TCB pointer when CHAP applies to specified TCB or 0 when CHAP applies to current task.	Irrelevant.
	2-14	SVC registers.	SVC registers.
	15	Irrelevant.	Irrelevant.
IEAVEDR	0	Function code.	Status indicators if condition code = 8, otherwise unpredictable.
	1	Receiving CPU PCCA address.	Unpredictable.
	2-13	Irrelevant.	Unchanged.
	14	Return address.	Return address.
	15	Entry point address.	Return code.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVEDS0			
IEA0DS	0-12	n/a	Caller's register.
	13	Caller's save area address.	Caller's registers.
	14	Return address.	Caller's registers.
	15	Address of entry point.	Caller's registers.
IEAPDS2	0-15	Same as IEA0DS.	Same as IEA0DS.
IEAPDS6	0-15	Same as IEA0DS.	Same as IEA0DS.
IEAPDS7	0-15	Same as IEA0DS.	Same as IEA0DS.
IEAPDSRT	0-15	Same as IEA0DS.	Same as IEA0DS.
GSLSDISP	0-10	Irrelevant.	Unchanged.
	11-12	Irrelevant.	Irrelevant.
	13	Entry point address.	Return code.
	14	Return address.	Return address.
	15	Irrelevant.	Unchanged.

Module Name	Register Number	Contents at Entry	Contents at Exit
LLCOSVCF	0-10	Irrelevant.	Unchanged.
	11-12	Irrelevant.	Irrelevant.
	13	Entry point address.	Return code.
	14	Return address.	Return address.
	15	Irrelevant.	Unchanged.
LRELDISP	0-10	Irrelevant.	Unchanged.
	11-12	Irrelevant.	Irrelevant.
	13	LLREL address.	Return code.
	14	Return address.	Return address.
	15	Irrelevant.	Unchanged.
LRELEXPR	0-10	Irrelevant.	Unchanged.
	11	Entry point address.	Irrelevant.
	12	Irrelevant.	Irrelevant.
	13	LLREL address.	Return code.
	14	Return address.	Return address.
	15	Irrelevant.	Unchanged.
IEAVELKW	0-5	Irrelevant.	Unchanged.
	6	Entry point address.	Unchanged.
	7	Return address.	Unchanged.
	8	LCCA address.	Unchanged.
	9	Irrelevant.	Unchanged.
	10	Work register.	Irrelevant.
	11-15	Irrelevant.	Unchanged.
IEAVSPCR	0	Work register.	Irrelevant.
	1	SSRB address or 0.	Irrelevant.
	2-3	Work registers.	Irrelevant.
	4-6	Irrelevant.	Unchanged.
	7	LCCA address.	Unchanged.
	8	ASCB address.	Unchanged.
	9	Irrelevant.	Irrelevant.
	10-11	Irrelevant.	Unchanged.
	12	Entry point address.	Unchanged.
	13	Irrelevant.	Unchanged.
	14	Irrelevant.	Irrelevant.
	15	Return address.	Unchanged.
DSJSTCSR	0-3	Work registers.	Irrelevant.
	4-6	Irrelevant.	Unchanged.
	7	LCCA address.	Unchanged.
	8	ASCB address.	Unchanged.
	9	Entry point address.	Unchanged.
	10-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVEDSR	0	Irrelevant.	Unpredictable.
	1	SDWA address.	SDWA address.
	2-14	Irrelevant.	Unpredictable.
	15	Irrelevant.	Return address to IEAVESPR.
IEAVEED0			
IGC062	0	Irrelevant.	Unchanged.
	1	Address of pointer to subtask. TCB for DETACH— Bit 0: indicates STAE=NO 1: indicates STAE=UES.	Unchanged.
	2-14	SVC registers.	Unchanged.
	15	Irrelevant.	Return Code.
IGC062R1	0	Entry code. 0-Special DETACH processing for R/TM. 1-EOT processing.	Unchanged.
	1	Address if TCB to be detached.	Unchanged.
	2-12	Irrelvant.	Unchanged.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IGC062R1.	Unchanged.
IEAVEEE0			
IEA0EF03	0-6	Irrelevant.	Changed.
	7	Current ASXB address.	Unchanged.
	8	Current ASCB address.	Unchanged.
	9	Irrelevant.	Unchanged.
	10-13	Irrelevant.	Changed.
	14	Return address.	Unchanged.
	15	Entry point address.	Changed.
IEAVEEEP	0	Irrelevant.	Irrelevant.
	1	Address of pointer to the resource manager parameter list.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return Address.	Unchanged.
	15	Entry point address.	Irrelevant.
IEAVEEER	0	Address of 200 byte work area passed by RTM.	Unchanged
	1	Address SDWA.	Unchanged.
	2-6	Irrelevant.	Volatile.
	7-10	Irrelevant.	Unchanged.
	11-13	Irrelevant.	Volatile.
	14	Return address.	Volatile.
	15	Entry point address.	Volatile.
IEAVEEE2	0	Address of SRB, if one is passed; otherwise, irrelevant.	Unchanged.
	1	0 (if SRB is passes via register 0) or complemented address of IQE or address of RQE.	Unchanged.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Irrelevant.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVEES	0-1	Irrelevant.	Irrelevant.
	2	Return address.	Return address.
	3-9	Irrelevant.	Irrelevant.
	10	Address of entry point.	Irrelevant.
	11-15	Irrelevant.	Irrelevant.
IEAVEEXP			
IEAVEXPR	0-1	To be returned to issuer of SVC.	To TCBGRS.
	2-14	Irrelevant.	Irrelevant.
	15	To be returned to issuer of SVC.	To TCBGRS.
IEAVEXP1	Identical to IEAVEXPR.		
IEAVEXSV	0-1	Irrevelant	To TCBGRS.
	2-14	SVC registers	Irrelevant.
	15	ESR code 8.	To TCBGRS.
IEAVEEXT	0-15	n/a	n/a
IEAVEE1R	0	Irrelevant.	Irrelevant.
	1	Irrelevant.	ABEND code.
	2-14	Irrelevant.	Irrelevant.
	15	Entry point address.	IEAVEABD address (ABEND issuer).
IEAVEE2R	0	Irrelevant.	Irrelevant.
	1	Irrelevant.	ABEND code.
	2-14	Irrelevant.	Irrelevant.
	15	Entry point address.	IEAVEABD address (ABEND issuer).
IEAVEE3R	0	Irrelevant.	Irrelevant.
	1	Irrelevant.	ABEND code.
	2-14	Irrelevant.	Irrelevant.
	15	Entry point address.	IEAVEABD address (ABEND issuer).
IEAVEF00			
IGC043	0	Virtual address of asynchronous exit.	Irrelevant.
	1	Bit indicators.	Irrelevant.
	2-3	Irrelevant.	Irrelevant.
	4	Current TCB address.	Irrelevant.
	5-12	Irrelevant.	Irrelevant.
	13	Caller's save area address.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Address of entry point.	Irrelevant.
IGC043BR	0	Virtual address of entry point to asynchronous routine.	Caller's register.
	1	Bit indicators.	Caller's register.
	2-3	Irrelevant.	Caller's registers.
	4	Current TCB address.	Caller's registers.
	5-12	Irrelevant.	Caller's registers.
	13	Caller's save area address.	Caller's registers.
	14	Return address.	Caller's registers.
	15	Address of entry point.	Caller's registers.
IEAVEIO	0-15	Irrelevant.	Irrelevant.
IEAVEIOR	0	Irrelevant.	Irrelevant.
	1	Irrelevant.	ABEND code.
	2-14	Irrelevant.	Irrelevant.
	15	Entry point address.	IEAVEABD address (ABEND issuer).

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVEIPR	0	Irrelevant.	Irrelevant.
	1	SDWA.	Irrelevant.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Entry point address.	Irrelevant.
IEAVELCR	0-12	Irrelevant.	Irrelevant.
	13	Irrelevant.	Save area address.
	14	Return address.	Irrelevant.
	15	Entry point address.	SETLOCK recovery address.
IEAVELK			
GSLSCOBT	0-10	Irrelevant.	Irrelevant.
	11	Lock address.	Irrelevant.
	12	Mask to update CPUs locks-held string.	Irrelevant.
	13	Entry point address.	Return code.
	14	Return address.	Unchanged.
	15	Irrelevant.	Irrelevant.
GSLMUOBT	0-15	Same as entry GSLMCOBT.	
GSLSUOBT	0-15	Same as entry GSLSCOBT.	
GSLMREL	0-15	Same as entry GSLMCDBT.	
GSLSREL	0-15	Same as entry GSLSCOBT.	
GSLMRELD	0-15	Same as entry GSLMCOBT.	
GSLSRELD	0-15	Same as entry GSLSCDBT.	
LLCOBT	0-10	Irrelevant.	Irrelevant.
	11	Irrelevant.	Irrelevant.
	12	Irrelevant.	Irrelevant.
	13	Entry point address.	Return code.
	14	Return address.	Unchanged.
	15	Irrelevant.	Irrelevant.
GSLMCOBT	0-10	Irrelevant.	Irrelevant.
	11	Lock address.	Irrelevant.
	12	Offset of lock entry in table of locks held by CPU.	Irrelevant.
	13	Entry point address.	Return code.
	14	Return address.	Unchanged.
	15	Irrelevant.	Irrelevant.
LLREL	0-15	Same as entry LLCOBT.	Irrelevant.
LLUOBT	0-15	Same as entry LLCOBT.	Irrelevant.
CMSUOBT	0-10	Same as entry LLCOBT.	Irrelevant.
	11	Lock address.	Irrelevant.
	12	0	Irrelevant.
	13-15	Same as entry LLCOBT.	Register 13 contains return code.
CMSCOBT	Same as CMSOUBT.		Irrelevant.
CMSREL	Same as CMS0UBT.		
RELGS0	0-10	Irrelevant.	Irrelevant.
	11	String indicating which spin locks are to be released.	Irrelevant.
	12	Address of indirect address list for lockwords.	Irrelevant.
	13	Entry point address.	Irrelevant.
	14	Return address.	Unchanged.
	15	Irrelevant.	Irrelevant.
IEAVELRM	0	Irrelevant.	Irrelevant.
	1	Address of parameter list.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVELKR			
IEAVELKR	0-12	Irrelevant.	Irrelevant.
	13	Address of RTM saved registers.	Unchanged.
	14	Irrelevant.	Irrelevant.
	15	Irrelevant.	Entry address of IEAVEVRR.
IEAVELKR (continued)			
IEAVLKRR	0	Irrelevant.	Irrelevant.
	1	Address of SDWA.	Unchanged.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevant.
IEAVEMCR	0	Standard.	Standard.
	1	Address of ASID.	Standard.
	2-15	Standard.	Standard.
IEAVEMDL	0	Standard.	Standard.
	1	Address of Interface block.	Standard.
	2-15	Standard.	Standard.
IEAVEMIN	0	Address of SRB.	Standard.
	1	Address of ASCB or Address of Master Scheduler TIOT or Address of Master Scheduler JSCB.	Standard
	2-15	Standard.	Standard.
IEAVEMRQ	0	Standard.	Standard.
	1	Address of Parameter list containing address of CSCB.	Standard.
	2-15	Standard.	Standard.
IEAVEMS0	0	CPU affinity mask (if reg. 1 is complemented) or Irrelevant.	Irrelevant.
	1	Address of ASCB or complemented address of ASCB (reg 0 contains CPU affinity mask) or 0.	Irrelevant.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Irrelevant.	0 (for IPC switch function).
IEAVENQ1			
IGC048	0	Irrelevant.	Unpredictable.
	1	Address of parameter list.	Unpredictable.
	2-14	SVC Registers,	Irrelevant.
	15	Irrelevant.	0 or address parm list.
IGC056	0-15	Same as IGC048.	Same as IGC048.
IEAVENQ2	(Same as IEARPOST in module IEAVSY50).		
IEAVEOR	0-1	To be returned to caller.	To TCBGRS.
	2-14	SVC registers.	Irrelevant.
	15	To be returned to caller.	To TCBGRS.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVEPC			
IEAVPSRB	0	Address of SRB.	Irrelevant.
	1	Address of parameters.	Irrelevant.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Entry point address.	Irrelevant.
IEAVSUSP	0-3	Irrelevant.	Irrelevant.
	4	Irrelevant.	Address of TCB or SRB.
	5	Irrelevant.	Address of RB or 0 if SRB.
	6	Irrelevant.	0 if unlocked TCB 4 if locked, 8 if unlocked SRB.
	7-12	Irrelevant.	Irrelevant.
	13	Irrelevant.	Save area address.
	14	Irrelevant.	Return address.
	15	Irrelevant.	Irrelevant.
IEAVRSET	0-3	Irrelevant.	Irrelevant.
	4	Address of TCB or SRB.	Irrelevant.
	5	Address of RB or 0 if SRB.	Irrelevant.
	6	0 if no error; non-0 if error.	Irrelevant.
	7-12	Irrelevant.	Irrelevant.
	13	Save area address.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Entry point address.	Irrelevant.
SPIE processing	0	Irrelevant.	Exit to SPIE (paging exception).
	1	Irrelevant.	Address of PIE.
	2-13	Irrelevant.	Irrelevant.
	14	Irrelevant.	Return address = SVC3.
	15	Irrelevant.	SPIE exit entry point address.
(all others at time of interrupt)			
PROGCK entry	1	Irrelevant.	Exit to R/TM (PROGCK). Negative 1 indicates registers are in first location in LCCA. X' 0FB8' indicates control register zero is bad.
System termination entry	1	Irrelevant	Exit to IGFPTERM (system termination). address of two word parameter list, containing address of WTO message and address at logrec record.
IEAVEPCR	0	Irrelevant.	Irrelevant.
	1	Irrelevant.	ABEND code.
	2-13	Irrelevant.	Irrelevant.
	14	Return address (for suspended-SRB entry).	Unchanged.
	15	Entry point address (entry point to RMTR for SRB entry).	Address of IEAVEABD.
IEAVEPDR			
IEAVEPDF	0	Address of 200 byte work area passed by RTM.	Unchanged.
	1	SDWA.	Unchanged.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Volatile.
	15	Entry point address.	Volatile.

Module Name	Register Number	Contents at Entry	Contents at exit
IEAVEPDR (continued)			
IEAVEPDE	0	=12 is no SDWA; otherwise irrelevant.	Retry address if retry was specified and no SDWA.
	1	Address of SDWA or ABEND code if no SDWA.	Irrelevant.
	2	Address of PURGEDQ work area in SVRB extended save area (if there is no SDWA).	Irrelevant.
	3-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Entry point address.	0 if percolation specified 4 if retry specified.
IEAVEPDS	0-4	Irrelevant.	Irrelevant.
	5	Irrelevant.	SVRB address.
	6-13	Irrelevant.	Irrelevant.
	14	Irrelevant.	Return address.
	15	Entry point address.	Irrelevant.
IEAVEPD0	0	User parameter (passed to RMTR).	Irrelevant.
	1	Address of parameter list.	ABEND code.
	2-14	Standard for SVC interruption handler.	Unchanged.
	15	Standard for SVC interruption handler.	Irrelevant.
IEAVEQR, IEAVEQRF IEAVEQR IEAVEQRF	0	Irrelevant.	Irrelevant.
	1	Address of parameter list.	Unchanged.
	2-12	Irrevelant.	Irrelevant.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return Code.
IEAVEQRI	0	Irrelevant.	Irrelevant.
	1	Real block number of available frame.	Zero, if frame taken for V=R space; unchanged, if frame not needed for V=R space.
	2	Irrelevant.	Unchanged.
	3	Address of PVT.	Unchanged.
	4-5	Irrelevant	Unchanged.
	6	PFTE address.	Unchanged.
	7-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVEQRC	0	SRB address.	Unpredictable.
	1	Address of root PCB defining allocation request.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Irrelevant.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVEQRP	0	SRB address.	Unpredictable.
	1	PCBR address.	Unpredictable.
	2-14	Irrelevant.	Unpredictable.
	15	Entry point address.	Unpredictable.
IEAVRMTR	0	Irrelevant.	Unpredictable.
	1	SRB address.	Unpredictable.
	2	PCBR address.	Unpredictable.
	3-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVEQV0	0	Parameter for element verification routine.	Unchanged.
	1	Address of the parameter list.	Unchanged.
	2-12	Irrelevant.	Irrelevent.
	13	Address of register save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return information: Bit 0 = 0 all error are recorded. = 1 more errors than could be recorded. Byte 1 count of errors recorded. Byte 2 count of errors detected. Byte 3 return code. 0 no errors detected. 4 no queue structure damage, bad data in elements removed. 8 possible queue damage.
IEAVERER	0	Irrelevant	Irrelevant.
	1	Irrelevant.	ABEND code.
	2-14	Irrelevant.	Irrelevant.
	15	Entry point address.	IEAVEABD address (ABEND issuer).
IEAVERES	0-15	Irrelevant.	Return to: DSS—registers 0-15 irrelevant. interrupted program—registers 0-15 restored. R/TM—register 1 contains X'90000000', registers 0-15 irrelevant.
IEAVERI	0	Function code.	Status indicators if condition code = 8; otherwise unpredictable.
	1	Receiving CPU PCCA.	Unpredictable.
	2-10	Irrelevant.	Irrelevant.
	11	Parameter address.	Unchanged.
	12	Entry point address.	Unchanged.
	13	Irrelevant.	Unchanged.
	14	Return address.	Return address.
	15	Entry point address.	Return code.
IEAVERP	0	Function code.	Status indicators if condition code = 8; otherwise unpredictable.
	1	Receiving CPU PCCA address.	Unpredictable.
	2-13	Irrelevant.	Unchanged.
	14	Return address.	Return address.
	15	Entry point address.	Return code.
IEAVESCR	0	Address of 200 byte work area passed by RTM.	Unchanged.
	1	SDWA.	Unchanged.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Volatile.
	15	Entry point address.	Volatile.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVESC0	0-2	Irrelevant.	Irrelevant.
	3	Return address.	Unchanged.
	4	Entry point address.	Irrelevant.
	5	Address of a queue of SRBs removed from GSMQ or LSMQ.	Irrelevant.
	6	Irrelevant.	Irrelevant.
	7	LCCA address.	Irrelevant.
	8-15	Irrelevant.	Irrelevant.
IEAVESPR			
IEAVESPR	0	SDWA work area address.	Irrelevant.
	1	SDWA address.	Parameter list is wait state.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Entry point address.	Return code (4 or wait state code 01C).
IEAVERTN	0	Irrelevant.	Irrelevant.
	1	SDWA address.	Irrelevant.
	2-14	Irrelevant.	Irrelevant.
	15	Entry point address.	Irrelevant.
IEAVESCV			
IEAQSC00	0-15	n/a	n/a
IGCERROR	0-12	n/a	n/a
	13	Caller's save area.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Entry point of IEAVESVC.	Irrelevant.
IEAVESVR			
IEAVEABD	0	Irrelevant.	Irrelevant.
	1	ABEND code.	ABEND code to ABEND.
	2-15	Irrelevant.	Irrelevant.
IEAVESVR	0	Irrelevant.	Irrelevant.
	1	Irrelevant.	ABEND code.
	2-14	Irrelevant.	Irrelevant.
	15	Entry point address.	IEAVEABD address (ABEND issuer).
IEAVEVAL	0	Irrelevant.	Unchanged.
	1	Starting address.	Unchanged.
	2	Ending address or zero if no range specified.	Unchanged.
	3	Irrelevant.	Unchanged.
	4	TCB address or zero.	Unchanged if TCB address or zero specified; otherwise register contains address of current TCB.
	5-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unpredictable.
IEAVEVRR			
IEAVEVRR	0	Irrelevant.	Irrelevant.
	1	FRR parameter address passed from IEAVELKR.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	RTM save area address.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Entry point address.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVEVRR (continued)			
IEAVVFRR	0	SWDA work area address.	Irrelevant.
	1	SDWA pointer.	Parameter list if wait state.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Entry point address.	Irrelevant.
IEAVEVT0			
IEAVEVT0	0	Input parameters: bit 0: WAIT=YES bit 1: WAIT=NO bit 2: ECB= was specified bits 8-32: ECB=address or LAST=address.	Irrelevant.
	1	Table address.	Address of the first completed event; zero if WAIT=NO specified and no events were completed; the table address if ECB initialization alone was requested.
	2-13	Irrelevant.	Unchanged.
	14	Return Address.	Unchanged.
	15	Entry point address.	Irrelevant.
IGC125	0	Same as IEAVEVT0.	Same as IEAVEVT0.
	1	Same as IEAVEVT0.	Same as IEAVEVT0.
	2-3	Irrelevant.	Irrelevant.
	4	TCB address.	Irrelevant.
	5	RB address.	Irrelevant.
	6	Entry point address.	Unchanged.
	7	ASCB address.	Irrelevant.
	8-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Irrelevant.	Zero.
IEAVEVT1	0	Input parameters: bit 0: if on, table create bits 15-32: number of entries (32,767 maximum).	Irrelevant.
	1	Table address or zero.	New table address if table create.
	2-3	Irrelevant.	Irrelevant.
	4	TCB address.	Irrelevant.
	5	RB address.	Irrelevant.
	6	Entry point address.	Unchanged.
	7	ASCB address.	Irrelevant.
	8-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	ESR code 5.	Zero.
EVENTFRR	0	Work area address.	Irrelevant.
	1	SDWA address.	Unchanged.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevant.
IEAVEXS	0-1	Unpredictable.	Unpredictable.
	2	Return address.	Return address.
	3-9	Unpredictable.	Unpredictable.
	10	Entry point address.	Unpredictable.
	11-15	Unpredictable.	Unpredictable.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVFP			
IEAVFP1	0	Irrelevant.	Virtual Storage Address of page table entry or, in error case, unpredictable.
	1	Virtual Storage Address.	Virtual Storage Address of external page table or in error case, unpredictable.
	2-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Return Code.
IEAVFP2	0	Irrelevant.	Virtual Storage Address of page table entry or, in error case, unpredictable.
	1	Virtual Storage Address.	Virtual Storage Address of external page table entry or, in error case, unpredictable.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Return Code.
IEAVFRCL	0	CPID of pool to be freed from.	Unchanged, except for extent deletion; in that case, subpool number and length of deleted extent.
	1	Address of cell to be freed.	Zero, except for extent deletion; in that case, address of deleted extent.
	2	Unpredictable.	Unpredictable.
	3	Address of CVT.	Unpredictable.
	4-13	Unpredictable.	Unpredictable.
	14	Return Address.	Unchanged.
	15	Entry point address.	Return code.
IEAVFREE	0	Irrelevant.	Irrelevant.
	1	Irrelevant.	Irrelevant.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4	Address of CIWA.	Unchanged.
	5-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVFXLD			
IEAVFXLD	0-1	Irrelevant.	Unpredictable.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4	Address of CIWA.	Unchanged.
	5-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVFXLD (continued)			
IEAVFXL	0	Irrelevant.	Unchanged.
	1	Address of the root PCB to be processed.	Unchanged.
	2	Address of the RSM Header.	Unchanged.
	3	Address of the PVT.	Unchanged.
	4-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVGCAS			
IEAVGCAS	0	Irrelevant.	Irrelevant.
	1	Address of ASCB.	Unchanged.
	2-12	Irrelevant.	Irrelevant.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVGFAS	0	Irrelevant.	Irrelevant.
	1	Address of parameter list.	Unchanged.
	2-12	Irrelevant.	Irrelevant.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAQSPET	0-3	Irrelevant.	Irrelevant.
	4	Address of TCB.	Unchanged.
	5-6	Irrelevant.	Irrelevant.
	7	Address of ASCB.	Unchanged.
	8-12	Irrelevant.	Irrelevant.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVGFA			
IEAVGFA	0	Irrelevant.	Irrelevant.
	1	Address of PCB chain to be processed.	Unpredictable.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return Code.
IEAVGFAD	0	Address of SRB.	Unpredictable.
	1	Value of SRBPARM, in this case the address of ASCB for address space in which retry is requested.	Unpredictable.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVGM00			
IGC004	0	Irrelevant.	Irrelevant.
IGC005	1	Address of parameter list.	Unchanged.
	2,3	Irrelevant.	Irrelevant.
	4	Address of current TCB.	Irrelevant.
	5	Address of RB at top of the RB queue.	Irrelevant.
	6	Irrelevant.	Irrelevant.
	7	Address of current ASCB.	Irrelevant.
	8-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Irrelevant.	Return code.
GMBRANCH	0	Irrelevant.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVGM00 (continued)			
FMBRANCH	1	Address of parameter list.	Unchanged.
	2,3	Irrelevant.	Unchanged.
	4	Address of TCB.	Unchanged.
	5,6	Irrelevant.	Unchanged.
	7	Address of current ASCB.	Unchanged.
	8,13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Return code.
IGC010	0	Subpool ID in byte 0, length in bytes 1-3; bytes 1-3 are zero for a subpool FREEMAIN.	Irrelevant.
	1	Negative value for GETMAIN, address to be freed for FREEMAIN, zero for subpool FREEMAIN.	Address of allocated area for GETMAIN, unchanged for FREEMAIN.
	2,3	Irrelevant.	Irrelevant.
	4	Address of current TCB.	Irrelevant.
	5	Address of RB at top of the RB queue.	Irrelevant.
	6	Irrelevant.	Irrelevant.
	7	Address of current ASCB.	Irrelevant.
	8-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Irrelevant.	Zero.
RMBRANCH	0	Subpool ID in byte 0, length in bytes 1-3; bytes 1-3 are zero for a subpool FREEMAIN.	Unchanged.
QCBRANCH	1	Negative value for GETMAIN, address to be freed for FREEMAIN, zero for subpool FREEMAIN.	Address of allocated area for GETMAIN, unchanged for FREEMAIN.
	2,3	Irrelevant.	Unchanged.
	4	Address of TCB.	Unchanged.
	5,6	Irrelevant.	Unchanged.
	7	Address of current ASCB.	Unchanged.
	8-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Zero.
IGC120	0	Length requested; zero for subpool FREEMAIN.	Irrelevant.
	1	Zero for GETMAIN, address to be freed for FREEMAIN, zero for subpool FREEMAIN.	Address of allocated area for GETMAIN, unchanged for FREEMAIN.
	2,3	Irrelevant.	Irrelevant.
	4	Address of current TCB.	Irrelevant.
	5	Address of RB at top of the RB queue.	Irrelevant.
	6	Irrelevant.	Irrelevant.
	7	Address of current ASCB.	Irrelevant.
	8-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Bytes 0,1=0; byte 2=SPID; byte 3=flags*	Return code.
CRBRANCH	0	Length requested; zero for subpool FREEMAIN.	Unchanged.
	1	Zero for GETMAIN, address to be freed for FREEMAIN, zero for subpool FREEMAIN.	Address of allocated area for GETMAIN unchanged for FREEMAIN.
	2	Irrelevant.	Unchanged.
	3	Byte 0=0, byte 1=protect key, byte 2=SPID, byte 3=flags*.	Unchanged.
	4	Address of TCB.	Unchanged.
	5,6	Irrelevant.	Unchanged.
	7	Address of current ASCB.	Unchanged.
	8-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Return code.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVGM00 (continued)			
GLBRANCH	0	Length requested.	Unchanged.
	1	Zero for GETMAIN, address to be freed for FREEMAIN.	Address of allocated area for GETMAIN, unchanged for FREEMAIN.
	2	Irrelevant.	Unchanged.
	3	Byte 0=0, byte 1=protect key, byte 2=SPID, byte 3=flags*.	Unchanged.
	4	Address of global save area for GETMAIN.	Unchanged.
	5-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Return code.

*flags for the previous three entry points are:

bits 0-4 (reserved)
bit 5 =0 doubleword boundary
=1 page boundary
bit 6 =0 conditions request
=1 unconditional request
bit 7 =0 GETMAIN
=1 FREEMAIN

GETMAING	0	Irrelevant.	Unchanged.
	1	Subpool ID in byte 0, length in bytes 1-3.	Address of allocated area.
	2,3	Irrelevant.	Unchanged.
	4	Address of local data area (LDA).	Unchanged.
	5-7	Irrelevant.	Unchanged.
	8	First base register.	Unchanged.
	9-12	Irrelevant.	Unchanged.
	13	Second base register.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry address.	Unchanged.
MRELEASF	0,1	Irrelevant.	Irrelevant.
	2,3	Irrelevant.	Unchanged.
	4	Address of LDA.	Unchanged.
	5	Irrelevant.	Unchanged.
	6	Size of area to be returned to an FBQE.	Unchanged.
	7	Address of PQE that storage was obtained from.	Unchanged.
	8	First base register.	Unchanged.
	9	Address of area being returned to an FBQE.	Unchanged.
	10-12	Irrelevant.	Unchanged.
	13	Second base register.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry address.	Irrelevant.
MRELEASR	0	Zero for local storage, one for global storage.	Unchanged.
	1	Virtual address of the page to be returned to an FBQE.	Unchanged.
	2-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry address.	Irrelevant.
IEAVGPRR	0	Address of work area.	Unpredictable.
	1	Address of SDWA.	Unchanged.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVGTCL	0	Cell pool identifier.	Unchanged.
	1	Unpredictable.	Address of allocated cell.
	2	Unpredictable.	Unpredictable.
	3	Address of CVT.	Unpredictable.
	4-13	Unpredictable.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVID00	0	Address of symbolic name or parameter list.	Unchanged from time SVC was issued.
	1	Address of entry point or zero.	Unchanged from time SVC was issued.
	2	Irrelevant.	Unchanged from time SVC was issued.
	3	Address of CVT.	Unchanged from time SVC was issued.
	4	Address of TCB.	Unchanged from time SVC was issued.
	5	Address of SVRB.	Unchanged from time SVC was issued.
	6	Entry point.	Unchanged from time SVC was issued.
	7	Address of ASCB.	Unchanged from time SVC was issued.
	8-13	Irrelevant.	Unchanged from time SVC was issued.
	14	Return address.	Unchanged from time SVC was issued.
	15	Irrelevant.	Return code.
IEAVINV			
IEAVINV	0	Irrelevant.	Irrelevant.
	1-1	Address of page table entry or zeros.	Unchanged.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVINVA	0-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVIOCP			
IEAVCPBR	0	Bytes 0-2 Zero Byte 3: 01 if local lock held by caller; 00 if local lock not held by caller.	Unpredictable.
	1	Address of last PCB on PCB string or zero.	Unpredictable.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVIOCP	0	Address of SRB.	Unpredictable.
	1	Value of SRBPARM.	Unpredictable.
	2-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVITAS	0	Address of ASCB for new address space.	Unpredictable.
	1	Virtual storage address of area obtained by GETMAIN from master address space.	Virtual storage address of beginning of RSM area (in master address space input page).
	2-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address	Return code.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVLK00			
IGC006	0	Irrelevant	Changed
	1	Address of user parameter list.	Transparent.
	2	Irrelevant.	Transparent.
	3	CVT address.	Transparent.
	4	Current TCB address.	Transparent.
	5	Current RB address.	Transparent.
	6-13	Irrelevant.	Transparent.
	14	Irrelevant.	Return address.
	15	Parameter list.	Entry point address.
IGC007	0-15	Same as IGC006.	Same as IGC006.
IGC008	0	Address of entry point name or address of DE in complemented form.	Address of load module requested.
	1	DCB address of zero.	Authorization code in high order byte; size of module in low order three bytes.
	2	Irrelevant.	Transparent.
	3	Address of CVT.	Transparent.
	4	Address of current TCB.	Transparent.
	5	Address of current RB.	Transparent.
	6-15	Irrelevant.	Transparent.
IGC009	0-15	Same as IGC008.	Same as IGC008.
IGC012	0-2	Irrelevant.	Unchanged.
	3	CVT address.	Unchanged.
	4	TCB address.	Unchanged.
	5	RB address.	Unchanged.
	6-13	Irrelevant.	Unchanged.
	14	Irrelevant.	Address of an SVC exit.
	15	Address of entry point to get control.	Unchanged.
IEAQCS01	0	Address of requested module name or PDS DE (complemented).	Changed.
	1	DCB address.	Transparent.
	2	Irrelevant.	Transparent.
	3	CVT address.	Transparent.
	4	TCB address.	Transparent.
	5	SVRB address.	Transparent.
	6-13	Irrelevant.	Transparent.
	14	Irrelevant.	Return address.
	15	Irrelevant.	Entry point address.
IEAQCS02	0	n/a	Changed.
	1-2	n/a	Transparent.
	3	CVT address.	Transparent.
	4	TCB address.	Transparent.
	5	RB address.	Transparent.
	6	Base address.	Transparent.
	7	n/a	Transparent.
	8	CDE queue to search.	Transparent.
	9	Address of name requested.	Transparent.
	10	Address of DCB or zero.	Transparent.
	11-13	n/a	Transparent.
	14	n/a	Return address.
	15	n/a	Entry point address.
IEAQCS03	0-10	Same as IEAQCS02.	Same as IGC006.
	11	Address of requested CDE.	
	12	Address of major CDE.	

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVLK00 (continued)			
IEAVVMSR	0	Left half of name.	LPDE address if found.
	1	Right half of name.	n/a
	2	Irrelevant.	n/a
	3	CVT address.	n/a
	4,5	Irrelevant.	n/a
	6	Irrelevant.	Changed.
	7	Irrelevant.	n/a
	8,9	Irrelevant.	Changed.
	10-13	Irrelevant.	n/a
	14	Return address.	n/a
	15	Irrelevant.	n/a
IEAQCDSR	0	Irrelevant.	Left half of name.
	1	Irrelevant.	Right half of name.
	2-7	Irrelevant.	n/a
	8	Address of pointer to first CDE on the queue.	n/a
	9	Address of name.	n/a
	10	Irrelevant.	n/a
	11	Irrelevant.	CDE address or zero.
	12,13	Irrelevant.	n/a
	14	Return address.	n/a
	15	Irrelevant.	Changed.
IEAQCS04	0-15	None.	None.
IEAVLKO1		(Exit to search LPAQ)	
	0	First 4 characters of module name.	Irrelevant.
	1	Last 4 characters of module name.	Irrelevant.
	2	Irrelevant.	Irrelevant.
	3	Irrelevant.	CVT address.
	4	TCB address.	TCB address.
	5	SVRB address.	SVRB address.
	6	Address of IEAVLKO1 (base register).	Base register for IEAVLKOO.
	7	Irrelevant.	Irrelevant.
	8	Address of last CDE queue searched.	CDE queue origin to searched.
	9	Address of name.	Address of name.
	10	DCB address.	DCB address.
	11-15	Irrelevant.	Irrelevant.
		(Exit to found module).	
	0-7		Same as "Exit to search CPAQ" above.
	8-10		Irrelevant.
	11		Requested CDE.
	12		Major code for request.
	13-15		Irrelevant.
		(Exit to process alias request).	
	0-10		Same as "Exit to search LPAQ" above.
	11		CDE for request.
	12		Major code for request.
	13-15		Irrelevant.
		(Exit to check usability of found major name).	
	0		First 4 characters of module name.
	1		Last 4 characters of module name.
	2-10		Same as "Exit to search LPAQ" above.
	11		CDE for request.
	12		Major code for request.
	13-15		Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVLKO1 (continued)			
		(Error exit).	
	0-12		Irrelevant.
	13		ABEND code.
	14		Irrelevant.
	15		Reason code for ABEND.
IEAVLKO2			
CDHKEEP	0-3	Irrelevant.	Changed.
	4	Current or terminating TCB address.	Changed.
	5	Irrelevant.	Unchanged.
	6-10	Irrelevant.	Irrelevant.
	11	Major CDE.	Unchanged.
	12	Terminating CDE address.	Unchanged.
	13	Irrelevant.	Unchanged.
	14	Return address.	Changed.
	15	Irrelevant.	Changed.
IEAPPGMX	0-4	Irrelevant.	Irrelevant.
	5	Terminating RB.	Irrelevant.
	6-12	Irrelevant.	Irrelevant.
	13	Save area of caller.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Irrelevant.	Return code.
IEAPPGMA	0	Irrelevant.	Irrelevant.
	1	Pointer to parameter list pointer.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Save area of caller.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Irrelevant.	Return code.
IEAVLK03	0	Irrelevant.	Unchanged.
	1	Pointer to SDWA.	Unchanged.
	2-13	Irrelevant.	Unchanged.
	14	Return address.	Return address.
	15	Irrelevant.	Unchanged.
IEAVMASV			
IEAVTPUT	0-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVMDOM	0-3	Irrelevant.	Unchanged.
	4	UCM prefix address.	Unchanged.
	5-12	Irrelevant.	Unchanged.
	13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVMDSV	0	Irrelevant.	Irrelevant.
	1	Input code. 0 = DEVSERVA 4 = DEVSERVB 8 = DQCLNUP	Irrelevant.
	2	UCME address.	Irrelevant.
	3	DEVSERVA and DQCLNUP only; address of next UCME.	Irrelevant.
	4	UCM-prefix address.	Unchanged.
	5	Irrelevant.	Unchanged.
	6	DEVSEVB only; ECB address posted for I/O completion.	Unchanged.
	7-10	Irrelevant.	Unchanged.
	11	DEVSERB only; UCME address posted for the console that received the I/O completion or attention.	Unchanged.
	12-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevent.
IEAVMFRR			
(From FRR)	0	200 byte work area address.	Irrelevant.
	1	SDWA address.	Irrelevant.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevant.
(From ESTAE)	0	SDWA indicator.	Irrelevant.
	1	SDWA address or ABEND code.	Irrelevant.
	2	Address of user's parameter list if there is no SDWA.	Irrelevant.
	3-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevant.
IEAVMNTR	0	Standard.	Standard.
	1	Address of parameter list address.	Standard.
	2-15	Standard.	Standard.
IEAVMODE			
IGC107	0	Irrelevant.	Unpredictable.
	1	Bits that define request.	Unpredictable.
	2-14	SVC registers.	Irrelevant.
	15	Irrelevant.	Irrelevant.
IEAVMQR0	0	Irrelevant.	Unchanged.
	1	Pointer to parameter list.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVMQWR (After first entry.)	0-1	Irrelevant.	n/a (permanent task)
	2	UCM address.	n/a (permanent task)
	3	Next UCME address.	n/a (permanent task)
	4	UCM-prefix address.	n/a (permanent task)
	5	EIL address. Irrelevant.	n/a (permanent task)
	6-7	Irrelevant.	n/a (permanent task)
	8	EIL address.	n/a (permanent task)
	9	Base register.	n/a (permanent task)
	10	Irrelevant.	n/a (permanent task)
	11	UCME-BXLE address-begin address.	n/a (permanent task)
	12	UCME-BXLE address-increment.	n/a (permanent task)
	13	UCME-BXLE address-end address.	n/a (permanent task)
	14-15	Irrelevant.	n/a (permanent task)
IEAVMWSV	0-1	Irrelevant.	Irrelevant.
	2	UCM address.	Irrelevant.
	3	Irrelevant.	Irrelevant.
	4	UCM prefix address.	Irrelevant.
	5-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevant.
IEAVMWTO	0	Irrelevant.	Unchanged.
	1-2	Irrelevant.	Unchanged.
	3	CVT address.	Unchanged.
	4	TCB address.	Unchanged.
	5	SVRB address.	Unchanged.
	6	WPL address.	Unchanged.
	7	ASCB address.	Unchanged.
	8-9	Irrelevant.	Unchanged.
	10	UCM base address.	Unchanged.
	11	Irrelevant.	Unchanged.
	12	XVSAV area in SVRB.	Unchanged.
	13	Save area pointer.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.
IEAVNPA6	0-1	Irrelevant.	(Normal exit: registers unchanged.
	2	NIP vector table address.	Abnormal exit: register 15 is a
	3	CVT address.	pointer to NIP SWAIT routine;
	4-12	Irrelevant.	registers 0-14 are unpredictable.)
	13	A 72 byte register save area address.	
	14	Return address.	
	15	Entry point address.	
IEAVOUT	0,1	Irrelevant.	Irrelevant.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4	Address of CIWA.	Unchanged.
	5-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Irrelevant.	Irrelevant.
IEAVPCB	0	If build option, number of PCBs desired; otherwise, TQN of input PCBs.	Unchanged.
	1	If build option, 0; otherwise, address of PCB to be enqueued or dequeued or moved.	If build option, address of first PCB obtained for this request; otherwise, unchanged.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVPFTE	0	Queue flag, TQID, and RBN.	Unchanged.
	1	Address of ASCB for TQID local frame queues.	Unchanged.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVPIOI	0	Address of SRB.	Unpredictable.
	1	Address of AIA.	Unpredictable.
	2-13	Irrelevant.	Irrelevant.
	14	Irrelevant.	Return address.
	15	Entry point address.	Unpredictable.
IEAVPIOP			
IEAVPIOP	0	Irrelevant.	Irrelevant.
	1	Address of AIA.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVOPBR	0	Irrelevant.	Irrelevant.
	1	Address of PCB with or without related PCBs.	Unchanged.
	2	Irrelevant.	Unchanged.
	3	Address of PVT.	Unchanged.
	4-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVPIX	0-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVPREF	0	Pass number (tells IEAVPREF which criteria to use).	Unchanged.
	1	Real block number (RBN).	Unchanged.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVPRT0			
IEAVPRT0	0	For entry through SVC 10 or 120 and GETMAIN, region size; otherwise irrelevant.	Unpredictable.
	1	For SVC 4 entry, address of parameter list; for SVC 10 or 120 entry, a negative value; for a FREEMAIN entry, a positive non-zero value.	Unpredictable.
	2,3	Irrelevant.	Irrelevant.
	4	Address of LDA.	Unpredictable.
	5	Bytes 0-2: irrelevant byte 3: subpool ID.	Unpredictable.
	6,7	Irrelevant.	Irrelevant.
	8	Base register for IEAVGM00.	Unpredictable.
	9-12	Irrelevant.	Irrelevant.
	13	Base register for IEAVGM00.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVPRT0 (continued)			
IEALIMIT	0	Irrelevant.	Irrelevant.
	1	Number of bytes requested.	Limit size; zero if no limit.
	2-12	Irrelevant.	Irrelevant.
	13	Save area address.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVPSI			
IGC112	0	Low address of virtual storage area to be released.	Unpredictable.
	1	High address of virtual storage area to be released.	Unpredictable.
	2	Irrelevant.	Irrelevant.
	3	Address of CVT.	Unpredictable.
	4	Address of TCB.	Unpredictable.
	5	Address of RB.	Unpredictable.
	6	Address of entry point.	Unpredictable.
	7	Address of ASCB.	Unpredictable.
	8-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Irrelevant.	Return code.
IGC113	0	Address of ECB or zero.	Unpredictable.
	1	If byte 0, bit 0 is one, address of VSL; if zero, first half of a VSL entry.	Unpredictable.
	2	Irrelevant.	Irrelevant.
	3	Address of CVT.	Unpredictable.
	4	Address of TCB.	Unpredictable.
	5	Address of RB.	Unpredictable.
	6	Entry point address.	Unpredictable.
	7	Address of ASCB.	Unpredictable.
	8-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	If register 1, byte 0, bit 0 is one, irrelevant; if zero, last half of a VSL entry.	Return code.
IEAVPSIB IEAVPSIX IEAVPSIF	0	Address of ECB or zero.	Unchanged.
	1	If byte 0, bit is one, address of VSL; if zero, first half of a VSL entry.	Unchanged.
	2	If register 1, byte 0, bit 0 is zero, last half of a VSL entry; if one, irrelevant.	Unchanged.
	3	Irrelevant.	Irrelevant.
	4	Address of TCB or zero.	Unchanged.
	5-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Return Code.
IEAVPSII	0	0 or address of ECB residing in fixed storage.	Unchanged.
	1	First half of VSL entry.	Unchanged.
	2	Last half of VSL entry.	Unchanged.
	3	Address of PVT.	Unchanged.
	4	Address of TCB or 0.	Unchanged.
	5-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVPSI (continued)			
NEXTVSL	0,1	Irrelevant.	Irrelevant.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4	Address of CIWA.	Unchanged.
	5-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVRCF			
IEAVRCF	0	Irrelevant.	Irrelevant.
	1	Address of parameter list.	Unchanged.
	2-12	Irrelevant.	Irrelevant.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVRCFI	0	Irrelevant.	Irrelevant.
	1	Real Block Number of intercepted frame.	0, if the frame was accepted by the interception routine; unchanged, if the frame was rejected because no request existed for it in a root PCB.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4-5	Irrelevant.	Irrelevant.
	6	PFTE address.	Unchanged.
	7-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVRCFC	0	Address of SRB.	Unchanged.
	1	Value of the SRBPARM field, in this case the address of the root PCB.	Unchanged.
	2-12	Irrelevant.	Irrelevant.
	13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVRCV			
IEAVRCV	0	Address of work area.	Unpredictable.
	1	Address of SDWA.	Unchanged.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Irrelevant.	Irrelevant.
IEAVRCV2	0	Address of work area.	Unpredictable.
	1	Address of SDWA.	Unchanged.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Irrelevant.	Irrelevant.
IEAVRELS			
IEAVRELS	0,1	Irrelevant.	Irrelevant.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4	Address of CIWA.	Unchanged.
	5-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVRELS (continued)			
IEAVRELV	0	Low Address of Virtual Storage Area to be Released.	Unchanged.
	1	High Address of Virtual Storage Area to be Released.	Unchanged.
	2	Address of ASCB.	Address of first or only virtual storage area to be freed by FREEMAIN; if no FREEMAIN, zero;
	3	Address of PVT.	If return code 8, Address of Virtual Page put into Deferred Release status; otherwise, unchanged.
	4-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVRELF	0	Irrelevant.	Irrelevant.
	1	Address of virtual page to be released.	Unchanged.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVRFR			
IEAVRFR	0	Irrelevant.	Irrelevant.
	1	Address of parameter list.	Unchanged.
	2-12	Irrelevant.	Irrelevant.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVRFRA	0	SRB address.	Unpredictable.
	1	Address of RSM Header.	Unpredictable.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
FREEPAGE	0,1	Irrelevant.	Unchanged.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4	Address of PFTE.	Unchanged.
	5	Address of parameter list.	Unchanged.
	6	PFTE index.	Unchanged.
	7,8	Irrelevant.	Unchanged.
	9	Base register.	Unchanged.
	10-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVRTI0			
IEA0TI00	0,1	Irrelevant.	Irrelevant.
	2	Return address.	Unchanged.
	3-15	Irrelevant.	Irrelevant.
IEAQTE00	0	Irrelevant.	Irrelevant.
	1	Address of the subject TQE.	Unchanged.
	2	Return address.	Unchanged.
	3-12	Irrelevant.	Unchanged.
	13	Irrelevant.	Unpredictable.
	14	Irrelevant.	Unchanged.
	15	Irrelevant.	Unchanged.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVRTI0 (continued)			
IEAQTD00	0	Irrelevant.	Unchanged.
	1	Address of subject TQE.	Unchanged.
	2	Return address.	Unchanged.
	3-12	Irrelevant.	Unchanged.
	13	Irrelevant.	Unpredictable.
	14	Irrelevant.	Unchanged.
	15	Irrelevant.	Unpredictable.
IEAVRCKQ	0,1	Irrelevant.	Unchanged.
	2	Return address.	Unchanged.
	3-12	Irrelevant.	Unchanged.
	13	Irrelevant.	Unpredictable.
	14	Irrelevant.	Unchanged.
	15	Irrelevant.	Unpredictable.
IEAVRQCK	0-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unpredictable.
IEAVRSAE	0	Address of SRB.	Unpredictable.
	1	Value of SRBPARM; in this case, address of TQE.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unpredictable.
IEAVRTI1			
IEAQPGTM	0	Irrelevant.	Unchanged.
	1	Address of RMPL.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Always zero.
IEAVRSPG	0	Address of SRB.	Unpredictable.
	1	Value of SRBPARM; in this case, address of TQE.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVRCLS	0	If ACR error, physical address of failing CPU; otherwise, irrelevant.	Unchanged.
	1	If machine check error, address of LRB.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.
IEAVRCLX	0-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unpredictable.
IEAVRNEW	0-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.
IEAVRSPN	0-15	Irrelevant.	Unpredictable.
IEAVRFRR	0	Irrelevant.	Unpredictable.
	1	Address of SDWA.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unpredictable.
IEAVRTVR	0	Address of TQE.	Unchanged.
	1	Address of SDWA.	Unchanged.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVRTOD			
IEAVRSSC	0	If the caller is IEEVCPU, 0; if the caller is IEAVRCLA, 1.	Unchanged.
	1	Address of fullword in storage containing address of PCCA.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return Code.
IEAVRCLA	0-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unpredictable.
IEAVRNOT	0	Irrelevant.	Unpredictable.
	1	Address of fullword in storage containing address of PCCA.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return Code.
IEAVRCAN	0	Irrelevant.	Unpredictable.
	1	Address of fullword in storage containing address of PCCA.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVRT00			
IGC0004F	0	Address of area in which to store interval remaining if MIC is specified.	Unchanged, if MIC is specified; if TU is specified, remaining time interval in timer units.
	1	Bytes 0-2: irrelevant Byte 3: if Bit 5 is one, ERRET is specified; if Bit 6 is one, MIC is specified; if bit 6 is zero, TU is specified; and if Bit 7 is one, CANCEL is specified.	Unchanged.
	2	Irrelevant.	Irrelevant.
	3	Address of CVT.	Unpredictable.
	4	Address of requestor's TCB.	Unpredictable.
	5	Address of SVRB.	Unpredictable.
	6-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Irrelevant.	0, if successful; 8, if unsuccessful and ERRET specified.
IGC0004G	0	Byte 1: bits 0-3 0000, TUINTVL 0001, BINTVL 0010, MICVL 0011, DINTVL 0110, GMT 0111, TOD specified on macro instruction; if bit 4 is one, ERRET has been specified; bits 5-7 indicate type of request: 000, task type; 001, = wait type 001, real type; Bytes 1-3 contain the address of the user's asynchronous exit routine.	Unchanged.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVRT00 (continued)			
IGC0004G (continued)	1	Address of desired time interval.	Unchanged.
	2	Irrelevant.	Unpredictable.
	3	Address of CVT.	Unpredictable.
	4	Address of requestor's TCB.	Unpredictable.
	5	Address of SVRB.	Unpredictable.
	6	Irrelevant.	Unpredictable.
	7	Address of requestor's ASCB.	Unpredictable.
	8-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Irrelevant.	0, if successful; 8, if unsuccessful and ERRET is specified.
TTSTSTAE	0	12, if no SDWA is present; ≠ 12 otherwise.	Unchanged.
	1	Address of SDWA, if one is supplied.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.
IEAVRT01			
IGC0001A	0	Address of area in which to store time if STCK or MIC is specified.	Requested time, if TU, BIN, or DEC is specified; otherwise, unchanged.
	1	Bytes 0-2: Irrelevant Byte 3: Type of units. specified: if bit 0 is 1, GMT is requested; if bit 1 is 1, ERRET has been specified; bits 4-7 indicate units—0000 for TU, 0001 for BIN, 0010 for DEC, 0011 for MIC, or 0100 for STCK.	Requested date, if TU, BIN, or DEC is specified; otherwise, unchanged.
	2	Irrelevant.	Unpredictable.
	3	Address of CVT.	Unpredictable.
	4	Address of requestor's TCB.	Unpredictable.
	5	Address of SVRB.	Unpredictable.
	6-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Irrelevant.	0, if successful; 8, if no usable TOD clock was found; 12, terminated because key of storage passed by caller doesn't match TCB protect key.
IEAVRTME	0	Irrelevant.	Unpredictable.
	1	Address of TPC.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return Address.	Unchanged.
	15	Irrelevant.	Unpredictable.
TIMESTAE	0	12, if no SDWA is present; ≠ 12, otherwise.	Unchanged.
	1	Address of SDWA, if present.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVRT02			
IEAVRDIE	0	Irrelevant.	Volatile.
	1	TQE address.	Unchanged.
	2-12	Parameters for input to DIE.	2-10 unchanged. 11-12 volatile.
	13	Irrelevant.	Volatile.
	14	Return address.	Volatile.
	15	Irrelevant.	Volatile.
IEAVROCL	0-3	Irrelevant.	Volatile.
	4-10	Irrelevant.	Unchanged.
	11	Address of STCK area.	Unchanged.
	12-13	Irrelevant.	Volatile.
	14	Return address.	Volatile.
	15	Irrelevant.	Volatile.
IEAVRTAP	0	Irrelevant.	Volatile.
	1	Address of STCK area.	Volatile.
	2-13	Irrelevant.	Volatile.
	14	Return address.	Volatile.
	15	IEAVRTAP entry point address.	Volatile.
IEAVSETS			
IGC079	0	Primary mask if ND, else ASID.	Unchanged.
	1	TCB address (optional); bit 0=1 (Reset); bit 0=0 (Set).	
	2-14	Standard SVC registers.	Standard SVC registers.
	15	Secondary mask if SD, else ASID (optional).	0—normal completion 4—stop-start failed.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVSETS (continued)			
IG07902	0	Primary mask if ND, else ASID. bit 0 = 1 Reset bit 0 = 0 Set.	Unchanged.
	1	TCB address (optional).	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Secondary mask if SD, else ASID (optional).	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	0-normal completion.
IGC07903	0	ASID in high order two bytes. X'0D in low order byte.	Unchanged.
	1	bit 0 = 1	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	0—normal completion.
IEAVESSS	0-3	Irrelevant.	Unchanged.
	4	Address TCB	Unchanged.
	5-6	Irrelevant.	Unchanged.
	7	Address ASCB.	Unchanged.
	8-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVSSNQ	0-10	Irrelevant.	Unchanged.
	11-13	Irrelevant.	Unpredictable.
	14	Entry point address.	Unpredictable.
	15	Return address.	Unchanged.
IEATRSCN	0-6	Irrelevant.	Unchanged.
	7	Irrelevant.	Unpredictable.
	8	Address of highest level task in tree to be searched.	Unchanged.
	9	Irrelevant.	Unpredictable.
	10	Address of task from which search is to start.	Address of selected task (if found).
	11	Return address (if no TCB found).	Unchanged.
	12-13	Irrelevant.	Unchanged.
	14	Return address (if TCB was found).	Unchanged.
	15	Entry point address of IGC07902.	Unchanged.
IEAVSOUT	0	Irrelevant.	Irrelevant.
	1	Address of ASCB.	Unpredictable.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return Code.
IEAVSQA	0	Irrelevant.	Irrelevant.
	1	0 or vitual storage address of page needing frame.	Frame Real Block Number.
	2	Address of RSM Header.	Unchanged.
	3	Address of PVT.	Unchanged.
	4-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Return Code or, in error case, return code from IEAVFP.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVSTAA	0	Situation indicator: 0 = I/O quiesce. 4 = I/O halted. 8 = I/O inactive. 12 = SDWA was not obtained. 16 = I/O processing was not performed.	Irrelevant.
	1	If register 0: ≠ 12 then SDWA address. = 12 then ABEND completion code.	Irrelevant.
	2	If register 0: = 12 then address of user supplied parameter list. ≠ 12 then irrelevant.	Irrelevant.
	3-12	Irrelevant.	Irrelevant.
	13	If register 0: ≠ 12 then address of the user supplied parameter list. = 12 the irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevant.
IEAVSTA0			
IGC00060	0	Action Code (create, overlay, etc.)	Irrelevant.
	1	Parameter list address.	Irrelevant.
	2	Irrelevant.	Irrelevant.
	3	CVT address.	Irrelevant.
	4	Address of TCB under which SVC 60 will operate.	Irrelevant.
	5	SVC 60 SVRB address.	Irrelevant.
	6	Entry Point.	Irrelevant.
	7	ASCB address.	Irrelevant.
	8-13	Irrelevant.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Irrelevant.	Return Code (0,4,8,12,16,20).
ESTAEBRE	0	Action Code (ct,ov,etc.).	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Register save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return Code (same as SVC entry).
ESTAFRR	0	Irrelevant.	Irrelevant.
	1	Pointer to RTCA.	Irrelevant.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Irrelevant.	Irrelevant.
IEAVSWCH	0	Irrelevant.	Irrelevant.
	1	SVRB extended save area address.	Irrelevant.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVSWIN			
IEAVSWIN	0	Address of SRB.	Unpredictable.
	1	SRBPARM value, in this case the address of the ASCB.	Unpredictable.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVSIRT	0	Irrelevant.	Irrelevant.
	1	Address of root PCB.	Unchanged.
	2	Irrelevant.	Irrelevant.
	3	Address of PVT.	Unchanged.
	4-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVSY50			
IGC001	0	Number of events that must occur before the issuing task can continue in control. bit 0 = 1 long wait = 0 short wait.	Unchanged.
	1	ECB address or ECB list address in complemented form.	Unchanged.
	2-13	Irrelevant.	Irrelevant.
	14	Irrelevant.	Address of IEAVEXP1.
	15	Irrelevant.	Unpredictable.
IGC002	0	Completion code.	Unpredictable.
	1	Address of ECB or XMPOST parameter list address.	Unpredictable.
	2-13	Standard SVC registers.	Standard SVC register.
	14	Standard SVC register.	Address of IEAVEXP1.
	15	Irrelevant.	Unpredictable.
IEAVWAIT	0	Same as IGC001	Set up by caller in TCBGRS.
	1	Same as IGC001.	Unchanged.
	2-14	Irrelevant.	Set up by caller in TCBGRS.
	15	Address of IEAVWAIT.	Set up by caller in TCBGRS.
IEA0PT02	0-9	Irrelevant.	Unchanged.
	10	Completion code.	Unpredictable.
	11	Address ECB.	Unpredictable.
	12-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEA0PT01			
Condition A	If reg 11 is non-zero, and bit 0=0:		
	0-15	Same as IEA0PT02.	Same as IEA0PT02.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVSY50 (continued)			
IEAOPT01 (continued)			
Condition B	If reg 11, bit 0=1 and bits 1-31 non-zero:		
	0-8	Irrelevant.	Unpredictable or unchanged. If bit 0 of register 12=1, or local lock is not held, then registers 0-8 are unpredictable otherwise, registers 0-8 are unchanged.
	9	Irrelevant.	Unchanged.
	10	Completion code.	Unpredictable.
	11	Address ECB.	Unpredictable.
	12	ERRET address.	Unpredictable.
	13	ASCB address.	Unpredictable.
	14	Same as IEA0PT02.	Unchanged.
	15	Same as IEA0PT02.	Unpredictable.
Condition C	If reg 11 is zero:		
	0-9	Same as IEA0PT02.	Same as IEA0PT02.
	10	Address of RB being posted.	Same as IEA0PT02.
	11	0.	Same as IEA0PT02.
	12-15	Same as IEA0PT02.	Same as IEA0PT02.
Condition D	If reg 11, bit 0=1 and bits 1-31=0:		
	0-9	Same as "C" above.	Same as Condition B.
	10	Address RB posted.	Same as Condition B.
	12-15	Same as "C" above.	Same as Condition B.
IEA0PT03			
Condition A	If reg 11 is non-zero and bit 0=0:		
	0-14	Same as IEA0PT01 condition A.	Unchanged.
	15	Entry point address.	Unpredictable.
Condition B	If reg 11, bit 0=1 and bits 1-31 are non-zero:		
	0-9	Irrelevant.	Unchanged.
	10-14	Same as IEA0PT01 condition B.	Unchanged.
	15	Entry point address.	Unpredictable.
Condition C	If reg 11 is zero:		
	0-14	Same as IEA0PT01 condition C.	Unchanged.
	15	Entry point address.	Unpredictable.
Condition D	If reg 11, bit 0=1 and bits 1-31 are zero:		
	0-14	Same as IEA0PT01 condition D.	Unchanged.
	15	Entry point address.	Unpredictable.
IEARPOST	0	Irrelevant.	Unchanged.
	1	Address of word containing address of RMPL.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Address save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Zero.
IEAVTABD	0	Irrelevant.	Irrelevant.
	1	RTM2WA address.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Caller's save area address.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry address.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVTABI	0-1	Irrelevant.	Unchanged.
	2	NIP Vector Table (NVT)	Unchanged.
	3	CVT.	Unchanged.
	4-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point.	Unchanged.
IEAVTACR	0	Irrelevant.	Irrelevant.
	1	Address of LOGREC buffer for the CPU requiring recovery.	Real address of system termination parameter list (error exit).
	2	Return address of interruption handler.	Irrelevant.
	3-9	Irrelevant.	Irrelevant.
	10	Irrelevant.	Address of current LCCA.
	11-15	Irrelevant.	Irrelevant.
IEAVTAS1	0	Irrelevant.	Irrelevant.
	1	RTM2 work area addres.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Register save area address.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Entry point address.	Irrelevant.
IEAVTAS2	0-2	Irrelevant.	Irrelevant.
	3	Address of RTM2 work area.	Irrelevant.
	4	Irrelevant.	Irrelevant.
	5	Address of SCB.	Irrelevant.
	6	Irrelevant.	Irrelevant.
	7	Address of SDWA.	Irrelevant.
	8-15	Irrelevant.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVTAS3	0-2	Irrelevant.	Irrelevant.
	3	Address of RTM2 work area.	Irrelevant.
	4	Irrelevant.	Irrelevant.
	5	Address of SCB.	Irrelevant.
	6	Irrelevant.	Irrelevant.
	7	Address of SDWA.	Irrelevant.
	8-15	Irrelevant.	Irrelevant.
IEAVTB00			
IGC0001D	0	Irrelevant.	Unpredictable.
	1	Address of PICA built by SPIE macro.	Address of previous PICA.
	2-14	Standard SVC registers.	Irrelevant.
	15	Irrelevant.	Unpredictable.
IGC00040	0	Irrelevant.	Unpredictable.
	1	Address of parm list.	Unpredictable.
	2-14	SVC registers.	Unpredictable.
	15	Irrelevant.	Unpredictable.
IGC00040+8	0	Irrelevant.	Unpredictable.
	1	Address of parm list.	Unpredictable.
	2	Irrelevant.	Unpredictable.
	3	Irrelevant.	Address of CVT.
	4	Address TCB.	Unchanged.
	5-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
IEAVSPIE	0-15	(Same as IEARPOST).	
IEAVTCR1	0	If entered when PSAACR is on, contains RTM1 function code; otherwise, irrelevant.	If PSAACR on, restored to values set by mainline ACR; if PSAACR off, irrelevant.
	1	If entered when PSAACR is not on, contains pointer to SDWA; otherwise irrelevant.	If PSAACR on, restored to values set by mainline ACR; if PSAACR off, irrelevant.
	2-13	Irrelevant.	If PSAACR on, restored to values set by mainline ACR; if PSAACR off, irrelevant.
	14	If entered when PSAACR is not on, contains return address; otherwise, irrelevant.	If PSAACR on, restored to values set by mainline ACR; otherwise, return point.
	15	Entry point address.	If PSAACR on, address of retry point within mainline, otherwise, irrelevant.
IEAVTERM	0	Irrelevant.	Irrelevant.
	1	Virtual storage address of input parameter list, the RMPL.	Unpredictable.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVTEST			
IEAVTEST	0	Authorization code, if supplied; otherwise negative.	Unpredictable.
	1	Function code.	Unpredictable.
	2-3	Irrelevant.	Unpredictable.
	4-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point.	Return code: 0-task authorized 4-task not authorized.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVTEST (continued)			
IGC119	0	Authorization code, if supplied; otherwise negative.	Unpredicatable.
	1	Function code.	Unpredictable.
	2-14	Standard SVC registers	Standard SVC registers.
	15	Irrelevant.	Return code: 0-task authorized. 4-task not authorized.
IEAVTFMT	0	Irrelevant.	Irrelevant.
	1	Address of print dump parameter list.	Unchanged.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVTMMT	0	Irrelevant.	All registers restored.
	1	RTM2WA address.	All registers restored.
	2-12	Irrelevant.	All registers restored.
	13	Pointer to register save area.	All registers restored.
	14	Return address.	All registers restored.
	15	Entry point address.	All registers restored.
IEAVTMRM	0	Irrelevant.	Unchanged.
	1	Pointer to RMPL address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Register save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVTMTC	0	Irrelevant.	n/a
	1	Address of an ECB to be posted.	n/a
	2-14	Irrelevant.	n/a
	15	Entry point address.	n/a
IEAVTMTR	0	Irrelevant.	Unchanged.
	1	Address of terminating ASCB.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area.	Unchanged.
	14	Return point.	Unchanged.
	15	Entry point.	Unchanged.
IEAVTPMT	0	Irrelevant.	Unchanged.
	1	Pointer to RMPL address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Register save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVTRCE			
TREX	0-1	Unchanged from time of interruption.	Unchanged.
	2-9	Irrelevant.	Unchanged.
	10	Entry point address of trace code.	Unpredictable.
	11	Return address.	Unchanged.
	12-14	Irrelevant.	Unchanged.
	15	Unchanged from time of interruption.	Unchanged.
TRIO	0-2	Unchanged from time of interruption.	Unchanged.
	3-4	Irrelevant.	Unchanged.
	5-9	Unchanged from time of interruption.	Unchanged.
	10	Entry point of address of trace code.	Unpredictable.
	11	Return address.	Unchanged.
	12-15	Unchanged from time of interruption.	Unchanged.
TRPI	0-9	Unchanged from time of interruption.	Unchanged.
	10	Entry point address of trace code.	Unpredictable.
	11	Return address.	Unchanged.
	12-15	Unchanged from time of interruption.	Unchanged.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVTRCE (Continued)			
TRSVC	0-15	Same as entry point TRPI.	Same as entry point TRPI.
TRSIO	0	Irrelevant.	Irrelevant.
	1	16 bit address related to the SIO.	Irrelevant.
	2-5	Irrelevant.	Irrelevant.
	6	Address of device.	Irrelevant.
	7-8	Irrelevant.	Irrelevant.
	9	Condition code.	Irrelevant.
	10	Entry point address of trace code.	Irrelevant.
	11-15	Irrelevant.	Irrelevant.
TRDISP	0-9	Irrelevant.	Irrelevant.
	10	Entry point address of trace code.	Irrelevant.
	11	Return address.	Unchanged.
	12	New RB address.	Irrelevant.
	13	Irrelevant.	Irrelevant.
	14	New TCB address.	Irrelevant.
	15	Unchanged from time of dispatch.	Irrelevant.
IEAVTRER	0	Flags & length of data to be recorded.	Irrelevant.
	1	Address of parameter list address of data to be recorded.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Save area address.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Entry point address.	Return code 0,4,8,12,16, or 20 depending on processing requested and performed.
IEAVTRET	0	Irrelevant.	n/a
	1	Address of ECB to be posted.	n/a
	2-15	Irrelevant.	n/a
IEAVTRTC	0-3	Irrelevant.	Irrelevant.
	4	Address of TCB.	Irrelevant.
	5-7	Irrelevant.	Irrelevant.
	8	Address of RTM2WA.	Irrelevant.
	9-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address of IEAVTRTC.	Irrelevant.
IEAVTRTE	0-3	Irrelevant.	n/a
	4	TCB address.	n/a
	5	SVRB address.	n/a
	6-7	Irrelevant.	n/a
	8	RTM2WA address.	n/a
	9-12	Irrelevant.	n/a
	13	Register save area address.	n/a
	14	Return address.	n/a
	15	Entry point address.	n/a
IEAVTRTH	0	Entry point indicator for machck.	Entry point indicator for machck.
	1-3	Irrelevant.	Irrelevant.
	4	Address of FRR's parm. area.	Address of FRR's parm. area.
	5	Irrelevant.	Irrelevant.
	6	Address of logrec buffer.	Pointer to acquired EEDs.
	7-8	Irrelevant.	Irrelevant.
	9	IEAVTRTM's base reg.	IEAVTRTM's base reg.
	10	Irrelevant.	Pointer to RTM's WSAC.
	11-13	Irrelevant.	Irrelevant.
	14	Return address to IEAVTRTM.	Return address to IEAVTRTM.
	15	Entry point address.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVTRTM	0	Entry point indicator.	Exit type indicator.
	1	CALLRTM flags and comp. code.	Exit parameter/meaningful only for retry and MACHCK exits.
	2	ASID/abterm or memterm entries address of 1st half of PSW/slih mode entries.	Irrelevant. (con't some pages forward).
	3	TCB addr/service routine entries SRB addr/pgioerr entry if RB=0 address of 2nd half of PSW/slih mode entries.	Irrelevant.
	4	Pointer to FRR's parm area/service Routine entries Pointer to FRR stack to be used by IEAVTRTS for recovery.	Pointer to FRR's parm area/service routine entries.
	5	Address of dump options/abterm entry repair status info./machck reentry.	Irrelevant.
	6	Address of logrec buffer/MACHCK entry Address of EEDS/RTM1 reentries.	Irrelevant.
	7	RB addr/PGIOERR entry.	Irrelevant.
	8-12	Irrelevant.	Irrelevant.
	13	Address of registers at time of error.	Irrelevant.
	14	Return address in IEATVRT1.	Return address in IEAVTRT1.
	15	Entry point address.	Irrelevant.
XMABTERM	0	Function code.	Exit code.
	1	Completion code.	Unchanged.
	2	ASID of address space into which RB is to be scheduled.	Unchanged.
	3	Address of TCB to be serviced.	Unchanged.
	4	Address of tracking area.	Unchanged.
	5	Dump options or 0 (no dump options).	Unchanged.
	6	Address of EED chain, if any, 0 if no EEDs acquired, or 1 if previous attempt to acquire EED all failed	0 (no EEDs are to be freed) or address of EEDs acquired
	7-12	Irrelevant.	Irrelevant.
	13	Address of register save area.	Unchanged.
	14-15	Irrelevant.	Irrelevant.
MEMTERM	0	Function code.	Altered if validity checks fail.
	1	Completion code.	Irrelevant.
	2	ASID of address space to be terminated.	Irrelevant.
	3	Irrelevant.	Irrelevant.
	4	Address of tracking area.	Irrelevant.
	5-15	Irrelevant.	Irrelevant.
IEAVTRTR			
FREEDCELL	0-5	Irrelevant.	Irrelevant.
	6	Pointer to chain of EEDs to be freed.	Irrelevant.
	7-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Entry point.	Irrelevant.
RTMRSFRR	0	Pointer to 200 byte work area.	Irrelevant.
	1	Pointer to SDWA.	Pointer to SDWA.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Entry point address.	Irrelevant.
RTHFRR	0	Pointer to 200 byte work area.	Irrelevant.
	1	Pointer to SDWA.	Pointer to SDWA.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Entry point address.	Irrelevant.
RTMSMFRR	0	Pointer to 200 byte work area.	Irrelevant.
	1	Pointer to SDWA.	Pointer to SDWA.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Entry point address.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVTRTR (continued)			
RECVRRTM	0-13	Same as entry regs for slih mode entries to IEAVTRTM.	Internal register values to continue processing in either IEAVTRTM or IEAVTRTS.
	14	Return address.	Return address.
	15	Entry point address.	Irrelevant.
SLIP	0-1	Irrelevant.	Irrelevant.
	2	Pointer to stack for IEAVTRTS's recovery.	Pointer to stack for IEAVTRTS's recovery.
	3-4	Irrelevant.	Irrelevant.
	5	Pointer to SDWA.	Pointer to SDWA.
	6-7	Irrelevant.	Irrelevant.
	8	Pointer to interrupted stack.	Pointer to interrupted stack.
	9-12	Irrelevant.	Irrelevant.
	13	Address of save area passed by IEAVTRTS.	Address of save area passed by IEAVTRTS.
	14	Return address.	Return address.
	15	Entry point address.	Irrelevant.
RCOVGETM	0	Pointer to 200 byte work area.	Irrelevant.
	1	Pointer to SDWA.	Pointer to SDWA.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Entry point address.	Irrelevant.
RCOVRCRD	0	Pointer to 200 byte work area.	Irrelevant.
	1	Pointer to SDWA.	Pointer to SDWA.
	2	Address of recovery stack used by IEAVTRTS to define its own FRR's.	Irrelevant.
	3-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Entry point address.	Irrelevant.
RCOVRGTF	0	Pointer to 200 byte work area.	Irrelevant.
	1	Pointer to SDWA.	Pointer to SDWA.
	2	Address of recovery stack used by IEAVTRTS to define its own FRR's.	Irrelevant.
	3-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Entry point addresss.	Irrelevant.
RCOVSLP1	0	Pointer to 200 byte work area.	Irrelevant.
	1	Pointer to SDWA.	Pointer to SDWA.
	2	Address of recovery stack used by IEAVTRTS to define its own FRR's.	Irrelevant.
	3-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Entry point address.	Irrelevant.
IEAVTRTL	0-8	Irrelevant.	Same as input.
	9,10	Irrelevant.	Indeterminate.
	11	Irrelevant.	Irrelevant.
	12	Irrelevant.	Same as input.
	13	Address of reg save area.	Indeterminate.
	14	Return address.	Same as input.
	15	Entry point.	Same as input.
SLIP2FRR	0	Pointer to 200 byte work area.	Irrelevant.
	1	Pointer to SDWA.	Pointer to SDWA.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Entry point.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVTRTS	0	Irrelevant.	Irrelevant.
	1	Completion code for error.	Irrelevant.
	2	Irrelevant.	Irrelevant.
	3	Irrelevant.	Irrelevant.
	4	IEAVTRTS recovery FRR stack.	Irrelevant.
	5	Dumpopts/abbreviated mach check data.	Irrelevant.
	6	Complete machine check data.	Irrelevant.
	7	Irrelevant.	Irrelevant.
	8	Irrelevant.	Irrelevant.
	9	IEAVTRTM base register.	IEAVTRTM base register.
	10	Irrelevant.	Irrelevant.
	11	Irrelevant.	Irrelevant.
	12	Irrelevant.	Irrelevant.
	13	Registers at time of error.	Irrelevant.
	14	Return address to IEAVTRTM.	Return address to IEAVTRTM.
	15	Entry point address.	Irrelevant.
IEAVTRT1			Note: Exit values differ; For retry, the registers contain the same values as at the time of error, or values updated by the FRRs. For restart or resume, the registers contain the values as at the time of the restart interruption. For return, the registers contain the restored values.
	0	TCB address or 0 for abterm entry. SRB address for cross MEMTERM or ABTERM reentry. Irrelevant for other entry points.	Retry register 0 if exit is retry, restart register 0 if exit is restart. Caller's register 0 if the exit is return to caller irrelevant for other exits.
	1	CALLRTM flags and completion code for ABTERM, DATERR, PGIOERR, MEMTERM, PROGCK, MACHCK reentry Address of logrec buffer for machck Irrelevant for other entry points.	Retry register 1. Restart register 1. Caller's register 1. Address of RTM's WSAC if entry is MACHCK, irrelevant for other exits.
	2	ASID/ABTERM or MEMTERM entry address of first half of PSW for MACHCK reentry, irrelevant for other entry points.	Retry register 2 Restart register 2. Caller's register 2. Irrelevant for other exits.
	3	Address of dumpopts for ABTERM entry Address of 2nd half of PSW for MACHCK reentry Irrelevant for other entry points.	Retry register 3. Restart register 3. Caller's register 3. Irrelevant for other exits.
	4	TCB address or 0 for PGIOERR entry. Irrelevant for other entry points.	Retry register 4. Restart register 4. Caller's register 4. Irrelevant for other exits.
	5	RB address or 0 for PGIOERR entry. Repair status info. for MACHCK reentry. Irrelevant for other entry points.	Retry register 5. Restart register 5. Caller's register 5. Irrelevant for other exits.
	6	Pointer to acquired EEDS for MACHCK reentry. Irrelevant for other entry points.	Retry register 6. Restart register 6. Caller's register 6. Irrelevant for other exits.
	7-12	Irrelevant.	Retry registers 7-12. Restart registers 7-12. Caller's register 7-12. Irrelevant for other exits.

continued

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVTRT1 (continued)	13	Save area address for MACHCK, MEMTERM, PGIOERR, cross MEMTERM or ABTERM. Address of error regs for MACHCK reentry. Irrelevant for other entry points.	Retry register 13. Restart register 13. Caller's register 13. Irrelevant for other exits.
	14	Return address for ABTERM, MACHCK, PGIOERR, MEMTERM entry points. Irrelevant for other entry points.	Retry register 14. Restart register 14. Return address if exit is return to caller. Irrelevant for other exits.
	15	Entry point address.	Retry address if exit type is retry. Restart register 15. Address of dispatcher if exit is to dispatch address of SRB dispatched if exit is to SRB dispatcher. Address of exit prologue routine if exit is SVC exit. Caller's register 15.
IEAVTRT2	0	ASCB address or dump option's address.	Irrelevant.
	1	Completion code and flags indicating type of request and options.	Irrelevant.
	2	Irrelevant.	Irrelevant.
	3	CVT address.	Irrelevant.
	4	TCB address.	Irrelevant.
	5	SVRB address.	Irrelevant.
	6	Entry point address.	Irrelevant.
	7	ASCB address.	Irrelevant.
	8-13	Irrelevant.	Irrelevant.
	14	Address of exit prologue to be used for return.	Irrelevant.
	15	Irrelevant.	Irrelevant.
IEAVTRV	0	Irrelevant.	Address space ID of translated virtual storage address, right-justified; in case of error, unpredictable.
	1	Real storage address to be translated.	Virtual Storage Address corresponding to input real storage address; in case of error, unpredictable.
	2-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Return code.
IEAVTSBP	0,1	R0=0, R1 irrelevant. *or* R0=address of RB issuing XCTL R1=address of RB to get control as result of XCTL *or* R0=address of RB issuing EXIT R1=0.	Unchanged.
	2,3	Irrelevant.	Unchanged.
	4	TCB address.	Unchanged.
	5-12	Irrelevant.	Unchanged.
	13	Register save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code, depending on the exit.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVTSDC	0-2	Irrelevant.	Unchanged.
	3	ESTAE/FRR work area address.	Unchanged.
	4-6	Irrelevant.	Unchanged.
	7	SVC dump work area address.	Unchanged.
	8-12	Irrelevant.	Unchanged.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevant.
IEAVTSDF (same register usage as IEAVTSDC).			
IEAVTSDG (same register usage as IEAVTSDC).			
IEAVTSDH (same register usage as IEAVTSDC).			
IEAVTSDI	0-1	Irrelevant.	Unchanged.
	2	NIP Vector Table.	Unchanged.
	3	CVT address.	Unchanged.
	4-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point.	0
IEAVTSDL (same register usage as IEAVTSDC).			
IEAVTSDO (same register usage as IEAVTSDC).			
IEAVTSDR	0	Irrelevant.	Unchanged.
	1	Contains address of fullword which contains address of RMPC.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	0
IEAVTSDT	0	Irrelevant.	Irrelevant.
	1	If attached in MASTER address space, address of ECB to post.	Irrelevant.
	2-14	Irrelevant.	Irrelevant.
	15	Entry point.	Irrelevant.
IEAVTSDW (same register usage as IEAVTSDC).			
IEAVTSDX	0	Irrelevant.	Unchanged.
	1	Address of SVC, DUMP, PARM LIST.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code: 0-dump scheduled 8-not scheduled.
IEAVTSIN	0	Irrelevant.	Irrelevant.
	1	PSA address.	Unchanged.
	2-12	Irrelevant.	Irrelevant.
	13	Caller's save area address.	Unchanged.
	14	Irrelevant.	Irrelevant.
	15	Irrelevant.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVTSSD	0	Irrelevant.	Unchanged.
	1	SVC Dump parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code (0 – always).
IEAVTSSE	0-2	Irrelevant.	Unchanged.
	3	ESTAE/FRR work area address.	Unchanged.
	4-6	Irrelevant.	Unchanged.
	7	SVC Dump work area address.	Unchanged.
	8-12	Irrelevant.	Unchanged.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVTSKT	0	Irrelevant.	Irrelevant.
	1	RTM2WA address.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Save area address.	Irrelevant.
	14	Return address.	Irrelevant.
	15	Entry point address.	Return code.
IEAVTSLP	0	Irrelevant.	Irrelevant.
	1	Address of parameter list.	Unchanged.
	2-12	Irrelevant.	Irrelevant.
	13	Save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEAVVCRA	0	Irrelevant.	Unchanged.
	1	IOSB address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Local lock save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEAVVCRX	0-1	Irrelevant.	Irrelevant.
	2	Return address.	Unchanged.
	3-5	Irrelevant.	Irrelevant.
	6	Entry point address.	Irrelevant.
	7-15	Irrelevant.	Irrelevant.
IEAVVCTR	0	Passed from caller of SVC 72.	Unchanged.
	1	Address of parameter list.	Address of extended save area (XSA)
	2	Irrelevant.	Irrelevant.
	3	Address of CVT.	Unchanged.
	4	Address of the TCB.	Unchanged.
	5	Address of the SVRB.	Unchanged.
	6	Address of IEAVVCTR.	Unchanged.
	7	Address of ASCB.	Unchanged.
	8-12	Irrelevant.	Irrelevant.
	13	Passed from caller of SVC 72.	Unchanged.
	14	Exit address.	Unchanged.
	15	Passed from caller of SVC 72.	If XCTL exit, address of XSA; otherwise, address of IEAV1052.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEAVVRP1	0-1	Standard.	Standard.
	2	Address of XSA.	Standard.
	3-15	Standard.	Standard.
IEAVVRP2	0	Address of SRB.	Standard.
	1-15	Standard.	Standard.
IEAVVWTO	0	Message identification, console identification, or zero.	Irrelevant.
	1	Write parameter list address.	WQE message identification.
	2	Irrelevant.	Irrelevant.
	3	CVT address.	Irrelevant.
	4	Current TCB address.	Irrelevant.
	5	Our SVRB address.	Irrelevant.
	6	Entry point address.	Irrelevant.
	7	Our ASCB address.	Irrelevant.
	8-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Irrelevant.	Irrelevant.
IEAVXDOM	0	Type of DOM request: = 0 then single WTO identification. = 4 then single WTOR identification. = 12 then list of WTO/WTOR identifications. = negative value then list of WTO identifications.	Irrelevant.
	1	If register 0 = 0 or 4 then this is the WQE message identification. If register 0 = 12 or negative value this is a pointer to the parameter list containing the WQE message identifications.	Irrelevant.
	2	Irrelevant.	Unchanged.
	3	CVT address.	Unchanged.
	4	Caller's TCB address.	Unchanged.
	5	Our SVRB address.	Unchanged.
	6	Entry point address.	Unchanged.
	7	Caller's ASCB address.	Unchanged.
	8-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Irrelevant.
IEAV1052	0	Irrelevant.	Irrelevant.
	1	Address of XSA.	Unchanged.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address of IEAV1052.	Irrelevant.
IEAV1443	0	Irrelevant.	Irrelevant.
	1	Address of CXSA.	If normal exit, irrelevant; if error exit, address of CXSA.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address of IEAV1443.	If normal exit, irrelevant; if error exit, address of console switch routine (IEAVSWCH).
IEAV2540	0	Irrelevant.	Irrelevant.
	1	Address of parameter list.	Unchanged.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Irrelevant.	If normal exit, irrelevant; if error exit, address of IEAVSWCH (console switch routine).

Module Name	Register Number	Contents at Entry	Contents at Exit
IECDAFMT	0	Irrelevant.	Irrelevant.
	1	Address of print dump parameter list.	Unchanged.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Irrelevant.
	14	Return Address.	Unchanged.
	15	Entry point address.	Return code.

IECDAFT1

IECDADCB (same register usage as IECDAFMT).

IECDADEB (same register usage as IECDAFMT).

IECDAIOB (same register usage as IECDAFMT).

IECIOFMT (same register usage as IECDAFMT).

IECIOFT1

IECIOEXD (same register usage as IECDAFMT).

IECIOUCB (same register usage as IECDAFMT).

Module Name	Register Number	Contents at Entry	Contents at Exit
IEDAY3	0	Standard.	Standard.
	1	Address of new user's ASID.	Same as at entry.
	2-15	Standard.	Standard.
IEEAB400	0-15	Standard for PLS.	Standard for PLS.
IEEAB401	0-15	Standard for PLS.	Standard for PLS.
IEECB800	0	Standard.	Standard.
	1	Address of CSCB.	Address of Parameter List with address to CSCB and meg buffer.
	2-15	Standard.	Standard.
IEECB801	0	Standard.	Standard.
	1	Address of parameter list.	Standard.
	2-15	Standard.	Standard.
IEECB860	0	= 12 indicates no SDWA.	Standard.
	1	For STAE creation, address of CSCB, command name and length -or- For ABEND recovery, address of SDWA (RO≠12) or completion code (RO=12).	Standard.
	2	Address of parameter list (RO=12).	Standard.
	3-15	Standard.	Standard.
IEECB866	0	Standard.	Standard.
	1	Address of CSCB.	Standard.
	2-15	Standard.	Standard.
IEECB900	0	Standard.	Standard.
	1	Address of CSCB.	Irrelevant.
	2-15	Standard.	Standard.
IEECB901	0	Standard.	Standard.
	1	Address of parameter list.	Standard.
	2-15	Standard.	Standard.
IEECB904	0-1	Standard.	Standard.
	2	Address of dummy XSA.	Same as at entry.
	3-9	Standard.	Standard.
	10	IEE3603D's return address.	Same as at entry.
	11	Standard.	Standard.
	12	IEE3603D's base address.	Same as at entry.
	13-15	Standard.	Standard.
IEECB905	0	Irrelevant.	Irrelevant.
	1	Address of CSCB.	Irrelevant.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevant.
IEECB906	0	Indicates if SDWA exists.	Irrelevant.
	1	Pointer to SDWA, if one exists.	Unchanged.
	2	Pointer to user parameters, if SDWA does not exist.	Irrelevant.
	3-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEECB907	0	Irrelevant.	Irrelevant.
	1	Address of CSCB/SCE.	Irrelevant.
	2-13	Irrelevant.	Irrelevant.
	14	Return Address.	Unchanged.
	15	Entry point address.	Irrelevant.
IEECB908	0	Irrelevant.	Irrelevant.
	1	Address of parameter list.	Unchanged.
	2-12	Irrelevant.	Irrelevant.
	13	Save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IEECLEAN	0-5	Standard.	Standard.
	6	Address of work area.	Standard.
	7-15	Standard.	Standard.
IEECMENQ	0	Irrelevant.	Irrelevant.
	1	WQE address.	Irrelevant.
	2	UCM address.	Irrelevant.
	3	Irrelevant.	Unchanged.
	4	UCM prefix address.	Unchanged.
	5-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEECMQCN	0-1	Irrelevant.	Irrelevant.
	2	UCM address.	Irrelevant.
	3	Irrelevant.	Unchanged.
	4	UCM prefix address.	Unchanged.
	5	Irrelevant.	Irrelevant.
	6	WQE address.	Irrelevant.
	7-8	Irrelevant.	Irrelevant.
	9	Return address.	Irrelevant.
	10	Irrelevant.	Irrelevant.
	11	UCME address.	Irrelevant.
	12-14	Irrelevant.	Irrelevant.
	15	Entry point address.	Unchanged.
IEECVETA	0	Irrelevant.	Irrelevant.
	1	Address of CXSA.	Unchanged.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevant.
IEECVETC	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVETD	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVETE	0-15	Sames as IEECVETA.	Same as IEECVETA.
IEECVETF	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVETG	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVETH	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVETJ	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVETK	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVETP	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVETR	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVETU	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVETW	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVET1	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVET2	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVET3	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVET4	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVET6	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVET7	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVET8	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVET9	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVFTA	0	Irrelevant.	Irrelevant.
	1	Address of CXSA.	Unchanged.
	2-14	Irrelevant.	Irrelevant.
	15	Entry point address.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEECVFTB	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVFTD	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVFTG	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVFTL	0	Irrelevant.	Irrelevant.
	1	Address of CXSA.	Unchanged.
	2-9	Irrelevant.	Irrelevant.
	10	Address of in-line multiple-line WTO message to be displayed.	Irrelevant.
	11-14	Irrelevant.	Irrelevant.
	15	Entry point address.	Irrelevant.
IEECVFTM	0	Irrelevant.	Irrelevant.
	1	Address of CXSA.	Unchanged.
	2-6	Irrelevant.	Irrelevant.
	7	Irrelevant.	Address of SACB for interface with IEECVFTO or IEECVFTQ.
	8-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevant.
IEECVFTN	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVFTO	0	Irrelevant.	Irrelevant.
	1	Address of CXSA.	Unchanged.
	2-6	Irrelevant.	Irrelevant.
	7	Address of SACB.	Irrelevant.
	8-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevant.
IEECVFTP	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVFTQ	0-15	Same as IEECVFTO.	Same as IEECVFTO.
IEECVFTT	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVFT1	0-15	Same as IEECVETA.	Same as IEECVETA.
IEECVFT2	0-15	Same as IEECVETA.	Same as IEECVETA.
IEEC2740	0-15	Same as IEECVETA.	Same as IEECVETA.
IEEDISPD	0	Standard.	Standard.
	1	Address of CSCB.	Address of CSCB.
	2-15	Standard.	Standard.
IEEJB840	0	Irrelevant.	Unchanged.
	1	Irrelevant.	Unchanged.
	2	Irrelevant.	Unchanged.
	3	Irrelevant.	Unchanged.
	4	Address of the TCB.	Unchanged.
	5	Address of the SVRB.	Unchanged.
	6	Address of the WPL.	Unchanged.
	7	Address of the ASCB.	Unchanged.
	8	Irrelevant.	Unchanged.
	9	Irrelevant.	Unchanged.
	10	Address of the UCM.	Unchanged.
	11	Irrelevant.	Unchanged.
	12	Irrelevant.	Unchanged.
	13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IEEMB803	0-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEEMB804	0	Standard.	Standard.
	1	Address of record text.	Standard.
	2-15	Standard.	Standard.
IEEMB805	0	Standard.	Standard.
	1	Address of parameter list.	Standard.
	2-15	Standard.	Standard.
IEEMB806	0	=12 indicates no SDWA.	Address of retry routine.
	1	Address of SDWA (R0≠12) or completion code (R0=12).	Standard.
	2	Address of user's STAE parameter list (R0=12).	Standard.
	3-15	Standard.	Standard.
IEEMB807	0	Standard.	Standard.
	1	Address of parameter list.	Standard.
	2-15	Standard.	Standard.
IEEMB810	0	Standard.	Standard.
	1	Address of CSCB	Standard.
	2	Standard.	Address of XSA.
	3-15	Standard.	Standard.
IEEMB811	0	Standard.	Standard.
	1	Address of CSCB.	Address of parameter list.
	2-15	Standard.	Standard.
IEEMB812	0	Irrelevant.	Unchanged.
	1	Address of IEAIPS suffix.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Return code.
IEEMB813	0	Standard.	Standard.
	1	Address of CSCB.	Address of parameter list.
	2-15	Standard.	Standard.
IEEMB814	0	Standard.	Standard.
	1	Address of parameter list.	Standard.
	2-5	Standard.	Standard.
	6	Address of CSCB.	Standard.
	7-15	Standard.	Standard.
IEEMB815	0-1	Irrelevant.	Unchanged.
	2	Address of XSA.	Address of XSA.
	3-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address to IEE0403D.	Return address.
	15	Entry point address.	Unchanged.
IEEMB825	0	=indicates no SDWA.	Standard.
	1	Address of SDWA (R0≠12). -or- ABEND completion code (R0=12).	Standard.
	2-15	Standard.	Standard.
IEEMB826	0-15	Standard.	Standard.
IEEMB827	0	Posted ECB address.	Standard.
	1	Posted ASCB address.	Standard.
	2-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEEMB828	0-15	Standard.	Standard.
IEEMB829	0-15	Standard.	Standard.
IEEMB830	0	Standard.	Standard.
	1	Address of record to be transferred.	Standard.
	2-15	Standard.	Standard.
IEEMPDM	0	Standard.	Standard.
	1	Address of CSCB.	Standard.
	2-15	Standard.	Standard.
IEEMPS03	0	Standard.	Standard.
	1	Address of CSCB.	Standard.
	2-15	Standard.	Standard.
IEEMPVST	0	Standard.	Standard.
	1	Address of CSCB.	Standard.
	2-15	Standard.	Standard.
IEEPALTR	0	Parameter area length.	Standard.
	1	Address of parameter list.	Standard.
	2-15	Standard.	Standard.
IEEPRTN2	0	Standard.	Standard.
	1	Address of ASCB.	Standard.
	2-15	Standard.	Standard.
IEEPRWI2	0	Standard.	Standard.
	1	Address of ASCB.	Address of ASCB.
	2-15	Standard.	Standard.
IEESB601	0-15	Standard.	Standard.
IEESB605			
IEESB605	0	Standard.	Standard.
	1	Address of JSEL.	Address of IEL.
	2-15	Standard.	Standard.
IEEVIC	0	Standard.	Standard.
	1	Address of IEL.	Address of ASCB.
	2	Standard.	Standard.
	3	Standard.	Address of JSEL.
	4-15	Standard.	Standard.
IEEVICER	0	Standard.	Standard.
	1	Address of JSWA.	Standard.
	2-15	Standard.	Standard.
IEESB665			
IEESB665	0	12, if no RTCA exists.	Standard.
	1	Address of RTCA.	Standard.
	2-15	Standard.	Standard.
IEESB667	0-15	Standard.	Standard.
IEESB670	0	12, if no RTCA exists.	Standard.
	1	Address of RTCA.	Standard.
	2-15	Standard.	Standard.
IEESTPRS	0	Wait State Code.	Standard.
	1	0 or address of status save area.	Standard.
	2-15	Standard.	Standard.
IEEVALST	0	Standard.	Standard.
	1	Address of parameter list.	Standard.
	2-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEEVCPU	0	Standard.	Standard.
	1	Address of CSCB.	Standard.
	2-15	Standard.	Standard.
IEEVDEV	0	Standard.	Standard.
	1	Address of parameter list.	Standard.
	2-15	Standard.	Standard.
IEEVIPL	0	=12 indicates no SDWA.	Standard.
	1	Address of SDWA (R0≠12).	Address of initiator entrance list.
	2	Address of ESTAE parameter list R0=12).	Standard.
	3-15	Standard.	Standard.
IEEVJCL	0	Standard.	Standard.
	1	Address of STC parameter area.	Address of JSEL.
	2-15	Standard.	Standard.
IEEVMNT1	0	Standard.	Standard.
	1	Address of ASCB.	Normal: (Address of parameter list). Error: Address of ASCB.
	2-15	Standard.	Standard.
IEEVMNT2	0-15	Standard.	Standard.
IEEVMSG	0-15	Standard.	Standard.
IEEVPTH	0	Standard.	Standard.
	1	Address of CSCB.	Standard.
	2-15	Standard.	Standard.
IEEVSEND	0	Standard.	Standard.
	1	Address of CSCB.	Standard.
	2-15	Standard.	Standard.
IEEVSND2	0	Standard.	Standard.
	1	Address of parameter list.	Standard.
	2-15	Standard.	Standard.
IEEVSND3	0	Standard.	Standard.
	1	Address of parameter list.	Standard.
	2-15	Standard.	Standard.
IEEVSND4	0	Standard.	Standard.
	1	Address of parameter list.	Standard.
	2-15	Standard.	Standard.
IEEVSND6	0	Standard.	Standard.
	1	Address of parameter list.	Standard.
	2-15	Standard.	Standard.
IEEVSND8	0	Standard.	Standard.
	1	Address of parameter list.	Standard.
	2-15	Standard.	Standard.
IEEVSND9	0	Standard.	Standard.
	1	Address of parameter list.	Standard.
	2-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEEVSTAR	0	Standard.	Standard.
	1	Address of ASCB.	Address of parameter list.
	2-15	Standard.	Standard.
IEEVSTOP	0-15	Standard.	Standard.
IEEVWAIT			[Contents upon attaching a task.]
	0	Irrelevant.	Irrelevant.
	1	Irrelevant.	Address of the CSCB if the task does not create an address space; address of an ASID if the task creates an address space (START, LOGON, or MOUNT).
	2	Irrelevant.	Irrelevant.
	3	Irrelevant.	Address of the name of the module to be attached.
	4-8	Irrelevant.	Irrelevant.
	9	Irrelevant.	Dispatching priority.
	10	Irrelevant.	Irrelevant.
	11	Irrelevant.	Limit priority.
	12-14	Irrelevant.	Irrelevant.
	15	Irrelevant.	Address of the ATTACH parameter list.
IEEVWKUP IEEVCPU	0	Standard.	Standard.
	1	Address of Master Memory Segment Table.	Standard.
	2-15	Standard.	Standard.
IEEXEDNA	0	Standard.	Standard.
	1	Address of CSCB.	Standard.
	2-15	Standard.	Standard.
IEE0003D	0	Invoking routine.	Standard.
	1	Information.	Standard.
	2-4	Standard.	Standard.
	5	Address of SVRB.	Standard.
	6-14	Standard.	Standard.
	15	Address of ABTERM parameter list.	Standard.
IEE00110	0	Standard.	Standard.
	1	Address of CSCB.	Standard.
	2-15	Standard.	Standard.
IEE0303D	0-1	Function action.	Standard.
	2-15	Standard.	Standard.
IEE0403D	0	Standard.	Standard.
	1	Address of command input buffer.	Standard.
	2	Address of XSA.	Address of XSA.
	3-9	Standard.	Standard.
	10	Standard.	Address of BASEA.
	11-15	Standard.	Standard.
IEE0503D	0-1	Standard.	Standard.
	2	Address of XSA.	Standard.
	3-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEE0603D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-9	Standard.	Standard.
	10	Address of BASEA.	Address of BASEA.
	11-15	Standard.	Standard.
IEE0703D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEE0803D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEE10110	0	Standard.	Standard.
	1	Address of XSA.	Standard.
	2	Address of CSCB.	Address of CSCB.
	3	Standard.	Address of XSA.
	4-15	Standard.	Standard.
IEE11110	0-1	Standard.	Standard.
	2	Address of CSCB.	Address of CSCB.
	3	Address of XSA.	Address of XSA.
	4-15	Standard.	Standard.
IEE12110	0-1	Standard.	Standard.
	2	Address of CSDB.	Address of CSCB.
	3	Address of XSA.	Standard.
	4-15	Standard.	Standard.
IEE1403D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEE1603D	0-1	Standard.	Standard.
	2	Address of XSA.	Standard,
	3-15	Standard.	Standard.
IEE20110	0-1	Standard.	Standard.
	2	Address of CSCB.	Standard.
	3-8	Standard.	Standard.
	9	Standard.	Address of UCB.
	10	Standard.	Standard.
	11	Standard.	Address of CSCB.
	12-15	Standard.	Standard.
IEE21110	0-10	Standard.	Standard.
	11	Address of CSDB.	Address of CSCB.
	12-15	Standard.	Standard.
IEE22110	0-10	Standard.	Standard.
	11	Address of CSCB.	Address of CSCB.
	12-15	Standard.	Standard.
IEE2303D	0	Standard.	Standard.
	1	Standard.	XAD switches.
	2	Address of dummy XSA.	Address of dummy XSA.
	3-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEE23110	0-10	Standard.	Standard.
	11	Address of CSCB.	Address of CSCB.
	12-15	Standard.	Standard.
IEE2903D	0-1	Standard.	Standard.
	2	Address of XSA.	Standard.
	3-15	Standard.	Standard.
IEE3103D	0-1	Standard.	Standard.
	2	Address of dummy XSA.	Standard.
	3-15	Standard.	Standard.
IEE3203D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEE3303D	0-1	Standard.	Standard.
	2	Address of dummy XSA.	Address of dummy XSA.
	3-15	Standard.	Standard.
IEE3503D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEE3603D	0	Standard.	Standard.
	1	Address of CSCB.	Address of parameter list.
	2	Standard.	Address of dummy XSA.
	3-15	Standard.	Standard.
IEE3703D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-14	Standard.	Standard.
	15	Entry point address.	Address of IEE0503D.
IEE40110	0-1	Standard.	Standard.
	2	Address of CSCB.	Standard.
	3-15	Standard.	Standard.
IEE4103D	0	Standard.	Standard.
	1	Address of message area.	Standard.
	2	Address of XSA.	Address of XSA.
	3-11	Standard.	Standard.
	12	Zero or address of unit field.	Standard.
	13-15	Standard.	Standard.
IEE4203D	0-1	Standard.	Standard.
	2	Address of dummy XSA.	Address of XSA.
	3	Standard.	Standard.
	4	Standard.	Graphics indicator.
	5-15	Standard.	Standard.
IEE4303D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEE4403D	0	Standard.	Standard.
	1	Command Authority	Unit flags.
	2	Address of XSA.	Address of dummy XSA.
	3-15	Standard.	Standard.
IEE4603D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEE4703D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEE4803D	0	Standard.	Address of UCM entry.
	1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-8	Standard.	Standard.
	9	Address of current unit name.	Standard.
	10	Standard.	Standard.
	11	Message indicator.	Address of UCM entry.
	12-15	Standard.	Standard.
IEE4903D	0	Standard.	Standard.
	1	Address of buffer.	Address of buffer.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEE5103D	0	Entry indicator.	Standard.
	1	Address of SDWA.	Standard.
	2	Address of parameter area.	Standard.
	3-15	Standard.	Standard.
IEE5403D	0	Address of UCM, Reader authority, ASID.	Standard.
	1	Address of command buffer.	Standard.
	2	Address of XSA.	Standard.
	3-15	Standard.	Standard.
IEE5503D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEE5603D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3	Standard.	Current position in message buffer.
	4-5	Standard.	Standard.
	6	Standard.	Current message length.
	7	Standard.	Standard.
	8	Standard.	Address of transient DCM.
	9-15	Standard.	Standard.
IEE5703D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEE5803D	0-1	Standard.	Standard.
	2	Address of XSA.	Standard.
	3-15	Standard.	Standard.
IEE5903D	0-1	Standard.	Standard.
	2	Address of XSA.	Standard.
	3	Current position in message buffer.	Standard.
	4-5	Standard.	Standard.
	6	Current message length.	Standard.
	7	Standard.	Standard.
	8	Address of transient DCM.	Standard.
	9-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEE6303D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-10	Standard.	Standard.
	11	Standard.	Address of MSGRT verb.
	12-15	Standard.	Standard.
IEE6403D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-10	Standard.	Standard.
	11	Address of First Operand.	Standard.
	12-15	Standard.	Standard.
IEE6503D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEE6603D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEE6703D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEE6803D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEE6903D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEE70110	0	Standard.	Standard.
	1	Address of CSCB.	Standard.
	2-15	Standard.	Standard.
IEE7103D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEE7203D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-4	Standard.	Standard.
	5	Standard.	Address of message area.
	6-11	Standard.	Standard.
	12	Address of unit.	Standard.
	13-15	Standard.	Standard.
IEE7303D	0	Address of UCM entry.	Standard.
	1	Address of message storage area.	Address of message storage area.
	2	Address of XSA.	Address of XSA.
	3	Standard.	Standard.
	4	Process switches.	Process switches.
	5	Address of unit UCM entry.	Standard.
	6-10	Standard.	Standard.
	11	Address of hardcopy unit.	Address of hardcopy unit.
	12	Address of message area.	Standard.
	13-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEE7503D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-14	Standard.	Standard.
	15	Standard.	For JES2 only, return code.
IEE7703D	0-1	Standard.	Standard.
	2	Address of XSA.	Standard.
	3-15	Standard.	Standard.
IEE7803D	0-1	Standard.	Standard.
	2	Address of XSA.	Standard.
	3-15	Standard.	Standard.
IEE8603D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEE90110	0	Standard.	Standard.
	1	Address of XSA.	Standard.
	2-15	Standard.	Standard.
IEE9403D	0-1	Standard.	Standard.
	2	Address of XSA.	Address of XSA.
	3-15	Standard.	Standard.
IEFAB4A0	0-15	Standard.	Standard.
IEFAB4A2	0-15	Standard.	Standard.
IEFAB4A3	0-15	Standard.	Standard.
IEFAB4A4	0-15	Standard.	Standard.
IEFAB4A6	0-15	Standard.	Standard.
IEFAB4A8	0-15	Standard.	Standard.
IEFAB4B0	0-15	Standard.	Standard.
IEFAB4B2	0-15	Standard.	Standard.
IEFAB4DC	0-15	Standard.	Standard.
IEFAB4DD	0	Indicates if SDWA exists.	Irrelevant.
	1	Pointer to SDWA, if one exists.	Pointer to SDWA, if one exists.
	2	Pointer to user parameters, if SDWA does not exist.	Irrelevant.
	3-13	Irrelevant.	Irrelevant.
	14	Return address (standard for PLS).	Return address (standard for PLS).
	15	Address of module entry point (standard for PLS).	Return code, if no SDWA exits.
IEFAB4DE	0	Indicates if SDWA exists.	Irrelevant.
	1	Pointer to SDWA, if one exists.	Pointer to SDWA, if one exists.
	2	Pointer to user parameters, if SDWA does not exist.	Irrelevant.
	3-13	Irrelevant.	Irrelevant.
	14	Return address (standard for PLS).	Return address (standard for PLS).
	15	Address of module entry point (standard for PLS).	Return code, if no SDWA exists.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEFAB4EA	0	Indicates if SDWA exists.	Irrelevant.
	1	Pointer to SDWA, if one exists.	Pointer to SDWA, if one exists.
	2	Pointer to user parameters, if SDWA does not exist.	Irrelevant.
	3-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Address of module entry point (standard for PLS).	Return code, if no SDWA exists.
IEFAB4EB	0-15	Standard.	Standard.
IEFAB4EC	0-15	Standard.	Standard.
IEFAB4EE	0-15	Standard.	Standard.
IEFAB4EF	0-15	Standard.	Standard.
IEFAB4E0	0-15	Standard.	Standard.
IEFAB4E1	0	Irrelevant.	Irrelevant.
	1	Pointer to SDWA.	Pointer to SDWA.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Address of module entry point.	Irrelevant.
IEFAB4E2	0	Indicates if SDWA exists.	Irrelevant.
	1	Pointer to SDWA, if one exists.	Pointer to SDWA, if one exists.
	2	Pointer to user parameters, if SDWA does not exist.	Irrelevant.
	3-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Address of module entry point.	Return code, if no SDWA exists.
IEFAB4E3	0-15	Standard.	Standard.
IEFAB4E4		(Input registers are those originally passed to allocation by IEFSD162).	
	0	Indicates if SDWA exists.	Irrelevant.
	1	Pointer to SDWA, if one exists.	Pointer to SDWA, if one exists.
	2	Pointer to user parameters, if SDWA does not exist.	Irrelevant.
	3-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Address of module entry point.	Return code, of no SDWA exists.
IEFAB4E5	0-15	Standard.	Standard.
IEFAB4E6	0	Irrelevant.	Irrelevant.
	1	Pointer to SDWA.	Pointer to SDWA.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Address of module entry point.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEFAB4E7	0	Indicates if SDWA.	Irrelevant.
	1	Pointer to SDWA if one exists.	Pointer to SDWA if one exists.
	2	Pointer to user parameters if SDWA does not exist.	Irrelevant.
	3-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Address of module entry point.	Irrelevant.
IEFAB4E8	0	Indicates if SDWA exists.	Irrelevant.
	1	Pointer to SDWA, if one exists.	Pointer to SDWA, if one exists.
	2	Pointer to user parameters, if SDWA does not exist.	Irrelevant.
	3-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address (standard for PLS).
	15	Address of module entry point.	Return code, if no SDWA exists.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEFAB4E9	0-15	Standard.	Standard.
IEFAB4FA	0-15	Standard.	Standard.
IEFAB4FC	0-15	Standard.	Standard.
IEFAB4FD	0-15	Standard.	Standard.
IEFAB4FE	0-15	Standard.	Standard.
IEFAB4F0	0-15	Standard.	Standard.
IEFAB4F1	0-15	Standard.	Standard.
IEFAB4F2	0-15	Standard.	Standard.
IEFAB4F3	0-15	Standard.	Standard.
IEFAB4F4	0-15	Standard.	Standard.
IEFAB4F5	0-15	Standard.	Standard.
IEFAB4F6	0	Subpool and length.	Irrelevant.
	1	Address of storage to be freed or address of 4K block obtained or address of previous 4K block.	Address of storage.
	2-5	Irrelevant.	Unpredictable.
	6-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point.	Unchanged.
IEFAB4F7	0-15	Standard.	Standard.
IEFAB4F8	0-15	Standard.	Standard.
IEFAB4F9	0-15	Standard.	Standard.
IEFAB4M4		Not applicable (non-executable module).	
IEFAB4M5		Not applicable (non-executable module).	
IEFAB4M6		Not applicable (non-executable module).	
IEFAB4M7		Not applicable (non-executable module).	
IEFAB4M9		Not applicable (non-executable module).	
IEFAB4UV	0-15	Standard.	Standard.
IEFAB421	0-15	Standard.	Standard.
IEFAB422	0-15	Standard.	Standard.
IEFAB423	0-15	Standard.	Standard.
IEFAB424	0-15	Standard.	Standard.
IEFAB425	0-15	Standard.	Standard.
IEFAB426	0-15	Standard.	Standard.
IEFAB427	0-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEFAB428	0-15	Standard.	Standard.
IEFAB430	0-15	Standard.	Standard.
IEFAB431	0-15	Standard.	Standard.
IEFAB432	0-15	Standard.	Standard.
IEFAB433	0-15	Standard.	Standard.
IEFAB434	0-15	Standard.	Standard.
IEFAB435	0-15	Standard.	Standard.
IEFAB436	0-15	Standard.	Standard.
IEFAB438	0-15	Standard.	Standard.
IEFAB440	0-15	Standard.	Standard.
IEFAB441	0-15	Standard.	Standard.
IEFAB442	0-15	Standard.	Standard.
IEFAB445		not applicable (non-executable module).	
IEFAB451	0-15	Standard.	Standard.
IEFAB452	0-15	Standard.	Standard.
IEFAB453	0-15	Standard.	Standard.
IEFAB454	0-15	Standard.	Standard.
IEFAB455	0-15	Standard.	Standard.
IEFAB456	0-15	Standard.	Standard.
IEFAB457	0-15	Standard.	Standard.
IEFAB458	0-15	Standard.	Standard.
IEFAB459	0-15	Standard.	Standard.
IEFAB461	0-15	Standard.	Standard.
IEFAB463	0-15	Standard.	Standard.
IEFAB464	0-15	Standard.	Standard.
IEFAB466	0-15	Standard.	Standard.
IEFAB469	0-15	Standard.	Standard.
IEFAB470	0-15	Standard.	Standard.
IEFAB471	0-15	Standard.	Standard.
IEFAB472	0-15	Standard.	Standard.
IEFAB473	0-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEFAB474	0-15	Standard.	Standard.
IEFAB475	0-15	Standard.	Standard.
IEFAB476	0-15	Standard.	Standard.
IEFAB477	0-15	Standard.	Standard.
IEFAB478	0-15	Standard.	Standard.
IEFAB479	0-15	Standard.	Standard.
IEFAB48A	0-15	Standard.	Standard.
IEFAB480	0-15	Standard.	Standard.
IEFAB481	0-15	Standard.	Standard.
IEFAB485	0-15	Standard.	Standard.
IEFAB486	0-15	Standard.	Standard.
IEFAB487	0-15	Standard.	Standard.
IEFAB488	0-15	Standard.	Standard.
IEFAB489	0-15	Standard.	Standard.
IEFAB49A	0-15	Standard.	Standard.
IEFAB49B	0-15	Standard.	Standard.
IEFAB49C	0-15	Standard.	Standard.
IEFAB490	0-15	Standard.	Standard.
IEFAB491	0-15	Standard.	Standard.
IEFAB492	0-15	Standard.	Standard.
IEFAB493	0-15	Standard.	Standard.
IEFAB494	0-15	Standard.	Standard.
IEFAB495	0-15	Standard.	Standard.
IEFAB496	0-15	Standard.	Standard.
IEFAB498	0-15	Standard.	Standard.
IEFAB499	0-15	Standard.	Standard.
IEFAB820		(See IEFSMFAT).	
IEFATECB		Not applicable (non-executable module).	
IEFBB4M1		Not applicable (non-executable module).	
IEFBB4M2		Not applicable (non-executable module).	
IEFBB4M3		Not applicable (non-executable module).	

Module Name	Register Number	Contents at Entry	Contents at Exit
IEFBB4M4		Not applicable (non-executable module).	
IEFBB4M5		Not applicable (non-executable module).	
IEFBB401	0-15	Standard.	Standard.
IEFBB402	0-15	Standard.	Standard.
IEFBB404	0-15	Standard.	Standard.
IEFBB410	0-15	Standard.	Standard.
IEFBB412	0-15	Standard.	Standard.
IEFBB414	0-15	Standard.	Standard.
IEFBB416	0-15	Standard.	Standard.
IEFDB4A0	0-15	Standard.	Standard.
IEFDB4A1	0-15	Standard.	Standard.
IEFDB4D0	0-15	Standard.	Standard.
IEFDB4FA	0-15	Standard.	Standard.
IEFDB4FB	0-15	Standard.	Standard.
IEFDB4FC	0-15	Standard.	Standard.
IEFDB4FD	0-15	Standard.	Standard.
IEFDB4FE	0-15	Standard.	Standard.
IEFDB4FF	0-15	Standard.	Standard.
IEFDB4F8	0-15	Standard.	Standard.
IEFDB4F9	0-15	Standard.	Standard.
IEFDB400	0-3	Standard.	Standard.
	4	Pointer to TCB.	Pointer to TCB.
	5-6	Standard.	Standard.
	7	Pointer to ASCB.	Pointer to ASCB.
	8-15	Standard.	Standard.
IEFDB401	0-15	Standard.	Standard.
IEFDB402	0	Indicates if SDWA exists.	Irrelevant.
	1	Pointer to SDWA, if one exists.	Pointer to SDWA, if one exists.
	2	Pointer to user parameters, if SDWA does not exist.	Irrelevant.
	3-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address.
	15	Address of module entry point (standard for PLS).	Return code, if no SDWA exists.
IEFDB403	0	Irrelevant.	Irrelevant.
	1	Pointer to SDWA.	Pointer to SDWA.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Return address. (standard for PLS).
	15	Address of module entry point.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEFDB410	0-15	Standard.	Standard.
IEFDB411	0-15	Standard.	Standard.
IEFDB412	0-15	Standard.	Standard.
IEFDB413	0-15	Standard.	Standard.
IEFDB414	0-15	Standard.	Standard.
IEFDB417	0-15	Standard.	Standard.
IEFDB418	0-15	Standard.	Standard.
IEFDB450	0-15	Standard.	Standard.
IEFDB460	0-16	Standard.	Standard.
IEFDB470	0-15	Standard.	Standard.
IEFDB480	0-15	Standard.	Standard.
IEFDB490	0-15	Standard.	Standard.
IEFDB481	0-15	Standard.	Standard.
IEFDPOST	0-15	Standard.	Standard.
IEFDSLST	0-15	Standard.	Standard.
IEFDSTB1	0-15	Standard.	Standard.
IEFIB600	0	Standard.	Standard.
	1	Address of SSOB.	Standard.
	2-15	Standard.	Standard.
IEFIB605	0	Standard.	Standard.
	1	Address of SSOB.	Standard.
	2-15	Standard.	Standard.
IEFIB620	0	Code indicating existence of RTCA.	Standard.
	1	Address of RTCA.	Standard.
	2-15	Standard.	Standard.
IEFIB621	0	Standard.	Standard.
	1	Address of RTCA.	Variable.
	2-15	Standard.	Standard.
IEFIB645	0	Standard.	Standard.
	1	Address of RTCA.	Standard.
	2-15	Standard.	Standard.
IEFIB660	0-15	Standard.	Standard.
IEFICATL	0-15	Standard.	Standard.
IEFICPUA	0-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEFIIC	0	Standard.	Standard.
	1	Address of pointer. to initiator, start procedure parm field.	Address of IEL.
	2-15	Standard.	Standard.
IEFIMASK	0-15	Standard.	Standard.
IEFIRECM	0	Code indicating existence of RTCA.	Standard.
	1	Address of RTCA if one exists.	Irrelevant.
	2-15	Standard.	Standard.
IEFISEXR	0	Code indicating existence of RTCA.	Standard.
	1	Address of RTCA.	Irrelevant.
	2-15	Standard.	Standard.
IEFI922B	0-15	Standard.	Standard.
IEFJACTL	0	Irrelevant.	Irrelevant.
	1	Address of an RPL.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IEFJACTL.	Return code.
IEFJCDLT	0	Irrelevant.	Irrelevant.
	1	Address of parameter list.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IEFJCDLT.	Irrelevant.
IEFJCNTL	0	Irrelevant.	Irrelevant.
	1	Address of parameter list.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IEFJCNTL.	Return code.
IEFJDIRD	0	Irrelevant.	Irrelevant.
	1	Address of RPL.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IEFJDIRD.	Irrelevant.
IEFJDSNA	0	Address of SSCVT.	Irrelevant.
	1	Address of SSOB.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IEFJDSNA.	Return code.
IEFJDWRT	0	Irrelevant.	Irrelevant.
	1	Address of RPL.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address of IEFJDWRT.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEFJJCLS	0	Irrelevant.	Irrelevant.
	1	Address of parameter list.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IEFJJCLS.	Return code.
IEFJJOBS	0	Irrelevant.	Irrelevant.
	1	Address of SSOB.	Unchanged.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IEFJJOBS.	Irrelevant.
IEFJJTRM	0	Address of SSCVT.	Irrelevant.
	1	Address of SSOB.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IEFJJTRM.	Return code.
IEFJRASP	0	Address of master subsystem's SSCVT.	Irrelevant.
	1	Address of SSOB.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IEFJRASP.	Return code.
IEFJREAD	0	Irrelevant.	Irrelevant.
	1	Address of RPL.	Unchanged.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IEFJREAD.	Irrelevant.
IEFJRECM	0	Code indicating existence of RTCA.	Standard.
	1	Address of RTCA.	Standard.
	2-15	Standard.	Standard.
IEFJSDTN	0	Address of SSCVT.	Irrelevant.
	1	Address of SSOB.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IEFJSDTN.	Return code.
IEFJSREQ	0	Irrelevant.	Address of SSCVT.
	1	Address of SSOB pointer.	Address of SSOB.
	2-14	Standard.	Unchanged.
	15	Standard.	Address of function routine -or- return code if error exit.
IEFJWRTE	0	Irrelevant.	Irrelevant.
	1	Address of RPL.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IEFJWRTE.	Irrelevant.
IEFJSWT	0-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEFJWTOM	0	Irrelevant.	Irrelevant.
	1	Address of RPL.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IEFJWTOM.	Return code.
IEFNB901	0-9	Standard.	Standard.
	10	Address of local work area.	Standard.
	11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFNB903	0	Standard.	Standard.
	1	Address of NEL.	Standard.
	2-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFQB550	0	Standard.	Standard.
	1	Address of QMPA.	Standard.
	2-15	Standard.	Standard.
IEFQB555	0	Standard.	Standard.
	1	Address of local parameter list.	Standard.
	2-15	Standard.	Standard.
IEFQB580	0	Standard.	Standard.
	1	Address of QMGRIO parameter list.	Standard.
	2-15	Standard.	Standard.
IEFQB585	0	Standard.	Standard.
	1	Address of QMPA.	Address of QMPA.
	2-15	Standard.	Standard.
IEFRPREP	0	Standard.	Standard.
	1	Address of LCT.	Standard.
	2-15	Standard.	Standard.
IEFSD060	0	Standard.	Standard.
	1	Address of IEL.	Address of LCT.
	2-15	Standard.	Standard.
IEFSD061	0	Standard.	Standard.
	1	Address of LCT.	Variable.
	2-15	Standard.	Standard.
IEFSD062	0	Standard.	Standard.
	1	Variable.	Address of IEFPARAM.
	2-15	Standard.	Standard.
IEFSD064	0	Standard.	Standard.
	1	Address of IEFPARAM.	Variable.
	2-15	Standard.	Standard.
IEFSD066	0	Standard.	Standard.
	1	Address of IEFPARAM.	Address of LCT.
	2-15	Standard.	Standard.
IEFSD101	0	Standard.	Standard.
	1	Address of LCT.	Address of LCT.
	2-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEFSD102	0	Standard.	Standard.
	1	Address of LCT.	Address of LCT.
	2-15	Standard.	Standard.
IEFSD103	0	Standard.	Standard.
	1	Address of IEFPARAM.	Address of IEFPARAM.
	2-15	Standard.	Standard.
IEFSD160	(see IEFSD060)		
IEFSD161	(see IEFSD061)		
IEFSD162	(see IEFSD062)		
IEFSD164	(see IEFSD064)		
IEFSD166	(see IEFSD066)		
IEFSD263	0	Standard.	Standard.
	1	Address of IEFPARAM.	Address of IEFPARAM.
	2-15	Standard.	Standard.
IEFSMFAT	0	Address of initiator's TCB.	Standard.
	1	Address of TIOT pointer.	Standard.
	2-15	Standard.	Standard.
IEFSMFIE	0	Standard.	Standard.
	1	Address of LCT.	Standard.
	2-15	Standard.	Standard.
IEFVDA	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEFVDBSD	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVEA	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVFA	0-8	Standard.	Standard.
	9	Address of JCL statement.	Standard.
	10	Address of JCL statement parameter list.	Standard.
	11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVFB	0	Standard.	Standard.
	1	Delimiter pointer.	Standard.
	2	Standard.	Error message code.
	3-7	Standard.	Standard.
	8	Address of local work area.	Standard.
	9-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVGK	0-1	Standard.	Standard.
	2	Standard.	Length of current parm. in text.
	3	Standard.	Address of length byte of current parm.
	4	Standard.	Address of PDT for Keyword parm.
	5-9	Standard.	Standard.
	10	Address of local work area.	Standard.
	11	Base register of calling routine.	Standard.
	12	Address of work area.	Standard.
	13-15	Standard.	Standard.
IEFVGM	0-1	Standard.	Standard.
	2	0 for JCL statement.	Standard.
	3-8	Error message code.	Standard.
	9	Address of JCL statement.	Standard.
	10-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVGT	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVHA	0-8	Standard.	Standard.
	9	Address of input buffer.	Standard.
	10-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEFVHC	0-8	Standard.	Standard.
	9	Address of JCL statement.	Standard.
	10	Address of JCL statement parameter list.	Standard.
	11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVHCB	0-8	Standard.	Standard.
	9	Address of JCL statement.	Standard.
	10	Address of JCL statement parameter list.	Standard.
	11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVHE	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVHEB	0-8	Standard.	Standard.
	9	Address of JCL statement.	Standard.
	10	Address of JCL statement parameter list.	Standard.
	11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVHF	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVHH	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVHL	0-8	Standard.	Standard.
	9	Standard.	Address of procedure input buffer.
	10-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVHM	0-4	Standard.	Standard.
	5	Address of JCL statement verb.	Standard.
	6-8	Standard.	Standard.
	9	Address of JCL statement.	Standard.
	10	Address of JCL statement parameter list.	Standard.
	11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVHN	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVHQ	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVHR	0-4	Standard.	Standard.
	5	Address of message.	Standard.
	6-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEFVH1	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVINA	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVINB	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVINC	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVIND	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVINE	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVJA	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEFVKMSG		Not applicable (non-executable module).	
IEFXB500	0	Standard.	Standard.
	1	Address of journal parameter list.	Standard.
	2-15	Standard.	Standard.
IEFXB601	0	Standard.	Standard.
	1	Address of MEL.	Standard.
	2-15	Standard.	Standard.
IEFXB602	0	Standard.	Standard.
	1	Address of QMPA.	Standard.
	2-15	Standard.	Standard.
IEFXB603	0-15	Standard.	Standard.
IEFXB604	0	Standard.	Standard.
	1	Address of LCT.	Standard.
	2-15	Standard.	Standard.
IEFXB609	0	Standard.	Standard.
	1	Address of LCT.	Standard.
	2-15	Standard.	Standard.
IEFXB610	0	Standard.	Standard.
	1	Address of parameter list.	Standard.
	2-15	Standard.	Standard.
IEFXVNSL	0-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IEZDCODE	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IEZNCODE	0-11	Standard.	Standard.
	12	Address of work area.	Address of work area.
	13-15	Standard.	Standard.
IGC07902	0-1	Same as IGC079.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Secondary mask or ASID.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IGC07902.	Return code.
IGC07903	0	Bits 0-15: ASID Bits 16-31: code 13.	Unchanged
	1	Bit 0: B'1'.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IGC07903.	Return code.
IGX00013	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2	Irrelevant.	Unchanged.
	3	CVT address.	Unchanged.
	4	TCB address.	Unchanged.
	5	SVRB address.	Unchanged.
	6-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry address.	Return code.
IGX00014	0	Irrelevant.	Unchanged.
	1	Irrelevant.	DTMVT address.
	2	Irrelevant.	Unchanged.
	3	CVT address.	Unchanged.
	4	TCB address.	Unchanged.
	5	SVRB address.	Unchanged.
	6-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry address.	Return code.
IKJEFLA	0	Irrelevant.	Irrelevant.
	1	Address of ASCB.	Address of ASCB.
	2	Irrelevant.	Irrelevant.
	3	Irrelevant.	Address of JSEL.
	4-14	Irrelevant.	Irrelevant.
	15	Entry point address of IKJEFLA.	Irrelevant.
IKJEFLB	0	Irrelevant.	Irrelevant.
	1	Address of ASCB.	Address of JSEL.
	2	Irrelevant.	Irrelevant.
	3	Address of JSEL.	Irrelevant.
	4-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address of IKJEFLB.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IKJEFLC	0-1	Irrelevant.	Irrelevant.
	2	Address of LWA.	Address of LWA.
	3-10	Irrelevant.	Irrelevant.
	11	Address of dynamic area.	Irrelevant.
	12	Address of code.	Irrelevant.
	13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Entry point address of IKJEFLC.	Irrelevant.
IKJEFLCM	Non-executable module containing message segments for IKJEFLC.		
IKJEFLE	0-1	Irrelevant.	Irrelevant.
	2	Address of the LOGON work area.	Irrelevant.
	3-10	Irrelevant.	Irrelevant.
	11	Address of dynamic area.	Irrelevant.
	12	Address of code.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IKJEFLE.	Irrelevant.
IKJEFLEA	0-1	Irrelevant.	Irrelevant.
	2	Address of the LOGON work area.	Irrelevant.
	3-10	Irrelevant.	Irrelevant.
	11	Address of dynamic area.	Irrelevant.
	12	Address of code.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IKJEFLEA.	Return code.
IKJEFLF	0	Post code.	Standard.
	1	Address of ASCB for cancellation.	Standard.
	2-15	Standard.	Standard.
IKJEFLG	0	Irrelevant.	Irrelevant.
	1	Address of parameter list.	Irrelevant.
	2-10	Irrelevant.	Irrelevant.
	11	Address of code.	Irrelevant.
	12	Address of dynamic area.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IKJEFLG.	Return code.
IKJEFLGB	0	Irrelevant.	Irrelevant.
	1	Address of SDWA.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IKJEFLGB.	Retry or termination indicator.
IKJEFLGH	Non-executable module containing the message text for IKJEFLG.		
IKJEFLGM	0	Irrelevant.	Irrelevant.
	1	Address of parameter list.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IKJEFLGH.	Return code.
IKJEFLGN	Non-executable module containing the message text for IKJEFLGM.		

Module Name	Register Number	Contents at Entry	Contents at Exit
IKJEFLH	0	Irrelevant.	Irrelevant.
	1	Address of LOGON work area.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IKJEFLH.	Irrelevant.
IKJEFLI	0	Irrelevant.	Irrelevant.
	1	Address of LOGON work area.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IKJEFLI.	Irrelevant.
IKJEFLJ	0	Irrelevant.	Irrelevant.
	1	Address of a pointer to LCT.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IKJEFLJ.	Return code.
IKJEFLK	0	Irrelevant.	Irrelevant.
	1	Address of LCT.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IKJEFLK.	Return code.
IKJEFLL	0	Irrelevant.	Irrelevant.
	1	Address of LOGON work area.	Irrelevant.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IKJEFLL.	Irrelevant.
IKJEFLLM	Non-executable module containing message text for IKJEFLL.		
IKJEFLPA	0	Irrelevant.	Irrelevant.
	1	Address of parameter list.	Unchanged.
	2-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IKJEFLPA.	Irrelevant.
IKJEFLP0	Non-executable module containing SYSGEN-dependent LOGON values.		
IKJEFLS	0	Irrelevant.	Retry address, if it is a retry.
	1	Address of SDWA.	Address of ASCB.
	2-6	Irrelevant.	Irrelevant.
	7	Address of the LOGON work area.	Irrelevant.
	8-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address of IKJEFLS.	Return code.
IKJL4T00	0	Address of SRB.	Standard.
	1	SRBPARM field contents.	Standard.
	2-15	Standard.	Standard.

Module Name	Register Number	Contents at Entry	Contents at Exit
IKJ5803D	0-1	Standard.	Standard.
	2	Address of XSA.	Standard.
	3-15	Standard.	Standard.
ILRACT	0	Irrelevant.	Unchanged.
	1	Address of ACE.	Unchanged.
	2	Address of RSMHD.	Unchanged.
	3	Address of ASMVT.	Unchanged.
	4	Address of EPATH.	Unchanged.
	5-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRCMP			If R12=0 on entry.
ILRCMP	0	Address of SRB.	Unpredictable.
	1	Address of IOSB.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRCMPAE	0	Address of SRB.	Unchanged.
	1	Address of IOSB.	Unchanged.
	2-6	Irrelevant.	Unchanged.
	7	Irrelevant.	Unpredictable.
	8-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRCMPDI	0, 1	Irrelevant.	Unchanged.
	2	Address of IOSB.	Unchanged.
	3-6	Irrelevant.	Unchanged.
	7	UCB address (not used).	Unchanged.
	8-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
ILRCMPNE	0	Address of SRB.	Unchanged.
	1	Address of IOSB.	Unchanged.
	2-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
ILRCMP01	0	Address of 200-byte work area.	Unpredictable.
	1	Address of SDWA.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRFMT00	0	Irrelevant.	Unchanged.
	1	Address of parameter list.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.

Module Name	Register Number	Contents at Entry	Contents at Exit
ILRFMTCV			
ILRFMTC	0	Irrelevant.	Unchanged.
	1	Address of parameter list.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRFMTH	0	Irrelevant.	Unchanged.
	1	Address of parameter list.	Unchanged.
	2	Irrelevant.	Unchanged.
	3	Address of RSMHD.	Unchanged.
	4-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRFMTV	0	Irrelevant.	Unchanged.
	1	Address of parameter list.	Unchanged.
	2	Irrelevant.	Unchanged.
	3	Address of ASMVT.	Unchanged.
	4-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRFMTPG	0-15	Same as entry ILRFMTV of ILRFMTCV.	
ILRFMTSW	0-15	Same as entry ILRFMTV of ILRFMTCV.	
ILRFRR01			
ILRPSRMT	0	Address of purged SRB.	Unpredictable.
	1-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRVACE	0	Address of potential ACE.	Unchanged.
	1-15	Same as entry ILRVAIAC.	
ILRVACEQ	0	Address of first ACE.	Unchanged.
	1-15	Same as entry ILRVLPRQ.	
ILRVACQ2	0	Address of first ACE.	Unchanged.
	1-15	Same as entry ILRVLPRQ.	
ILRVAIA	0	Address ot potential AIA.	Unchanged.
	1-15	Same as entry ILRVAIAC.	
ILRVAIAC	0	Address of potential AIA/ACE.	Unchanged.
	1	Address of SDWA.	Unchanged.
	2-7	Irrelevant.	Unchanged.
	8-13	irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRVAIAQ	0	Address of first AIA.	Unchanged.
	1-15	Same as entry ILRVLPRQ.	

Module Name	Register Number	Contents at Entry	Contents at Exit
ILRFRR01 (continued)			
ILRVIOE	0	Address of potential IOE.	Unchanged.
	1	Address of SDWA.	Unchanged.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRVIOEQ	0	Address of address of first IOE.	Unchanged.
	1	Address of SDWA.	Unchanged.
	2-7	Irrelevant.	Unpredictable.
	8	Address of work area.	Unchanged.
	9-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRVIORB	0	Address of potential IORB.	Unchanged.
	1-15	Same as entry ILRVAIAC.	
ILRVLGE	0	Address of potential LGE.	Unchanged.
	1-15	Same as entry ILRVAIAC.	
ILRVLPRQ	0	Address of LGE.	Unchanged.
	1	Address of SDWA.	Unchanged.
	2-4	Irrelevant.	Unchanged.
	5-7	Irrelevant.	Unpredictable.
	8	Address of 92-byte work area.	Unchanged.
	9-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRVPCB	0	Address of potential PCB.	Unchanged.
	1-15	Same as entry ILRVAIAC.	
ILRVPCBQ	0	Irrelevant.	Unchanged.
	1	Address of SDWA	Unchanged.
	2	Address of RSMHD.	Unchanged.
	3, 4	Irrelevant.	Unchanged.
	5-7	Irrelevant.	Unpredictable.
	8	Address of 92-byte work area.	Unchanged.
	9-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRVPCCW	0	Address of potential PCCW.	Unchanged.
	1-15	Same as entry ILRVAIAC.	
ILRVPCWQ	0	Address of first PCCW.	
	1-15	Same as entry ILRVLPRQ.	
ILRVSCCW	0	Address of potential SCCW.	Unchanged.
	1-15	Same as entry ILRVAIAC.	
ILRVSCWQ	0	Address of first SCCW.	Unchanged.
	1-15	Same as entry ILRVLPRQ.	
ILRVSPAQ	0-15	Same as entry ILRVAIAQ.	

Module Name	Register Number	Contents at Entry	Contents at Exit
ILRFRR01 (continued)			
ILRVSWTQ	0-15	Same as entry ILRVASGQ.	
ILRFRSLT			
ILRFRSLT	0	Non-zero for private area pages.	Unchanged.
	1	XPTE address.	Unchanged.
	2	RSMHD address.	Unchanged.
	3-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
ILRFRSL1	Same as ILRFRSW1.		
ILRFRSW1	0	Irrelevant.	Unchanged.
	1	LSID.	Unchanged.
	2	Irrelevant.	Unchanged.
	3	Address of ASMVT.	Unchanged.
	4-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
ILRGOS			
ILRGOS	0	Irrelevant.	Unchanged.
	1	Address of ACA.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRFRELG	0	Irrelevant.	Unchanged.
	1	Address of LGE.	Unchanged.
	2	Irrelevant.	Unchanged.
	3	Address of ASMVT.	Unchanged.
	4-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRGOS01			
ILRGOS01	0	Address of 200-byte work area.	Unpredictable.
	1	Address of SDWA.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRCGOSE	0	Indicate if SDWA exists.	
	1	Address of SDWA, if reg. 0 is not 12.	
	2	Address of parameter list, if reg. 0 is 12.	Unchanged, if save area in reg. 13.
	3-12	Irrelevant.	Unpredictable otherwise.
	13	Address of save area, if reg. 0 is not 12.	
	14	Return address.	
	15	Entry point address.	Return code.

Module Name	Register Number	Contents at Entry	Contents at Exit
ILRIOFRR			
ILRIOFRR	0	Address of 200-byte work area.	Unpredictable.
	1	Address of SDWA.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRCQIOE	0	Irrelevant.	Unpredictable.
	1	Address of SDWA.	Unpredictable.
	2	Irrelevant.	Unpredictable.
	3	Address of ASMVT.	Unpredictable.
	4	Address of ATA.	Unpredictable.
	5-7	Irrelevant.	Unpredictable.
	8	Address of 92-byte work area.	Unpredictable.
	9	Address this routine's work area.	Unpredictable.
	10-13	Irrelevant.	
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRJTERM			
ILRJTERM	0-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRJTM01	0	Address of 200-byte work area.	Unpredictable.
	1	Address of SDWA.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRMSG00			
ILRMSG00	0	Irrelevant.	Unchanged.
	1	Address of parameter list.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRMSGSP	0-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
ILROPS00	0	Irrelevant.	Unchanged.
	1	Address of parameter list.	Unchanged but are return parameters.
	2	Address of NVT (at NIP time).	Unchanged.
	3	Address of CVT (at NIP time).	Unchanged.
	4-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRPAGCM	0	Irrelevant.	Unchanged.
	1	Address of first AIA.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Address of 18-word save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.

Module Name	Register Number	Contents at Entry	Contents at Exit
ILRPAGIO			
ILRPAGIO	0	Irrelevant.	Unpredictable.
	1	Address of first AIA.	Unpredictable.
	2	Address of RSMHD.	Unchanged.
	3-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRQIOE	0, 1	Irrelevant.	Unpredictable.
	2	Irrelevant.	Unchanged.
	3	Address of ASMVT.	Unchanged.
	4	Address of AIA.	Unchanged.
	5-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRPEX	0	Irrelevant.	Unchanged.
	1	Address of pool controller to be extended.	0 or address of a cell.
	2-10	Irrelevant.	Unchanged.
	11, 12	Irrelevant.	Unpredictable.
	13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
ILRPGEXP			
ILRPGEXP	0	Irrelevant.	Unpredictable.
	1	Address of CSCB.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ESTAER	0	Irrelevant.	Unpredictable.
	1	Address of parameter list.	Unpredictable.
	2	Address of parameter list if SDWA not available.	Unpredictable.
	3-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRPOS			
ILRPOS	0, 1	Irrelevant.	Unchanged.
	2	Address of RSMHD.	Unchanged.
	3	Address of ASMVT.	Unchanged.
	4	Address of ATA.	Unchanged.
	5-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRESTRT	0	Irrelevant.	Unchanged.
	1	Address of AIA.	Unchanged.
	2	Address of RSMHD.	Unchanged.
	3	Address of ASMVT.	Unchanged.
	4	Address of ATA.	Unchanged.
	5-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.

Module Name	Register Number	Contents at Entry	Contents at Exit
ILRPOS (continued)			
ILRTRANS	0	Address of save area for free slot.	Unchanged.
	1	Address of ACE.	Unchanged.
	2	Address of RSMHD.	Unchanged.
	3	Address of ASMVT.	Unchanged.
	4	Address of ATA.	Unchanged.
	5-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
ILRTRPAG	0	Irrelevant.	Unchanged.
	1	Address of ACA.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRPREAD			
ILRPREAD	0	Irrelevant.	Unchanged.
	1	Address of parameter list.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return code.	Unchanged.
	15	Entry point address.	Return code.
PREADABN	0	Irrelevant.	Unchanged.
	1	Address of IOSB (not used).	Unchanged.
	2-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
PREADNRM	0-15	Same as entry PREADABN.	
PREADTRM	0	Irrelevant.	Unpredictable.
	1	Address of IOSB.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ESTAEXIT	0	Indicate SDWA existence.	Unpredictable.
	1	Address of SDWA, if reg. 0 is not 12.	Unpredictable.
	2	Address of parameter list, if reg. 0 is 12.	Unpredictable.
	3-13	Irrelevant.	Unpredictable.
	14	Return address.	Unpredictable.
	15	Entry point address.	Unpredictable.
ILRPTM	0	Address of SRB.	Unpredictable.
	1-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.

Module Name	Register Number	Contents at Entry	Contents at Exit
ILRRLG	0	Irrelevant.	Unchanged.
	1	Address of ACE.	Unchanged.
	2	Address of RSMHD.	Unchanged.
	3	Address of ASMVT.	Unchanged.
	4	Address of EPATH.	Unchanged.
	5-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRSAV	0	Irrelevant.	Unchanged.
	1	Address of ACE.	Unchanged.
	2	Address of RSMHD.	Unchanged.
	3	Address of ASMVT.	Unchanged.
	4	Address of EPATH.	Unchanged.
	5-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRSRBC			
ILRSRBC	0	Address of SRB.	Unpredictable.
	1-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRSRBRM	0	Irrelevant.	Unpredictable.
	1	Address of SRB.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRSRB01	0	Address of FRR work area.	Unpredictable.
	1	Address of SDWA.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRSRT	0	Irrelevant.	Unchanged.
	1	Address of parameter list.	Unchanged.
	2, 3	Irrelevant.	Unchanged.
	4	Address of ATA.	Unchanged.
	5-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRSRT01	0	Address of 200-byte work area.	Unpredictable.
	1	Address of SDWA.	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.

Module Name	Register Number	Contents at Entry	Contents at Exit
ILRSWAP			
ILRSWAP	0	Irrelevant.	Unpredictable.
	1	Address of ATA.	Unpredictable.
	2	Address of RSMHD.	Unchanged.
	3-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
ILRSLSQA	0, 1	Irrelevant.	Unpredictable.
	2	Address of RSMHD.	Unchanged.
	3	Address of ASMVT.	Unchanged.
	4	Address of ATA.	Unchanged.
	5-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRSWPDR	0-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRSWP01			
ILRSWP01	0	Irrelevant.	Unpredictable.
	1	Address of SDWA.	Unpredictable.
	2	Irrelevant.	Unpredictable.
	3	Address of ASMVT.	Unpredictable.
	4	Address of ATA.	Unpredictable.
	5-7	Irrelevant.	Unpredictable.
	8	Address of 92-byte work area.	Unpredictable.
	9	Address of this routine's work area.	Unpredictable.
	10-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRCSWAP	0-15	Same as entry ILRSWP01.	
ILRCSLSQ	0-15	Same as entry ILRSWP01.	
ILRTERMR			
ILRTERMR	0	Irrelevant.	Unchanged.
	1	Address of parameter.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
ILRSLTRV	0, 1	Irrelevant.	Unchanged.
	2	Address of RSMHD.	Unchanged.
	3	Address of PVT.	Unchanged.
	4-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
TERMFRR	0	Address of FRR work area.	Unpredictable.
	1	Address of SDWA.	Unchanged.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.

Module Name	Register Number	Contents at Entry	Contents at Exit
ILRTMI01	0	SDWA indicator.	
	1	Address of SDWA, if reg. 0 is not 12.	Unchanged if save area in reg. 13
	2	Address of EPATH, if reg. 0 is 12.	otherwise reg. 2 = address of
	3-12	Irrelevant.	EPATH.
	13	Save address reg, if reg. 0 is not 12.	Reg. 0 = address of ILRCRTMX when
	14	Return address.	reg. 15 is 4.
	15	Entry point address.	Return code.
ILRTMRLG	0	Irrelevant.	Unpredictable.
	1	Address of ECB (Master Sched. Init.'s)	Unpredictable.
	2-13	Irrelevant.	Unpredictable.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRVIOCM	0, 1	Irrelevant.	Unchanged.
	2	Address of RSMHD.	Unchanged.
	3	Address of ASMVT.	Unchanged.
	4	Address of ATA.	Unchanged.
	5-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unpredictable.
ILRVSAMI	0	Irrelevant.	Unchanged.
	1	Address of parameter list.	Unchanged.
	2	Irrelevant.	Unchanged.
	3	Address of ASMVT.	Unchanged.
	4-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IRARMCNS	Non-executable module containing pre-assembled SRM tables.		
IRARMCPM	0	Irrelevant.	Unchanged.
	1	Address of RMEP.	Unchanged.
	2	Address of RMCT.	Unchanged.
	3	Address of RRPA.	Unchanged.
	4	Address of OUCB.	Unchanged.
	5	Address of ASCB.	Unchanged.
	6-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IRARMCTL	0	Irrelevant.	Unchanged (if exit by BR 14).
	1	Address of RMEP (certain functions).	Unchanged (if exit by BR 14).
	2	Address of RMCT.	Unchanged (if exit by BR 14).
	3	Address of RRPA.	Unchanged (if exit by BR 14).
	4	Address of OUCB (certain functions).	Unchanged (if exit by BR 14).
	5-12	Irrelevant.	Unchanged (if exit by BR 14).
	13	Address of SRM save area (certain functions).	Unchanged (if exit by BR14).
	14	Return address (certain functions).	Unchanged (if exit by BR 14).
	15	Entry point address.	Return code (certain functions).
IRARMERR	0	Address of 200 byte save area.	Irrelevant.
	1	Address of SDWA.	Irrelevant.
	2-13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Irrelevant.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IRARMEVT	0	Irrelevant.	Irrelevant.
	1	Irrelevant.	Irrelevant.
	2	Address of RMCT.	Irrelevant.
	3	Address of RRPA.	Irrelevant.
	4	Address of OUCB.	Irrelevant.
	5	Address of ASCB.	Irrelevant.
	6-12	Irrelevant.	Irrelevant.
	13	Address of save area.	Irrelevant.
	14	Irrelevant.	Irrelevant.
	15	Irrelevant.	Irrelevant.
IRARMINT	0	SYSEVENT code and ASID of associated address space.	Irrelevant.
	1	Input parameters.	Return indicators (some SYSEVENTs).
	2-12	Irrelevant.	Unchanged (for branch entry).
	13	Address of save area (branch entry only).	Unchanged (for branch entry).
	14	Address of type 1 SVC exit routine (SVC entry), or return address (branch entry).	Unchanged (for branch entry).
	15	Entry point address (branch entry).	Return code (some SYSEVENTs).

Module Name	Register Number	Contents at Entry	Contents at Exit
IRARMIOM	0	Irrelevant.	Unchanged.
	1	Address of RMEP.	Unchanged.
	2	Address of RMCT.	Unchanged.
	3	Address of RRPA.	Unchanged.
	4	Address of OUCB.	Unchanged.
	5	Address of ASCB.	Unchanged.
	6-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IRARMIPS	0	Standard.	Standard.
	1	Address of Parameter List.	Address of Parameter List.
	2-15	Standard.	Standard.
IRARMMSG	Non-executable module containing SRM messages.		
IRARMRMR	0-1	Irrelevant.	Unchanged.
	2	Address of RMCT.	Unchanged.
	3	Address of RRPA.	Unchanged.
	4-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevant.
IRARMSET	0-1	Irrelevant.	Unchanged.
	2	Address of RMCT.	Unchanged.
	3	Address of RRPA.	Unchanged.
	4-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Address of IRARMSET.	Irrelevant.
IRARMSRV	0	Length of storage request (entry IRARMI04 only).	Unchanged.
	1	Address of parameter list (and next SRM timer interruption for XMPOST) (or address of any storage to be freed - entry IRARMI04 only).	Address of storage obtained (entry IRARMI04 only).
	2-4	Irrelevant.	Unchanged.
	5	Address of ASCB (for IRARMI06 and IRARMI07 entries only.).	Unchanged.
	6-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IRARMSTM	0	Irrelevant.	Unchanged.
	1	Address of RMEP.	Unchanged.
	2	Address of RMCT.	Unchanged.
	3	Address of RRPA.	Unchanged.
	4	Address of OUCB.	Unchanged.
	5	Address of ASCB.	Unchanged.
	6-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.

Module Name	Register Number	Contents at Entry	Contents at Exit
IRARMWAR	0-1	Irrelevant.	Unchanged.
	2	Address of RMCT.	Unchanged.
	3	Address of RRPA.	Unchanged.
	4	Address of OUCB (certain functions).	Unchanged.
	5	Address of WMST (certain functions).	Unchanged.
	6	Address of WAMT (certain functions).	Unchanged.
	7-12	Irrelevant.	Unchanged.
	13	Address of save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevant.

Module Name	Register Number	Contents at Entry	Contents at Exit
IRARMWLM	0	Service rate (entry IRARMWM4 only).	Workload level (entry IRARMWM4 only).
	1	Performance objective (entry IRARMWM4 only).	Plateau (entry IRARMWM4 only).
	2	Address of RMCT.	Unchanged.
	3	Address of RRPA.	Unchanged.
	4	Address of OUCB.	Unchanged.
	5	Address of ASCB (certain functions).	Unchanged.
	6	Address of OUXB (certain functions).	Unchanged.
	7-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Irrelevant.
IRBMFALL	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.
IRBMFANL	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IRBMFCNV	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Parameter list address.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry address.	Return code.
IRBMFDCP*	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Parameter list address.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.
IRBMFDDP*	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Parameter list address.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.
IRBMFDEA	0	Identifies existence of SDWA (not exist if = 12).	Unchanged.
	1	SDWA address.	Unchanged.
	2	Parameter list address if SDWA not exist.	Unchanged.
	3-11	Irrelevant.	Irrelevant.
	12	Unspecified.	Destroyed.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Destroyed; return code if reg=12 on entry.

*These modules' usage shows most common entry usage and exit usage.

Module Name	Register Number	Contents at Entry	Contents at Exit
IRBMFDHP*	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Parameter list address.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.
IRBMFDPP*	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Parameter list address.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.
IRBMFDTA	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.
IRBMFDWP*	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Parameter list address.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.
IRBMFECH	0	Irrelevant.	Unchanged.
	1	Parameter register from MFROUTER measurement vector table.	Unchanged.
	2	Address of save area containing regs 0-14 at point of MFROUTER invocation.	Unchanged.
	3-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.
IRBMFEDV	0	Irrelevant.	Unchanged.
	1	Parameter reg from MFROUTER measurement vector table.	Unchanged.
	2	Address of area containing contents of regs 0-14 at point of MFROUTER macro invocation.	Unchanged.
	3-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry address.	Unchanged.
IRBMFEVT*	0	MFROUTER entry code.	Unchanged.
	1-13	Irrelevant.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry address.	Irrelevant.
IRBMFFUR	0	Irrelevant.	Irrelevant.
	1	Parameter address.	Unchanged.
	3-11	Irrelevant.	Irrelevant.
	12	Unspecified.	Irrelevant.
	13	Irrelevant.	Irrelevant.
	14	Return address.	Unchanged.
	15	Irrelevant.	Irrelevant.

*These modules' usage shows most common entry usage and exit usage.

Module Name	Register Number	Contents at Entry	Contents at Exit
IRBMFICP	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IRBMFIDV	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry address.	Return code.
IRBMFIHA	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry address.	Return code.
IRBMFINP	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry address.	Unchanged.
IRBMFIOI*	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Unchanged.
IRBMFIPG	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry point address.	Return code.
IRBMFIWK	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry address.	Return code.
IRBMFLCV	Non-executable.		
IRBMFLDV	Non-executable.		
IRBMFLDH	Non-executable.		
IRBMFLMV	Non-executable.		
IRBMFLPV	Non-executable.		

*These modules' usage shows most common entry usage and exit usage.

Module Name	Register Number	Contents at Entry	Contents at Exit
IRBMFLTV	Non-executable.		
IRBMFLWV	Non-executable.		
IRBMFMFC	0	Irrelevant.	Unchanged.
	1	Exec statement parameter address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry address.	Return code.
IRBMFMLN	0	12 if SDWA exists; otherwise not equal to 12.	Unchanged.
	1	SDWA address if Reg 0=12.	Unchanged.
	2	Parameter list address when ESTAE invoked	Unchanged.
		if Reg 0≠12.	Unchanged.
	3-11	Irrelevant.	Irrelevant.
	12	Irrelevant.	Irrelevant.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Irrelevant; return code if reg 0=12 on entry.
IRBMFMPR	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry address.	Unchanged.
IRBMFRCR	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry address.	Unchanged.
IRBMFRDR	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.
IRBMFRGM	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.
IRBMFRHR	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.

Module Name	Register Number	Contents at Entry	Contents at Entry
IRBMFRPR	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Entry address.	Unchanged.
IRBMFRWR	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.
IRBMFSAR	0	=12 indicates no SDWA, ≠12 indicates SDWA exists. 0 - Active I/O quiesced and restorable. 4 - Active I/O halted and not restorable. 8 - No active I/O. 16 - No I/O processing performed.	Unchanged.
	1	SDWAPARM address (R0≠12) or completion code (R0=12).	Irrelevant.
	2	Address of user parameter list (R0=12) or irrelevant (R0≠12).	Irrelevant.
	3-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area (R0≠12) or irrelevant (R0=12).	Irrelevant.
	14	Return address.	Unchanged.
	15	Irrelevant.	Return code (R0=12 on entry).
IRBMFSDE*	0	12 if SDWA exists, otherwise not equal to 12.	Unchanged.
	1	SDWA address if Reg 0 = 12.	Unchanged.
	2	Address of parameters passed when ESTAE invoked if Reg 0 not equal to 12.	Unchanged.
	3-11	Irrelevant.	Irrelevant.
	12	Irrelevant.	Irrelevant.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Irrelevant; return code of reg 0 = 12 on entry.
IRBMFTCH	0	Irrelevant.	Unchanged.
	1	Parameter register from MFROUTER measurement vector table.	Unchanged.
	2	Address of save area containing reg 0-14 at point of MFROUTER invocation.	Unchanged.
	3-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.
IRBMFTMA IRBMFTMA	0-12	Irrelevant.	Unchanged.
	13	Address of caller's save area.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.

*These modules' usage shows most common entry usage and exit usage.

Module Name	Register Number	Contents at Entry	Contents at Entry
IRBMFTMA (continued)			
IRBMFTXR	0	=12 indicates no SDWA; otherwise, SDWA exists.	Irrelevant.
	1	Address of SDWA (R0≠12).	Irrelevant.
	2	Address of parameters passed when ESTAE was invoked (R0=12).	Irrelevant.
	3-12	Irrelevant.	Irrelevant.
	13	Address of caller's save area (R0≠12).	Irrelevant.
	14	Return address.	Unchanged.
	15	Irrelevant.	Irrelevant or return code (R0=12 on entry).
IRBMFTRM*	0	Irrelevant.	Unchanged.
	1	Parameter list address.	Unchanged.
	2-12	Irrelevant.	Unchanged.
	13	Save area address.	Unchanged.
	14	Return address.	Unchanged.
	15	Irrelevant.	Unchanged.

*These modules' usage shows most common entry usage and exit usage.

Communications Task Diagnostic Aids

For a diagram showing the relationship of the control blocks and the respective pointers that are used by the communication task, see Figure 5-1.

Initial Check

Determine if the write queue element (WQE) chain is intact. Check the following:

- In the unit control module (UCM):
  UCMWTOQ--The pointer to the first WQE on the WQE chain.
  UCMWQEND--The pointer to the last WQE on the WQE chain.
- In the WQE:
  WQELKPA--The pointer to the next WQE on the WQE chain.

Console Not Responding to Attention

Check the following:

- IEAVVCRA may not be posting the UCMAECB attention event control block (ECB) in the unit control module (UCM) base. The communication task will not process the attention interruption until this ECB is posted.
- UCMAF in the unit control module entry (UCME) for the console causing the attention interruption. UCMAF indicates an attention pending for this device. It is turned on after the UCMAECB event control block has been posted. (Also note the next paragraph.)
- UCMBF in the unit control module entry (UCME) is the device busy indicator; if UCMBF is also on, the attention interruption will not be processed until an I/O complete interruption is received from the console device. This processing is done by the specific device processor module in load module IGC0007B. It is turned on while the console device is waiting for the completion of some I/O operation. It is turned off when the I/O completion operation is processed.

Enabled Wait State

Check the following:

Normal Case: There is no work for the communication task. Check the following event control blocks (ECBs):

UCMXECB--The external interrupt ECB in the unit control module (UCM); used to switch the master console to its alternate.

UCMAECB--Attention interrupt ECB in the UCM; used to prepare the console to receive an operator command.

UCMOECB--WTO or WTOR output ECB in the UCM; used to process WTO and WTOR messages.

UCMDECB--DOM processing ECB in the UCM; used to eliminate WTOR messages from the WQE chain and to delete messages from graphic devices.

UCMARECB--Alternate CPU recovery ECB in the UCM; used when switching multiprocessing systems.

UCMNPECB--NIP message processing ECB in the UCM prefix; used to write NIP messages to the hardcopy log.

UCMECB-- I/O completion ECB in the unit control module entry (UCME); used to indicate that a console I/O operation has finished.

The system limit for write queue elements (WQEs) or operator reply elements (OREs) has been reached. Check the following fields:

UCMSYSI--System cleanup needed. This bit in the unit control module (UCM) prefix is checked by IEAVMQWR and IEAVMDSV. This bit is set by IEAVMDOM, IEAVMQWR, IEAVMWSV, IEAVMWTO, and IEAVVWTO.

UCMSYSJ--This bit in the UCM prefix indicates that at least one message needs to be sent to the hardcopy log. Possibly the WQE space is filled with WQEs that need to be sent to the hardcopy log. This bit is referenced by IEQVMQWR and IEAVMDSV. This bit is set by IEQVMQWR or IEAVSWCH.

UCMSYSM--This bit in the UCM prefix indicates a failure in a composite console. This bit is used by IEAVSWCH.

UCMSYSO--This bit in the UCM prefix is a dummy attention interrupt. It is checked by IEAVMQWR, and it is set by IEAVVWTO.

UCMWQNR--This halfword in the UCM base indicates the current number of WQEs in the system. The UCMWQLM field in the UCM has the count of how many WQEs can be built.

UCMRQNR--This halfword in the UCM base indicates the current number of OREs in the system. The UCMRQLM field in the UCM has the count of how many OREs can be built.

Disabled Wait State

The communication task issues only one wait state code, code 07. This code is issued by IEAVNP01 during NIP when the system is without a master console. See wait state code 07 in the *OS/VS2 System Initialization Logic,* SY28-0623.

No Messages on One Console

Check the following:

- UCMBF in the unit control module entry (UCME) is the device-busy indicator; if UCMBF is one, the message will not be processed until an I/O complete interruption is received from the console device. This processing is done by the specific device processor module in load module IGC0007B. It is turned on while the console device is waiting for the completion of some I/O operation. It is turned off when the I/O completion operation is processed.
- If the console is not busy, check the console queue element (CQE) chain starting at each unit control module entry (UCME). The pointer to the first CQE is UCMOUTQ. Each CQE is one word; one byte for control bits, and three bytes for a pointer to a WQE. The CQEs are built in groups of six. The first five CQEs point to WQEs and the last CQE points to the next group of CQEs in the CQE chain.
- If the CQE chain is valid, then check the general condition of the UCME, the WQE chain, and the unit control block (UCB) for this console.

Messages Going to Wrong Console

IEAVMWSV queues the messages for the consoles. Check the following:

- Check the console queue element (CQE) chain starting at the unit control module entry (UCME). The pointer to the first CQE is UCMOUTQ. Each CQE is one word; one byte for control bits, and three bytes for a pointer to a WQE. The CQEs are built in groups of six. The first five CQEs point to WQEs and the last CQE points to the next group of CQEs in the CQE chain.
- Check the routing codes for each console. UCMRTCD in each console's unit control module entry (UCME) defines the routing codes for the respective consoles.
- Check the routing codes for the messages that are being incorrectly routed:
  - In the regular WTO and WTOR write queue element (WQE), check the WQEROUT field.
  - In the major multiple-line write queue element (WQE), check the WMJMRTC field.

Truncated Messages

When the length of the message text is being shortened, then either:

- The messages exceed the maximum allowable number of bytes for console messages.
- The console operator may have requested either that messages be time stamped or for jobnames to appear with the messages. Check the following:

UCMDISPI--This bit in the unit control module entry (UCME) for the console indicates that messages are to appear with both jobnames and time stamps.

UCMIDSPJ--This bit in the unit control module entry (UCME) for the console indicates that only jobnames are to appear with the messages, no time stamps.

Console Switching

Console switched by IEAVSWCH as a result of the following indications:

- An I/O error from the console device. Check the UCMECB in the unit control module entry (UCME) for the failing console. '7F' is a successful completion.
- An external interrupt is usually caused by the master console operator pressing the external interrupt key. Check the UCMXECB event control block (ECB) in the unit control module (UCM) base.
- An abnormal termination in the device processor that supports the failing console. Check the appropriate device processor in load module IGC0007B.

Reply Command Problems

When reply commands are not accepted, reply is not outstanding, and so forth, check the following:

- The console queue element (CQE) chain starting at the unit control module entry (UCME). The pointer to the first CQE is UCMOUTQ. Each CQE is one word; one byte for control bits, and three bytes for a pointer to a WQE. The CQEs are built in groups of six. The first five CQEs point to WQEs and

the last CQE points to the next group of CQEs in the CQE chain.
- All of the control bits in the first byte of the CQEs.
- The WQE and its associated ORE.

DIDOCS Trace Table

A DIDOCS trace table exists beginning at field DCMTRACE in the pageable DCM (IEETDCM. The trace table contains the IDs of up to 16 of the last DIDOCS modules to receive control on the console represented by the pageable DCM.

After each DIDOCS module receives control, it places a two-byte identifier in the trace table. The first byte of the identifier states whether the module is an "E" module (for example IEECVETA) or an "F" module (for example IEECVFTA). The second byte of the identifier is the last character in the module name. For example, the ID for IEECVETA is "EA"; the ID for IEECVFT1 is "F1". Whenever DIDOCS is entered for the first time to perform an operation, the first DIDOCS module to receive control (module IEECVET1) places two bytes of asterisks in the trace table before it stores its ID. The asterisks signal the beginning of a DIDOCS operation.

DIDOCS-In-Operation Bit

At offset X'11F' in a console's pageable DCM (IEETDCM) is a field labeled DCMMCSST. When DIDOCS is processing, bit DCMUSE (X'80') in DCMMCSST is set on. This bit remains on during any SVC processing initiated by DIDOCS (SVC34, GETMAIN, FREEMAIN, EXCP). DIDOCS turns the bit off when DIDOCS exits (BR14).

DIDOCS Locking

DIDOCS uses two fields in the communications extended save area (CXSA) to control locking during DIDOCS. Figure 6-1 summarizes the use of the two fields.

If the lock is available, field CSAXB contains the address of a subroutine that obtains the lock; field CSAXC contains the address of a BR14 instruction. After a DIDOCS module obtains the lock, the obtain subroutine sets the address of the release subroutine in field CSAXC and sets field CSAXB to the address of a BR14 instruction; therefore, if any other DIDOCS module attempts to obtain the lock, the attempt results in a NOP.

After the DIDOCS module releases the lock, the release subroutine resets field CSAXB to the address of the obtain subroutine and resets field CSAXC to the address of a BR14 instruction.

	If lock is available	If lock is already held
Field CSAXB contains	Address of obtain subroutine	Address of a BR14 instruction
Field CSAXB contains	Address of a BR14 instruction	Address of a release subroutine

Figure 6-1. DIDOCS Locking

Started Task Control ABEND and Reason Codes

In case of an irreparable error, the started task control (STC) routines issue these ABEND codes:

0B8 – An error occurred while STC routines were processing a START, MOUNT, or LOGON command.

In each case, the command task is terminated; for a START or MOUNT command, the STC routines issue message IEE824I.

The following error codes can appear in register 15 at the time of the ABEND:

04 – Module IEFPRWI2 or IEFJSWT detected an invalid command code in the CSCB; the command code was incorrect for a START, MOUNT, or LOGON command.

08 – Module IEESB605 invoked IEFAB4FC (an Allocation routine) to build a TIOT for the START, MOUNT, or LOGON task; IEFAB4FC returned control to IEESB605 with a return code indicating failure.

12 – Module IEESB605 invoked IEFJSWT (an STC routine) to write the internal JCL text for the START, MOUNT, or LOGON command into system data set; IEFJSWT returned control to IEESB605 with a return code indicating that it failed in its attempt to open the data set.

0B9 – Module IEESB605 invoked the Master Subsystem via the Subsystem Interface to determine whether a START command was issued to start a subsystem; an error occurred during Master Subsystem processing.

The command task is terminated; for a START or MOUNT command, IEESB605 issues message IEE824I.

0BA – Module IEESB605 invoked the Master Subsystem via the Subsystem Interface to determine whether a START command was issued to start a subsystem; an error occurred during Subsystem Interface processing.

The command task is terminated; for a START or MOUNT command, IEESB605 issues message IEE824I.

LOGON Scheduling Diagnostic Aids

The following two tables contain information that can be used for diagnosing problems that occur during LOGON scheduling.

Field Name and Contents		Name of Executing Module	Common Name of Module
LWAINX1	=1	IKJEFLD	Installation Exit (written by installation)
LWALA	=1	IKJEFLA	LOGON Initialization
LWALB	=1	IKJEFLB	LOGON Scheduling
LWALC	=1	IKJEFLC	LOGON Monitor
LWALE	=1	IKJEFLE	LOGON/LOGOFF Verification
LWALEA	=1	IKJEFLEA	Parse/Scan Interface
LWALI	=1	IKJEFLI	Installation Interface
LWALH	=1	IKJEFLH	LOGON Synchronizer
LWALL	=1	IKJEFLL	LOGOFF Processing
LWALGM	=1	IKJEFLGM	LOGON Message Handler
LWALJ	=1	IKJEFLJ	Pre-attach Exit
LWALK	=1	IKJEFLK	Post-attach Exit
LWALG	=1	IKJEFLG	Attention Exit
LWALGB	=1	IKJEFLGB	LOGON Monitor Recovery
LWALS	=1	IKJEFLS	LOGON Scheduling Recovery and Retry
LWALTBC	=1	IKJEFLH	Mail and Notices Processing
LWAMCK		IKJEFLGB	ABEND was a machine check
LWAPCK		IKJEFLGB	ABEND was a program check
LWAPHASE	=0	Any LOGON module except IKJEFLH	LOGON/LOGOFF Verification
LWAPHASE	=1	IKJEFLH	LOGON Synchronizer
LWAPSW		IKJEFLGB	Console Restart key depressed
LWATNBT		IKJEFLG	Attention Routine

Figure 6-2. LOGON Work Area Bits That Indicate the Currently Executing Module

Module Issuing POST	Module Being Posted	Location of ECB	Post Code	Condition of Module Issuing POST	Action Taken by Module Being Posted
IKJEFLB	IKJEFLC	field LWASECB in LWA	16	Ready to invoke job scheduling subroutine (IEESB605).	Invoke LOGON information routine (IKJEFLH).
			24	Terminating for LOGOFF or for unusual termination of LOGON monitor (IKJEFLC).	Perform clean-up operations and terminate.
IKJEFLC	IKJEFLB	field LWAPECB in LWA	12	Termination or attention requested.	Issue DEQ on user identification.
			16	Verified and processed the LOGON parameters.	Schedule a terminal session.
			24	Processing a LOGOFF command.	Terminate.
IKJEFLE	IKJEFLB	field LWAPECB in LWA	8	Authorized the user identification.	Issue ENQ on user identification.
			12	Error processing.	Issue DEQ on user identification.
IKJEFLJ	IKJEFLH	field LWASECB in LWA	20	Detects that the initiator is ready to attach the TMP.	Finish LISTBC processing; return to caller.
IKJEFLH	IKJEFLJ	field LWAPECB in LWA	20	Finished LISTBC processing.	Terminate so the initiator can attach the TMP.

Figure 6-3. LOGON Scheduling Post Codes

SWA Manager Reason Codes

In case of an irreparable error, the SWA manager routines issue a 0B0 ABEND. Before abending, both object modules IEFQB550 and IEFQB555 place a code in register 15 indicating the exact cause of the error.

These are the error codes that can appear in register 15:

04 – The routine that called SWA manager requested an invalid function.

08 – The routine that called SWA manager passed an invalid SWA virtual address (SVA). Either the SVA does not point to the beginning of a SWA prefix or the SWA prefix has been destroyed.

0C – A SWA manager routine has attempted to read a record not yet written into SWA.

10 – Either IEFQB550 (move mode module) has attempted to read or write a block which is not 176 bytes or IEFQB555 (locate mode module) has attempted to assign a block with a specified length of 0 or a negative number.

14 – The routine that called SWA manager has specified an invalid count field. For move mode, an invalid count is 0 for a READ, WRITE, or ASSIGN function; an invalid count for WRITE/ASSIGN is 00.

18 – The routine that called SWA manager by issuing the QMNGRIO macro instruction specified both or neither of the READ or WRITE options.

1C – The routine that called SWA manager was attempting to write into a SWA block for the first time; it either passed a nonexistent ID or failed to pass one at all.

20 – IEFQB555 has attempted to write a block using an invalid pointer to the block.

0C4 Abend Code Occurring in IEFAB4FC

An 0C4 abend code can occur when all the following conditions are true:

- A specific unit request was being processed by allocation.
- Module IEFAB4FC was executing.
- The SIOT DSAB pointer is 0.

The reason is probably because the device-type field in the UCB does not match the device-type field in the EDT; that is, the device-type field in the UCB was changed -- for example, by the installation or because of a failure in the system.

Allocation/Unallocation Reason Codes

The reason codes listed here are divided into three groups:

- Reason Codes set by batch and common allocation modules and by JFCB housekeeping modules.
- Reason codes set by unallocation modules.
- Reason codes set by dynamic allocation modules.

Common and Batch Allocation and JFCB Housekeeping Reason Codes

The reason codes set by common and batch allocation and by JFCB Housekeeping are divided into step-related reason codes and DD-related reason codes.

The following are DD-related error reason codes set by allocation and JFCB housekeeping modules and placed in the SIOTRSNC field of the SIOT. The reason codes serve as an index into message module IEFBB4M3. The prologue of IEFBB4M3 lists the modules which detect the error conditions.

Reason Code	Dynamic Allocation Error Reason Code	Message	Meaning
1	1700	IEF212I	Data set not found.
2	0244	IEF371I	
3	0210	IEF211I	Unable to ENQ on data set name.
4	020C	IEF211I	Unable to ENQ on data set name.
5	0458	IEF365I	Referenced data set name is GDG ALL.
6	0214	IEF702I	Unable to allocate.
7	*	IEF221I	Invalid backward reference to a step.
8	021C	IEF210I	Invalid UNIT parameter.
9	0480	IEF195I	Maximum number of devices for statement exceeded.
10	0224	IEF192I	Not enough eligible devices.
11	0398	IEF194I	Volume sequence number incorrect.
12	4714	IEF246I	Insufficient space on storage volumes.
13	*	IEF721I	Protection conflict in ISAM requests (SU 32 only).
14	*	IEF372I	VOL=REF to unresolved DD.
15	*	IEF318I	UNIT=AFF to new direct data set.
16	47A8	IEF719I	Data set previously defined (SU 32 only).
17	47AC	IEF720I	User not authorized to define this data set (SU 32 only).
18	*	IEF688I	Nullfile and DSNAME conflict in ISAM concatenation.
19	reserved		
20	039C	IEF245I	Inconsistent unit name and volser.
21	0228	IEF474I	Unit or volume in use by system task.
22	4704	IEF253I	Duplicate data set name on direct access volume.
23	4708	IEF254I	Insufficient space in VTOC.
24	470C	IEF193I	Space not obtained because of I/O error.
25	4710	IEF256I	Absolute track not available.
26	4714	IEF257I	Space requested not available.
27	4718	IEF258I	Invalid record length in SPACE parameter.
28	*	IEF260I	Incorrect DSORG or DISP.
29	*	IEF261I	No prime area request for ISAM data set.
30	*	IEF262I	Prime area must be requested before overflow area.
31	*	IEF263I	Space request not on cylinder boundary.
32	*	IEF264I	Duplication of DSNAME element.
33	4734	IEF266I	Invalid JFCB or partial DSCB pointer.
34	4738	IEF140I	Directory space request too large.
35	reserved		
36	4740	IEF273I	Invalid user label request.
37	reserved		
38	474C	IEF127I	No SPACE parameter or zero space request at ABSTR 0.
39	*	IEF128I	Invalid request for ISAM index.
40	*	IEF129I	Multivolume index request.
41	*	IEF130I	DSNAME element wrong.
42	*	IEF131I	Multivolume OVFLOW request.
43	*	IEF132I	CYL and ABSTR conflict in SPACE parameter.
44	*	IEF133I	CYL and CONTIG conflict in SPACE parameter.
45	*	IEF134I	Subparameter wrong in SPACE parameter.
46	476C	IEF135I	Zero primary space request.
47	*	IEF136I	Index area requested twice.
48	4780	IEF267I	Space request for directory larger than primary space request.
49	*	IEF145I	Space request not ABSTR for DOS volume.
50	*	IEF141I	Index request did not precede prime request.
51	*	IEF143I	Last concatenated DD card unnecessary or invalid.
52	035C	IEF366I	Relative GDG generation number contains syntax error.

* — means that the error cannot be set in dynamic allocation.

Reason Code	Dynamic Allocation Error Reason Code	Message	Meaning
53	0390	IEF219I	GDG group name exceeds 35 characters.
54	0394	IEF286I	DISP field incompatible with data set name.
55	*	IEF466I	Unable to recover from DADSM failure.
56	0218		Mounting required but not allowed.
57	0494	IEF704I	Can't access SYSCATLG data set on CVOL.
58	022C	IEF475I	Volume on ineligible permanently resident or reserved device.
59	0214	IEF467I	Units required not available – waiting not allowed.
60	0220	IEF485I	Volumes required not available – waiting not allowed.
61	4794	IEF476I	Data sets overlap in VTOC.
62	4798	IEF477I	DOS split cylinder data sets overlap.
63	479C	IEF478I	Possible VTOC error.
64	*	IEF479I	VTOC error on second or later volume of ISAM prime data set.
65	*	IEF481I	Same unit request twice – conflicts exist.
66	0230	IEF482I	Permanently resident or reserved volume on requested unit.
67	0488	IEF217I	Volume containing pattern DSCB not mounted.
68	048C	IEF218I	Pattern DSCB record not found in VTOC.
69	47A4	IEF703I	New data set requested on DOS stacked pack format volume.
70	0214		Can't wait for offline devices.
71	0240	IEF483I	Requested device is a console.
72	04B8	IEF726I	MSS not initialized.
73	04BC	IEF725I	MSS select error.
74	0234	IEF484I	More units required for demand request.
75	*	IEF493I	Invalid JOBCAT or STEPCAT parameters.
76	*	IEF492I	Invalid data set name for JOBCAT or STEPCAT.
77	*	reserved	
78	0470		Unauthorized requestor of subsystem data set.
79	046C	IEF480I	Invalid destination requested.
80	*	reserved	
81	0490	IEF701I	Error changing allocation assignments.
82	17FF	IEF213I	Error processing cataloged data set.
83	022C	IEF687I	Requested volume mounted on JES3 manager unit (JES3 release 3.0).
84-94	*	reserved	
95	04B4	IEF740I	Data set/volume could not be RACF protected – RACF not active (SU 32 only).
96	03A4	IEF741I	Protect request failed – invalid data set/volume specification (SU 32 only).

The following are step-related error reason codes set by allocation and JFCB housekeeping modules in an area pointed to by the allocation work area (ALCWA). With the exception of reason code 1, the reason codes serve as an index into message module IEFBB4M2. The prologue of IEFBB4M2 lists the modules which detect the error condition. Reason code 1 is set by IEFAB469 and is returned to dynamic allocation.

Reason Code	Dynamic Allocation Error Reason Code	Message	Meaning
1	023C		Catalog not mounted.
2	0204	IEF180I	GETMAIN error.
3	0220	IEF713I	MSS volume not available.
4	*	reserved	
5	0484	IEF251I	Job cancelled.
6	0238	IEF240I	No space in TIOT.
7	0220	IEF485I	Volumes not available and waiting not allowed.
8	049C	IEF714I	MSS volume not defined.
9	0474	IEF473I	System Resources Manager error.
10	0248	IEF716I	Unable to mount MSS volume.
11	0450	IEF491I	Number of DDs exceeds 1635.
12	172C	IEF363I	Not enough storage for processing cataloged data set.
13	1718	IEF364I	Permanent I/O error processing cataloged data set.
14	670C	IEF367I	I/O error obtaining pattern DSCB.
15	0478	IEF465I	Unable to allocate subsystem data set.
16	047C	IEF456I	Error issuing ESTAE macro.
17	0214	IEF700I	Environment changed – no longer able to allocate.
18	0490	IEF701I	Error changing allocation assignments.
19	0468	IEF361I	Unable to allocate private catalog.
20	*	IEF362I	Unable to unallocate private catalog.
21	*	IEF202I	Stop not run because of COND=ONLY.
22	*	IEF202I	Stop not run because of condition codes.
23	0498	IEF715I	MVS volume inaccessible.
24	04A0	IEF717I	Specified virtual volume group (VVGRP) name does not exist.
25	*	IEF718I	Space or virtual volume group (VVGRP) required for nonspecific MSS request.

* – means that the error cannot be set in dynamic allocation.

Common and Batch Unallocation Reason Codes

The following reason codes are set by common and batch unallocation modules. Reason codes 1, 2, and 4 serve as an index into message module IEFBB4M5. Reason code 3 does not result in a message; it is returned to dynamic allocation.

Reason Code	Message	Meaning	Module Setting
1	IEF468I	GETMAIN error.	IEFBB410, IEFBB414, IEFBB416, IEFAB4A0
2	IEF469I	Data sets not released.	IEFAB4A0, IEFAB4A6
3	------	Volumes not released. (Dynamic allocation only).	IEFAB4A0, IEFAB4A8
	IEF724I	Step catalogs not allocated. (Warm start only).	IEFAB4A2
4	IEF456I	Error issuing ESTAE macro.	IEFBB410, IEFAB4A0

In addition, IEFAB4A2 (Disposition Processor) receives return codes returned by the data management catalog and scratch functions (called by IEFAB4A2 to perform disposition processing). If the allocation is dynamic, these return codes are returned to dynamic allocation as reason codes in a field in the unallocation request block. For batch allocation, the return code is converted to a code for a disposition message.

Dynamic Allocation Reason Codes

Reason codes set by dynamic allocation modules are four bytes long and include two fields – a two-byte error reason code followed by a two-byte information reason code. Error reason codes are divided into error classes; the second hexadecimal digit of the error reason code defines the class, as follows:

class 1 – reserved
class 2 – unavailable system resource
class 3 – invalid parameter list
class 4 – environment error
class 5 – reserved
class 6 – reserved
class 7 – system routine error.

Information reason codes convey additional information about the error, or, if the function was successful, information about a special situation. Either field (error reason code or information reason code) might be zero.

All dynamic allocation reason codes are hexadecimal.

Dynamic Allocation Information Reason Codes

Reason Code	Meaning	Dynamic Functions Related to Reason Code
0004	Reserved.	
0008	Overriding disposition ignored.	Unallocation
000C-001C	Reserved.	
002w	Data set was successfully unallocated but completion of the requested CATLG or UNCATLG disposition was unsuccessful. The digit "w" is a code representing the reason for the failure. The meaning of each possible code is the same as that for the "w" in message IEF287I.	Unallocation

Reason Code	Meaning	Dynamic Functions Related to Reason Code
003x	Data set was successfully unallocated but completion of requested DELETE disposition was unsuccessful, or in-use attribute removed from data set but VIO paging space not released. The digit "x" is a code representing the reason for the failure. The meaning of each possible code is the same as that for the "x" in message IEF283I.	Unallocation

Dynamic Allocation Error Reason Codes

Reason Code	Meaning	Dynamic Functions Related to Reason Code
Class 2		
0204	Storage unavailable.	dsname allocation
0208	Reserved.	
020C	Request for exclusive use of a shared data set cannot be honored.	dsname allocation
0210	Requested data set unavailable. The data set is allocated to another job and its usage attribute conflicts with this request.	dsname allocation
0214	Unit(s) not available.	dsname allocation
0218	Specified volume not mounted, and user does not have volume mounting authorization.	dsname allocation
021C	Unit name specified is undefined.	dsname allocation
0220	Requested volume not available.	dsname allocation
0224	Eligible device types do not contain enough units.	dsname allocation
0228	Specified volume or unit in use by system.	dsname allocation
022C	Volume mounted on ineligible permanently resident or reserved unit.	dsname allocation
0230	Permanently resident or reserved volume on required unit.	dsname allocation
0234	More than one device required for a request specifying a specific unit.	dsname allocation
0238	Space unavailable in Task Input/Output Table (TIOT).	dsname allocation, concatenation
023C	Required catalog not mounted, and user does not have volume mounting authorization.	dsname allocation
0240	Requested device is a console.	dsname allocation
0248	Unable to mount volume for 3850 Mass Storage System (MSS).	dsname allocation
Class 3		
0304-0354	Reserved	
0358	Overriding disposition of DELETE invalid for data set allocated as SHR.	unallocation
035C	Invalid PARM specified in text unit.	all functions
0360	Invalid KEY specified in text unit.	all functions
0364	JOBLIB/STEPLIB/JOBCAT/STEPCAT specified as ddname, or associated with specified dsname.	dsname allocation, ddname allocation, unallocation, concatenation, deconcatenation
0368	Authorized function requested by unauthorized user.	all functions
036C	Invalid parameter list format.	all functions
0370	Reserved.	
0374	Invalid number specified in text unit.	all functions
0378	Duplicate KEY specified in text unit.	all functions
037C	Invalid LEN specified in text unit.	all functions
0380	Mutually exclusive KEY specified in text unit.	dsname allocation, unallocation, information retrieval, remove In-Use

Reason Code	Meaning	Dynamic Functions Related to Reason Code
Class 3 (contd)		
0384	Mutually inclusive KEY not specified.	unallocation, dsname allocation
0388	Required key not specified.	ddname allocation, information retrieval, concatenation, deconcatenation, remove In-Use, unallocation
038C	Duplicate ddnames specified for concatenation.	concatenation
0390	GDG group name specified with relative generation number exceeds 35 characters.	dsname allocation
0394	Status and relative generation number are incompatible.	dsname allocation
0398	Volume sequence number exceeds the number of volumes.	dsname allocation
039C	Device type and volume are incompatible.	dsname allocation
03A4	Unable to RACF Protect data set/tape volume because of conflicting key specification.	dsname allocation
Class 4		
0404-040C	Reserved.	
0410	Specified ddname unavailable.	dsname allocation, ddname allocation
0414-041C	Reserved.	
0420	Specified ddname associated with an open data set.	ddname allocation, concatenation, deconcatenation, unallocation, dsname allocation
0424	Deconcatenation would result in duplicate ddnames.	deconcatenation
0428-0430	Reserved.	
0434	Ddname specified in ddname allocation request is associated with a convertible or non-permanently allocated resource.	ddname allocation
0438	Specified ddname not found.	information retrieval, ddname allocation, concatenation, deconcatenation, unallocation
043C	Resources could not be unallocated to decrease the number of resources held in anticipation of reuse; control value exceeded.	dsname allocation
0440	Specified dsname not found.	information retrieval, unallocation
0444	Relative entry number specified in information retrieval request not found.	information retrieval
0448	Data set requested NEW found allocated	dsname allocation
044C	Existing data set request; data set found allocated as eligible for deletion.	dsname allocation
0450	Request would cause the limit of 1635 concurrent allocations to be exceeded.	dsname allocation
0454	Ddname in DCB reference not found.	dsname allocation
0458	Dsname in DCB reference or volume reference is a GDG group name.	dsname allocation
045C	Specified dsname to be unallocated is a member of permanently concatenated group.	unallocation
0460	Specified dsname or member to be unallocated is not associated with specified ddname.	unallocation
0464	Specified dsname to be unallocated is a private catalog.	unallocation

Reason Code	Meaning	Dynamic Functions Related to Reason Code
Class 4 (contd)		
0468	Error while allocating or OPENing a private catalog.	dsname allocation
046C	Remote work station not defined to Job Entry Subsystem.	dsname allocation, unallocation
0470	User unauthorized for Job Entry Subsystem request.	dsname allocation
0474	Error while attempting to select optimum device.	dsname allocation
0478	Unable to process Job Entry Subsystem request.	dsname allocation, unallocation
047C	Unable to establish ESTAE environment.	dsname allocation
0480	The number of units needed to satisfy the request exceeds the limit.	dsname allocation
0484	Request denied by operator.	dsname allocation
0488	GDG pattern DSCB not mounted.	dsname allocation
048C	GDG pattern DSCB not found.	dsname allocation
0490	Error changing allocation assignments.	dsname allocation
0494	Error processing OS CVOL.	dsname allocation
0498	MSS volume inaccessible.	dsname allocation
049C	MSS volume not defined.	dsname allocation
04A0	Specified virtual volume group (VVGRP) name does not exist.	dsname allocation
04B4	Protect not processed, RACF not active or not in system.	dsname allocation
04B8	MSS not initialized for allocation	dsname allocation
04BC	MSS volume select error	dsname allocation
Class 7	(zz in these codes is the return code returned by the failing system routine.)	
17zz	LOCATE error. (Note: Hexadecimal '08', '18', and '2C' are the only expected LOCATE return codes. 'FF' is returned as the value of zz if an unexpected return code is returned by LOCATE.)	dsname allocation
27zz	Reserved.	
37zz	Reserved.	
47zz	DADSM error.	dsname allocation
57zz	CATALOG error.	dsname allocation
67zz	OBTAIN error.	dsname allocation, information retrieval

Notes:

For error reason codes 358, 364, 420, 424, 45C, and 464, the information reason code field will be 0004 if an occurrence of a specified data set has been unallocated, although an error was encountered processing another occurrence of the data set, as indicated in the error reason code field.

For error reason codes 35C, 360, 374, 378, 37C, 380, and 384, the information reason code field will contain the value of the text unit KEY causing the error.

For the error reason code of 04BC, the information reason code field will contain the MSS error information code.

Real Storage Management ABEND Reason Codes

The following reason codes are put into the RCARCRD field when Real Storage Management issues ABEND with a code of C0D.

Code (hex)	Meaning
01	Findpage, Translate Real to Virtual, or the LRA instruction returned an unexpected code for a segment, page, or frame whose existence was implied by some RSM control block or function. Findpage, Translate Real to Virtual, or LRA is assumed to be correct.
02	A GETCELL or FREECELL for the RSM cell pool failed. If FREECELL, the error is ignored; if GETCELL, asynchronous retry is attempted where possible.
03	A FREEMAIN failed for space originally obtained by RSM or VSM using GETMAIN. The error is ignored.
04	The return code from ASM (ILRSWAP or ILRPAGIO) indicates an invalid request. The recovery action taken by RSM varies with the type of request, but the RSM function being performed is usually terminated if ASM resources were being requested, or continued if ASM resources were being returned.
05	A GETMAIN for RSM control block space was unsuccessful. The function for which the space was required is terminated.
06	An attempt was made to release a lock which was not held. RSM tables may be damaged due to the loss of serialization. RSM attempts to continue normal operation.
07	RSM control information indicated a PCB for a page should exist on an I/O Active Queue or on the Defer Queue, but searching of the queue(s) failed to find the PCB. It is assumed the control information is in error and no such PCB exists.
08	The existence of a V=R or Offline root PCB was implied but no appropriate PCB could be found on the V=R or Offline root queue. The error is ignored and indicators are reset.
09	Swap-in's XMPOST error exit was entered, so Restore will not run. The target address space is terminated.
0A	An incorrect fix count was detected in a PFTE. The count is adjusted to the expected value.
0B	The Interprocessor Communication service routine (RISIGNL) could not signal another CPU as requested by IEAVINV. The condition is ignored and normal operation continues.
0C	IEAVPIOP has discovered an undefined combination of I/O completion status flags in the AIA after a page-in or page-out. The condition is treated as an I/O error.
0D	IEAVDSEG was requested to destroy a non-existent or common area segment. The request is denied.
0E	A PCB was required but none were available. The routine needing a PCB is terminated.
0F	The attempt to complete processing of a previously deferred FREEMAIN Release has failed.
10	An FOE could not be found on the specified TCB's Fix Ownership List.
11	An internal RSM invocation of the PGOUT function was unsuccessful. The page remains in real storage.
12	A swap (in or out) was requested for an address that already has a swap in progress or no SPCT exists for the address space to be swapped. The request is denied.
13	Swap-In could not re-establish the address space due to missing or incorrect control information (SPCT or PCBs). The address space is abnormally terminated.

Code (hex)	Meaning
14	An internal invocation of PGFREE failed. The error is ignored.
15	Swap-Out has detected an inconsistency in the SPCT fix entries it has created. The error is suppressed and recovery attempted.
16	ASCBCHAP could not enqueue or dequeue an ASCB during a Swap-in or Swap-out operation. The address space is terminated.
17	Swap-out has detected an error in the allocated frame count (ASCBFMCT) for the address space. If possible, the count is corrected and the swap-out continued; otherwise, the swap-out is suppressed.
18	No SPCT segment entry could be found for a segment whose existence was implied by other RSM control information. The error is ignored and the SPCT update is skipped.
19	An internal RSM function issued a return code which was either invalid or not applicable. System action depends on the nature of the function.
1A	Swap-in detected a common area page that was not obtained using GETMAIN among the input working set. The page is not made available to the incoming address space. Some other address space must have freed the page using FREEMAIN while the current one was swapped-out. Probable user error.
1B	A one-to-one match does not exist between virtual and real addresses during the attempt to free the frames of a V=R region.
1C	IEAVPSI attempted to fix the ECB for a page service that will complete asynchronously, but IEAVFXLD returned a code indicating the fix was not accomplished.
1D	A PCB that has already been marked I/O complete indicating that it was previously processed by IEAVPIOP has been passed to IEAVPIOP from ASM.
1E	A software error in the AIA passed from ASM to RSM for an I/O request has been found. Either the AIA contains data inconsistent with previous AIAs on the original input chain to ASM or the LSID or LPID in the XPTE was invalid. Also, a hardware I/O error could have occurred to a page-out PCB.

Additional ASM Data Areas

The following four ASM data areas are not contained in *OS/VS2 Data Areas,* SYB8-0606. For debugging ASM, BSHEADER (bad slot record) may be especially helpful.

BSHEADER

Acronym: BSHEADER

Full Name: ASM error record (bad slots).

Macro ID: None.

Size: 1024 bytes.

Function: Trace table of the last 253 slots that ASM has found to be bad. Patterns of bad LSIDs can indicate where and what paging data sets are having difficulties.

Location: Pointed to by ASMVT (ASMEREC).

Offset	*Length*	*Name*	*Description*
0 (0)	4	BSCURR	Current bad slot entry filled.
4 (4)	4	BSFIRST	Beginning address of table.
8 (8)	4	BSLAST	End address of table.
12 (C)	1012	BSLIST	253 four-byte bad slot identifiers (LSIDs).
BSLIST entry			
0 (0)	1	BSFLAG	
1...		BSSPLSID	if 1, LSID entry is swap. if 0, LSID entry is page.
.... 1...		BSRDLSID	if 1, LSID entry is for a read error. if 0, LSID entry is for a write error.
1 (1)	3	BSTABNTY	LSID that is bad.

BUFCONBK

Acronym: BUFCONBK

Full Name: VSAM Buffer Control Block.

Macro ID: None.

Size: 12 bytes.

Function: Queue VIO Group operation for later processing until VSAM resources are available.

Location: Pointed to by ASMVT (ASMGOSQS).

Offset	*Length*	*Name*	*Description*
0 (0)	4	BUFCHAIN	Pointer to next BUFCONBK.
4 (4)	4	BUFASCB	Pointer to ASCB.
8 (8)	4	BUFACE	Pointer to ACE.

DSNLIST

Acronym: DSNLIST.

Full Name: Data Set Name List (ASM).

Macro ID: None.

Size: 44 times number of possible page/swap data sets. There are two DSNLISTs, one for page data sets and one for swap data sets.

Function: Make data set names available in non-fixed (pageable) storage.

Location: Pointed to by PART (PARTDSNL) for page data sets, and by SART (SARDSNL) for swap data sets.

Offset	*Length*	*Name*	*Description*
0 (0)	44	DSNENTRY	Data set name left-justified and padded with blanks.

MSGBUFER

Acronym: MSGBUFER.

Full Name: ASM message buffer.

Macro ID: None.

Size: 376 bytes.

Function: Ensure that WTOR with LOGREC request will have a buffer to use.

Location: Pointed to by ASMVT (ASMMSGBF).

Offset	*Length*	*Name*	*Description*
0 (0)	4	MSGCURR	Pointer to current buffer used.
4 (4)	4	MSGFIRST	Pointer to first buffer.
8 (8)	4	MSGLAST	Pointer to last buffer.
12 (C)	4	MSGTERM	Pointer to special termination buffer.
16 (10)	240	MSGBFRS	Three 80-byte buffers.
256 (100)	120	MSGTBFR	Special termination buffer.

ASPCT and Locating LSIDs of VIO Data Sets

Locate ASPCT

The ASCB (of the desired address space) points to the RSMHD. Included in RSMHD is the ASMHD. ASHLGEQ in ASMHD is the queue of LGEs (active VIO data sets) related to this address space. LGEASPCT in the LGE is the address of the ASPCT for this VIO data set.

ASPCT Expansion

The ASPCT is used to record the auxiliary storage locations (LSIDs) of VIO data set pages. Only a 1088 byte base ASPCT is created at ASSIGN LGN time. This ASPCT can handle up to 1 megabyte of VIO data set space. If more than 1 megabyte of VIO space is used, the ASPCT is expanded as follows:

1. For each 256 megabytes of space up to 1 billion bytes, an ASST extension is built.
2. For each megabyte of space, a LMPE extension is built.

Each ASST or LPME extension requires 1088 bytes of storage. Each ASST extension contains a vector table of LPME extension addresses. The ASPCT (base and all extensions) resides in the LSQA of the associated address space.

Locating an LSID from an LPID

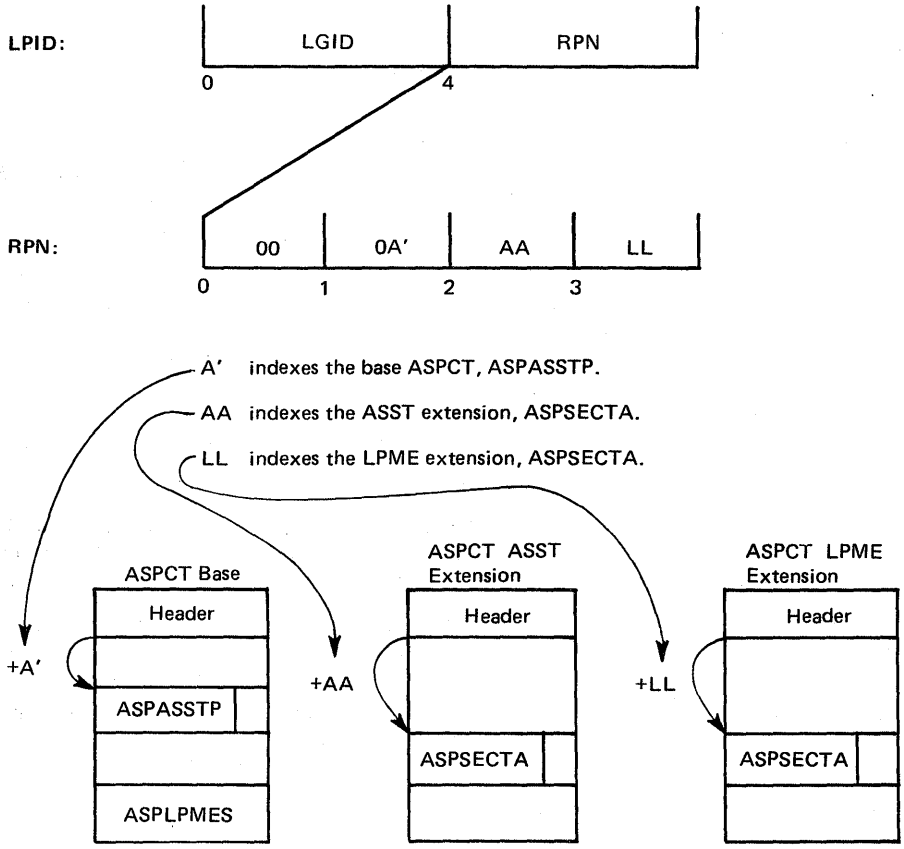


Figure 6-4. Locating An LSID From An LPID

Figure 6-4 illustrates the pointers and control blocks discussed below.

The LPID is 8 bytes. The first 4 bytes contain an LGID, LG (VIO data set) identifier. The last 4 bytes contain an RPN, relative page number.

Searching through the address space's ASHLGE queue, one of the LGEs will have an LGELGID field that matches this LGID. This same LGE has the address of the needed ASPCT (LGEASPCT).

Another way to locate an ASPCT from an LGID is to follow the CVT to the LGVT (CVTASMVT, ASMLGVT). Using the LGID as an index, locate the appropriate LGVT entry. The LGVT entry contains the address of the LGE that contains the address of the needed ASPCT.

With the appropriate ASPCT, now use the RPN portion of the LPID as an index to locate the LPME containing the associated LSID.

If A' and AA are both zero, use the LL to index ASPLPMES in the ASPCT base for the LPME containing the LSID.

Otherwise, use A' to index ASPASSTP for the address of the appropriate ASST extension. Use AA to index the ASPSECTA of the ASST extension for the address of the appropriate LPME extension. And use LL to index the ASPSECTA of the LPME extension for the LPME containing the LSID.

The LSID is the slot identifier for this page of the VIO data set. This LSID can be related to the ASM control blocks PART and PAT and to the actual paging device. See "Relating A Virtual Address to the PART, PAT and DEVICE", the next section.

Relating A Virtual Address to the PART, PAT, and DEVICE

Given a virtual address = 07A12C:

segment number is 07

page number is A

location within page is 12C.

Locate XPTE

Obtain the current ASCB or desired ASCB (memory) in which the problem occurred. Finding the segment tables (SGT), take the segment number and multiply it by the length of SGTE to locate the appropriate segment table entry. The real address of the PGT, page table for this segment, is in this SGTE. Convert this address to a virtual address, then calculate the address of the XPTE as follows:

Address of XPTE for this page = (virtual address of PGT) + 16 • (length PGTE) + page number • (length XPTE)

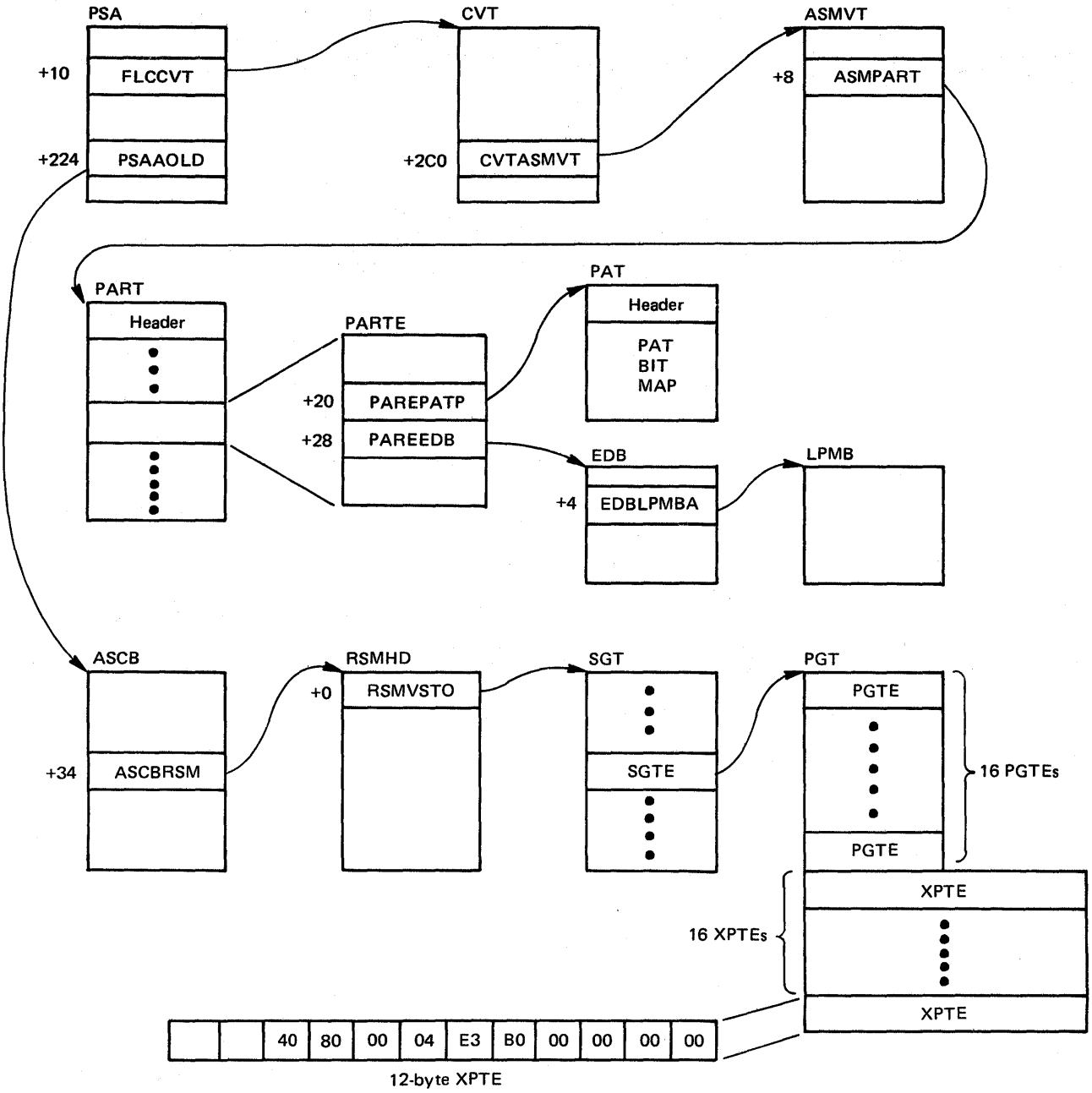


Figure 6-5. Relating the Virtual Address to the PART and PAT

Inside the XPTE is information about this page. If either XPTVALID or XPTVIOLP flags are on, there is a slot for this page. If XPTVALID is on, the LSID (slot identifier) is in the XPTE. If the page is duplexed, two LSID's will be in the XPTE (one for each slot). If XPTVIOLP flag is on, instead of an LSID, a LPID is in the XPTE. To relate the LPID to an LSID, see "ASPCT and Locating LSIDs of VIO Data Sets" Diagnostic Aids Section.

Locate PAT Bit

Suppose LSID 0004E3B0 was found in the XPTE that represents the sample address 07A12C:

PART entry index is 04.

Relative byte address (RBA) is E3B0.

The PART has one entry for each page data set, each having a pointer to its PAT. The PAT is a cylinder bit mapping of this page data set. PATCYLMW is the number of words that map a cylinder. PATCYLSZ, slots per cylinder, is the number of significant bits in each cylinder mapping.

For device 2305-2:

PATCYLMW is 1

PATCYLSZ is 1A (26).

To locate the bit in the PAT map for slot E3B0(58288):

1. address of map word = (address of PATMAP) + 8964 = (address of PATMAP) + (58288/26) • PATCYLMW • (bytes in a word)

2. bit in the map word (origin 0) = 58288//26 = 22.

Calculate MBBCCHHR For Device 2305-2

(Reference IDAEDB and IDALPMB macros for fields) M = extent number = 0
RBA = $E3B0_{16}$ = 58288_{10} BB = return code = 00

Relative CC = (RBA – EDBLORBA)/LPMAUSZ
= 58,288 – 0/53,248 = 1

Relative HH = (RBA – EDBLORBA)//LPMAUSZ/LPMBPTRK
= 5040/13,312 = 0

CC = (Rel CC * LPMTRRAU + Rel HH + EDBSTTRK)/LPMTPC
= (1 * 4 + 0 + 8)/8 = 1st cylinder

HH = (Rel CC * LPMTRRAU + Rel HH + EDBSTTRK)//LPMTPC
= 12//8 = 4th track

R = [((((RBA – EDBLORBA)//LPMAUSZ)//LPMBPTRK) +
LPMBLKSZ–1)/LPMBBLKSZ] +1

= ((5040 + 4095)/4096) + 1 = 3rd record

∴ MBBCCHHR = 0000000001000403

COD ABEND Meanings for ASM

An ASM routine has found one of the following conditions which should not occur:

RC 4 – The count of available swap sets for a specific swap data set is non-zero but no available swap sets could be found.

RC 8 – The total count of available swap sets is non-zero but none of the swap data sets contain available swap sets.

RC 12 – The group operations starter has returned from one of the group operators but the ACE is not the first one on the LGE queue.

RC 16 – The memory termination resource manager for ASM has found that LSQA is not available for a memory abnormally terminating for one of the following reasons:

1. memory is not swapped in
2. memory is in process of being swapped in
3. RSMLSQA frame queue is unusable.

RC 20 – The ASM SRB controller has found an AIA or ACE on the LGE process queue which does not have the LPID converted flag on.

System Action: A software error record is written to SYS1.LOGREC and processing continues.

Operator Response: None.

Programmer Response: Probable system error, notify your IBM programming support representative.

ASM Recovery Control Blocks

ASM Tracking Area (ATA)

The ATA contains information necessary for the recovery or cleanup processing performed by the ASM recovery routines. The ATA is mapped to the six word work area returned by SETFRR when an FRR is established. For task mode routines, the ATA is mapped to the parameter area that is passed via the ESTAE macro.

The mapping macro name is: ILRATA.

Disp	*Name*	*Size*	*Description*
0	ATA	24	ASM Tracking Area
0	ATAMODID	1	ID of module establishing recovery routine.
	ATAMPGIO	01	ILRPAGIO module ID.
	ATAMPGCM	02	ILRPAGCM module ID.
	ATAMSWAP	03	ILRSWAP module ID.
	ATAMTRPG	04	ILRTRPAG module ID.
	ATAMSWPD	05	ILRSWPDR module ID.
	ATAMGOS	06	ILRGOS module ID.
	ATAMPTM	07	ILRPTM module ID.
	ATAMSRBC	08	ILRSRBC module ID.
	ATAMCMPD	09	ILRCMPDI module ID.
	ATAMCMPN	0A	ILRCMPNE module ID.
	ATAMCMPA	0B	ILRCMPAE module ID.
	ATAMCMP	0C	ILRCMP module ID.
1	ATASFLGS	3	Bit map representing logical sections of ASM routines; set to 1 on entry, set to 0 on exit.
	ATAQIOE	800000	ILRQIOE flag.
	ATASLSQA	400000	ILRSLSQA flag.
	ATASCOMP	200000	SWAPCOMP flag.
	ATAVIOCM	100000	ILRVIOCM flag.
	ATAPCOMP	080000	PAGECOMP flag.
	ATAPOS	040000	ILRPOS flag.
	ATAPAGIO	020000	ILRPAGIO flag.
	ATAPAGCM	010000	ILRPAGCM flag.
	ATASWAP	008000	ILRSWAP flag.
	ATATRPAG	004000	ILRTRPAG flag.
	ATASWPDR	002000	ILRSWPDR flag.
	ATASRT	001000	ILRSRT flag.

The remaing flags are reserved:

ATARFLGS	2	Other recovery flags.
ATACNVRT	8000	ILRSLSQA flag-converting between forward chained AIA's and lateral chained ATA's.
ATASGNST	4000	ILRSLSQA flag-in ASSIGNSET subroutine.
ATASCCWP	2000	ILRSLSQA flag-in SCCWPROC subroutine.
ATABADPK	1000	ILRCMPAE flag-in BADPACK subroutine.

The remaining flags are reserved:

6	ATARCRSN	1	Recursion flags.
	ATARCRF1	80	Recursion flag-function 1.
	ATARCRF2	40	Recursion flag-function 2.
	ATARCRF3	20	Recursion flag-function 3.
	ATARCRF4	10	Recursion flag-function 4.
	ATARCRF5	08	Recursion flag-function 5.
	ATARCRF6	04	Recursion flag-function 6.
	ATARCRF7	02	Recursion flag-function 7.
	ATARCRF8	01	Recursion flag-function 8.
7	ATARCODE	1	Reason code for ASM-issued ABEND's.

Disp	*Name*	*Size*	*Description*

The mapping of the remaining four words is dependent on the recovery routine involved.

For the recovery routine ILRIOFRR:

8	ATAWORDS	16	Maximum size of four-word area.
8	ATAAIA	4	Address of in-process AIA.
8	ATAACE	4	Address of in-process ACE.
C	ATAASCB	4	Address of in-process ASCB, or TRAS'd-to address space.
C	ATALGE	4	Address of in-process LGE.
C	ATAAIAQ	4	Address of AIA queue.

For the recovery routine ILRSWP01:

8	ATACLEAR	16	Definition allowing next four words to be cleared.
8	ATAAIA	4	Address of in-process AIA.
C	ATASARTE	4	Address of SART entry.
10	ATASCCW	4	Address of in-process SCCW.
14	ATAIORB	4	Address of in-process IORB.

For the recovery routine ILRGOS01:

8	ATAWORKA	4	Address of work-area cell.
C	ATAEPATH	4	Address of EPATH.

For the recovery routine ILRSRT01:

8	ATAWORKA	4	Address of PTM work-area cell.
C	ATAEPATH	4	Address of EPATH.

For the recovery routine ILRSRB01:

8	ATAAIACE	4	Address of in-process AIA/ACE.
C	ATAAIAQ	4	Address of AIA queue.
10	ATAACEQ	4	Address of ACE queue.
14	ATAEPATH	4	Address of EPATH.

For the recovery routine ILRCMP01:

8	ATAIOSB	4	Address of in-process IOSB.
C	ATAPCCWQ	4	Queue of PCCWs to be put back on PCCW available queue.
10	ATACOMPQ	4	Queue of AIAs to be returned to ILRPAGCM.
14	ATACPCCW	4	Address of in-process PCCW, not on IORB queue and not on ATAPCCWQ.

Disp	*Name*	*Size*	*Description*
For the recovery routine ILRJTM01:			
8	ATASAVE	4	Address of register save area.
8	ATAACEQ	4	Address of ACE queue.
For the recovery routine TERMRFRR:			
8	ATARMPL	4	Address of RMPL, resource manager parameter list.
C	ATAWORKA	4	Address of work-area.

Recovery Audit Trail Area (EPATH)

The EPATH is a communication area between the mainline routine and its corresponding recovery routine. The EPATH is necessary when the 6 word ATA is not large enough to accomodate the data to be tracked. The mapping of the EPATH is dependent on the recovery routine or mainline routine including the macro.

EPATH for ILRPTM, ILRSRT, and recovery routine ILRSRT01:

Disp	*Name*	*Size*	*Description*
0	EPAPARM	4	Address of parameter list.
4	EPAIOEIP	4	Address of IOE currently being processed.
8	EPAIOEQP	4	Address of first IOE on 'WORK' read IOE queue.
C	EPAFFIOE	4	Address of first IOE on free IOE internal queue.
10	EPALFIOE	4	Address of last IOE on free IOE internal queue.
14	EPAWRTQ	4	Address of write queue from which last write IOEs removed.
18	EPAWTPAT	4	Address of SCYLWRT which is used to update current CYL Map.
1C	EPACYLA	4	Address of current CYL Map.
20	EPAMSPAD	4	Address of 2 word parameter list for ILRMSG00. Also serves as a switch for ILRPTM.
24	EPAWRTCT	2	Number of writes prepared for current CYL.
26	EPACPUID	2	CPU locking count for current Part Monitor processing.

EPATH for VIO Group Operators and their recovery routines – ILRGOS, ILRSAV, ILRRLG, ILRACT, ILRVSAMI, ILRGOS01, ILRTMRLG, ILRTMI00, ILRTMI01, ILRSRBC, and ILRSRB01. ILRGOS01 is the recovery routine for ILRGOS which calls ILRSAV, ILRRLG, and ILRACT which call ILRVSAMI. ILRTMI01 is the recovery routine for ILRTMRLG which calls ILRVSAMI and ILRTMI00. ILRSRB01 is the recovery routine for ILRSRBC which calls ILRRLG. The first section is common because of the use of ILRVSAMI. The second section is dependent on the recovery routine involved.

Disp	*Name*	*Size*	*Description*
0	EPAOWKA	4	Group Operator's or ILRTMRLG's work-area address.
4	EPAVWKA	4	ILRVSAMI workarea address also points to RPL in workarea.
4	EPATMWKA	4	ILRTMI00 workarea address.
4	EPASWKA	4	ILRSRBC workarea address.
8	EPAAASP	4	Address of active ASPCT.
8	EPADSLST	4	Address of data set name list storage.
C	EPABASP	4	Address of buffer ASPCT.
C	EPATMIBA	4	Base address value for ILRTMI00.
10	EPARASP	4	Address of retrieved ASPCT.
10	EPATMACB	4	Address of storage used to build ACB for STGINDEX in ILRTMI00.
14	EPARTYRG	4	Address of 15 word save area containing retry registers R0-R14 for record-only abends.
14	EPABKSLT	4	Backing slots, only used for assign processing.
18	EPAFLAG1	1	Recovery flags.
	EPAVSAMI	X'80'	ILRVSAMI currently processing.
	EPAGRPOP	X'70'	One of group operators processing.
	EPARLG	X'40'	ILRRLG is currently processing.
	EPASAVE	X'20'	ILRSAV is currently processing.
	EPAACT	X'10'	ILRACT is currently processing.
	EPAACASR	X'08'	Activate or assign request.
	EPAASGN	X'04'	Assign processing – backing slots count (ASMBKSLT) has been updated.
	EPAUNSAV	X'02'	Mark slots unsaved in active ASPCT.
	*	X'01'	Reserved.
19	EPAFLAG2	1	Recovery Flags.
	EPATMXIT	X'80'	ILRTMI00 completed processing.
	EPAWARM	X'40'	ILRTMI00 warm start is processing.
	EPACOLD	X'20'	ILRTMI00 CVIOSTRT is processing.
	EPABUILD	X'10'	ILRTMI00 BUILDSNL is processing.
	EPAMAST	X'08'	Master Scheduler initialization has been posted.
	EPATMI	X'04'	ILRTMI00 is currently processing.
	EPARECUR	X'02'	Recursion indicator for retry into mainline ILRTMRLG.
	*	X'01'	Reserved.

For ILRGOS01, ILRSAV, ILRACT, ILRRLG, ILRSRBC, and ILRSRB01:

Disp	*Name*	*Size*	*Description*
1A	EPALSIZE	2	Size of LGVT expansion.
1C	EPALGVTP	4	New LGVT address for LGVT expansion in ILRGOS.
20	EPALGEP	4	Logical group entry for request being processed.
24	EPASRB	4	Address of SRB for SRB Controller.

Disp	*Name*	*Size*	*Description*
28	EPAACE	4	Address of current ACE being processed.
2C	EPARBASP	4	Address of rebuilt ASPCT (LSQA).
30	EPARSIZE	2	LSQA block storage size for rebuilt ASPCT.
32	*	2	Reserved.

For ILRTMI01 and ILRTMRLG, and ILRTMI00:

Disp	*Name*	*Size*	*Description*
1A	*	2	Reserved.
1C	EPAACE	4	Address of ACE currently being processed.
1C	EPAMSECB	4	Address of Master Scheduler initialization ECB.
20	EPATMRSV	4	Address of ILRTMRLG save area.
24	EPAABEND	4	Retry address for record-only abends.
24	EPATMIRT	4	Current retry address for failure in ILRTMI00.
28	EPATPART	4	Address of TPARTBLE while in ILRTMI00.

ASM Serialization

Serialization of ASM processing is done using the SALLOC and ASM global locks, the local lock of the current address, compare-and-swap (CS) logic and control block queueing.

SALLOC Lock

ASM uses the SALLOC lock to serialize most page and swap processing in I/O control. The I/O control modules interface directly with RSM, the principle user of SALLOC, either as the called routine or the calling routine. The SALLOC is held throughout processing including calls to the VIO ILRPOS and completion routines. The SALLOC is used to serialize most processing of:

XPTEs	– complete coverage.
PCB/AIAs	– complete coverage, except AIA noted below.
SPCTs	– complete coverage.
SART	– complete coverage, except where noted below.
SATs	– complete coverage.

Specific areas of other control blocks serialized by the SALLOC lock are:

ASMVT – Work save areas.
I/O control section fields.
Flags –
ASMDUPLX
ASMNOCWQ
ASMCALLQ
ASMNODPX
ASMPLPAF
ASMCOMMF

ASMVT (continued)	– Non-VIO slot allocated count. Expansion of ASM pools.
ASMHD	– I/O control flags. Swap and page counters. Swap queue ..
ASCB	– Non-VIO slot allocated count.
LGVT	– Available LGVTE queue. Expansion of the LGVT.
PART	– Count of local page data sets.

Modules whose processing is serialized by the SALLOC lock are:

ILRPAGIO	– complete coverage, held by caller.
ILRPAGCM	– complete coverage, obtained at entry.
ILRFRSLT	– complete coverage, except ILRFRSL1 entry point where caller may or may not hold the lock. The lock is not obtained by this module, held only if by caller.
ILRSWAP	– complete coverage, held by caller.
ILRPTM	– only obtained to process data set full conditions for non-local page data sets.
ILRCMP	– only obtained to process I/O completion error conditions that may require operator notification.
ILRMSG00	– complete coverage for main entry point, held by caller.
ILRPOS	– complete coverage, held by caller.
ILRVIOCM	– complete coverage, held by caller.
ILRGOS	– only obtained for LGVT processing and GETMAIN/FREEMAIN requests.
IRLPGEXP	– only obtained to adjust the SART to reflect addition of a new swap set data and update the count of local page data sets on the PART .
ILRTERMR	– obtained when referencing above control blocks.
ILRPEX	– obtained when expanding an ASM pool.

ASM Class Locks

The ASM lock is a global spin class lock. A lockword must be provided when obtaining or releasing an ASM class lock. A class lock exists for each active address space. The lockword is in the ASMHD. It is used by the VIO controller modules. A class lock is also defined for the PART write queues with its lock word in the PART header. This lock serializes the four FIFO IOE write queues in the PART. The address space class locks serialize processing of the following control blocks:

AIA	– VIO Controller flags, LPID field.
ASMHD	– VIO Controller flags, LGE queue base pointer.
ASCB	– VIO slot allocation count.
LGE	– complete coverage.
ACE	– complete coverage.
ASPCT	– complete coverage while group operations are in progress. Group operations and page operations can be executed in parallel. VIO controller processing of the LGE process queue provides this serialization.

The address space class locks serialize processing in the following modules:

ILRGOS	– partial, obtained where processing above control blocks.
ILRPOS	– complete coverage.
ILRSRBC	– partial, obtained when searching LGE queue and LGE process queues.
ILRVIOCM	– complete coverage.
ILRJTERM	– partial, obtained when adding ACEs to LGE process queue.

Local Lock of Current Address Space

The local lock is used by VIO Controller and VIO Group Operator modules to serialize certain VIO related operations. It is used by ILRGOS (held on entry) and ILRJTERM (obtained) to serialize Release LG requests with the internal ASM Deactivate function used to clean up VIO logical groups for a terminating job. The local lock is also used by most VIO-related modules to allow use of branch entry GETMAIN, rather than the SVC route.

Compare and Swap (CS) Serialization

Certain modules of ASM run without locks, requiring CS serialization of pointers, flags, and counts. Where routines running with the locks change fields used by unlocked routines, CS must be used. The I/O subsystem and VIO group operators run unlocked and are the principle users of compare and swap. Control blocks serialized via CS include:

PART — a special CS lock exists for each PARTE controlled by PART monitor. This lock is used mainly for execution control. Most fields are still serialized by CS. The IOE write queues are the exception described above.

PATs — complete coverage .

ASMVT — I/O subsystem and Group operator sections.
I/O error count.
unreserved slot count.
pool controllers.

SART — A special CS lock exists in each SARTE to serialize swap driver processing of the swap data sets. Other fields updated by swap driver or I/O completion processing of the I/O subsystem are updated with CS.

The ASM modules that run without locks, using CS to serialize control block fields are:

ILRSWPDR
ILRPTM
ILRSRT
ILRCMP
ILRSAV
ILRACT
ILRRLG
ILRTMRLG
ILRVASMI

Serialization via Control Block Queues

Certain ASM control blocks are serialized via their available queues. The blocks are kept on available queues and removed when needed. While in use the block is so marked and associated with a specific operation and/or control block. Control blocks included in this category are PCCWs, IORBs, and SCCWs.

The ASPCT is a special case. VIO Control enforces the rule that page and group operations cannot be performed in parallel for a given logical group and its ASPCT. This is controlled by the LGE process queue. While paging operations are being performed, the ASPCT is serialized via the ASM class lock of the owning address space. While a group operation is in progress, ASPCT serialization is maintained by the ACE for the group operation that is on the LGE process. This ACE prevents any other processing of ASPCT until the group operation completes.

ASM modules adhere to the following register conventions when calling other ASM modules. There are some exceptions where certain addresses are not required.

REGISTER:	0	– Parameter register, if required.
	1	– Parameter register, if required.
	2	– RSMHD address for the current address space or the address space identified by an input parameter in register 0 or 1. The ASMHD is addressable as part of the RSMHD.
	3	– ASMVT address.
	4	– Address of ATA or EPATH currently active for recovery tracking.
	13	– Address of register save area, if required.
	14	– Return address.
	15	– Entry point address.

The I/O subsystem does not use the ASMHD and therefore does not maintain register 2 convention.

Duplex Status	Error Conditions		Message(s) Issued	ASM Action Taken
Duplexing Active	PLPA Full	Common *Available	ILR005I	Spill to Common
		Common **Unavailable	ILR010I	Duplex Only
	PLPA Bad		ILR009I, ILR010I	Duplex Only
	Common Full	PLPA Available	ILR006I	Spill to PLPA
		PLPA Unavailable	ILR010I	Duplex Only
	Common Bad		ILR009I, ILR010I	Duplex Only
	Duplex Full	PLPA or Common Available	ILR007I	Suspend Duplexing
		PLPA and Common Unavailable	ILR008W	Wait X'03C'
	Duplex Bad	PLPA and Common Available	ILR007I	Suspend Duplexing
		PLPA or Common Full	ILR007I	Suspend Duplexing
		PLPA or Common Bad	ILR008W	Wait X'02E'
		PLPA and Common Unavailable	ILR008W	Wait X'02E'
Duplexing Not Active	PLPA Full		ILR005I	Spill to Common
	PLPA Bad		ILR008W	Wait X'02E'
	Common Full		ILR006I	Spill to PLPA
	Common Bad		ILR008W	Wait X'02E'
	PLPA and Common Full		ILR008W	Wait X'03C'
In Either Case	Local Bad		ILR009I	Stop Writes to Bad Data Set
	Last Local Bad		ILR008W	Wait X'02E'
	Swap Bad		ILR009I	Stop Swap-outs to Bad Data Set
	Last Swap Bad		ILR009I	All Swap-outs Done to Page Data Sets

*Available — Data Set Neither Full Nor Bad
**Unavailable — Data Set Either Full or Bad

Figure 6-6. Page/Swap Data Set Error Action Matrix

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