

SCA WRITE/READ BUFFER + LINE NOISE DETECTION

```

0000           ORG      **1500
              *      EQUATE TABLE
              *****
              *      THIS TABLE EQUATES TEST PROGRAM LABELS
              *      TO THEIR EQUIVALENT DIAGNOSTIC MONITOR
              *      ADDRESSES.
              *****
              *-----*
              *      MONITOR ENTRY ADDRESSES
              *-----*
0160      BEGIN EQU /160         BEGIN ROUTINE
0161      START EQU /BEGIN+1     SUPERVISOR ROUTINE
0162      ERROR EQU /START+1     ERROR LOG ROUTINE
0163      LOG EQU /ERROR+1       STATUS LOG ROUTINE
0164      END EQU /LOG+1         END ROUTINE
              *-----*
              *      MONITOR CONTROL WORD ADDRESSES
              *-----*
0165      RTNEN EQU /END+1       ROUTINE START SW
0166      ERLCK EQU /END+2       LOCK ON ERR CONTRL
0167      LOGBY EQU /END+3       I/O BUSY SW ADDR
0168      RLCF EQU /END+4       RELOC FACTOR ADDR
              *-----*
              *      INTERRUPT TRANSFER VECTOR ADDRESSES
              *-----*
017A      IL0 EQU /17A         INTERRUPT LEVEL 0
018A      IL1 EQU /IL0+16     INTERRUPT LEVEL 1
019A      IL2 EQU /IL1+16     INTERRUPT LEVEL 2
01AA      IL3 EQU /IL2+16     INTERRUPT LEVEL 3
01BA      IL4 EQU /IL3+16     INTERRUPT LEVEL 4
01BB      RQTY EQU /IL4+1     COM/PRINT REQUEST
01BC      RQKB EQU /RQTY+1     USE KEYBOARD REQUEST
01BD      SVKB EQU /RQKB+1     KB SERVICE REQUEST
              *-----*
              *      SCA WRT/RD BFR, LINE NOISE DETECTION
              *-----*
              *      PROGRAM STATUS TABLE
              *-----*
05DC 0 0311    PID DC /0311     PROGRAM ID NUMBER
05DD 0 0000    RID DC /0000     ROUTINE NUMBER
05DE 0 0000    RAD DC /0000     ROUTINE ADDRESS
05DF 0 0000    SW0 DC 0         PROGRAM CONTROL
05E0 0 0000    SW1 DC 0         ROUTINE SELECTION
05E1 0 0000    SW2 DC 0
05E2 0 0000    SW3 DC 0
05E3 1 0690    DC SRT         LOOP PROGRAM
05E4 1 0690    DC SRT         RESTART ADDRESS
05E5 1 0690    RLSCF DC SRT    ENTRY SET IN HW/LINE
05E6 0 FFFF    DC /FFFF       TERMINATOR
              *-----*
              *      LOCK ON FUNCTION ROUTINE
              *-----*
05E7 0 0000    LOCK DC /0000
05E8 01 C4005DF LD L SW0      LD SW0
05EA 00 EC800166 OR R ERLCK   OR WITH MON LOCK SW
05EC 0 100A    SLA 10         CHECK BIT 10
05ED 01 4C9005E7 BSC 1 LOCK, BR IF NOT LOCK/FUNC
  
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05EF 01 C40005F5 LD L LOOP     LOAD LOOP ADDR
05F1 01 4C4005F5 BSC 1 LOOP,Z LOOP ON LAST FUNC
05F3 01 4C8005E7 BSC 1 LOCK, IF LOOP ADDR NOT 0
05F5 0 0000      LOOP DC 0     LOCK/ERR LOOP ADR.
              *****
              *      TEST INITIALIZATION
              *****
05F6 00 44800160 BEGIN,GET I BEGIN MON INITIALIZATION
05F9 1 060C      BSC 1 PID     PST/LOGS
              *-----*
              *      START OF TEST AND SINGLE PASS INITIALIZATION
              *-----*
              *****
              *      PROGRAM WAIT
              *-----*
              *****
              *      THIS ROUTINE IS USED BY THE TEST PROGRAM
              *      TO WASTE TIME WHILE WAITING FOR AN INTERRUPT
              *      TO OCCUR.
              *-----*
05F9 0 0000      WCNT DC      DELAY COUNTER
05FA 0 1000      DELY3 DC /1000 DELAY CONSTANT
              *-----*
05FB 01 C40008B9 WAIT1 LD L R00B1 1 PASS
05FD 0 00F3      STO WCNT     STORE DELAY COUNT
05FE 01 440007BA BSC L MIC2
0600 0 7005      RMX WAIT2
              *-----*
0601 0 C0F8      WAIT3 LD DELY3 4 SEC. CONSTANT
0602 0 D0F5      STO WCNT     STORE DELAY COUNT
0603 01 440007BA BSC L MIC2
0605 0 7000      RMX WAIT2
              *-----*
0606 01 C400071E WAIT2 LD L PRTPN CHK FOR INTERRUPT
0608 01 4C200636 BSC L UNTR,Z INT. OCCURRED
060A 01 67000610 LDX L3 WAIT4 SET UP MON RETURN
060C 01 6F0005E5 STX L3 MLSCF
060E 00 44800161 BSI I START GO TO MONITOR
              *-----*
0610 01 74F005F9 WAIT4 MDX L WCNT,-1 REDUCE DELAY COUNT
0612 0 70F3      MDX WAIT2   DELAY NOT OVER
0613 01 0C000706 XIQ L DSHSN SENSE AND RESET DSW
0615 01 44000664 BSI L SNDSW GO MAKE ANALYSIS
              *****
              *      INTERRUPT
              *****
              *      THE MONITOR RETURNS CONTROL TO THIS ROUTINE
              *      AFTER DETECTING A SCA INTERRUPT.
              *-----*
              *      UNEXPECTED INTERRUPT OCCURRED
              *-----*
0617 0 0000      INTR DC 0
0618 01 0C000706 XIQ L DSHSN SENSE RESET DSW
061A 01 04000A88 STO L CANSN STORE DSW
061C 01 0C00070C XIQ L R0IA1 READ DIAG WD 1
061E 01 0C00070E XIQ L R0IA2 READ DIAG WD 2
0620 01 C4000725 LD L 0IA2
0622 0 1890      SAR 16
0623 0 1810      SLA 16
0624 0 1086      SLT 6
  
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0625 0 1001      SLA      1      31101370
0626 01 EC000724  DR      L  DIAW1  COMBINE DIAG WORDS 31101380
0628 01 DC00071C  STD      L  DIAWD  STORE DIAG WORDS 31101390
062A 01 4C00071E  STX      L  PRTPN  TURN ON INTERRUPT SW 31101400
062C 01 C40005DE  LD       L  RAD    CHK IF LINE NOISE RT 31101410
062E 01 94000662  S        L  RTTBL+4 31101420
0630 01 4C980617  BSC      I  INTR,+  YES, EXIT TO RETURN 31101430
0632 01 0C000700  XIO      L  RESET  NO, EXECUTE RESET 31101440
0634 01 4C800617  BSC      I  INTR   EXIT TO RETURN 31101450
*
0636 01 44000838  UINTR   BSI  L  INTAN  MAKE INTRPT ANAYSIS 31101460
0638 01 4C000677  BSC      L  MODES  MAKE ANALYSIS 31101470
*
*****
* THIS ROUTINE CHECKS SWITCHES AND CONTROLS 31101480
* THE SEQUENCE IN WHICH TEST ROUTINES ARE RUN 31101490
*****
*
063A 0 0000      CNTRL  DC    *--* 31101500
063B 01 C40005E0  LD       L  SW1    31101510
063D 01 4C080649  BSC      L  CN20,+  BRANCH IF NO RTN SELECTED 31101520
063F 01 D40005DD  STO      L  RID    SAVE NEW RTN NUMBER 31101530
0641 0 9021      S        S  RIDCK  31101540
0642 01 4C080650  BSC      L  CN30,+  BR IF VALID RTN 31101550
0644 0 1810      SRA      16      31101560
0645 01 D40005E0  STO      L  SW1    IF INVALID RTN GO 31101570
0647 01 D40005DD  STO      L  RID    TO RTN ONE 31101580
0649 01 740105DD  CN20    MDX  L  RID,1  ADV TO NEXT RTN 31101590
064B 01 C40005DD  LD       L  RID    31101600
064D 0 9015      S        S  RIDCK  31101610
064E 00 44800164  BSI      I  END,-Z  END PROGRAM 31101620
0650 01 678005DD  CN30    LD      L3 RID  31101630
0652 01 C700065D  LD      L3 RTTBL-1 31101640
0654 01 D40005DE  STO      L  RAD    TO RTN ONE 31101650
0656 01 D40005F5  STO      L  LOOP   ADV TO NEXT RTN 31101660
0658 01 D40005E5  STO      L  MLSCF  31101670
065A 00 D4000165  STO      L  RTNSW  31101680
065C 00 44800161  BSI      I  START  GO TO MONITOR 31101690
*
*****
* ROUTINE SEQUENCE TABLE 31101700
*****
*
065E 1 06C3      RTTBL  DC    RT1    31101710
065F 1 06C9      DC      RT2    31101720
0660 1 06CF      DC      RT3    31101730
0661 1 0726      DC      RT4    31101740
0662 1 0751      LRTN   DC    RT5    31101750
*
*****
*
0663 0 0005      RIDCK  DC    LRTN-RTTBL+1 31101760
*
0664 0 0000      SNDSW  DC    *--* 31101770
0665 01 0C000706  XIO      L  DSWSN  SENSE DSW 31101780
0667 01 D4000AE8  STO      L  CADSW  STORE DSW 31101790
0669 01 0C00070C  XIO      L  RDIA1  READ DIAG WORD 1 31101800
066B 01 0C00070E  XIO      L  RDIA2  READ DIAG WORD 2 31101810
066D 01 C4000725  LD       L  DIAW2  31101820
066F 0 1890      SRT      16      31101830
0670 0 1010      SLA      16      31101840
0671 0 1086      SLT      6      31101850
0672 0 1001      SLA      1      31101860
0673 01 EC000724  DR      L  DIAW1  COMBINE DIAG WORDS 31101870
0675 01 DC00071C  STD      L  DIAWD  STORE DIAG WORDS 31101880
*
0677 01 C4000719  MODES  LD      L  RESMD  CHK FOR RESET MODE 31101890
0679 01 4C200942  BSC      L  RESAN,Z 31101900
067B 01 C400071A  LD      L  RDMD   31101910

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067D 01 4C200956  BSC      L  RDAN,Z 31102050
067F 01 C400071B  LD       L  WRTMD  CHK FOR WRITE MODE 31102060
0681 01 4C200961  BSC      L  WRTAN,Z 31102070
*
0683 01 C400068F  RETRN  LD      L  PRTSW  CHK FOR PRINT SW ON 31102080
0685 01 4C20068B  BSC      L  PRNT,Z 31102090
0687 01 4400069B  BSI      L  OFF    RESET PROG SWITCHES 31102100
0689 01 4400063A  BSI      L  CNTRL  GO TO CONTROL 31102110
*
068B 0 1010      PRNT   SLA      16 31102120
068C 0 0002      STO      L  PRTSW  TURN OFF ERR SW 31102130
068D 01 4C800664  BSC      I  SNDSW  GO MAKE ANALYSIS 31102140
*
068F 0 0000      PRTSW  DC    *--* 31102150
*****
*****
* THIS ROUTINE PERFORMS THE REQUIRED 31102160
* INITIALIZATION FOR RESTART OF THE PROGRAM. 31102170
* IT LOADS THE FIRST ROUTINE OR THE DESIRED 31102180
* ROUTINE VIA SELECT SWITCHES INTO THE 31102190
* MAINLINE SEQUENCE CONTROL FIELD. 31102200
*****
*****
0690 0 6300      STRT   LD      L3 0 31102210
0691 01 6F0005DD  STX      L3 RID  STORE ROUTINE NO. 31102220
0693 01 65000617  LD      L1 INTR  SET-UP INTRPT ADDR 31102230
0695 00 6D000189  STX      L1 ILL-1 31102240
0697 01 4400069B  BSI      L  OFF    RESET PROG SWITCHES 31102250
0699 01 4400063A  BSI      L  CNTRL  GO TO CONTROL 31102260
*
*****
*****
* TURN OFF ALL SWITCHES 31102270
*****
*****
069B 0 0000      OFF    DC    *--* 31102280
069C 01 C40005DE  LD       L  RAD    RETURN ADDRESS 31102290
069E 01 94000662  S        L  LRTN  31102300
06A0 01 4C1806A4  BSC      L  OFF+9,+ 31102310
06A2 01 0C000700  XIO      L  RESET  31102320
06A4 0 1010      SLA      16      31102330
06A5 0 630C      LD      L3 12 31102340
06A6 01 D7000AD9  OFF0    STO      L3 TABLE-1 31102350
06A8 0 73FF      MDX      3 -1 31102360
06A9 0 70FC      MDX      OFF0 31102370
06AA 0 630C      LD      L3 12 31102380
06AB 01 D7000AE5  OFF1    STO      L3 DSWXX-1 31102390
06AD 0 73FF      MDX      3 -1 31102400
06AE 0 70FC      MDX      OFF1 31102410
06AF 0 630D      LD      L3 13 31102420
06B0 01 D7000718  OFF2    STO      L3 RESMD-1 31102430
06B2 0 73FF      MDX      3 -1 31102440
06B3 0 70FC      MDX      OFF2 31102450
06B4 0 6304      LD      L3 4 31102460
06B5 01 D70009CE  OFF3    STO      L3 DSWHI-1 31102470
06B7 0 73FF      MDX      3 -1 31102480
06B8 0 70FC      MDX      OFF3 31102490
06B9 01 440005E7  BSI      L  LOCK   CK LOCK,LOOP/ERR FNC 31102500
06BB 0 C006      LD       L  LOOPE  LOAD LOOP SW 31102510
06BC 0 6300      LD      L3 0 31102520
06BD 0 6804      STX      3 LOOPE  RESET LOOP SW 31102530
06BE 01 4CA005F5  BSC      I  LOOP,Z 31102540
06C0 01 4C80069B  BSC      I  OFF    31102550
*
06C2 0 0000      LOOPE  DC    *--* 31102560
*

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0742 0 7002          MDX  JUMP      SET UP PATTERN      31104100
0743 01 4C800739    BSC  I SHFT      EXIT TO RETURN      31104110
*
0745 01 478007DE    JUMP  BSI  I3 PATNA    GO TO PATTERN ADDR  31104120
0747 0 088C          XIO  WRCAT      WRT PATT. IN BUFFER  31104130
0748 0 0889          XIO  RDCAT      RD PATT. FROM BUFFER  31104140
0749 0 00D5          LD   RCVED      LD PATTERN READ     31104150
074A 0 00D5          EOR  SEND       EOR WITH PATT. SENT  31104160
074B 0 6803          STX  3 XR3A+1    SAVE IXR 3          31104170
074C 01 442006EC    BSI  L BFRCK,Z  CHK FOR DATA ERROR  31104180
074E 00 67000000    XR3A LDX L3 *-+  RESTORE IXR 3      31104190
0750 0 70F0          MDX  SHFT+8     GO ON WITH ROUTINE  31104200
*
*****
* LINE NOISE DETECTION
*****
*
0751 01 0C000710    RT5  XIO  L ENDP    EXECUTE ENDP         31104210
0753 0 0884          XIO  STRD      EXECUTE START READ  31104220
0754 01 440007B1    BSI  L DLYTM    GO TO DELAY         31104230
0756 0 0883          XIO  STST      INHIBIT TIMEDUT  31104240
0757 00 67004000    LDX  L3 /4000   SET UP 16 SEC. DELAY  31104250
0759 01 6F0005F9    STX  L3 WCNT    SET UP 16 SEC. DELAY  31104260
075B 01 440007BA    BSI  L MIC2     SENSE DSW           31104270
075D 0 08A8          XIO  DSWSN     SET UP READY CHECK  31104280
075E 0 0E09          AND  MASK1     CHECK FOR READY     31104290
075F 01 4C20078D    ESC  L RDY,Z   SET UP TO LOG MSG 1  31104300
0761 01 67000A4D    LOG01 LDX L3 ADR7 SET UP TO LOG MSG 1  31104310
0763 01 6F000ADD    STX  L3 TABLE+3 SET UP TO LOG MSG 1  31104320
0765 0 6301          LDX  3 1       SET UP TO LOG MSG 1  31104330
0766 01 6F000ADA    STX  L3 TABLE SET UP TO LOG MSG 1  31104340
0768 0 6300          LDX  3 0       SET UP TO LOG MSG 1  31104350
0769 01 6F000ADC    STX  L3 TABLE+2 EXECUTE ALARM ON     31104360
076B 01 0C000712    XIO  L BZON    LOG SET UP DATA SET  31104370
076D 00 44800163    BSI  I LOG     CHECK FOR READY     31104380
076F 1 0ADA          DC   TABLE    SET UP TO LOG MSG 1  31104390
0770 01 0C000714    XIO  L BZOFF   EXECUTE ALARM OFF   31104400
0772 01 44000775    BSI  L WAIT5   GO TO DELAY         31104410
0774 0 70E2          MDX  RT5+6     SET UP DELAY AGAIN  31104420
*
*****
* OPERATING INSTRUCTIONS
*****
* WHEN SCA CONSOLE RCV LITE COMES ON, READY
* CONDITION WILL HAVE TO BE ESTABLISHED IN
* ORDER TO BEGIN LINE LISTENING. 1. PHONE
* ANOTHER TERMINAL. 2. REQUEST THAT THEIR
* DATA SET BE PUT IN TALK POSITION AND
* PLACE THE HAND PHONE IN THE DATA SET
* CRADLE. *NOTE... IF THE HAND PHONE AT THEIR
* TERMINAL IS NOT PLACED IN THE CRADLE, THE
* MOUTH PIECE WILL PICK UP NOISES WITHIN THE
* ROOM. 3. AT YOUR DATA SET, PUSH THE DATA
* BUTTON. THIS SHOULD CAUSE THE SCA RDY LITE
* TO COME ON AND CAUSE THE PROGRAM TO BRANCH
* TO LINE LISTENING. ANY LINE CONDITIONS
* EFFECTING THE COMMUNICATIONS TERMINAL
* CIRCUITRY WILL BE DETECTED AND PRINTED OUT
* ON THE CONSOLE TYPEWRITER. LINE LISTENING
* WILL CONTINUE FOR APPROX 10 MINUTES,
* UNLESS TERMINATED BY MAKING THE DATA SET
* NOT READY. 4. PLACE YOUR DATA SET HAND
* PHONE IN ITS CRADLE.
*****
0775 0 0000          WAITS DC *-+    SET UP MON RETURN   31104430
0776 01 6700077C    LDX  L3 RTRN5  SET UP MON RETURN   31104440
0778 01 6F0005E5    STX  L3 MLSCF  SET UP MON RETURN   31104450

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077A 00 44800161    RTRN5 BSI I START  GO TO MONITOR       31104780
077C 01 74FF05F9    MDX  L WCNT,-1  REDUCE DELAY COUNT  31104790
077E 0 70F7          MDX  WAIT5+1    CK FOR DELAY OVER  31104800
077F 01 4C800775    BSC  I WAIT5    EXIT TO RETURN      31104810
*
0781 0 0000          WAIT6 DC *-+    SET UP MON RETURN   31104820
0782 01 67000788    LDX  L3 RTRN6  SET UP MON RETURN   31104830
0784 01 6F0005E5    STX  L3 MLSCF  SET UP MON RETURN   31104840
0786 00 44800161    BSI  I START  GO TO MONITOR       31104850
0788 01 74FF05F9    RTRN6 MDX L WCNT,-1 REDUCE DELAY COUNT  31104860
078A 0 700C          MDX  LSTNN     GO LISTEN SOME MORE 31104870
078B 01 4C800781    BSC  I WAIT6   EXIT TO RETURN      31104880
*
078D 0 6314          RDY  LDX  3 20  SET UP LISTEN TIME  31104890
078E 0 6821          STX  3 PASS    SET UP LISTEN TIME  31104900
078F 00 67007800    LDX  L3 /7800  SET UP LISTEN TIME  31104910
0791 01 6F0005F9    STX  L3 WCNT   SET UP LISTEN TIME  31104920
0793 01 0C000714    XIO  L BZOFF   EXECUTE ALARM OFF   31104930
0795 01 440007BA    BSI  L MIC2    EXECUTE ALARM OFF   31104940
*
*****
* LISTEN FOR LINE NOISE
*****
*
0797 01 0C000706    LSTNN XIO L DSWSN SENSE DSW           31104950
0799 01 E4000718    AND  L MASK1   SET UP READY CHECK  31104960
079B 01 4C1807C7    BSC  L HNGUP,+ CHECK FOR READY     31104970
079D 01 0C00070E    XIO  L RDIA2   READ DIAG WD 2     31104980
079F 01 C4000725    LD   L DIAW2   PUT DIAG WRD IN ACCUM 31104990
07A1 01 E4000717    AND  L MASK    SET UP NOISE CHECK  31105000
*
*
*
07A3 01 4C1807A9    BSC  L CNTNR,+ DUNT BR IF ANY THING 31105010
07A5 01 D4000ADF    PRTER STO L TABLE+5 SET BITS IN MSG     31105020
07A7 01 4C00090D    BSC  L NOISE   GO PRINT NOISE     31105030
*
*
*
07A9 01 44000781    CNTNR BSI L WAIT6 GO TO DELAY         31105040
07AB 01 74FF07B0    MDX  L PASS,-1 REDUCE DELAY COUNT  31105050
07AD 0 70E1          MDX  RDY+2     GO LISTEN SOME MORE 31105060
07AE 01 4C0008CE    BSC  L LOG02   GO LOG RESET MESSAGE 31105070
*
*
*
07B0 0 0000          PASS DC *-+    SET UP MON RETURN   31105080
*
*
*
07B1 0 0000          DLYTM DC *-+    SET UP MON RETURN   31105090
07B2 0 6306          LDX  3 6       SET UP MON RETURN   31105100
07B3 0 6805          STX  3 TMDLY   SET UP MON RETURN   31105110
07B4 01 74FF07B9    MDX  L TMDLY,-1 SET UP MON RETURN   31105120
07B6 0 70FD          MDX  *-3      SET UP MON RETURN   31105130
07B7 01 4C8007B1    BSC  I DLYTM   SET UP MON RETURN   31105140
*
*
*
07B9 0 0000          TMDLY DC *-+   SET UP MON RETURN   31105150
*
*
*
07BA 0 0000          MIC2 DC *-+    SET UP MON RETURN   31105160
07BB 01 C40005E1    LD   L SW2     SET UP MON RETURN   31105170
07BD 0 100B          SLA  11        SET UP MON RETURN   31105180
07BE 01 4C9007BA    BSC  I MIC2,-  SET UP MON RETURN   31105190
07C0 01 C40005F9    LD   L WCNT    SET UP MON RETURN   31105200
07C2 0 1001          SLA  1         SET UP MON RETURN   31105210
07C3 01 D40005F9    STO  L WCNT    SET UP MON RETURN   31105220
07C5 01 4C8007BA    BSC  I MIC2    SET UP MON RETURN   31105230
*
*
*
07C7 01 0C000700    HNGUP XIO L RESET EXECUTE RESET         31105240
07C9 01 67000A6D    LDX  L3 ADR9   SET UP RTN TERM MSG 31105250
07CB 01 6F000ADD    STX  L3 TABLE+3 SET UP RTN TERM MSG 31105260
07CD 0 6303          LDX  3 3       SET UP RTN TERM MSG 31105270
07CE 01 6F000ADA    STX  L3 TABLE SET UP RTN TERM MSG 31105280
07D0 0 6300          LDX  3 0       SET UP RTN TERM MSG 31105290

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07D1 01 6F000ADC	STX	L3	TABLE+2	31105550	
07D3 01 0C000712	XIO	L	BZON	EXECUTE ALARM ON	31105560
07D5 00 44800163	BSI	I	LOG	LOG RTN TERM MESSAGE	31105570
07D7 1 0ADA	DC		TABLE	31105580	
07D8 01 0C000714	XIO	L	BZOFF	EXECUTE ALARM OFF	31105590
07DA 01 4400069B	BSI	L	OFF	RESET PROG SWS	31105600
07DC 01 4400063A	BSI	L	CNTRL	GO TO CONTROL	31105610

 * TRANSFER VECTOR FOR SCA BUFFER
 * WRT AND READ/BACK PATTERNS

07DE 1 084F	PATNA	DC	A15	31105620
07DF 1 0848	DC		A14	31105630
07E0 1 0841	DC		A13	31105640
07E1 1 083A	DC		A12	31105650
07E2 1 0833	DC		A11	31105660
07E3 1 082C	DC		A10	31105670
07E4 1 0825	DC		A9	31105680
07E5 1 081E	DC		A8	31105690
07E6 1 0817	DC		A7	31105700
07E7 1 0810	DC		A6	31105710
07E8 1 0809	DC		A5	31105720
07E9 1 0802	DC		A4	31105730
07EA 1 07FB	DC		A3	31105740
07EB 1 07F4	DC		A2	31105750
07EC 1 07ED	DC		A1	31105760

 * WRITE READ/BACK PATTERNS

07ED 0 0000	A1	DC	0	31105830
07EE 00 65008000	LDX	L1	/8000	31105840
07F0 01 6D000720	STX	L1	SEND	31105850
07F2 01 4C8007ED	BSC	I	A1	31105860
07F4 0 0000	A2	DC	0	31105870
07F5 00 65004000	LDX	L1	/4000	31105880
07F7 01 6D000720	STX	L1	SEND	31105890
07F9 01 4C8007F4	BSC	I	A2	31105900
07FB 0 0000	A3	DC	0	31105910
07FC 00 65002000	LDX	L1	/2000	31105920
07FE 01 6D000720	STX	L1	SEND	31105930
0800 01 4C8007FB	BSC	I	A3	31105940
0802 0 0000	A4	DC	0	31105950
0803 00 65001000	LDX	L1	/1000	31105960
0805 01 6D000720	STX	L1	SEND	31105970
0807 01 4C800802	BSC	I	A4	31105980
0809 0 0000	A5	DC	0	31105990
080A 00 65008000	LDX	L1	/8000	31106000
080C 01 6D000720	STX	L1	SEND	31106010
080E 01 4C800809	BSC	I	A5	31106020
0810 0 0000	A6	DC	0	31106030
0811 00 65000400	LDX	L1	/0400	31106040
0813 01 6D000720	STX	L1	SEND	31106050
0815 01 4C800810	BSC	I	A6	31106060
0817 0 0000	A7	DC	0	31106070
0818 00 65000200	LDX	L1	/0200	31106080
081A 01 6D000720	STX	L1	SEND	31106090
081C 01 4C800817	BSC	I	A7	31106100
081E 0 0000	A8	DC	0	31106110
081F 00 65000100	LDX	L1	/0100	31106120
0821 01 6D000720	STX	L1	SEND	31106130
0823 01 4C80081E	BSC	I	A8	31106140
0825 0 0000	A9	DC	0	31106150
0826 00 65000200	LDX	L1	/0200	31106160
0828 01 6D000720	STX	L1	SEND	31106170

SCA WRITE/READ BUFFER + LINE NOISE DETECTION

082A 01 4C800825	BSC	I	A9	31106230
082C 0 0000	A10	DC	0	31106240
082D 00 65000400	LDX	L1	/0400	31106250
082F 01 6D000720	STX	L1	SEND	31106260
0831 01 4C80082C	BSC	I	A10	31106270
0833 0 0000	A11	DC	0	31106280
0834 00 65000800	LDX	L1	/0800	31106290
0836 01 6D000720	STX	L1	SEND	31106300
0838 01 4C800833	BSC	I	A11	31106310
083A 0 0000	A12	DC	0	31106320
083B 00 65001000	LDX	L1	/1000	31106330
083D 01 6D000720	STX	L1	SEND	31106340
083F 01 4C80083A	BSC	I	A12	31106350
0841 0 0000	A13	DC	0	31106360
0842 00 65002000	LDX	L1	/2000	31106370
0844 01 6D000720	STX	L1	SEND	31106380
0846 01 4C800841	BSC	I	A13	31106390
0848 0 0000	A14	DC	0	31106400
0849 00 65004000	LDX	L1	/4000	31106410
084B 01 6D000720	STX	L1	SEND	31106420
084D 01 4C800848	BSC	I	A14	31106430
084F 0 0000	A15	DC	0	31106440
0850 00 65008000	LDX	L1	/8000	31106450
0852 01 6D000720	STX	L1	SEND	31106460
0854 01 4C80084F	BSC	I	A15	31106470

 * TRANSFER VECTOR FOR SCA BUFFER
 * WRT AND READ/BACK PATTERNS

0856 1 08C7	PATNB	DC	B15	31106530
0857 1 08C0	DC		B14	31106540
0858 1 08B9	DC		B13	31106550
0859 1 08B2	DC		B12	31106560
085A 1 08AB	DC		B11	31106570
085B 1 08A4	DC		B10	31106580
085C 1 089D	DC		B9	31106590
085D 1 0896	DC		B8	31106600
085E 1 088F	DC		B7	31106610
085F 1 0888	DC		B6	31106620
0860 1 0881	DC		B5	31106630
0861 1 087A	DC		B4	31106640
0862 1 0873	DC		B3	31106650
0863 1 086C	DC		B2	31106660
0864 1 0865	DC		B1	31106670

 * WRITE READ/BACK PATTERNS

0865 0 0000	B1	DC	0	31106680
0866 00 65007F00	LDX	L1	/7F00	31106690
0868 01 6D000720	STX	L1	SEND	31106700
086A 01 4C800865	BSC	I	B1	31106710
086C 0 0000	B2	DC	0	31106720
086D 00 6500BF00	LDX	L1	/BF00	31106730
086F 01 6D000720	STX	L1	SEND	31106740
0871 01 4C80086C	BSC	I	B2	31106750
0873 0 0000	B3	DC	0	31106760
0874 00 6500DF00	LDX	L1	/DF00	31106770
0876 01 6D000720	STX	L1	SEND	31106780
0878 01 4C800873	BSC	I	B3	31106790
087A 0 0000	B4	DC	0	31106800
087B 00 6500EF00	LDX	L1	/EF00	31106810
087D 01 6D000720	STX	L1	SEND	31106820
087F 01 4C80087A	BSC	I	B4	31106830
0881 0 0000	B5	DC	0	31106840

SCA WRITE/READ BUFFER + LINE NOISE DETECTION

```

0882 00 6500F700      LDX L1 /F700
0884 01 6D000720      STX L1 SEND
0886 01 4C800881      BSC I B5
0888 0 0000          DC 0
0889 00 6500FB00      LDX L1 /FB00
088B 01 6D000720      STX L1 SEND
088D 01 4C800888      BSC I B6
088F 0 0000          DC 0
0890 00 6500FD00      LDX L1 /FD00
0892 01 6D000720      STX L1 SEND
0894 01 4C80088F      BSC I B7
0896 0 0000          DC 0
0897 00 6500FE00      LDX L1 /FE00
0899 01 6D000720      STX L1 SEND
089B 01 4C800896      BSC I B8
089D 0 0000          DC 0
089E 00 6500FD00      LDX L1 /FD00
08A0 01 6D000720      STX L1 SEND
08A2 01 4C80089D      BSC I B9
08A4 0 0000          DC 0
08A5 00 6500FB00      LDX L1 /FB00
08A7 01 6D000720      STX L1 SEND
08A9 01 4C8008A4      BSC I B10
08AB 0 0000          DC 0
08AC 00 6500F700      LDX L1 /F700
08AE 01 6D000720      STX L1 SEND
08B0 01 4C8008AB      BSC I B11
08B2 0 0000          DC 0
08B3 00 6500EF00      LDX L1 /EF00
08B5 01 6D000720      STX L1 SEND
08B7 01 4C8008B2      BSC I B12
08B9 0 0000          DC 0
08BA 00 6500DF00      LDX L1 /DF00
08BC 01 6D000720      STX L1 SEND
08BE 01 4C8008B9      BSC I B13
08C0 0 0000          DC 0
08C1 00 6500BF00      LDX L1 /BF00
08C3 01 6D000720      STX L1 SEND
08C5 01 4C8008C0      BSC I B14
08C7 0 0000          DC 0
08C8 00 6500F700      LDX L1 /F700
08CA 01 6D000720      STX L1 SEND
08CC 01 4C8008C7      BSC I B15

```

* RESET ADAPTER AND HANG UP TELEPHONE

```

08CE 01 4C0005E0      LOGO2 LD L SW1      CK FOR LOOP RTN OPT
08D0 01 94000837      S L K0005      CHK FOR NOISE RTN
08D2 0 4820          BSC Z
08D3 0 7002          MDX LGO2+8      LOOP NOISE ROUTINE
08D4 01 4400063A      BSI L CNTRL      GO TO CONTROL
08D6 01 67000A5C      LDX L3 ADR8      SET UP RESET MESSAGE
08D8 01 6F000ADD      STX L3 TABLE+3
08DA 0 6302          LDX 3 2
08DB 01 6F000ADA      STX L3 TABLE
08DD 0 6300          LDX 3 0
08DE 01 6F000ADC      STX L3 TABLE+2
08E0 01 0C000712      XIO L BZON      EXECUTE ALARM ON
08E2 00 44800163      BSI I LOG        LOG RESET MESSAGE
08E4 1 0ADA          DC TABLE
08E5 01 0C000714      XIO L BZOFF      EXECUTE ALARM OFF
08E7 01 4C0007C7      BSC L HNGUP      GO TERMINATE ROUTINE

```

* BFRER STX L PRISW TURN ON ERROR SW
STX 3 XR3+1 SAVE REGISTER 3
BSI L SNDSW GO MAKE ANALYSIS

SCA WRITE/READ BUFFER + LINE NOISE DETECTION

```

08EE 0 6305          LDX 3 5
08EF 01 6F000ADA      STX L3 TABLE    SET UP MSG ID
08F1 01 4C000836      LD L K0003        MODIFIERS 2
08F3 01 64000ADC      STO L TABLE+2   SET UP MODIFIER
08F5 01 67000A36      LDX L3 ADR5
08F7 01 6F000ADD      STX L3 TABLE+3  SET UP ALPHA MSG
08F9 01 4C000720      LD L SEND         S/B PATTERN SENT TO BFR
08FB 01 64000AEO      STO L TABLE+6
08FD 01 4C00071F      LD L RCVED        WAS PATTERN RD FROM BFR
08FF 01 64000ADF      STO L TABLE+5
0901 01 0C000712      XIO L BZON        EXECUTE ALARM ON
0903 00 44800162      BSI I ERROR      GO PRINT
0905 1 0ADA          DC TABLE
0906 1 0A7D          DC LOPRT         LOOP ADDR
0907 01 0C000714      XIO L BZOFF      EXECUTE ALARM OFF
0909 00 67000000      XR3 LDX L3 *-*   RESTORE REG 3
090B 01 4C8006EC      BSC I BFRCK      EXIT TO RETURN

```

* NOISE LDX 3 6
STX L3 TABLE SET UP MSG ID
LDX 3 0
STX L3 LOPRT INHIBIT LOOP ON ERR
LD L K0001
STO L TABLE+2 SET UP MODIFIER
LDX L3 ADR6
STX L3 TABLE+3 SET UP ALPHA MSG
XIO L BZON EXECUTE ALARM ON
BSI I ERROR GO PRINT
DC TABLE
DC LOPRT
XIO L BZOFF EXECUTE ALARM OFF
XIO L ENDOP EXECUTE ENDOP
XIO L STRD EXECUTE START READ
BSI L DLYTM GO TO DELAY
XIO L STST INHIBIT TIMEOUT INT
BSC L CNTR RETURN TO ROUTINE

```

0920 0 0080          DSW00 DC /0080
092E 0 0080          DSW01 DC /0080

```

* PREDETERMINED VALUES FOR SCA DSM. THESE
* VALUES ARE PRINTED OUT AS S/B, SHOULD BE
* DSM WHEN A DISCREPANCIE EXIST.

```

092F 0 0000          DIA00 DC /0000
0930 0 1000          DIA01 JC /1000

```

```

0931 0 0000          STOR1 DC *-*      SAVED RETURN ADDR
0932 01 64000AED      STO L STWD        STORE S/B DSM WORD
0934 01 64000AE6      STO L DSWXX       STORE S/B DSM WORD
0936 01 4400096C      BSI L ANAL1       GO TO ANAL. 1
0938 01 4C800931      BSC I STOR1       EXIT TO RETURN

```

```

093A 01 64000AED      STOR2 STO L STWD  STORE S/B DIAG WORD
093C 01 64000AEE      STO L DIAXX       STORE S/B DIAG WORD
093E 01 4400097A      BSI L ANAL2       GO TO ANAL. 2
0940 01 4C000683      BSC L RETRN       BRANCH TO RETRN

```

SCA WRITE/READ BUFFER + LINE NOISE DETECTION

```

*****
0942 01 C40009D1 RESAN LD L BNSW1 LOAD BRANCH SW 1
0944 0 4820 BSC Z CHK IF IT IS ON
0945 0 7006 MDX REST1 BRANCH SW 1 IS ON
0946 01 C40009D2 LD L BNSW2 LOAD BRANCH SW 2
0948 0 4820 BSC Z CHK IF IT IS ON
0949 0 7007 MDX REST2 BRANCH 2 IS ON
094A 01 4C000683 BSC L RETRN BRANCH TO RETURN
*
094C 0 C0E0 REST1 LD DSW00 LOAD BRANCH SW 00
094D 01 44000931 BSI L STOR1 GO STORE 1T
094F 0 C0DF LD DIA00 LOAD S/B DIAG WORD
0950 0 70E9 MDX STOR2 GO STORE 2
*
0951 0 C0DC REST2 LD DSW01 LD S/B DSW01 WORD
0952 01 44000931 BSI L STOR1 GO STORE 1T
0954 0 C0DB LD DIA01 LD S/B DIAG 01 WORD
0955 0 70E4 MDX STOR2 GO STORE 2
*
0956 01 C40009D1 RDAN LD L BNSW1 LD BRANCH SW 1
0958 01 4C20095C BSC L RTR01,Z DDNT BR IF OFF
095A 01 4C000683 BSC L RETRN RETURN
*
095C 0 C0D0 RTR01 LD DSW00 LD S/B DSW WORD
095D 01 44000931 BSI L STOR1
095F 0 C0CF LD DIA00 LD S/B DIAG WORD
0960 0 70D9 MDX STOR2 GO STORE 2
*
0961 01 C40009D1 WRTAN LD L BNSW1 LGAD BRANCH SWT 1
0963 01 4C200967 BSC L WTR01,Z DDNT BRANCH IF OFF
0965 01 4C000683 BSC L RETRN RETURN
*
0967 0 C0C5 WTR01 LD DSW00 LD S/B DSW WORD
0968 01 44000931 BSI L STOR1
096A 0 C0C4 LD DIA00 LD S/B DIAG WORD
096B 0 70CE MDX STOR2 GO STORE 2
*
*****
* CHECK TO SEE IF DSW ERROR EXISTS
*****
096C 0 0000 ANAL1 DC *-* SAVED RETURN ADDR
096D 01 C4000AE8 LD L CADSW LOAD SENSED DSW
096F 01 F4000AE6 EDR L DSWXX EDR WITH S/B DSW
0971 01 D4000AE7 STO L DSWRD STORE IN DSW ERR BIT
0973 0 4820 BSC Z CHK IF ERR BITS ON
0974 0 7001 MDX ANL01 ERR BITS ARE ON
0975 0 7002 MDX ANL01+2 ERR BITS ARE NOT ON
0976 01 44000988 ANL01 BSI L DSWAN CHK WHICH BITS ON
0978 01 4C80096C BSC I ANAL1 BRANCH TO RETURN ADR
*
*****
* CHECK TO SEE IF DIAG TRIG ERROR EXISTS
*****
097A 0 0000 ANAL2 DC *-* SAVED RETURN ADDR
097B 01 C400071C LD L DIAWD LD DIAGNOSTIC
097D 01 F4000AEE EDR L DIAXX EDR WITH S/B DIAG WD
097F 01 D4000AEF STO L TRGWD SET IN DIAG ERR BITS
0981 0 4820 BSC Z CHKIF ERR BITS ON
0982 0 7001 MDX ANL02 ERROR BITS ARE ON
0983 0 7002 MDX ANL02+2 ERR BITS ARE NOT ON
0984 01 44000A81 ANL02 BSI L TRGAN CHK WHICH ERR BIT ON
0986 01 4C80097A BSC I ANAL2 BRANCH TO SAVED ADDR
*
*****

```

SCA WRITE/READ BUFFER + LINE NOISE DETECTION

```

0988 0 0000 DSWAN DC *-* SAVED RETURN ADDRESS
0989 01 C4000AE8 LD L CADSW LOAD DSW WAS
098B 01 E4000AE7 AND L DSWRD AND WITH DSW ERR
098D 01 D40009CF STO L DSWW1 STORE ON/F ERR BITS
098F 01 C4000AE7 LD L DSWRD LOAD DSW ERR BITS
0991 01 F40009CF EDR L DSWW1 EDR WITH ON/F ERR
0993 01 D40009D0 STO L DSWW2 STORE DSW F/ON ERR
0995 01 440009D3 BSI L MSGID CHK C.E. BITS
0997 0 6302 LDX 3 2 SET UP MSG ID
0998 01 6F000ADA STX L3 TABLE
099A 00 6700007F LDX L3 /007F
099C 01 6F000ADC STX L3 TABLE+2 SET IN MODIFIER ID
099E 01 670009DE LDX L3 ADRI LD ALPHA 1 ADDR
09A0 01 6F000ADD STX L3 TABLE+3 SET UP ALPHA MSG
09A2 01 C40009CF LD L DSWW1 LOAD ON/F ERR BITS
09A4 01 D4000AE3 STO L TABLE+9
09A6 01 C40009D0 LD L DSWW2 LOAD F/ON ERR BITS
09A8 01 D4000AE4 STO L TABLE+10
09AA 01 C4000AE6 LD L DSWXX LD DSW SHOULD/BE
09AC 01 D4000AE2 STO L TABLE+8 SET UP ERR MESSAGE
09AE 01 C4000AE8 LD L CADSW LOAD SENSED DSW
09B0 01 D4000AE1 STO L TABLE+7 SET UP ERR MESSAGE
09B2 01 C4000725 LD L DIAW2 LD DIAG WD 2 WAS
09B4 0 1890 SRT 16 MASK C.E. BITS
09B5 0 1010 SLA 16
09B6 0 1086 SLT 6
09B7 0 100A SLA 10
0988 01 D4000AE0 STO L TABLE+6 SET UP ERR MESSAGE
098A 01 C4000724 LD L DIAW1 LD DIAG WD 1 WAS
098C 01 D4000ADF STO L TABLE+5 SET UP ERR MESSAGE
098E 01 0C000712 XIO L BZON EXECUTE ALARM ON
0990 00 44800162 BSI I ERROR GO PRINT ERROR
0992 01 0ADA DC TABLE MESSAGE TABLE
0993 01 0A7D DC LOPRT LOOP ADDRESS
0994 01 0C000714 XIO L BZOFF EXECUTE ALARM OFF
0996 0 6300 LDX 3 0
0997 01 6F0009CF STX L3 DSWW1 RESET OFF,S/B ON
0999 01 6F0009D0 STX L3 DSWW2 RESET ON,S/B OFF
099B 01 6F000AE7 STX L3 DSWRD RESET BIT ERR ID WD
099D 01 4C800988 WRDCK BSC I DSWAN EXIT TO RETURN
*
*****
* DSW ANALYSIS SWITCHES + STORAGE
*****
099C 0 0000 DSWW1 DC *-*
099D 0 0000 DSWW2 DC *-*
099E 0 0000 BNSW1 DC *-* BRANCH SW NO. 01
099F 0 0000 BNSW2 DC *-* BRANCH SW NO. 02
*
*****
09D3 0 0000 MSGID DC *-*
09D4 01 C400071D LD L DIAWD+1 LD DIAG WD 2 WAS
09D6 0 1890 SRT 16 FETCH C.E. BITS
09D7 0 1010 SLA 16
09D8 0 1083 SLT 3
09D9 0 1007 SLA 7
09DA 01 D4000AE5 STO L TABLE+11 STORE C.E. BITS
09DC 01 4C8009D3 BSC I MSGID EXIT TO RETURN
*
*****
* ALPHA MESSAGE FOR DSW ERROR
*****
09DE 0 3292 ADRI DC /3292 DW
09DF 0 FC21 DC /FC21 I
09E0 0 2132 DC /2132 D

```

SCA WRITE/READ BUFFER + LINE NOISE DETECTION

09E1 0	92D8	DC	/92D8	W2	31109630
09E2 0	2121	DC	/2121		31105640
09E3 0	923E	DC	/923E	WA	31109650
09E4 0	9A21	DC	/9A21	S	31109660
09E5 0	219A	DC	/219A	S	31109670
09E6 0	BC1A	DC	/BC1A	/B	31109680
09E7 0	2121	DC	/2121		31109690
09E8 0	5276	DC	/5276	ON	31109700
09E9 0	BC12	DC	/BC12	/F	31109710
09EA 0	2112	DC	/2112	F	31109720
09EB 0	BC52	DC	/BC52	/O	31109730
09EC 0	7621	DC	/7621	N	31109740
09ED 0	D61E	DC	/D61E	*C	31109750
09EE 0	36D6	DC	/36D6	E*	31109760
09EF 0	2132	DC	/2132	D	31109770
09F0 0	9A92	DC	/9A92	SW	31109780
09F1 0	2136	DC	/2136	E	31109790
09F2 0	6262	DC	/6262	RR	31109800
09F3 0	FFFF	DC	/FFFF	TERMINATOR	31109810

*

* ALPHA MESSAGE FOR DIAG WORD 1 ERROR

09F4 0	329A	ADR2 DC	/329A	DS	31109820
09F5 0	9221	DC	/9221	W	31109830
09F6 0	21D8	DC	/21D8	2	31109840
09F7 0	923E	DC	/923E	WA	31109850
09F8 0	9A21	DC	/9A21	S	31109860
09F9 0	FC92	DC	/FC92	1W	31109870
09FA 0	3E9A	DC	/3E9A	AS	31109880
09FB 0	21FC	DC	/21FC	1	31109890
09FC 0	9ABC	DC	/9ABC	S/	31109900
09FD 0	1A21	DC	/1A21	B	31109910
09FE 0	5276	DC	/5276	ON	31109920
09FF 0	BC12	DC	/BC12	/F	31109930
0A00 0	2112	DC	/2112	F	31109940
0A01 0	BC52	DC	/BC52	/O	31109950
0A02 0	7621	DC	/7621	N	31109960
0A03 0	D61E	DC	/D61E	*C	31109970
0A04 0	36D6	DC	/36D6	E*	31109980
0A05 0	2132	DC	/2132	D	31109990
0A06 0	92FC	DC	/92FC	W1	31110000
0A07 0	2136	DC	/2136	E	31110010
0A08 0	6262	DC	/6262	RR	31110020
0A09 0	FFFF	DC	/FFFF	TERMINATOR	31110030

*

* ALPHA MESSAGE FOR DIAG WORD 2 ERROR

0A0A 0	329A	ADR3 DC	/329A	DS	31110040
0A0B 0	9221	DC	/9221	W	31110050
0A0C 0	21FC	DC	/21FC	1	31110060
0A0D 0	923E	DC	/923E	WA	31110070
0A0E 0	9A21	DC	/9A21	S	31110080
0A0F 0	D892	DC	/D892	2W	31110090
0A10 0	3E9A	DC	/3E9A	AS	31110100
0A11 0	21D8	DC	/21D8	2	31110110
0A12 0	9ABC	DC	/9ABC	S/	31110120
0A13 0	1A21	DC	/1A21	B	31110130
0A14 0	5276	DC	/5276	ON	31110140
0A15 0	BC12	DC	/BC12	/F	31110150
0A16 0	2112	DC	/2112	F	31110160
0A17 0	BC52	DC	/BC52	/O	31110170
0A18 0	7621	DC	/7621	N	31110180
0A19 0	D61E	DC	/D61E	*C	31110190
0A1A 0	36D6	DC	/36D6	E*	31110200

SCA WRITE/READ BUFFER + LINE NOISE DETECTION

0A1B 0	2132	DC	/2132	D	31110310
0A1C 0	92D8	DC	/92D8	W2	31110320
0A1D 0	2136	DC	/2136	E	31110330
0A1E 0	6262	DC	/6262	RR	31110340
0A1F 0	FFFF	DC	/FFFF	TERMINATOR	31110350

*

* ALPHA MESSAGE FOR INTERRUPT ERROR

0A20 0	3292	ADR4 DC	/3292	DW	31110360
0A21 0	FC21	DC	/FC21	1	31110370
0A22 0	2132	DC	/2132	D	31110380
0A23 0	92D8	DC	/92D8	W2	31110390
0A24 0	2121	DC	/2121		31110400
0A25 0	329A	DC	/329A	DS	31110410
0A26 0	9221	DC	/9221	W	31110420
0A27 0	2152	DC	/2152	O	31110430
0A28 0	768C	DC	/768C	N/	31110440
0A29 0	1221	DC	/1221	F	31110450
0A2A 0	12BC	DC	/12BC	F/	31110460
0A2B 0	5276	DC	/5276	ON	31110470
0A2C 0	21D6	DC	/21D6	*	31110480
0A2D 0	1E36	DC	/1E36	CE	31110490
0A2E 0	D621	DC	/D621	*	31110500
0A2F 0	2121	DC	/2121		31110510
0A30 0	2121	DC	/2121		31110520
0A31 0	2122	DC	/2122	I	31110530
0A32 0	769E	DC	/769E	NT	31110540
0A33 0	2136	DC	/2136	E	31110550
0A34 0	6262	DC	/6262	RR	31110560
0A35 0	FFFF	DC	/FFFF	TERMINATOR	31110570

*

* ALPHA MESSAGE FOR BUFFER WRT/RD ERROR

0A36 0	923E	ADR5 DC	/923E	WA	31110580
0A37 0	9A21	DC	/9A21	S	31110590
0A38 0	219A	DC	/219A	S	31110600
0A39 0	BC1A	DC	/BC1A	/B	31110610
0A3A 0	2121	DC	/2121		31110620
0A3B 0	1A12	DC	/1A12	BF	31110630
0A3C 0	6221	DC	/6221	R	31110640
0A3D 0	9262	DC	/9262	WR	31110650
0A3E 0	9EBC	DC	/9EBC	T/	31110660
0A3F 0	6232	DC	/6232	RD	31110670
0A40 0	2136	DC	/2136	E	31110680
0A41 0	6262	DC	/6262	RR	31110690
0A42 0	5262	DC	/5262	OR	31110700
0A43 0	FFFF	DC	/FFFF	TERMINOR	31110710

*

* ALPHA MESSAGE FOR LINE NOISE ERROR

0A44 0	923E	ADR6 DC	/923E	WA	31110720
0A45 0	9A21	DC	/9A21	S	31110730
0A46 0	215E	DC	/215E	L	31110740
0A47 0	2276	DC	/2276	IN	31110750
0A48 0	3621	DC	/3621	E	31110760
0A49 0	7652	DC	/7652	NO	31110770
0A4A 0	229A	DC	/229A	IS	31110780
0A4B 0	3621	DC	/3621	E	31110790
0A4C 0	FFFF	DC	/FFFF	TERMINATOR	31110800

*

* ALPHA MESSAGE FOR SET UP DATA SET

SCA WRITE/READ BUFFER + LINE NOISE DETECTION

```

*****
*
ADR7 DC /9A36 SE 31110990
      DC /9E21 T 31111000
      DC /8256 UP 31111010
      DC /2132 D 31111020
      DC /3E9E AT 31111030
      DC /3E21 A 31111040
      DC /9A36 SE 31111050
      DC /9E21 T 31111060
      DC /1E52 CO 31111070
      DC /7676 NN 31111080
      DC /361E EC 31111090
      DC /9E22 TI 31111100
      DC /5276 ON 31111110
      DC /9A21 S 31111120
      DC /FFFF TERMINATOR 31111130
      31111140
      31111150
      31111160
      31111170
      31111180
      31111190
      31112000
      3111210
      3111220
      3111230
      3111240
      3111250
      3111260
      3111270
      3111280
      3111290
      3111300
      3111310
      3111320
      3111330
      3111340
      3111350
      3111360
      3111370
      3111380
      3111390
      3111400
      3111410
      3111420
      3111430
      3111440
      3111450
      3111460
      3111470
      3111480
      3111490
      3111500
      3111510
      3111520
      3111530
      3111540
      3111550
      3111560
      3111570
      3111580
      3111590
      3111600
      3111610
      3111620
      3111630
      3111640
      3111650
      3111660
*****
*
ALPHA MESSAGE FOR RESET DATA SET
*****
*
ADR8 DC /6236 RE 3111210
      DC /9A36 SE 3111220
      DC /9E21 T 3111230
      DC /3E32 AD 3111240
      DC /3E56 AP 3111250
      DC /9E36 TE 3111260
      DC /6221 R 3111270
      DC /3E76 AN 3111280
      DC /3221 D 3111290
      DC /263E HA 3111300
      DC /7616 NG 3111310
      DC /2182 U 3111320
      DC /5621 P 3111330
      DC /5626 PH 3111340
      DC /5276 ON 3111350
      DC /3621 E 3111360
      DC /FFFF TERMINATOR 3111370
      3111380
      3111390
      3111400
      3111410
      3111420
      3111430
      3111440
      3111450
      3111460
      3111470
      3111480
      3111490
      3111500
      3111510
      3111520
      3111530
      3111540
      3111550
      3111560
      3111570
      3111580
      3111590
      3111600
      3111610
      3111620
      3111630
      3111640
      3111650
      3111660
*****
*
ALPHA MESSAGE FOR LINE NOISE TERMINATE
*****
*
ADR9 DC /5E22 LI 3111430
      DC /7636 NE 3111440
      DC /2176 N 3111450
      DC /5222 OI 3111460
      DC /9A36 SE 3111470
      DC /2162 R 3111480
      DC /5282 OU 3111490
      DC /9E22 TI 3111500
      DC /7636 NE 3111510
      DC /219E T 3111520
      DC /3662 ER 3111530
      DC /7222 MI 3111540
      DC /763E NA 3111550
      DC /9E36 TE 3111560
      DC /3221 D 3111570
      DC /FFFF TERMINATOR 3111580
      3111590
      3111600
      3111610
      3111620
      3111630
      3111640
      3111650
      3111660
*****
*
LOPRT STX L LOOPE TURN ON LOOP ON ERR
ADR7 01 4C00069C BSC L OFF+1 GO TO HOUSE/KEEP RTN
*
*****
*
DIAG TRIGGER ANALYSIS
*
COMPARES *WAS* TRIGGERS WITH S/B
*
TRIGGERS AND ANALIZES ANY ERROR

```

SCA WRITE/READ BUFFER + LINE NOISE DETECTION

```

*****
*
TRGAN DC *-S SAVED RETURN ADDRESS
      LD L DIAXX LD COMBINED DIAGS
      SRT 16
      SLA 16
      SLT 9
      SLA 7
      STO DIAX1 STORE DIAG 1 WAS
      SLA 16
      SLT 6
      SLA 10
      STO DIAX2 STORE DIAG 2 WAS
      LD L TRGWD LOAD DIA ERORR BITS
      SRT 16
      SLA 16
      SLT 9
      SLA 7
      STO TRWD1 STORE DIAG WD 1 ERR
      SLA 16
      SLT 6
      SLA 10
      STO TRWD2 STORE DIAG WD 2 ERR
      LD TRWD1 LD DIAG WD 1 WAS
      BSC Z CHECK FOR ERR BIT ON
      MDX CKIT2 INITIAL ERR BIT CHK
      MDX WDICK GO CHK DIAG WD 2
*****
*
WD1 BSI L MSGID CHK FOR C.E. BITS
      LDX 3 3
      STX L3 TABLE SET UP MSG ID
      LDX 3 /007F
      STX 3 TABLE+2 SET UP MODIFIER ID
      LDX L3 ADR2 LD ALPHA 2 ADDR
      STX 3 TABLE+3
      LD L TGWD1 LD ON/F ERROR BITS
      STO TABLE+9
      LD L TGWD2 LD F/ON ERROR BITS
      STO TABLE+10
      LD DIAW1 LD DIAG WD 1 S/B
      STO TABLE+8
      LD L DIAW2 LD DIAG WD 2 WAS
      SRT 16 MASK OUT C.E. BITS
      SLA 16
      SLT 6
      SLA 10
      STO TABLE+6
      LD L CADSW LOAD DSW WAS
      STO TABLE+5
      XIO L BZON EXECUTE ALARM ON
      BSI I ERROR GO PRINT ERROR
      DC TABLE MESSAGE TABLE
      DC LOPRT LOOP ADDRESS
      XIO L BZOFF EXECUTE ALARM OFF
      LDX 3 0
      STX L3 TRWD1
      STX L3 TGWD1
      STX L3 TGWD2
      STX 3 DIAW1
      MDX WDICK GO CHK DIAG WD 2
*****
*
CKIT2 LD L DIAW1 LD DIAG WD 1 WAS
      AND L TRWD1 AND DIAG WD 1 ERR
      STO L TGWD2 STORE ON/F ERR BITS
      LD L TRWD1 LD DIAG WD 1 ERR
      EOR L TGWD2 EOR DIAG WD 1 ON/F

```


SCA WRITE/READ BUFFER + LINE NOISE DETECTION

```

OADA 0 0000
OADA 0 0000
OADA 0 0000
OADC 0 0000
OADD 0 0000
OADE 0 0000
OADF 0 0000
OAE0 0 0000
OAE1 0 0000
OAE2 0 0000
OAE3 0 0000
OAE4 0 0000
OAE5 0 0000

OAE6 0 0000
OAE7 0 0000
OAE8 0 0000
OAE9 0 0000
OAEA 0 0000
OAEB 0 0000
OAE C 0000
OAE D 0000
OAE E 0000
OAE F 0000
OAF0 0 0000
OAF1 0 0000

OAF2 0 C0F9
OAF3 0 1890
OAF4 0 1010
OAF5 0 1087
OAF6 0 4820
OAF7 0 7002
OAF8 01 4C800A81

OAF A 01 C4000AEA
OAF C 01 E4000AEC
OAF E 01 D4000AF1
OB00 01 C4000AEC
OB02 01 F4000AF1
OB04 01 D4000AF0
OB06 01 440009D3
OB08 0 6304
OB09 01 6F000ADA
OB0B 0 637F
OB0C 0 68CF
OB0D 01 67000A0A
OB0F 0 68C0
OB10 01 C4000AF0
OB12 0 D0DD
OB13 01 C4000AF1
OB15 0 D0CE
OB16 0 C0D3
OB17 0 D0CA
OB18 01 C4000725
OB1A 0 1890

```

```

31112350
31112360
31112370
31112380
31112390
31112400
31112410
31112420
31112430
31112440
31112450
31112460
31112470
31112480
31112490
31112500
31112510
31112520
31112530
31112540
31112550
31112560
31112570
31112580
31112590
31112600
31112610
31112620
31112630
31112640
31112650
31112660
31112670
31112680
31112690
31112700
31112710
31112720
31112730
31112740
31112750
31112760
31112770
31112780
31112790
31112800
31112810
31112820
31112830
31112840
31112850
31112860
31112870
31112880
31112890
31112900
31112910
31112920
31112930
31112940
31112950
31112960
31112970
31112980
31112990
31113000
31113010
31113020

```

SCA WRITE/READ BUFFER + LINE NOISE DETECTION

```

OB1B 0 1010
OB1C 0 1086
OB1D 0 100A
OB1E 0 D0C2
OB1F 01 C4000724
OB21 0 D0BE
OB22 0 C0C5
OB23 0 D0BB
OB24 01 C0000712
OB26 00 44800162
OB28 1 0ADA
OB29 1 0A7D
OB2A 01 C0000714
OB2C 0 6300
OB2D 0 68BE
OB2E 01 6F000AF0
OB30 01 6F000AF1
OB32 0 68B7
OB33 01 4C800A81

OB35 0 0001
OB36 0 0003
OB37 0 0005

OB38 0 0000
OB39 0 6300
OB3A 01 6F000AE2
OB3C 01 6F000AE3
OB3E 01 C4000AE8
OB40 01 E4000B92
OB42 01 D4000B90
OB44 01 C4000AE6
OB46 01 E4000B92
OB48 01 D4000B91
OB4A 01 F4000B90
OB4C 0 4820
OB4D 0 7002
OB4E 01 4C800B38

OB50 01 D4000B8F
OB52 01 C4000B90
OB54 01 E4000B8F
OB56 01 D4000AE2
OB58 01 C4000B8F
OB5A 01 F4000AE2
OB5C 01 D4000AE3
OB5E 0 7000

OB5F 01 C4000AE2
OB61 0 4820
OB62 0 7006
OB63 01 C4000AE3
OB65 0 4820
OB66 0 7002
OB67 01 4C800B38

OB69 01 440009D3
OB6B 0 6301

```

SCA WRITE/READ BUFFER + LINE NOISE DETECTION

0B6C 01 6F000ADA	STX L3 TABLE	SET UP MSG ID	31113710
0B6E 0 635F	LDX 3 /005F		31113720
0B6F 01 6F000ADC	STX L3 TABLE+2	SET UP MODIFIER ID	31113730
0B71 01 67000A20	LDX L3 ADR4	LOAD ALPHA 4 ADDRESS	31113740
0B73 01 6F000ADD	STX L3 TABLE+3	SET UP ALPHA MSG	31113750
0B75 01 C4000725	LD L DIAW2	LD DIAG WD 2 WAS	31113760
0B77 0 1890	SRT 16	MASK OUT C.E. BITS	31113770
0B78 0 1010	SLA 16		31113780
0B79 0 1086	SLT 6		31113790
0B7A 0 100A	SLA 10		31113800
0B7B 01 D4000AEO	STO L TABLE+6		31113810
0B7D 01 C4000724	LD L DIAW1	LD DIAG WD 1 WAS	31113820
0B7F 01 D4000ADF	STO L TABLE+5		31113830
0B81 01 C4000AEB	LD L CADSW	LOAD DSW WAS	31113840
0B83 01 D4000AE1	STO L TABLE+7		31113850
0B85 01 0C000712	XIO L BZON	EXECUTE ALARM ON	31113860
0B87 00 44800162	BSI I ERROR	GO PRINT ERROR	31113870
0B89 1 0ADA	DC TABLE	MESSAGE TABLE	31113880
0B8A 1 0A7D	DC LDPRT	LOOP ADDRESS	31113890
0B8B 01 0C000714	XIO L BZOFF	EXECUTE ALARM OFF	31113900
0B8D 01 4C800B38	BSC I INTAN	EXIT TO RETURN ADDR	31113910
			31113920
0B8F 0 0000	ERINT DC **	INTERRUPT ERR BITS	31113930
0B90 0 0000	INWAS DC **	DSW INT BITS WAS	31113940
0B91 0 0000	INSDB DC **	DSW INT BITS S/B	31113950
0B92 0 D800	INTMK DC /D800		31113960
	*		31113970
0B94 05F6	END BEGN	LAST STATEMENT	31113980

SCA WRITE/READ BUFFER + LINE NOISE DETECTION

CROSS REFERENCE LISTING

SYMBOL	VALUE	REFERENCES
ADR1	09DE	099E
ADR2	09F4	0AA3
ADR3	0A0A	0B0D
ADR4	0A20	0B71
ADR5	0A36	0BF5
ADR6	0A44	0917
ADR7	0A4D	0761
ADR8	0A5C	08D6
ADR9	0A6D	07C9
ANAL1	096C	0936,0578
ANAL2	097A	093E,0586
ANL01	0976	0974,0975
ANL02	0984	0982,0983
A1	07ED	07EC,07F2
A10	082C	07E3,0831
A11	0833	07E2,0838
A12	083A	07E1,083F
A13	0841	07E0,0846
A14	0848	07DF,084D
A15	084F	07DE,0854
A2	07F4	07E8,07F9
A3	07FB	07EA,0800
A4	0802	07E9,0807
A5	0809	07E8,080E
A6	0810	07E7,0815
A7	0817	07E6,081C
A8	081E	07E5,0823
A9	0825	07E4,082A
BEGIN	0160	05DC,05F6
BEGN	05F6	0B93
BFRCK	06EC	06DD,06E5,06FE,074C,090B
BFRER	08E9	06F9
BNSW1	09D1	06C4,06D0,0728,0942,0956,0961
BNSW2	09D2	06CA,0946
BZOFF	0714	0770,0793,07D8,08E5,0907,0921,09C4,0AC1,0B2A,0B8B
BZON	0712	076B,07D3,08E0,0901,091B,098E,0A8B,0B24,0B85
B1	0865	0864,086A
B10	0844	085B,08A9
B11	08AB	085A,08B0
B12	0882	0859,08B7
B13	0889	0858,08BE
B14	08C0	0857,08C5
B15	08C7	0856,08CC
B2	086C	0863,0871
B3	0873	0862,0878
B4	087A	0861,087F
B5	0881	0860,0886
B6	0888	085F,0887
B7	088F	085E,0894
B8	0896	085D,089B
B9	089D	085C,08A2
CADSW	0AE8	061A,0667,096D,0989,09AE,0A8B,0B22,0B3E,0B81
CKERR	0B5F	0B5E
CKIT2	0ACC	0A9A
CNTA	0722	06D4,06E7
CNTB	0723	06EF,06FB
CNTER	07A9	07A3,092B
CNTRL	063A	0689,0699,07DC,08D4
CN2D	0649	063D
CN30	0650	0642
DELY3	05FA	0601
DIARD	071C	0628,0675,097B,09D4
DIAW1	0724	0626,0673,070C,09BA,0AAE,0B1F,0B7D
DIAW2	0725	0620,066D,070E,079F,09B2,0AB1,0B1B,0B75
DIAXX	0AEE	093C,097D,0A82

SCA WRITE/READ BUFFER + LINE NOISE DETECTION

DIAX1	0AE9	0A88,0AAC,0ACA,0ACC
DIAX2	0AEA	0A8C,0AFA,0B16,0B32
DIA00	092F	094F,095F,096A
DIA01	0930	0954
DLYTM	07B1	0754,07B7,0927
DSWAN	0988	0976,09CD
DSWRD	0AE7	0971,098B,098F,09CB
DSWSN	0706	0613,0618,0665,075D,0797
DSWW1	09CF	06B5,098D,0991,09A2,09C7
DSWW2	09D0	0993,09A6,09C9
DSWXX	0AE6	06AB,0934,096F,09AA,0B44
DSW00	092D	094C,095C,0967
DSW01	092E	0951
END	0164	05DC,05DC,05DC,05DC,064E
ENDDP	0710	06CC,0751,0923
ERINT	088F	0B50,0B54,0B58
ERLCK	0166	05EA
ERROR	0162	05DC,0903,091D,09C0,0ABD,0B26,0B87
EVENI	06E0	06D6
HNGUP	07C7	079B,08E7
ILO	017A	05DC
IL1	018A	05DC,0695
IL2	019A	05DC
IL3	01AA	05DC
IL4	018A	05DC
INERR	0B50	0B4D
INSDB	0B91	0B48
INTAN	0B38	0636,0B4E,0B67,0B8D
INTER	0B69	0B62,0B66
INTMK	0B92	0B40,0B46
INTR	0617	0630,0634,0693
INWAS	0B90	0542,0B4A,0B52
JUMP	0745	072D,0733,0742
K0001	0B35	05F8,0913
K0003	0B36	08F1
K0005	0B37	08D0
LOCK	05E7	05ED,05F3,06B9
LOG	0163	05DC,076C,07D5,08E2
LOGBY	0167	
LOG01	0761	
LOG02	08CE	07AE,08D3
LODP	05F5	05EF,05F1,0656,068E
LOOPE	06C2	06BB,06BD,0A7D
LOPRT	0A7D	0906,0911,0920,09C3,0AC0,0B29,0B8A
LRTN	0662	0663,069E
LSTNN	0797	078A
MASK	0717	07A1
MASK1	0718	075E,0799
MIC2	078A	05F3,0603,075B,0795,07BE,07C5
MLSCF	05E5	060C,0658,06F3,073C,0778,0784
MODES	0677	0638
MSGID	09D3	0995,09DC,0A9C,0B06,0B69
NOISE	090D	07A7
ODEVN	0721	06D5,06D8,06E1
OFF	069B	0687,0697,06A0,06C0,07DA,0A7F
OFF0	06A6	06A9
OFF1	06AB	06AE
OFF2	0680	06B3
OFF3	06B5	06B8
ONES	0716	06E2
PASS	07B0	078E,07AB
PATNA	07DE	072B,0745
PATNB	0856	0731
PID	05DC	05F8
PRNT	068B	0685
PRTER	07A5	
PRTPN	071E	0606,062A
PRTSW	068F	0683,068C,08E9

SCA WRITE/READ BUFFER + LINE NOISE DETECTION

RAD	05DE	062C,0654,069C
RCVED	071F	06F7,0702,0749,08FD
RDAGN	06F0	06F1,06FD
RDAN	0956	067D
RDCAT	0702	06F0,0748
RDIA1	070C	061C,0669
RDIA2	070E	061E,066B,079D
RDMD	071A	067B,06CF
RDY	078D	075F,07AD
RESAN	0942	0679
RESET	0700	0632,06A2,06C6,06D2,072A,07C7
RESMD	0719	0677,0680,06C3,06C9
REST1	094C	0945
REST2	0951	0949
RETRN	0683	0940,094A,095A,0965
RID	05DD	063F,0647,0649,064B,0650,0691
RIDCK	0663	0641,064D
RLCF	0168	
RQKB	018C	05DC
RQTY	018B	05DC
RTNSW	0165	065A
RTRN5	077C	0776
RTRN6	0788	0782
RTR01	095C	0958
RTTBL	065E	062E,0652,0663
RT1	06C3	065E
RT2	06C9	065F
RT3	06CF	0660,06DF,06E9
RT4	0726	0661
RT5	0751	0662,0774
SEND	0720	06DB,06E3,06F8,0704,074A,07F0,07F7,07FE,0805,080C, 0813,081A,0821,0828,082F,0836,083D,0844,084B,0852, 0868,086F,0876,087D,0884,088B,0892,0899,08A0,08A7, 08AE,08B5,08BC,08C3,08CA,08F9 072F,0735,073A,0743,0750
SHFT	0739	0615,068D,08EC
SNDSW	0664	05DC,060E,065C,06F5,073E,077A,0786
START	0161	0938,094D,0952,095D,0968
STOR1	0931	0950,0955,0960,096B
STOR2	093A	0753,0925
STRD	0708	05E3,05E4,05E5
STRT	0690	0756,0929
STST	070A	0932,093A
STWD	0AED	
SVKB	018D	
SW0	05DF	05E8
SW1	05E0	063B,0645,08CE
SW2	05E1	078B
SW3	05E2	
TABLE	0ADA	06A6,0763,0766,0769,076F,07A5,07CB,07CE,07D1,07D7, 08D8,08DB,08DE,08E4,08EF,08F3,08F7,08FB,08FF,0905, 090E,0915,0919,091F,0998,099C,09A0,09A4,09A8,09AC, 09B0,09B4,09BC,09C2,09DA,0A9F,0AA2,0AA5,0AAB,0AAB, 0AAD,0AB0,0AB7,0ABA,0ABF,0B09,0B0C,0B0F,0B12,0B15, 0B17,0B1E,0B21,0B23,0B2B,0B3A,0B3C,0B56,0B5A,0B5C, 0B5F,0B63,0B6C,0B6F,0B73,0B7B,0B7F,0B83,0B89 0AA6,0AC6,0AD6,0B04,0B10,0B2E 0AA9,0AC8,0AD0,0AD4,0AFE,0B02,0B13,0B30 07B3,07B4 0984,0AF8,0B33 097F,0A8D 0A93,0A98,0AC4,0ACE,0AD2 0A97,0AF2,0AFC,0B00,0B2D 0608 06EA,0737 0600,0605,0612 06C7,06CD 060A 0772,077E,077F
TGWD1	0AF0	
TGWD2	0AF1	
THDLY	0789	
TRGAN	0A81	
TRGWD	0AEF	
TRWD1	0AEB	
TRWD2	0AEC	
UINTR	0636	
WAIT1	05FB	
WAIT2	0606	
WAIT3	0601	
WAIT4	0610	
WAIT5	0775	

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SCA WRITE/READ BUFFER + LINE NOISE DETECTION

WAIT6	0781	078B,07A9
WCNT	05F9	05FD,0602,0610,0759,077C,0788,0791,07C0,07C3
WD1	0A9C	0A08
WDICK	0AF2	0A9B,0ACB
WD2	0AFA	0AF7
WRCAT	0704	06DC,06E4,0747
WRDCK	09CD	
WRTAN	0961	0681
WRTMD	071B	067F,0726
WTRO1	0967	0963
XR3	0909	08EB
XR3A	074E	074B

DATE 15NOV66
EC NO. 419643

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1. PURPOSE

THE PURPOSE OF SCA WRT/RD BFR AND LINE NOISE DETECTION PROGRAM, IS TO CHECK THE SCA BUFFER FOR PROPER WRITE/READ OPERATION, AND MONITOR THE PHONE-LINE FOR UNDESIRABLE NOISE WHICH WILL CAUSE PROBLEMS IN THE SCA OPERATION.

2. PREREQUISITES

2.1*** PROGRAM PREREQUISITES

THIS PROGRAM MUST RUN UNDER CONTROL OF THE DIAGNOSTIC MONITOR. THE DIAGNOSTIC MONITOR USES 1.5 K STORAGE WORDS AND THIS PROGRAM USES 1.5 K STORAGE WORDS.

2.2*** EQUIPMENT PREREQUISITES

1. SET DATA TERMINAL SWITCH IN THE OPERATE POSITION. (SEE NOTE)
2. SET STR SWITCH TO STR POSITION.
3. SET C.E. MODE SWITCH TO OFF POSITION.
4. SET SCA ALARM SWITCH TO ON POSITION.
5. SET BAUD SELECTOR SWITCH TO 2400, 2000, 1200, OR 600 POSITION.

NOTE....IF DATA SET TERMINAL CABLE IS NOT INSERTED INTO DATA SET, IT IS POSSIBLE TO DETECT SETTING OF ANS. REQ. DSW BIT. THE REASON IS, THE ANS. REQ. SIGNAL LINE IS FLOATING, AND SUBJECT TO NOISE.

3. OPERATING PROCEDURE

THESE OPERATING PROCEDURES APPLY TO SINGLE PROGRAM OPERATION ONLY. FOR OVERLAP OPERATION REFER TO SECTION 3.2.3 OF THE 1130 DIAGNOSTIC MONITOR II DOCUMENTATION.

3.1*** PROGRAM LOADING

STANDARD MONITOR LOADING PROCEDURES APPLY. THESE PROCEDURES ARE SUMMARIZED HERE. SEE DM USE PROCEDURE FOR DETAILS.

1. SET FIRST TYPEWRITER TAB 20 CHARACTERS FROM LEFT MARGIN.
2. SET BIT SWITCH 15 OFF - LOAD AND GO
 ON - TO SPECIFY OPTIONS BEFORE RUNNING.

IF HALTED AFTER LOADING, SELECT PROGRAM OPTIONS THEN TURN OFF HALT SWITCH OR FOLLOW NORMAL RESTART PROCEDURE (SECTION 3.5).

3. LOAD DIAGNOSTIC MONITOR AND SCA WRT/RD BFR, LINE NOISE.
4. SELECT PROGRAM OPTIONS, IF DESIRED.

3.2*** PROGRAM OPERATION.

STANDARD MONITOR OPERATING PROCEDURES APPLY. THESE PROCEDURES ARE SUMMARIZED HERE. SEE DM USE PROCEDURE FOR DETAILS.

3.2.1 PROGRAM CONTROL - FUNCTION 0

1. SET SWITCHES 0-1 OFF.
2. SET SWITCHES 0-7 TO 01.

SW	FUNCTION
8	RESTART
9	ROUTINE START MESSAGE
10	LOCK ON FUNCTION
11	LOOP PROGRAM
12	LOOP ON ERROR
13	BYPASS ERROR PRINTOUT
14	HALT ON ERROR
15	HALT

3. PRESS INT REQ KEY ON CONSOLE.

3.2.2 ROUTINE SELECTION - FUNCTION 1

THE SELECTED ROUTINE WILL LOOP UNTIL A NEW ROUTINE IS SELECTED.

1. TO SET ROUTINE SELECTION
 - A. SET SWITCHES 0-7 TO 41.
 - B. SET ROUTINE NUMBER IN SWITCHES 10-15.

RTN	DESCRIPTION
1	PROGRAM RESET
2	END OP
3	ONES/ZERO BUFFER TEST
4	FLOATING BIT PATTERN BUFFER TEST
5	LINE NOISE LISTENING TEST

THIS OPTION WILL BE USED TO TROUBLE SHOOT FAILURES.

C. PRESS INT REG KEY ON CONSOLE.

2. TO RESET ROUTINE SELECTION SET AS IF SELECTING ROUTINE ZERO.

3.3*** PROGRAM HALTS

3.3.1 NORMAL HALTS

HALT NO. (B REG)	DESCRIPTION	RESTART ACTION
3001	PROGRAM STOP OR ADDRESS STOP	PRESS START
3002	HALT ON ERROR	DISPLAY MODE, PRESS START RUN MODE, PRESS START

3.3.2 ERROR HALTS

HALT NO. (B REG)	DESCRIPTION	RESTART ACTION
30F1	CHECK SUM ERROR ON FIRST CARD OF LOADER	RELOAD
30F2	READER DSW ERROR WHEN LOADING LOADER	RELOAD
30F3	CARD 2 OF LOADER DID NOT LOAD	RELOAD
30F4	CAN NOT CLEAR CORE - DUE TO ERROR IN ADDRESSING UPPER CORE.	
30F5	READER CHECK WHEN LOADING MONITOR OR TEST PROGRAM	NPRO THEN PLACE CARDS RUN OUT IN FRONT OF REMAINING DECK AND PRESS START.
30F6	MONITOR DID NOT LOAD	RELOAD
30F7	CHECK SUM WHEN LOADING MONITOR	RELOAD
30F8	READER NOT READY	MAKE READER READY
30F9	INVALID INTERRUPT WHICH WILL NOT RESET	PRESS RESET AND START
30FA	CONSOLE PRINTER HANG UP - BUSY WILL NOT GO OFF	FIX THE CONSOLE PRINTER OR NOP THIS WAIT

3.4*** PROGRAM TERMINATION

IF LOOP PROGRAM HAS NOT BEEN SPECIFIED, THE PROGRAM WILL TERMINATE
AT THE END OF ROUTINE 05.

IF ANY ROUTINE IS SELECTED, THAT ROUTINE WILL LOOP AND WILL NOT
TERMINATE.

3.5*** RESTART

1. TURN OFF SWITCHES 0-7.
2. TURN ON SWITCH 8.
3. SET DESIRED CONTROL IN SWITCHES 9-14.
4. PRESS INTERRUPT REQUEST KEY.

4. PRINTOUTS

ALL PRINTOUTS ARE IN THE STANDARD FORMAT.

APPNN OORR AAAA (MESSAGE)
OR
EPPNN OORR XXXX (MESSAGE)

WHERE A IDENTIFIES STATUS MESSAGES
E IDENTIFIES ERROR MESSAGES
PP IS THE PID OF THE PROGRAM CAUSING THE MESSAGE

THIS WILL BE EITHER 00 FOR MESSAGES ORIGINATED BY MON-
ITOR OR 11 FOR MESSAGES ORIGINATED BY THIS PROGRAM.

NN IS THE MESSAGE SEQUENCE NUMBER
RR IS THE ROUTINE NUMBER
AAAA IS THE ADDRESS OF THE ROUTINE
MESSAGE IS ANY VARIABLE INFORMATION

4.1*** STATUS MESSAGES

A0000 NUM PID ADRS RELF LD
XXXX XXXX XXXX XXXX

THIS MESSAGE IS PRINTED, FOLLOWING THE LOADING OF ANY PROGRAM
(EXCEPT MONITOR). THE MESSAGE GIVES THE LOAD SEQUENCE NUMBER,
THE PROGRAM ID, THE ADDRESS INTO WHICH THE PROGRAM WAS LOADED,
AND THE RELOCATION FACTOR.

A0001 SWS PID
XXXX XXXX

THIS MESSAGE IS PRINTED EACH TIME A VALID SWITCH ENTRY IS READ
BY THE MONITOR. THE MESSAGE CONTAINS THE SWITCH SETTING READ,
TOGETHER WITH THE PROGRAM ID OF THE PROGRAM INTO WHICH THE
CONTENTS OF SWITCHES 8-15 WERE STORED. IF THE SWITCH ENTRY
CALLED FOR HALT OF ANY PROGRAM, THE WORD HALT WILL FOLLOW THE
MESSAGE.

A1100 OORR AAAA

ROUTINE START MESSAGE - IF SWITCH 9, FUNCTION 0, IS TURNED ON,
THIS MESSAGE WILL BE PRINTED BEFORE THE START OF EACH ROUTINE.
R IS THE NUMBER OF THE NEXT ROUTINE AND AAAA IS THE STARTING
ADDRESS.

A1101 0005 AAAA SET UP DATA SET CONNECTIONS

THIS MESSAGE IS PRINTED OUT WHEN ROUTINE 05 IS ENTERED AND DATA
SET READY WAS SENSED, AND FOUND OFF. IF DATA SET CONNECTIONS ARE
NOT MADE, THIS MESSAGE WILL BE REPEATED EVERY 16 SECONDS.

TO MAKE DATA SET CONNECTIONS,

1. PHONE ANOTHER TERMINAL.
2. REQUEST THAT THEIR DATA SET BE PUT IN TALK POSITION, AND
PLACE THE HAND-PHONE IN THE DATA SET CRADLE.
NOTE.
IF THE HAND PHONE IS NOT PLACED IN THE CRADLE, THE MOUTH-
PIECE WILL PICK UP NOISES WITHIN THE ROOM.
3. AT YOUR DATA SET, PUSH THE DATA BUTTON. THIS SHOULD CAUSE
THE SCA RDY LITE TO COME ON, AND CAUSE THE PROGRAM TO
BRANCH TO LINE LISTENING. ANY LINE CONDITIONS EFFECTING
THE COMMUNICATIONS TERMINAL CIRCUITRY, WILL BE DETECTED
AND PRINTED OUT ON THE CONSOLE TYPEWRITER.
4. PLACE YOUR DATA SET HAND PHONE IN ITS CRADLE.

A1102 0005 AAAA RESET ADAPTER AND HANG UP PHONE

THIS MESSAGE WILL BE PRINTED OUT AFTER LINE LISTENING ROUTINE HAS
COME TO ITS NORMAL END. (APPROXIMATELY 10.5 MINUTES) THIS
MESSAGE WILL NOT BE PRINTED OUT IF ROUTINE 05 IS BEING LOOPED, OR
TERMINATED BY RESETTING THE ADAPTER AND HANGING UP THE PHONE

TO RESET ADAPTER AND HANG UP PHONE.

1. AT YOUR DATA SET, PUSH TALK PUSH-BUTTON.
2. PICK UP PHONE FROM CRADLE AND LISTEN FOR DIAL-TONE.
3. PLACE PHONE BACK INTO CRADLE. THIS WILL ALSO HANG UP
PHONE AT THE OTHER TERMINAL.

A1103 0005 AAAA LINE NOISE ROUTINE TERMINATED

THIS MESSAGE WILL BE PRINTED OUT WHENEVER THE DATA SET GOES NOT
READY. NORMALLY THIS WILL BE AUTOMATIC AT THE NORMAL END OF ROUTINE
05, OR WHEN THE C.E. PREMATURELY ENDS THE LINE LISTING ROUTINE BY
PUSHING THE TALK PUSH BUTTON ON THE DATA SET. THE PHONE SHOULD BE
PICKED UP, AND WHEN A DIAL TONE IS HEARD, PLACED BACK INTO
ITS CRADLE.

4.2*** ERROR MESSAGES

E0001 SWS INVLD
XXXX

THE SETTING OF SWITCHES 4-7 DID NOT EQUAL THE LOAD SEQUENCE
NUMBER OF ANY PROGRAM IN CORE.

E0003 OVR CORE

THE PROGRAM, WHICH THE LOADER WAS ATTEMPTING TO LOAD,
EXCEEDED AVAILABLE CORE. LOADING WAS TERMINATED.

E0004

CKSUM

A CHECK SUM ERROR WAS DETECTED WHILE LOADING A TEST PROGRAM.
THIS ERROR OCCURS UNDER ANY OF THE FOLLOWING CONDITIONS.

1. A CARD IS MISSING OR IS OUT OF SEQUENCE.
2. THERE IS AN EXTRA CARD IN THE DECK.
3. THE PUNCHED INFORMATION ON THE CARD IS NOT CORRECT.
4. DATA WAS LOST OR PICKED UP DUE TO A MACHINE MALFUNCTION.
5. DUE TO A CPU MALFUNCTION, THE CHECK SUM WAS NOT CORRECTLY CALCULATED.

WHEN THIS ERROR OCCURS, ATTEMPT TO RELOAD THE PROGRAM.

E0005

000N XXXX

THIS ERROR WILL OCCUR WHEN AN INTERRUPT OCCURS, BUT THE ILSW
WAS NOT CORRECT. N IS THE INTERRUPT LEVEL AND XXXX IS THE
ILSW. THIS PRINTOUT WILL ONLY OCCUR IF THE INTERRUPT IS RESET
BY A BOSI. NO ATTEMPT IS MADE BY THE ERROR ROUTINE TO RESET
THE REQUEST BIT.

WHENEVER AN SCA ERROR MESSAGE IS BEING PRINTED OUT, THE SCA ALARM
IS TURNED ON.

THE C.E. BITS CAN BE JUMPERED, AT C.E. DISCRETION, TO ANY POINT IN
THE SCA CIRCUITRY. IF THESE BITS ARE NOT WIRED, THEY WILL BE SET
TO A 1. (SEE LOGIC FCT32, NOTE 2).

IF ALL DSW ANALYSIS APPEAR GOOD AND MANY FAILURES IN DIAGNOSTIC WORDS,
OR MANY DSW FAILURES AND DIAGNOSTIC WORD APPEAR GOOD, TAKE INTO
CONSIDERATION THE SENSE DSW OR THE SENSE DIAGNOSTIC WORD COMMANDS
ARE FAILING.

E1101 000R AAAA DW1 DW2 DSW ON/F F/ON *CE* INT ERR
 XXXX XX00 XXX0 XX00 XX00 0XX0

NO INTERRUPTS EXPECTED, AND AT LEAST 1 RECEIVED.

DW1 = STORED CONTENTS OF DIAGNOSTIC WORD 1.
DW2 = STORED CONTENTS OF DIAGNOSTIC WORD 2.
DSW = STORED CONTENTS OF DSW.
ON/F = DSW INTERRUPT BITS THAT WERE ON AND SHOULD BE OFF.
F/ON = DSW INTERRUPT BITS THAT WERE OFF AND SHOULD BE ON.
CE = CE BITS OF DIAGNOSTIC WORD 2. (ON OR OFF)

INT ERR = THE CALCULATED DSW INTERRUPT BITS DID NOT COMPARE
WITH THE RECEIVED DSW INTERRUPT BITS.

REFER TO DIAGNOSTIC REFERENCE PAGES FOR ANALYSIS OF ERROR.

E1102 000R AAAA DW1 DW2 WAS S/B ON/F F/ON *CE* DSW ERR
 XXXX XX00 XXX0 XXX0 XXX0 0XX0

DW1 = STORED CONTENTS OF DIAGNOSTIC WORD 1.
DW2 = STORED CONTENTS OF DIAGNOSTIC WORD 2.
WAS = STORED CONTENTS OF DSW.
S/B = CALCULATED CONTENTS OF DSW.
ON/F = DSW BIT THAT WERE ON AND SHOULD BE OFF.
F/ON = DSW BIT THAT WERE OFF AND SHOULD BE ON.
CE = CE BITS OF DIAGNOSTIC WORD 2. (ON OR OFF)

DSW ERR = THE CALCULATED DSW DID NOT COMPARE WITH THE
RECEIVED DSW.

REFER TO DIAGNOSTIC REFERENCE PAGES FOR ANALYSIS OF ERROR.

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E1103 000R AAAA DSW DW2 1WAS 1S/B ON/F F/ON *CE* DW1 ERR
 XXXX XX00 XXX0 XXX0 XXX0 0XX0

DSW = STORED CONTENTS OF DSW.
DW2 = STORED CONTENTS OF DIAGNOSTIC WORD 2.
WAS = STORED CONTENTS OF DIAGNOSTIC WORD 1.
S/B = CALCULATED CONTENTS OF DIAGNOSTIC WORD 1.
ON/F = DIAGNOSTIC WORD 1 BITS THAT WERE ON AND SHOULD BE OFF.
F/ON = DIAGNOSTIC WORD 1 BITS THAT WERE OFF AND SHOULD BE ON.
CE = CE BITS OF DIAGNOSTIC WORD 2. (ON OR OFF)

DW1 ERR = THE CALCULATED DIAGNOSTIC WORD 1 DID NOT COMPARE
WITH RECEIVED DIAGNOSTIC WORD 1.

REFER TO DIAGNOSTIC REFERENCE PAGES FOR ANALYSIS OF ERROR.

E1104 000R AAAA DSW DW1 2WAS 2S/B ON/F F/ON *CE* DW2 ERR
 XXXX XX00 XXX0 XX00 XX00 0XX0

DSW = STORED CONTENTS OF DSW
DW1 = STORED CONTENTS OF DIAGNOSTIC WORD 1.
WAS = STORED CONTENTS OF DIAGNOSTIC WORD 2.
S/B = CALCULATED CONTENTS OF DIAGNOSTIC WORD 2.
ON/F = DIAGNOSTIC WORD 2 BITS THAT WERE ON AND SHOULD BE OFF.
F/ON = DIAGNOSTIC WORD 2 BITS THAT WERE OFF AND SHOULD BE ON.
CE = CE BITS OF DIAGNOSTIC WORD 2. (ON OR OFF)

DW2 ERR = THE CALCULATED DIAGNOSTIC WORD 2 DID NOT COMPARE
WITH RECEIVED DIAGNOSTIC WORD 2.

REFER TO DIAGNOSTIC REFERENCE PAGES FOR ANALYSIS OF ERROR.

E1105 000R AAAA WAS S/B BFR WRT/RD ERROR
 XX00 XX00

WAS = WORD READ FROM THE BUFFER
S/B = WORD SENT TO THE BUFFER

BFR WRT/RD ERROR = WORD WRITTEN INTO THE BUFFER WAS NOT THE SAME
AS THE WORD RECEIVED FROM THE BUFFER.

REFER TO DIAGNOSTIC REFERENCE PAGES FOR ANALYSIS OF ERROR.

/U10
E1106 000R AAAA WAS LINE NOISE
 X000

WAS = THE BIT CONFIGURATION OF THE BITS IN DIAGNOSTIC WORD 2, WHICH
CONCERNED WITH LINE NOISE DETECTION. THE DURATION OF THE
NOISE AS PER BIT CONFIGURATION IS AS FOLLOWS,

8000...NOISE OF 1 TRANSMISSION BIT LENGTH
9000...NOISE OF MORE THAN 1 TRANSMISSION BIT LENGTH
A000...NOISE OF 1 TRANSMISSION CHARACTER LENGTH

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PROG ID 0311-
PAGE 4A

5. COMMENTS

5.1*** PROGRAM PHILOSOPHY

THE SCA WRT/RD BUFFER, LINE NOISE DETECTION PROGRAM IS DESIGNED WITH TWO MAIN OBJECTIVES IN MIND. 1...TO BE ABLE TO DETECT BUFFER WRITE / READ ERRORS. AND 2...TO BE ABLE TO DETECT TELEPHONE LINE NOISES THAT WOULD HAMPER NORMAL OPERATION BETWEEN THIS SCA INSTALLATION, AND THE DESIRED REMOTE STATION. THE EXECUTE TIME OF THIS PROGRAM IS ABOUT 11 MINUTES.

THE FIRST BUFFER CHECK ROUTINE USES AN ALL ONES PATTERN AND AN ALL ZEROS PATTERN. THE DSW AND THE DIAGNOSTIC TRIGGERS ARE ALSO CHECKED FOR PROPER OPERATION.

THE SECOND BUFFER CHECK ROUTINE USES A FLOATING ONE AND A FLOATING ZERO PATTERN. AGAIN, THE DSW AND DIAGNOSTIC TRIGGERS ARE ALL CHECKED FOR PROPER OPERATION.

THE BUFFER CHECK ROUTINES TAKE APPROXIMATELY 20 SECONDS TO EXECUTE.

THE LINE NOISE DETECTION ROUTINE LISTENS FOR LINE NOISES THAT EFFECTS THE OPERATION OF NORMAL SCA DATA TRANSMISSION. THE OCCURANCE OF THIS TYPE OF LINE NOISE WILL BE PRINTED OUT AS AN ERROR MESSAGE.

THE LINE NOISE DETECTION ROUTINE TAKES APPROXIMATELY 10.5 MINUTES TO EXECUTE.

5.2*** ROUTINE DESCRIPTION

ROUTINE 01 ... PROGRAM RESET

THIS ROUTINE CHECKS THE ABILITY OF THE SCA RESET COMMAND TO ESTABLISH INITIAL RESET CONDITIONS OF THE SCA CIRCUITRY. ALL DIAGNOSTIC WORD TRIGGERS AND DSW TRIGGERS SHOULD BE TURNED OFF, WITH THE EXCEPTION OF THE SEND/RECEIVE RUN TRIGGER.

ROUTINE 02 ... END OPERATION

THIS ROUTINE ENSURES THAT THE EXECUTION OF THE ENDDP COMMAND DOES NOT BRING UP UNDESIRABLE LEVELS IN THE SCA CIRCUITRY.

ROUTINE 03 ... ONES / ZEROS TEST

THIS ROUTINE WILL FIRST LOAD THE BUFFER WITH ZEROS AND THEN READ THE BUFFER 1024 TIMES. A CHECK FOR DATA ERRORS IS MADE FOR EACH BUFFER READ OPERATION. IT WILL THEN LOAD THE SCA BUFFER WITH ALL ONES AND READ THE BUFFER 1024 TIMES. ANOTHER CHECK FOR DATA ERRORS IS MADE EACH TIME THE BUFFER IS READ. THESE TWO OPERATIONS ARE REPEATED ALTERNATELY 10 TIMES. THE ROUTINE RUN TIME IS APPROXIMATELY 20 SECONDS.

ROUTINE 04 ... FLOATING ONE / ZERO PATTERN

THIS ROUTINE CHECKS THE ABILITY TO WRITE AND READ A FLOATING ONE AND A FLOATING ZERO PATTERN WITHOUT PICKING OR DROPPING BITS. A CHECK IS MADE AFTER THE WRITE AND READ OPERATION OF EACH PATTERN. BIT FLOATING PATTERN IS STARTED IN THE HIGH ORDER POSITION (BIT POSITION 0) OF THE BUFFER AND PROGRESSES TO THE LOW ORDER POSITION. (BIT 7) THE PATTERN THEN PROGRESSES TO THE HIGH ORDER POSITION.

THE FOLLOWING IS THE PATTERN AND SEQUENCE OF THE BUFFER OPERATION

FLOATING 1	FLOATING 0
1000 0000	0111 1111
0100 0000	1011 1111
0010 0000	1101 1111
0001 0000	1110 1111
0000 1000	1111 0111
0000 0100	1111 1011
0000 0010	1111 1101
0000 0001	1111 1110
0000 0010	1111 1101
0000 0100	1111 1011
0000 1000	1111 0111
0001 0000	1110 1111
0010 0000	1101 1111
0100 0000	1011 1111
1000 0000	0111 1111

ROUTINE 05 ... LINE NOISE LISTENING ROUTINE

THIS ROUTINE LISTENS FOR TELEPHONE LINE NOISE CONDITIONS THAT WILL EFFECT THE SCA CIRCUITRY. ONLY NOISES EFFECTING SCA CIRCUITS WILL BE DETECTED, AND IDENTIFIED BY AN ERROR PRINTOUT.

THE OPERATION OF THIS ROUTINE IS PERFORMED WHILE ON-LINE WITH A REMOTE DATA SET. A REMOTE TERMINAL IS NOT REQUIRED.

NOISE SIMULATION FOR TEST PURPOSES, CAN BE GENERATED BY WHISTLING INTO THE TELEPHONE MOUTH-PIECE OF THE REMOTE DATA SET. THE WHISTLING TONE FREQUENCY MUST APPROXIMATELY MATCH THE FREQUENCY OF THE DATA NORMALLY COMING OVER THE PHONE LINE.

5.3*** DIAGNOSTIC REFERENCE TABLE

ERROR BIT REFERENCE

ERR BIT	BIT POSITION	DSW ERROR	DW1 ERROR	DW2 ERROR
8	0	READ RESP	SYNC TRIG	CLK GATE
4	1	WRT RESP	IDLE CHAR	SYNC CNTR
2	2	CHECK	CHAR CMPT	1ST TRANS
1	3	TIMEOUT	END OP	PHS CNTR
8	4	ANS REQ	DIAG MODE	RCV TAG
4	5	BUSY	TIMER TR	CHAR PHS
2	6	ENABLE	3 SEC TIMER	
1	7	READY	1.5 SEC TIMER	
8	8	RCV RUN	SEND DATA	

ROUTINE 01	COMMAND	FUNCTION	MODIFIER
	RESET	STR WRT	9

	DSW ERR	DW1 ERR	DW2 ERR	ALARM
	0 1 2 3 4 5 6 7 8	0 1 2 3 4 5 6 7 8	0 1 2 3 4 5	.
S/B DN X
N	F F F F F F F F	F F F F F F F F	F F F F F F	F
E	C C C C C C C C	C C C C C C C C	C C C C C C	C
T	3 3 3 3 3 3 3 2	3 5 6 3 3 3 3 6	1 6 1 2 5 5	3
	5 5 5 5 3 4 3 1	2 3 4 2 1 4 4 3	1 4 1 1 2 3	2
N	1 1 1 1 1 1 1 1	1 1 1 1 1 1 1 1	1 1 1 1 1 1	1
U	B A B A B A C A	B A B A B A A B A	A B A A B B	B
M	A L B M C J Q S V	C U M R E R V P L	U L T E H M	H
.	2 2 2 2 2 4 2 4 6	2 1 2 2 2 2 2 4 4	1 2 2 2 4 1	4

COMMENTS

THIS ROUTINE CHECKS FOR RESET CONDITION OF THE SCA CIRCUITRY. IT SHOULD BE ABLE TO TRAP SOLID AND INTERMITTENT PROBLEMS IN THIS AREA.

ROUTINE 02	COMMAND	FUNCTION	MODIFIER
	END OP	CONTROL	13

	DSW ERR	DW1 ERR	DW2 ERR	ALARM
	0 1 2 3 4 5 6 7 8	0 1 2 3 4 5 6 7 8	0 1 2 3 4 5	.
S/B ON X
N	F F F F F F F F	F F F F F F F F	F F F F F F	F
E	C C C C C C C C	C C C C C C C C	C C C C C C	C
T	3 3 3 3 3 3 3 2	3 5 6 3 3 3 3 6	1 6 1 2 5 5	3
	5 5 5 5 3 4 3 1	2 3 4 2 1 4 4 3	1 4 1 1 2 3	2
N	1 1 1 1 1 1 1 1	1 1 1 1 1 1 1 1	1 1 1 1 1 1	1
U	B A B A B A C A	B A B A B A A B A	A B A A B B	B
M	A L B M C J Q S V	C U M M E R V P L	U L T E H M	N
.	2 2 2 2 2 4 2 4 6	2 1 2 4 2 2 2 4 4	1 2 2 2 4 1	4

COMMENTS

THIS ROUTINE CHECKS THE END OPERATION COMMAND.

ROUTINE 03
 AND
 ROUTINE 04

BUFFER BIT

	0	1	2	3	4	5	6	7
N	F	F	F	F	F	F	F	F
E	C	C	C	C	C	C	C	C
T	7	7	7	7	7	7	7	7
	3	3	3	3	3	3	3	3
N	1	1	1	1	1	1	1	1
U	A	A	A	A	A	A	A	A
M	Y	X	H	V	U	T	S	R
.	4	4	4	4	4	4	4	4

COMMENTS

IF FAILURE APPEARS NOT TO BE BUFFER, RUN SCA PROGRAM INSTRUCTION FT. POINTS. BE SURE TO REMOVE JUMPER AFTER TEST.

SCA WRAP-AROUND TEST

```
0000      ORG      **150C      31200010
          *        EQLATE TABLE 31200020
          *                                     31200030
          *                                     31200040
          *                                     31200050
          * THIS TABLE EQLATES TEST PROGRAM LABELS 31200060
          * TO THEIR EQUIVALENT DIAGNOSTIC MONITOR 31200070
          * ADDRESSES.          31200080
          *                                     31200090
          *----- 31200100
          * MONITOR ENTRY ADDRESSES          31200110
          *----- 31200120
0160      BEGIN EQU    /160      BEGIN ROUTINE 31200130
0161      START EQU   BEGIN+1     SUPERVISOR ROUTINE 31200140
0162      ERKOR EQU   START+1     ERKOR LOG ROUTINE 31200150
0163      LOG EQU     ERKOR+1     STATUS LOG ROUTINE 31200160
0164      END EQU     LOG+1        END ROUTINE    31200170
          *                                     31200180
          *----- 31200190
          * MONITOR CONTROL WORD ADDRESSES 31200200
          *----- 31200210
0165      RTNSW EQU   END+1        ROUTINE START SW 31200220
0166      ERLCK EQU   END+2        LCK ON ERR CONTROL 31200230
0167      LGBY EQU   END+3        I/C BUSY SW ADDR 31200240
0168      RLCP EQU   END+4        RELOC FACTOR ADDR 31200250
          *                                     31200260
          *----- 31200270
          * INTERRUPT TRANSFER VECTOR ADDRESSES 31200280
          *----- 31200290
017A      IL0 EQU    /17A        INTERRUPT LEVEL 0 31200300
018A      IL1 EQU   IL0+16       INTERRUPT LEVEL 1 31200310
019A      IL2 EQU   IL1+16       INTERRUPT LEVEL 2 31200320
01AA     IL3 EQU   IL2+16       INTERRUPT LEVEL 3 31200330
01BA     IL4 EQU   IL3+16       INTERRUPT LEVEL 4 31200340
01BB     RQTY EQU   IL4+1        CCM/PRINT REQUEST 31200350
01BC     RQKB EQU   RQTY+1       USE KEYBOARD REQUEST 31200360
01BD     SVKB EQU   RQKB+1       KB SERVICE REQUEST 31200370
          *----- 31200380
          *                                     31200390
          *----- 31200400
          * SCA WRAP-AROUND TEST           31200410
          *----- 31200420
          *----- 31200430
          *----- 31200440
          * PROGRAM STATUS TABLE          31200450
          *----- 31200460
          *----- 31200470
          *                                     31200480
05DC C 0312  PID DC /0312    PROGRAM ID        31200490
05DD C 0000  RID DC /0000    ROUTINE NUMBER    31200500
05DE C 0000  RAD DC /0000    ROUTINE ADDRESS  31200510
05DF C 0000  SWU DC 0        PROGRAM CONTROL  31200520
05E0 C 0000  SW1 DC 0        ROUTINE SELECTION 31200530
05E1 C 0000  SW2 DC 0        OPTION SELECTION SW 31200540
05E2 C 0000  SW3 DC 0        RTN SCAN OPTION  31200550
05E3 I 0863  DC STRT        LCCP PROGRAM     31200560
05E4 I 0863  DC STRT        RESTART ADDRESS  31200570
05E5 I 0863  MLSCF DC STRT  ENTRY SET IN MN/LINE 31200580
05E6 C FFFF  DC /FFFF       TERMINATOR    31200590
          *----- 31200600
          *----- 31200610
          * LCK ON FUNCTION ROUTINE        31200620
          *----- 31200630
          *----- 31200640
05E7 C 0000  LOCK DC /0000   LD SWO        LD SWO        31200650
05E8 C1 C40005DF LD L SWO      OR WITH MON LOCK SW 31200660
05EA C0 EC800166 GR I ERLCK   OR WITH MON LOCK SW 31200670
05EC C 100A   SLA 10          CHECK BIT 10  31200680
05ED C1 4C9005E7 BSC I LGCK,- BR IF NCT LOCK/FUNC 31200690
```

SCA WRAP-AROUND TEST

```
05EF C1 C40005F5 LD L LCCP    LCAE LCCP ADDR 31200690
05F1 C1 4CA005F5 BSC I LCCP,2 LCCP ON LAST FUNC 31200700
05F3 C1 4C8005E7 BSC I LOCK   IF LOCP ADDR NCT 31200710
05F5 C 0000     LOCP DC *-*     LOCK/ERR LCCP ACR. 31200720
          *----- 31200730
          *----- 31200740
          * TEST INITIALIZATION          31200750
          *----- 31200760
          *----- 31200770
05F6 C0 4480016C BEGN BSI I BEGN    MGN INITIALIZATION 31200780
05F8 I 05DC     DC PID      PST TABLE      31200790
          *----- 31200800
          *----- 31200810
          * START OF TEST AND SINGLE PASS INITIALIZE 31200820
          *----- 31200830
          *----- 31200840
          *----- 31200850
          * PROGRAM WAIT                  31200860
          *----- 31200870
          *----- 31200880
          * THIS ROUTINE IS USED BY THE TEST PROGRAM 31200890
          * TC MARK TIME WHILE WAITING FOR AN INTERRUPT 31200900
          * TO OCCUR.          31200910
          *----- 31200920
          *----- 31200930
          *----- 31200940
05F9 C 0000     WCNT DC 0       WAIT COUNTER    31200950
05FA C 1000     DELY3 DC /1000  DELAY CCNSTANT 31200960
          *----- 31200970
          *----- 31200980
05FB C C0FE     WAIT3 LD DELY3    LD 4 SEC CCNSTANT 31200990
05FC C C0FC     STC          SET IN WAIT COUNTER 31201000
05FD C1 C40005E1 LD L SW2          31201010
05FF C 100B     SLA 11          31201020
0600 C 4810     BSC -          31201030
0601 C 7003     MDX WAIT1     31201040
0602 C C0F6     LD          WCNT    31201050
0603 C 1001     SLA 1          31201060
0604 C C0F4     STC          WCNT    31201070
0605 C 0864     WAIT1 XIO DSWSE   SENSE CSW    31201080
0606 C E87E     OR          DSWAL   SAVE DSW DATA 31201090
0607 C D07D     STC          DSWAL   31201100
0608 C 0867     XIC          RDIA1   REAC DIAG WORD 1 31201110
0609 C 0868     XIO          RDIA2   REAC DIAG WORD 2 31201120
060A C C078     LD          DIAW1   31201130
060B C E86E     OR          DIAL   SAVE DIAG 1 DATA 31201140
060C C D06D     STC          DIAL   31201150
060D C C079     LD          DIAW2   31201160
060E C E86D     OR          DIAL2   SAVE DIAG 2 DATA 31201170
060F C C06C     STC          DIAL2   31201180
0610 C1 67000616 LDX L3 WAITB   SET UP MGN RETURN 31201190
0612 C1 6F0005E5 STX L3 MLSCF   31201200
0614 C0 44800161 BSI 1 START    GO TO MONITOR 31201210
0616 C1 74FFC5F5 WAITB MDX L WCNT,-1 REDUCE WAIT COUNT 31201220
0618 C 70EC     MDX WAIT1    GO BACK TO DELAY 31201230
0619 C 0854     XIC          DSWSH   SENSE RESET DSW 31201240
061A C1 0C00C8BE XIO L RESET    EXECUTE RESET 31201250
061C C1 4400C8B3 BSI L DLYTM    DELAY          31201260
061E C 7077     MDX SWSW     GO MAKE ANALYSIS 31201270
          *----- 31201280
          *----- 31201290
          * INTERRUPT                    31201300
          *----- 31201310
          * THE MONITOR RETURNS CONTROL TO THIS ROUTINE 31201320
          * AFTER DETECTING A CAT INTERRUPT.          31201330
          *----- 31201340
          *----- 31201350
061F C 0000     INTR DC /0000   SENSE CSW    31201360
0620 C 084D     XIC          DSWSH   31201370
```

SCA WRAP-AROUND TEST

```

0621 0 DC73      STG   RDWRT   STCRE DSW   31201370
0622 0 E662      OR    DSWAL   SAVE DSW   31201380
0623 0 D061      STG   DSWAL   31201390
0624 0 0847      XIC   RDCAT   READ BLFFER 31201400
0625 0 C84A      XIC   RDIA1   READ DIAG WORD 1 31201410
0626 0 084B      XIC   RDIA2   READ DIAG WORD 2 31201420
0627 0 C05F      LD    DIAW2   LD CIAG WD 2   31201430
0628 0 E853      OR    DI2AL   SAVE DIAG 2 DATA 31201440
0629 0 D052      STG   DI2AL   31201450
062A 0 C05E      LD    DIAW1   LD CIAG 1 WD   31201460
062E 0 E84E      OR    DI1AL   SAVE DIAG 1 DATA 31201470
062C 0 D04D      STG   DI1AL   31201480
062D 0 C062      LD    DIASW   LD CIAG MODE SW 31201490
062E 0 4820      BSC   Z       31201500
062F 0 70CC      MDX   DIACK   DIAG MCCE SW IS ON 31201510
0630 01 7401C68E MDX L CNTIN,+1 ADD 1 TC INTRPT CNT 31201520
0632 0 100C      NUP                    31201530
0633 0 C65A      LD    CNTIN   LOAD INTRPT COUNT 31201540
0634 0 965C      S    STOCT   SUB INTRPT END CNT 31201550
0635 01 4C2E0662 BSC L GETON,+Z END COLNT NCT YET 31201560
0637 01 0C000674 ENDRT XIC L ENDDP EXECUTE END OP 31201570
0639 0 6850      STX   PRTPN   TURN ON INTRPT RCV 31201580
063A 01 4C80061F BSC I INTR   EXIT TC RETURN ADDR 31201590
*
063C 0 C058      DIACK LD   RDWRT   LOAD SENSEC DSW 31201600
063C 01 4C280644 BSC L CNTRD,+Z BRANCH IF READ INT 31201610
063F 0 C054      LD    WRCNT   LCAC WRT INTRPT SW 31201620
0640 0 4820      BSC   Z       IS WRT INTRPT SW ON 31201630
0641 0 7020      MDX   GETON   NO, GO WITH ROUTINE 31201640
0642 0 6851      STX   WRCNT   TURN ON WRT INT SW 31201650
0643 0 701F      MDX   GETCN+1 GO CN WITH ROUTINE 31201660
0644 01 7401C693 CNTRD MDX L RDCNT,+1 ADD 1 TC RD INT CNT 31201670
0646 0 C04C      LD    RDCNT   LCAC READ INTRPT CNT 31201680
0647 0 903A      S    K0008   CK IF 8TH RD INTRPT 31201690
0648 0 4820      BSC   Z       31201700
0649 0 7001      MDX   RD9TH   NO, CK IF 9TH RD INT 31201710
064A 0 700E      MDX   LUSYN   YES, CK IF SYNC MCDE 31201720
064B 0 C047      RD9TH LD   RDCNT   LD RD INTRPT CCUNT 31201730
064C 0 9036      S    K0009   CK IF 9TH RD INTRPT 31201740
064D 0 4820      BSC   Z       31201750
064E 0 7014      MDX   GETON+1 NO, GO WITH ROUTINE 31201760
064F 0 081C      XIC   RDCAT   READ BUFFER 31201770
0650 0 C033      LD    KCVED   SAVE DATA READ 31201780
0651 0 E83D      OR    INBUF   31201790
0652 0 C03C      STG   INBUF   31201800
0653 0 6836      STX   PRTPN   TURN ON INTRPT RCV 31201810
0654 0 6301      LDX   3 1     31201820
0655 0 683D      STX   3 RDCNT SET RD INT CNT TC 1 31201830
0656 01 7401068D MDX L PASS,+1 ADD 1, RTN PASS CNT 31201840
0658 0 700A      MDX   GETON+1 GO CN WITH ROUTINE 31201850
0659 0 C031      LDSYN LD   SYCMD CK IF SYNC MCDE 31201860
065A 0 4820      BSC   Z       31201870
065B 0 7003      MDX   SYCON   YES, LD SYNC/IDLE 31201880
065C 0 0821      XIC   WRCAT   NO, LD CHAR IN BUFF 31201890
065D 01 4C80061F BSC I INTR   EXIT TC INTRPT ADDR 31201900
*
065F 01 0C0008C0 SYCON XIC L SIREG LD SYNC/IDLE REG 31201910
0661 0 7001      MDX   GETON+1 GO CN WITH ROUTINE 31201920
*
0662 0 0813      GETON XIC L SYALT LD SYNC ALT PATTERN 31201930
0663 0 6826      STX   PRTPN   TURN ON INTRPT RCV 31201940
0664 0 C028      LD    PASS   LD RTN PASS CNT 31201950
0665 0 902C      S    STCPS   SUB END PASS CNT 31201960
0666 01 4C300637 BSC L ENDRT,-Z BRANCH IF LAST PASS 31201970
0668 01 4C80061F BSC I INTR   EXIT TC RETURN ADDR 31201980
*
*****
* IOCC , STORAGE , AND SWS FOR INTRPT RTN
31202000
31202010
31202020
31202030
31202040

```

SCA WRAP-AROUND TEST

```

*****
*
066A 0000      BSS E 0
066A 0 0000      DSWSW DC 0 SENSE CSW 31202050
066B 0 5700      DC /5700 31202060
066C 1 0684      RDCAT DC RCVEU READ CAT 31202070
066D 0 5200      DC /5200 31202080
066E 0 0000      DSWSN DC 0 SENSE CAT CSW 31202090
066F 0 5701      DC /5701 31202100
0670 1 0686      RDIA1 DC DIAW1 READ DIAG WORD 1 31202110
0671 0 5201      DC /5201 31202120
0672 1 0687      RDIA2 DC DIAW2 READ DIAG WORD 2 31202130
0673 0 5202      DC /5202 31202140
0674 0 0000      ENDDP DC 0 END OP 31202150
0675 0 5404      DC /5404 31202160
0676 1 C680      SYALT DC ALT LD SYNC ALT PATTERN 31202170
0677 0 5104      DC /5104 31202180
067E 0 0000      DIAWD DC *- COMBINED DIAG WORDS 31202190
067F 0 0000      DC *- 1 AND 2 MODIFIED 31202200
067A 0 0000      DI1AL DC *- DIAG 1 CATA 31202210
067B 0 0000      DC *- 31202220
067C 0 0000      DI2AL DC *- DIAG 2 CATA 31202230
067D 0 0000      DC *- 31202240
067E 1 0681      WRCAT DC SEND WRITE CAT 31202250
067F 0 5100      DC /5100 31202260
0680 0 AA00      ALT DC /AA00 PATTERN 10101010 31202270
0681 0 FF00      SEND DC /FF00 PATTERN 11111111 31202280
0682 0 C008      K0008 DC /C008 CONSTANT 8 31202290
0683 0 0009      K0009 DC /0009 CONSTANT 9 31202300
0684 0 C000      RCVED DC *- CAT RECEIVED WORD 31202310
0685 0 0000      DSWAL DC *- DSW DATA 31202320
0686 0 C000      DIAW1 DC *- SAVED CIAG 1 31202330
0687 0 C000      DIAW2 DC *- SAVED CIAG 2 31202340
0688 0 0000      CADSW DC *- SAVED CSW 31202350
0689 0 0000      LOPSW DC *- EXP INTRPT SW 31202360
068A 0 0000      PRTPN DC *- PRINT PEND SW 31202370
068B 0 C000      SYCMD DC *- SYNC MCCE SW 31202380
068C 0 0000      RDCPK DC *- DATA READ S/B 31202390
068D 0 0000      PASS DC *- RTN PASS CNT 31202400
068E 0 C000      CNTIN DC *- INTRPT COUNT 31202410
068F 0 0000      INBUF DC *- DATA READ WAS 31202420
0690 0 C000      DIASW DC *- CIAG MCCE SW 31202430
0691 0 0000      STOCT DC *- END INTRPT CNT 31202440
0692 0 0000      STOPS DC *- END RTN PASS CNT 31202450
0693 0 0000      KDCNT DC *- READ INTRPT CNT 31202460
0694 0 0000      WRCNT DC *- WRITE INTRPT SW 31202470
0695 0 0000      RDWRT DC *- SAVED CSW 31202480
*
*****
* SET UP DATA ANALYSIS
*****
*
0696 0 C0EE      SNDSW LD   DSWAL   LCAC DSW DATA 31202570
0697 0 D0F0      STC   CADSW   STCRE DSW DATA 31202580
0698 0 C0E3      LD    DI2AL   LD CIAG 2 DATA 31202590
0699 0 1890      SRT   16     31202600
069A 0 1010      SLA   16     31202610
069B 0 1086      SLT   6     31202620
069C 0 1001      SLA   1     31202630
069D 0 E8DC      OR    DI1AL   CCMEINE DIAG WCS 1+2 31202640
069E 0 D8D9      STD   DIAWD   SAVE CCMBINED DIA WD 31202650
069F 01 44C00C66 BSI L DATCK MAKE DATA CHECK 31202660
06A0 01 4400C03 BSI L INTAN MAKE INTRPT ANAL 31202670
06A1 01 4C00C9F3 BSC L CCNAN GO MAKE ANALYSIS 31202680
*****
*
*****
* CONTROL ROUTINE
31202690
31202700
31202710
31202720

```

SCA WRAP-ARCLND TEST

```

*****
* THIS ROUTINE CHECKS SWITCHES AND CONTRCLS
* THE SEQUENCE IN WHICH TEST ROUTINES ARE RUN
*****
*
*****
*          CONTRCL ROUTINE
*****
* THIS ROUTINE CHECKS SWITCHES AND CONTRCLS
* THE SEQUENCE IN WHICH TEST ROUTINES ARE RUN
*****
C6A5 C  CC00
06A6 01  C40005E0
C6A8 01  4C08C6B4
C6AA 01  D40005DD
C6AC C  904D
C6AD 01  4C08C6BB
C6AF 0  1810
C6B0 01  C400C5E0
C6B2 01  D40005DD
C6B4 01  7401C5DD
C6B6 01  C40005DD
C6B8 0  9C41
C6B9 01  4C3006CB
C6BE 01  6780C5DD
C6BC 01  C70006E2
C6BF 01  D40005DE
C6C1 01  C40005F5
C6C3 01  D40005E5
C6C5 00  D4000C165
C6C7 01  44000701
C6C9 00  44800161

06CB 0  630C
06CC 0  1010
C6CD 01  D7000BAF
C6CF 0  73FF
06DC 0  7CFC
06D1 01  D40005DD
06D3 01  6700C84E
C6D5 01  6F000BB3
C6D7 0  6304
06D8 01  6F000BB0
C6DA 01  CC00C86C
06DC 00  44800163
C6DE 1  0880
06DF 01  CC0008C4
06E1 00  44800164

*****
*          ROUTINE SEQUENCE TABLE
*****
06E3 1  08C8
C6E4 1  08D0
C6E5 1  08DE
06E6 1  C8EC
C6E7 1  08F5
C6E8 1  C8FE
06E9 1  0907
C6EA 1  C910
C6EB 1  0919
C6EC 1  C922
06ED 1  092B
C6EE 1  C945
C6EF 1  C955
C6FC 1  0965

CNTRL DC /0000
LD L SW1
BSC L CN20,+ BRANCH IF AC RTN SELECTED
STC L RID SAVE NEW RTN NUMBER
S RIDCK
BSC L CN30,+ ER IF VALID RTN
SRA 16
STO L SW1 IF INVALID RTN GO
STG L RID TO RTN ONE
CN20 MDX L RID,+1 ADV TO NEXT RTA
LD L RID
S RIDCK
BSC L ENMSG,+Z PRINT RESTORE MSG
CN30 LDX 13 RID
LD L3 RTNRL-1 FETCH RETURN ACRS
STO L RAD
STO L LCCP LGAC LCCP RTN ACGR.
STO L MLSCF SET MLSCF FOR RETURN
BSI L RTNSW
BSI L CKOPT CK FOR FUNC 2+3
BSI I START GO TO MCNITDR

ENMSG LDX 3 12 CLR PRINT TAELE
SLA 16
STC L3 TABLE-1
MDX 3 -1
MDX ENMSG+2
STC L RID RESET RTN ID
LDX L3 NMSG
STX L3 TABLE+3 SET ALPHA MSG
LDX 3 4
STX L3 TABLE SET UP MSG ID
XIC L BZDN EXECUTE ALARM ON
BSI I LOG GO TYPE MESSAGE
DC TABLE
XIC L BZCFF EXECUTE ALARM OFF
BSI I END ENC PRCGRAM

```

SCA WRAP-ARCLND TEST

```

G6F1 1  C970 DC RT0F
G6F2 1  C978 DC RT10
G6F3 1  C984 DC RT11
G6F4 1  C98D DC RT12
G6F5 1  C996 DC RT13
G6F6 1  C99F DC RT14
G6F7 1  C9A6 DC RT15
G6F8 1  C9B5 DC RT16
G6F9 1  C9C7 LRTN DC RT17
*****
*
C6FA 0  0017 RIDCK DC LRTN-RTTEL+1
C6FB 0  CC00 S2WAS DC *-# SW2 WAS
C6FC 0  0000 S3WAS DC *-# SW3 WAS
C6FD 01  7401C66E SAVE1 MDX L RSTSW,+1 ALLOW RESET
C6FF 0  0001 KOOCl DC /0001
C700 C  CC00 FSTCK DC *-#
*
C701 C  C000 CKOPT DC *-#
0702 0  C0F8 LD S2WAS LOAD SW2 WAS
0703 01  F40005E1 EDR L SW2 CGMPARE WITH SW2 NOW
C705 0  4820 BSC Z CHK FOR CHANGE
C706 0  7060 MDX S2PNT SET UP TO PRINT OPTS
0707 01  C40005E1 LD L SW2 LD RTN OPTICN SW
C705 C  100C SLA 12 CHK FOR DIAGNOSTIC
C70A 01  442EC79F BSI L DIAMD,+2 SET UP CIAG PD
070C 0  6302 LDX 3 2
070D 01  6F00C9EA STX L3 RT16+5 SET RTN PASS CNT / 2
070F 01  6F0009CC STX L3 RT17+5 SET RTN PASS CNT / 2
0711 01  C400C5E1 LD L SW2 LD RTN OPTICN SW
0713 0  1C0D SLA 13 CHK FOR ALT RESET
0714 01  442EC7AC BSI L NCRST,+2 SET UP FOR NC RESET
C716 C  C0E6 LD SAVE1 ALLOW RESET
C717 01  D4000871 STC L RSGFF
C715 C  C0E4 LD SAVE1+1
C71A 01  D4000872 STO L RSCFF+1
C71C 01  C400C5E1 LD L SW2 LD RTN OPTICN SW
071E 0  100E SLA 14 CHK FOR FAST PASS
C71F 01  442EC7E7 BSI L FAST1,+Z SET UP FAST PASS
0721 00  67C0100C LDX L3 /1000
0723 01  6F0005FA STX L3 DELY3 RESTORE NORMAL DELAY
G725 01  C40005E1 LD L SW2 LCAC SW2
0727 0  100A SLA 10 CHK IF SEL CHAR
C728 01  4C280733 BSC L CPRTN,+Z BRANCH IF SEL CHAR
072A C  C0D1 LD S3WAS LCAC SW3 WAS
072E 01  F40005E2 EDR L SW3 CGMPARE WITH SW3 NOW
072D 0  4820 BSC Z
C72E C  70C6 MDX S3PNT SET UP TO PRINT
* SCCPE LCOP
NOPNT LD L SW3 LD SCOPE OPT SW
C731 C  4820 BSC Z
0732 C  7C22 MDX SCOPE CK WHAT TIME BASE
0733 01  4C8C0701 OPRTN BSC I CKCPT EXIT TO RETURN
*
0735 01  C4C005E2 S3PNT LD L SW3 LCAC SW3
0737 01  D40006FC STO L S3WAS STORE IN SW3 WAS
C735 C  4820 BSC Z CHK IF CN
073A C  70C1 MDX LGG02 LCG SCCPE LCGP
073B 0  70F3 MDX NOPNT CHK FOR SCCPE OPTICN
*
073C 0  630C LOG02 LDX 3 12 CLEAR MSG TABLE
073D C  1010 SLA 16
C73E 01  D7000BAF STC L3 TABLE-1
0740 C  73FF MDX 3 -1
0741 C  7CFC MDX LGG02+2
0742 01  67000B36 LDX L3 ADR6 SET UP ALPHA MSG
C744 01  6F000BB3 STX L3 TABLE+3
0746 0  6302 LDX 3 2 SET UP MSG ID

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SCA WRAP-AROUND TEST

```

C80A C 7CCA          MDX   CHRTRN  GO SET UP CFAR          31205450
*
C80B C 0000          LOG10 DC    *--          31205460
C80C C 6306          LDX   3 6          LCAC SELECT CHAR      31205470
C80D C 01 C700C832  LD   L3 A1LOG-1    31205480
C80F C 01 D700C848  STC  L3 LCG1-1     31205490
C811 C 73FF          MDX   3 -1          31205500
C812 C 70FA          MDX   LCG10+2      31205510
C813 C 01 4C80C80B  BSC  I  LOG10      EXIT TO RETURN ACDR   31205520
*
C815 C 0000          LOG12 DC    *--          31205530
C816 C 6304          LDX   3 4          LD CIAG RUN FOR MSG  31205540
C817 C 1 C700C838  LD   L3 A1LOG-1    31205550
C818 C 1 C700C84F  STC  L3 LCG1A-1    31205560
C81E C 73FF          MDX   3 -1          31205570
C81C C 70FA          MDX   LOG12+2      31205580
C81D C 01 4C80C815  BSC  I  LOG12      EXIT TO RETURN ACDR   31205590
*
C81F C 0000          LOG13 DC    *--          31205600
C820 C 63C4          LDX   3 4          LD NO RESET FOR MSG  31205610
C821 C 1 C700C83C  LD   L3 B1LOG-1    31205620
C823 C 01 D700C854  STC  L3 LOG18-1    31205630
C825 C 73FF          MDX   3 -1          31205640
C826 C 70FA          MDX   LOG13+2      31205650
C827 C 01 4C80C81F  BSC  I  LCG13      EXIT TO RETURN ACDR   31205660
*
C829 C 1 C00        LOG14 DC    *--          31205670
C82A C 6305          LDX   3 5          LD FAST PASS FOR MSG 31205680
C82B C 01 C700C840  LD   L3 C1LOG-1    31205690
C82C C 01 C700C855  STC  L3 LGG1C-1    31205700
C82F C 73FF          MDX   3 -1          31205710
C830 C 70FA          MDX   LGG14+2      31205720
C831 C 01 4C80C825  BSC  I  LOG14      EXIT TO RETURN ACDR   31205730
*
C833 C 6A36          AOLOG DC    /9A36    SE          31205740
C834 C 5E36          DC    /5E36          LE          31205750
C835 C 1E9E          DC    /1E9E          CT          31205760
C836 C 211E          DC    /211E          C           31205770
C837 C 263E          DC    /263E          HA          31205780
C838 C 6200          DC    /6200          R           31205790
*
C839 C 3222          A1LOG DC    /3222    DI          31205800
C83A C 3E16          DC    /3E16          AG          31205810
C83B C 2162          DC    /2162          R           31205820
C83C C 0 B276          DC    /B276          UN          31205830
*
C83D C 7652          B1LOG DC    /7652    NC          31205840
C83E C 2162          DC    /2162          R           31205850
C83F C 369A          DC    /369A          ES          31205860
C840 C 369E          DC    /369E          ET          31205870
*
C841 C 123E          C1LOG DC    /123E    FA          31205880
C842 C 9A9E          DC    /9A9E          ST          31205890
C843 C 2156          DC    /2156          P           31205900
C844 C 3E9A          DC    /3E9A          AS          31205910
C845 C 9A21          DC    /9A21          S           31205920
*
C846 C 2121          D1LOG DC    /2121    DC          31205930
C847 C 2121          DC    /2121          DC          31205940
C848 C 2121          DC    /2121          DC          31205950
*
*****
* ALPHA MESSAGE FOR SCA PRDG OPTICNS
*****
C849 C 2121          LOG1  DC    /2121    DC          31205960
C84A C 2121          DC    /2121          DC          31205970
C84E C 2121          DC    /2121          DC          31205980

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SCA WRAP-AROUND TEST

```

C84C C 2121          DC    /2121          31206130
C84D C 2121          DC    /2121          31206140
C84E C 2121          DC    /2121          31206150
C84F C 2121          DC    /2121          31206160
*
C850 C 2121          LOG1A DC   /2121          31206170
C851 C 2121          DC    /2121          31206180
C852 C 2121          DC    /2121          31206190
C853 C 2121          DC    /2121          31206200
C854 C 2121          DC    /2121          31206210
C855 C 2121          DC    /2121          31206220
C856 C 2121          LOG1B DC   /2121          31206230
C857 C 2121          DC    /2121          31206240
C858 C 2121          DC    /2121          31206250
C859 C 2121          DC    /2121          31206260
C85A C 2121          LOG1C DC   /2121          31206270
C85B C 2121          DC    /2121          31206280
C85C C 2121          DC    /2121          31206290
C85D C 2121          DC    /2121          31206300
C85E C 2121          DC    /2121          31206310
C85F C 2121          LOG1D DC   /2121          31206320
C860 C 2121          DC    /2121          31206330
C861 C 2121          DC    /2121          31206340
C862 C FFFF          DC    /FFFF          31206350
*
***** TERMINATER *****
***** INITIALIZATION ROUTINE *****
* THIS ROUTINE PERFORMS THE REQUIRED
* INITIALIZATION FOR RESTART OF THE PROGRAM.
* IT LOADS THE FIRST ROUTINE OR THE DESIRED
* ROUTINE VIA SELECT SWITCHES INTO THE
* MAINLINE SEQUENCE CONTROL FIELD.
*****
C863 C 630C          STRT  LDX   3 0          31206360
C864 C 01 6F00C5DD  STX  L3 RID          RESET ROUTINE NC.    31206370
C865 C 01 6500C61F  LDX  L1 INTR          31206380
C866 C 00 6C00C185  STX  L1 ILL-1        SET UP INTRPT ACDR   31206390
C86A C 7009          MDX   GFF1          TURN OFF PRCG SWS   31206400
*
***** TURN OFF ALL SWITCHES *****
*
C86B C CC00          RSTSW DC   *--          PRCG RESET SW       31206410
*
C86C C 0000          OFF   DC           ENTRY AND SAVE AREA 31206420
C86D C C856          XIC   B2OFF          TURN OFF ALARM      31206430
C86E C C0FC          LD   RSTSW          LD PRCG RESET SW    31206440
C86F C 4820          BSC  Z              31206450
C870 C 7003          MDX  OFF1          SW ON , GO TO CFF1  31206460
C871 C 01 7401C86B  RSOFF MDX L RSTSW,+1 TURN ON RESET SW    31206470
C872 C 7002          MDX  OFF2          31206480
*
C873 C 6300          OFF1  LDX   3 0          31206490
C875 C 68F5          STX  3 RSTSW        31206500
*
C876 C 1010          OFF2  SLA   16          31206510
C877 C 6206          LDX  3 6            31206520
C878 C 01 D700C8BB  OFF3  STO  L3 DIAX1-1  31206530
C87A C 73FF          MDX  3 -1          31206540
C87B C 70FC          MDX  OFF3          31206550
C87C C 6312          LDX  3 18          31206560
C87D C 01 D700C883  OFF4  STG  L3 RCVED-1  31206570
C87E C 73FF          MDX  3 -1          31206580
C880 C 70FC          MDX  OFF4          31206590
C881 C 6302          LDX  3 2            31206600

```

SCA WRAP-AROUND TEST

```

C8CA 0 0877          STD  DSWXX
C8CB 01 7401C868    MDX  L  RSTSW,+1  TURN ON PRG RESET
C8CC 01 4C00C5FB    BSC  L  WAIT3
*
*****
*          ROUTINE 02
*****
*
C8CF C 0001          DC      1
C8D0 00 6700FF00    RT02  LDX  L3 /FF00
C8D2 01 6F00068C    STX  L3 RDCPR      SET UP DATA READ S/B
C8D4 0 6301          LDX  3 1
C8D5 01 6F000691    STX  L3 STCT      SET INT CCUNT
C8D7 C 0860          LDD  DSW02
C8D8 C 0E09          STD  DSWXX      SET UP DATA ANALYSIS
C8D9 01 7401CA38    MDX  L  MKSW1,+1
C8DE 01 4C00C9E0    BSC  L  DMRTN      GC TO RTN EXECUTE
*
*****
*          ROUTINE 03
*****
*
C8DE C 0001          DC      1
C8DE 00 67008000    RT03  LDX  L3 /8000
C8E0 01 6F00068C    STX  L3 RDCPR      SET UP DATA READ S/B
C8E2 0 6302          LDX  3 2
C8E3 01 6F000691    STX  L3 STCT      SET INT CCUNT
C8E5 C 0854          LDD  DSW03
C8E6 C 0855          STD  DSWXX      SET UP DATA ANALYSIS
C8E7 01 7401CA38    MDX  L  MKSW1,+1
C8E9 01 4C00C9E0    BSC  L  DMRTN      GC TO RTN EXECUTE
*
*****
*          ROUTINE 04
*****
*
C8EB 0 0001          DC      1
C8EC 00 6700C000    RT04  LDX  L3 /C000
C8EE 01 6F00068C    STX  L3 RDCPR      SET UP DATA READ S/B
C8F0 0 6303          LDX  3 3
C8F1 01 6F00C691    STX  L3 STCT      SET INT COUNT
C8F3 0 70F1          MDX  RT03+7
*
*****
*          ROUTINE 05
*****
*
C8F4 C 0001          DC      1
C8F5 00 6700E000    RT05  LDX  L3 /E000
C8F7 01 6F00068C    STX  L3 RDCPR      SET UP DATA READ S/B
C8F9 0 6304          LDX  3 4
C8FA 01 6F000691    STX  L3 STCT      SET INT CCUNT
C8FC 0 70E8          MDX  RT03+7
*
*****
*          ROUTINE 06
*****
*
C8FC C 0001          DC      1
C8FE 00 6700F000    RT06  LDX  L3 /F000
C900 01 6F00068C    STX  L3 RDCPR      SET UP DATA READ S/B
C902 0 6305          LDX  3 5
C903 01 6F00C691    STX  L3 STCT      SET INT COUNT
C905 C 70DF          MDX  RT03+7
*
*****
*          ROUTINE 07
*****

```

SCA WRAP-AROUND TEST

```

C882 01 D700CA79    OFF5  STG  L3 DSW1-1
C884 C 73FF          MDX  3 -1
C885 0 70FC          MDX  OFF5
C886 0 6304          LDX  3 4
C887 01 D700C5FC    OFF6  STC  L3 TRGWD-1
C889 0 73FF          MDX  3 -1
C88A C 70FC          MDX  OFF6
C88E 01 D4000942    STC  L  DSWXX
C88D 01 D400C943    STC  L  DIAXX
C88F 01 D400067A    STC  L  DIAL
C891 01 D400067C    STC  L  DIAL
C893 01 D400CA38    STC  L  MKSW1
C895 01 D4000A3A    STC  L  MKSW2
C897 00 67C01000    LDX  L3 /1000
C899 01 6F00C5FA    STX  L3 DELY3      RESTORE DELAY 3
C89B 01 C400C9C8    LD  L  RT17+1      RESTORE DATA WORD
C89C 01 C400C681    STC  L  SEND
C89F 00 67C0AA00    LDX  L3 /AA00      RESTORE SYNC ALT
C8A1 01 6F00068C    STX  L3 ALT
C8A3 01 4400C701    BSI  L  CKOPT      CK FOR FUNC 2+3
C8A5 01 440005E7    BSI  L  LCCK      CK LCCK,LCCK/ERR FNC
C8A7 0 000A          LD  L  LCCPE      LD LCCP ON ERR SW
C8A8 0 6300          LDX  3 0
C8A9 0 6B08          STX  3 LOOPE      CLR LCCP ON ERR SW
C8AA 01 4CAG05F5    BSC  I  LCCP,Z      LCCP IF SW IS CN
C8AC 01 C400086B    LD  L  RSTSW
C8AE 01 4C30C8C8    BSC  L  RT01,Z-    BRANCH TO RESET RTN
C8B0 01 4400C6A5    BSI  L  CNTRL
*
C8B2 0 0000          LOOPE DC  *-#      LCCP ON ERROR SW
*
*****
*          TIME DELAY - 140 MSEC
*****
*
C8B3 0 0000          DLYTM DC  *-#
C8B4 0 630C          LDX  3 12
C8B5 0 6B05          STX  3 TMDLY
C8B6 01 74FFC85B    MDX  L  TMDLY,-1
C8B8 C 70F0          MDX  *-3
C8B9 01 4C80C8B3    BSC  I  DLYTM
*
C8BB C 0000          TMDLY DC  *-#
*
*****
*          1130 SCA IOCC
*****
*
C8BC 0000          BSS  E
C8BC 0 C000          BZON  DC  0          ALARM CN
C8BD 0 5102          DC  /5102
C8BE 0 0000          RESET DC  0          PROGRAMED RESET
C8BF 0 5540          DC  /5540
C8C0 1 08C6          SIREG DC  0          SYNC/IDLE REG
C8C1 0 5104          DC  /5104
C8C2 0 0000          DIAG  DC  0          DIAGNOSTIC MCDE
C8C3 0 5408          DC  /5408
C8C4 0 0000          BZOFF DC  0          ALARM CFF
C8C5 0 5101          DC  /5101
C8C6 0 3900          ICHAR DC  /3900      IDLE CHARACTER
*
*****
*          ROUTINE 01
*****
*
C8C7 C 0001          DC      1
C8C8 0 08F5          RT01  XIO  RESET      EXECUTE RESET
C8C9 0 C86C          LDC  DSW01      SET UP ANALYSIS DATA

```


SCA WRAP-AROUND TEST

```

*
C906 G 0001          DC      1
C907 00 6700F800    RT07 LDX  L3 /F800
C909 01 6F00C68C    STX  L3 RDCPR      SET UP DATA READ S/B
C908 0 6306         LDX  3 6
C90C 01 6F00C691    STX  L3 STCCT      SET INT COUNT
C90E 0 70D6         MDX   RT03+7

```

```

*
*****
* ROUTINE 08
*****

```

```

C90F G 0001          DC      1
C910 00 6700FC00    RT08 LDX  L3 /FC00
C912 01 6F00C68C    STX  L3 RDCPR      SET UP DATA READ S/B
C914 0 6307         LDX  3 7
C915 01 6F00C691    STX  L3 STCCT      SET INT COUNT
C917 0 70CD         MDX   RT03+7

```

```

*
*****
* ROUTINE 09
*****

```

```

C918 0 0C01          DC      1
C919 00 6700FE00    RT09 LDX  L3 /FE00
C91E 01 6F00C68C    STX  L3 RDCPR      SET UP DATA READ S/B
C91C 0 6308         LDX  3 8
C91E 01 6F00C691    STX  L3 STCCT      SET INT COUNT
C920 0 70C4         MDX   RT03+7

```

```

*
*****
* ROUTINE 0A
*****

```

```

C921 0 C0C1          DC      1
C922 00 67C0FF00    KTOA LDX  L3 /FF00
C924 01 6F00C68C    STX  L3 RDCPR      SET UP DATA READ S/B
C926 0 6309         LDX  3 9
C927 01 6F00C691    STX  L3 STCCT      SET INT COUNT
C929 0 70B8         MDX   RT03+7

```

```

*
*****
* ROUTINE 0B
*****

```

```

C92A C 0001          DC      1
C92B 00 67C0FF00    RT0B LDX  L3 /FF00
C92D 01 6F00C68C    STX  L3 RDCPR      SET UP DATA READ S/B
C92F 0 630B         LDX  3 11
C930 01 6F00C691    STX  L3 STCCT      SET INT COUNT
C932 0 C807         LDD  DSW03
C933 0 D80E         STD  DSWXX      SET UP DATA ANALYSIS
C934 0 70B2         MDX   RT03+5

```

```

*
*****
* PREDETERMINED VALUES FOR SCA DSW AND DIAG
* WORDS. THESE VALUES ARE PRINTED AS S/B.
* WHEN AN ERROR EXISTS.
* THE DIA WORD IS THE VALUE FOR DIAGNOSTIC
* WORDS 1 AND 2 COMBINED.
*****

```

```

C936 0000          BSS  E
C936 0 0080        DSW01 DC  /0080
C937 0 0C00        DIA01 DC  /0C00
C938 0 6C80        DSW02 DC  /6080
C939 0 3862        DIA02 DC  /3862
C93A 0 E080        DSW03 DC  /E080

```

```

31208170
31208180
31208190
31208200
31208210
31208220
31208230
31208240
31208250
31208260
31208270
31208280
31208290
31208300
31208310
31208320
31208330
31208340
31208350
31208360
31208370
31208380
31208390
31208400
31208410
31208420
31208430
31208440
31208450
31208460
31208470
31208480
31208490
31208500
31208510
31208520
31208530
31208540
31208550
31208560
31208570
31208580
31208590
31208600
31208610
31208620
31208630
31208640
31208650
31208660
31208670
31208680
31208690
31208700
31208710
31208720
31208730
31208740
31208750
31208760
31208770
31208780
31208790
31208800
31208810
31208820
31208830
31208840

```

SCA WRAP-AROUND TEST

```

0938 0 3862        DIA03 DC  /3862
093C 0 E080        DSW04 DC  /E080
093C 0 386A        DIA04 DC  /386A
093E 0 EC80        DSW05 DC  /E080
093F 0 38EE        DIA05 DC  /38EE
0940 0 EG80        DSW06 DC  /E080
0941 0 3866        DIA06 DC  /3866

```

```

*
0942 C 0000        DSWXX DC  *-*
0943 C 0000        DIAXX DC  *-*

```

```

*****
* ROUTINE 0C
*****
C944 C 0001          DC      1
0945 00 67C0FF00    RT0C LDX  L3 /FF00
0947 01 6F00C68C    STX  L3 RDCPR      SET UP DATA READ S/B
C945 0 6301         LDX  3 1
C94A 01 6F00C691    STX  L3 STCCT      SET INT COUNT
C94C 0 C8E8         LDD  DSW02
C94C 0 D8F4         STD  DSWXX      SET UP DATA ANALYSIS
C94E 01 6C00066B    STX  L  SYCMD      TURN ON SYNC MODE SW
C950 01 7401CA38    MDX  L  MKSW1,+1
C952 01 4C0CC9E6    BSC  L  DMRTN      GC TO RTN EXECUTE

```

```

*
*****
* ROUTINE 0D
*****

```

```

C954 C 0C01          DC      1
0955 00 67C0E00C    RT0D LDX  L3 /8000
0957 01 6F00C68C    STX  L3 RDCPR      SET UP DATA READ S/B
0955 0 6302         LDX  3 2
095A 01 6F00C691    STX  L3 STCCT      SET INT COUNT
095C 0 C8D0         LDD  DSW03
095D 0 C8E4         STD  DSWXX      SET UP DATA ANALYSIS
095E 01 6C00066B    STX  L  SYCMD      TURN ON SYNC MODE SW
0960 01 74010A38    MDX  L  MKSW1,+1
0962 01 4C00C9E6    BSC  L  DMRTN      GC TO RTN EXECUTE

```

```

*
*****
* ROUTINE 0E
*****

```

```

C964 0 0001          DC      1
0965 00 67C0C000    RT0E LDX  L3 /C000
0967 01 6F00C68C    STX  L3 RDCPR      SET UP DATA READ S/B
0965 0 6303         LDX  3 3
096A 01 6F00C691    STX  L3 STCCT      SET INT COUNT
096C 0 C8C0         LDD  DSW03
096E 0 C8D4         STD  DSWXX      SET UP DATA ANALYSIS
096E 0 70EF         MDX   RT0D+5

```

```

*
*****
* ROUTINE 0F
*****

```

```

096F C 0001          DC      1
0970 00 67C0E000    RT0F LDX  L3 /E000
0972 01 6F00C68C    STX  L3 RDCPR      SET UP DATA READ S/B
0974 0 6304         LDX  3 4
0975 01 6F00C691    STX  L3 STCCT      SET INT COUNT
0977 0 C8C4         LDD  DSW04
0978 0 D8C9         STD  DSWXX      SET UP DATA ANALYSIS
0975 0 70E4         MDX   RT0D+5

```

```

*
*****

```

```

31208850
31208860
31208870
31208880
31208890
31208900
31208910
31208920
31208930
31208940
31208950
31208960
31208970
31208980
31208990
31209000
31209010
31209020
31209030
31209040
31209050
31209060
31209070
31209080
31209090
31209100
31209110
31209120
31209130
31209140
31209150
31209160
31209170
31209180
31209190
31209200
31209210
31209220
31209230
31209240
31209250
31209260
31209270
31209280
31209290
31209300
31209310
31209320
31209330
31209340
31209350
31209360
31209370
31209380
31209390
31209400
31209410
31209420
31209430
31209440
31209450
31209460
31209470
31209480
31209490
31209500
31209510
31209520

```

SCA WRAP-AROUND TEST

```

*          ROUTINE 10
*****
C97A 0 0001          DC          1
C97B 00 67002000    RT10 LDX  L3 /3000
C97C 01 6F00068C    STX  L3 RDCPR      SET UP DATA READ S/B
C97F 0 6305          LDX  3 5
C98C 01 6F000691    STX  L3 STCT      SET INT CCUNT
C982 0 70F4          MDX  RTOF+7
*
*          ROUTINE 11
*****
C983 0 0001          DC          1
C984 00 67009300    RT11 LDX  L3 /9800
C986 01 6F00068C    STX  L3 RDCPR      SET UP DATA READ S/B
C988 0 6306          LDX  3 6
C989 01 6F000691    STX  L3 STCT      SET INT COUNT
C98E 0 70E8          MDX  RTOF+7
*
*          ROUTINE 12
*****
C98C 0 0001          DC          1
C98C 00 6700CC00    RT12 LDX  L3 /CC00
C98F 01 6F00068C    STX  L3 RDCPR      SET UP DATA READ S/B
C991 0 6307          LDX  3 7
C992 01 6F000691    STX  L3 STCT      SET INT CCUNT
C994 0 70E2          MDX  RTOF+7
*
*          ROUTINE 13
*****
C995 0 0001          DC          1
C996 00 6700E600    RT13 LDX  L3 /E600
C998 01 6F00068C    STX  L3 RDCPR      SET UP DATA READ S/B
C99A 0 6308          LDX  3 8
C99B 01 6F000691    STX  L3 STCT      SET INT CCUNT
C99C 0 70D9          MDX  RTOF+7
*
*          ROUTINE 14
*****
C99E 0 0001          DC          1
C99F 00 67007300    RT14 LDX  L3 /7300
C9A1 01 6F00068C    STX  L3 RDCPR      SET UP DATA READ S/B
C9A3 0 6309          LDX  3 9
C9A4 01 6F000691    STX  L3 STCT      SET INT CCUNT
C9A6 0 70D0          MDX  RTOF+7
*
*          ROUTINE 15
*****
C9A7 0 0001          DC          1
C9A8 00 67003900    RT15 LDX  L3 /3900
C9AA 01 6F00068C    STX  L3 RDCPR      SET UP DATA READ S/B
C9AC 0 630B          LDX  3 11
C9AD 01 6F000691    STX  L3 STCT      SET INT CCUNT
C9AF 01 6F000691    LDX  L DSW04
C9B1 01 6F000691    STD  L DSWXX      SET UP DATA ANALYSIS
C9B3 0 70AA          MDX  RTOF+5
*****

```

```

31209530
31209540
31209550
31209560
31209570
31209580
31209590
31209600
31209610
31209620
31209630
31209640
31209650
31209660
31209670
31209680
31209690
31209700
31209710
31209720
31209730
31209740
31209750
31209760
31209770
31209780
31209790
31209800
31209810
31209820
31209830
31209840
31209850
31209860
31209870
31209880
31209890
31209900
31209910
31209920
31209930
31209940
31209950
31209960
31209970
31209980
31209990
31210000
31210010
31210020
31210030
31210040
31210050
31210060
31210070
31210080
31210090
31210100
31210110
31210120
31210130
31210140
31210150
31210160
31210170
31210180
31210190
31210200

```

SCA WRAP-AROUND TEST

```

*          ROUTINE 16
*****
C9B4 0 0000          DC          0
C9B5 00 67003500    RT16 LDX  L3 /3500
C9B7 01 6F00068C    STX  L3 RDCPR      SET UP DATA READ S/B
C9B9 00 67000002    LDX  L3 /0002
C9BB 01 6F000692    STX  L3 STCT      SET RTA PASS CCUNT
C9BC 01 7401069C    MDX  L DIASW,+1  TURN ON DIAG MCDE SW
C9BF 01 74010688    MDX  L SYCHD,+1  TURN ON SYNC MCDE SW
C9C1 01 6F000692    LDD  L DSW05
C9C3 01 6F000692    STD  L DSWXX      SET UP DATA ANALYSIS
C9C5 0 701A          MDX  DMRTN      GO TO RTN EXECUTE
*
*          ROUTINE 17
*****
C9C6 0 0000          DC          0
C9C7 00 6700FF00    RT17 LDX  L3 /FF00
C9C9 01 6F00068C    STX  L3 RDCPR
C9CB 01 6F000681    STX  L3 SEND
C9CC 00 6700AA00    LDX  L3 /AA00
C9CE 01 6F00068C    STX  L3 ALT
C9D1 01 6F0006E1    LD  L SW2        CHK FOR SELECT CHAR
C9D3 0 100A          SLA  10
C9D4 01 4420070B    BSI  L SELCH,+2  SET UP SELECTED CHAR
C9D6 00 67000002    LDX  L3 /0002
C9D8 01 6F000692    STX  L3 STCT      SET RTA PASS CCUNT
C9DA 01 7401069C    MDX  L DIASW,+1  TURN ON DIAG MCDE SW
C9DC 01 6F000692    LDD  L DSW06
C9DE 01 6F000692    STD  L DSWXX      SET UP DATA ANALYSIS
*
DMRTN XIC  L SIREG      LCAC SYNC/IDLE REG
C9E2 01 74010689    MDX  L LCPSW,+1  TURN EXP INTRPT SW
C9E4 01 6F00068B    LD  L SYCMD      CK IF SYNC MCDE ON
C9E6 0 4820          BSC  Z
C9E7 0 7008          MDX  SYNGO      YES, GO TO SYNGO
C9E8 01 6F000682    XIC  L DIAG      NO, EXECUTE DIAG MCDE
C9EA 01 6F000682    XIC  L DIAG      EXECUTE DIAG MCDE
C9EC 01 6F00067E    XIC  L WRCAT     LCAC BUFFER
C9EE 01 4C0005F8    DIAGO BSC  L WAIT3  BRANCH TO DELAY
*
C9F0 01 6F000682    SYNGO XIC  L DIAG  EXECUTE DIAG MCDE
C9F2 0 70FB          MDX  DIACC      GO TO DELAY
*
*****
*          CONTROL ANALYSIS
*          CONTROL ANALYSIS OF RESULTS FOR
*          FUNCTION LAST EXECUTED
*****
C9F3 01 6F000692    CUNAN LD  L DSWXX      LGAC S/B DSW
C9F5 01 44000A01    BSI  L STCR1
C9F7 0 0008          LD  L SWDIA      CHECK IF DIAG OPT CN
C9F8 01 4C10086C    BSC  L OFF+1,+  RESTORE PROGRAM SWS
C9FA 01 6F000692    LD  L DIAXX      LOAD S/B DIAG WCRDS
C9FC 0 700A          MDX  STGR2
*
*****
*          STORAGE FOR CONTROL ANALYSIS
*****
C9FD 0 0000          TRGWD DC  *--    DIAG WCRD ERR. BITS
C9FE 0 0000          STWD DC  *--    STCRD WCRD
C9FF 0 0000          DSWRD DC *--    DSW ERRCK BITS
CA00 0 0000          SWDIA DC *--    DIAG OPTION SW
*

```

```

31210210
31210220
31210230
31210240
31210250
31210260
31210270
31210280
31210290
31210300
31210310
31210320
31210330
31210340
31210350
31210360
31210370
31210380
31210390
31210400
31210410
31210420
31210430
31210440
31210450
31210460
31210470
31210480
31210490
31210500
31210510
31210520
31210530
31210540
31210550
31210560
31210570
31210580
31210590
31210600
31210610
31210620
31210630
31210640
31210650
31210660
31210670
31210680
31210690
31210700
31210710
31210720
31210730
31210740
31210750
31210760
31210770
31210780
31210790
31210800
31210810
31210820
31210830
31210840
31210850
31210860
31210870
31210880

```

SCA WRAP-AROUND TEST

```

*****
*
CA01 0 0000          STOR1 DC    *-*      SAVED RETURN ADDR
CA02 0 00FB          STC      STWD    STCRE S/B DSW WCRD
CA03 01 4400CA0C    BSI L  ANAL1  GC TO ANAL  1
CA05 01 4C80CA01    BSC I  STCR1  RETRN TO SAVED ADDR
*
*****
*
CA07 C 00F6          STOR2 STC    STWD    STCRE S/B DIAG WCRD
CA08 01 4400CA19    BSI L  ANAL2  GC TO ANAL  2
CA0A 01 4400C60C    BSI L  OFF    GC TO SW RESET RTN
*
*****
*
CA0C C 0000          ANAL1 DC    *-*      SAVED RETURN ADDR
CA0D 01 C4000689    LD L  CADSW  LCAC SENSEC DSW
CA0F 01 F4000942    EGR L  DSWXX EGR WITH S/B DSW
CA11 0 00ED          STC      DSWRD  STCRE IN DSW ERR BIT
CA12 0 4820          BSC      Z      CHK IF ERR BITS CN
CA13 0 7C01          MDX     ANL01  ERR BITS ARE ON
CA14 0 7C02          MDX     ANL01+2 ERR BITS ARE NCT CN
CA15 01 4400CA3C    ANL01 BSI L  DSWAN  CHK WHICH BITS CN
CA17 01 4C80CA0C    BSC I  ANAL1  BRANCH TO RETURN ACR
*
*****
*
CA19 0 0000          ANAL2 DC    *-*      SAVED RETURN ADDR
CA1A 0 001D          LD      MKSW1  LD DATA MASK SW 1
CA1B 0 4820          BSC      Z
CA1C 0 7C0F          MDX     DTMK2  GO MASK CUT DATA BIT
CA1D 0 001C          LD      MKSW2  LD DATA MASK SW 2
CA1E 0 4820          BSC      Z
CA1F 0 7012          MDX     DTMK3  GO MASK CUT DATA BIT
CA20 01 C4000678    CKIT3 LD L  DIAWD  LGAC WAS DIAG WCRD
CA22 01 F4000943    EGR L  DIAXX  EGR S/B DIAG WCRD
CA24 0 0006          STC      TRGW0  SET IN DIAG ERR BITS
CA25 0 4820          BSC      Z      CHKIF ERR BITS ARE CN
CA26 0 7C01          MDX     ANL02  ERRER EITS ARE CN
CA27 0 7002          MDX     ANL02+2 ERR BITS ARE NCT DN
CA28 01 4400065B    ANL02 BSI L  TRGAN  GC TO TRIGGER ANAL
CA2A 01 4C80CA19    BSC I  ANAL2  BRANCH TO SAVED ADDR
CA2C 01 C4000678    DTMK2 LD L  DIAWD  LD WAS DIAG WD
CA2E C 000A          AND     MASK2  MASK OUT DATA BITS
CA2F 01 D4000678    STC L  DIAWD  STORE IN DIAG WD WAS
CA31 0 7CEE          MDX     CKIT3
CA32 01 C4000678    DTMK3 LD L  DIAWD  LGAC WAS DIAG WD
CA34 0 0006          AND     MASK3  MASK CUT DATA BITS
CA35 01 D4000678    STC L  DIAWD  STORE IN DIAG WD WAS
CA37 0 70E8          MDX     CKIT3
*
*****
*
CA38 0 0000          MKSW1 DC    *-*      MASK SW 1
CA39 0 FF78          MASK2 DC    /FF78
CA3A 0 0000          MKSW2 DC    *-*      MASK SW 2
CA3B 0 FF77          MASK3 DC    /FF77
*
*****
*
*****
*
DSW ANALYSIS
*
COMPARE SHGULC/BF DSW WITH WAS DSW
*
ANALYZE ANY ERRORS
*****
*

```

SCA WRAP-AROUND TEST

```

*****
*
CA3C 0 0000          DSWAN DC    *-*      SAVED RETURN ADDRESS
CA3D 01 C4000688    LD L  CADSW  LCAC DSW WAS
CA3F 01 E400C9FF    AND L  DSWRD  AND WITH DSW ERR
CA41 01 D400CA7A    STO L  DSWW1  STCRE CN/F ERR BITS
CA43 01 C400C9FF    LD L  DSWRD  LCAC DSW ERR BITS
CA45 01 F4000A7A    EGR L  DSWW1  EGR WITH CN/F ERR
CA47 01 D4000A7B    STO L  DSWW2  STCRE DSW F/CN ERR
CA49 01 C000C6BC    XIC L  BZCN  EXECUTE ALARM CN
CA4B 0 6305          LDX     3 5    SET UP MESSAGE ID 5
CA4C 01 F0000ABF    STX L3 MSGNO  STCRE IN MESSAGE =
CA4E 01 4400CA7C    BSI L  MSG10  GO CHK WHICH MSG IC
CA50 00 6700C07F    LDX L3 /007F
CA52 01 6F00C6B2    STX L3 TABLE+2 SET IN MODIFIER IC
CA54 01 6700CAC0    LDX L3 ADRI  LD ALPHA 1 ADDR
CA56 01 6F00C6B2    STX L3 TABLE+3 SET UP ALPHA MSG
CA58 01 C4000A7A    LD L  DSWW1  LCAC CN/F ERR BITS
CA5A 01 D400C6B9    STO L  TABLE+9
CA5C 01 C400CA7B    LD L  DSWW2  LCAC F/CN ERR BITS
CA5E 01 D4000EBA    STO L  TABLE+10
CA60 01 C4000942    LD L  DSWXX  LD DSW SHGULC/EE
CA62 01 3400088E    STO L  TABLE+8 SET UP ERR MESSAGE
CA64 01 C40006E8    LD L  CADSW  LCAC SENSEC DSW
CA66 01 D40006B7    STC L  TABLE+7 SET UP ERR MESSAGE
CA68 01 C400067C    LD L  D12AL
CA6A 01 E400CC9D    AND L  MASK1  MASK OUT CE BITS
CA6C 01 D40008B6    STO L  TABLE+6 SET UP ERR MESSAGE
CA6E 01 C400067A    LD L  D11AL
CA70 01 D40006B5    STC L  TABLE+5 SET UP ERR MESSAGE
CA72 00 44800162    BSI I  ERROR  GO PRINT ERRCR
CA74 1 0B90          DC      TABLE MESSAGE TABLE
CA75 1 0B55          DC      LOPRT  LOGP ADDRESS
CA76 01 C000C8C4    XIC L  BZOFF  EXECUTE ALARM CFF
CA78 01 4C800A3C    BSC I  DSWAN  EXIT TO RETURN ADDR
*
*****
*
CA7A 0 0000          DSWW1 DC    *-*      DSW F/CN WCRD
CA7B 0 0000          DSWW2 DC    *-*      DSW CN/F WCRD
*****
*
*****
*
MSGID DC    *-*
LD L  DIAWD+1  LOAD *CE* EITS
SRT 16
SLA 16
SLT 3
SLA 7
STC L  TABLE+11 STCRE IN ERR MSG
LD L  RSTSW  CHK FOR P RESET ERR
BSC Z
MDX NOTRM  LET PRCG RESET MSG
LD TERM  NO PRCG RESET MSG
*
MSGGD STC L  TERM1
STC L  TERM2
STC L  TERM3
STC L  TERM4
LD INTSW
BSC L  IDRTN+2,+- BRANCH NOT INT MSG
LD L  LOPSW  CHK FOR INTRPT INFO
BSC Z
MDX PNDCK
LD L  PRTPN
BSC Z
MDX MID3

```

SCA WRAP-AROUND TEST

CA9C 0	6300	LDX	3 0		31212250
CA9E 0	6B1E	STX	3 INTID	RESET INTERRUPT ID	31212260
CA9F 0	6B1E	STX	3 INTSW	RESET INTERRUPT SW	31212270
CAAC 01	4C80CC03	BSC	1 INTAN	EXIT TO RETURN	31212280

CAA2 01	6F000ABC	IDRTN	STX L3	IATID	31212290
CAA4 0	C01A	LD		MSGND	31212300
CAA5 0	8C17	A		IATID	31212310
CAA6 01	C4000BBO	STO	L	TABLE	31212320
CAA6 0	6300	LDX	3 0		31212330
CAA9 0	6B13	STX	3 INTID	RESET INTERRUPT ID	31212340
AAA 0	6B13	STX	3 INTSW	RESET INTERRUPT SW	31212350
AAA 01	4C80CA7C	BSC	1	MSGID	31212360

QAAC 01	C400C6EA	PNDCK	LD L	PRTPN	31212370
QAAC 0	4620	BSC	Z		31212380
CABG 0	7002	MDX		MID1	31212390
CAB1 0	6502	LDX	3 2		31212400
CAB2 0	70CF	MDX		IDRTN	31212410

OAB3 0	6301	MID1	LDX 3 1		31212420
OAB4 0	70ED	MDX		IDRTN	31212430

CAB5 0	6303	MID3	LDX 3 3		31212440
OAB6 0	70EB	MDX		IDRTN	31212450

OAB7 01	74010ABF	NDTRM	MDX L	MSGND+1	31212460
CAB9 0	C902	LD		SPACE	31212470
CABA 0	70CF	MDX		MSGGD	31212480

CABB 0	FFFF	TERM	DC	/FFFF	31212490
OABC 0	2121	SPACE	DC	/2121	31212500

OABC 0	0000	INTID	DC	*-*	31212510
OABE 0	0000	INTSW	DC	*-*	31212520
CABF 0	0000	MSGND	DC	*-*	31212530

ALPHA MESSAGE FOR DS W ERRCR					

OACC 0	3292	ADR1	DC	/3292	31212540
CAC1 0	FC21	DC		/FC21	31212550
CAC2 0	2132	DC		/2132	31212560
CAC3 0	92D8	DC		/92D8	31212570
OAC4 0	2121	DC		/2121	31212580
CAC5 0	923E	DC		/923E	31212590
CAC6 0	9A21	DC		/9A21	31212600
CAC7 0	219A	DC		/219A	31212610
OAC8 0	BC1A	DC		/BC1A	31212620
CAC9 0	2121	DC		/2121	31212630
OACA 0	5276	DC		/5276	31212640
OACB 0	BC12	DC		/BC12	31212650
OACC 0	2112	DC		/2112	31212660
OACD 0	BC52	DC		/BC52	31212670
CACE 0	7621	DC		/7621	31212680
OACF 0	D61E	DC		/D61E	31212690
OADO 0	36D6	DC		/36D6	31212700
OAD1 0	2132	DC		/2132	31212710
CAD2 0	9A92	DC		/9A92	31212720
OAD3 0	2136	DC		/2136	31212730
CAD4 0	6262	DC		/6262	31212740
OAD5 0	FFFF	TERM1	DC	/FFFF	31212750
OAD6 0	6236	DC		/6236	31212760
CAD7 0	9A36	DC		/9A36	31212770
OAD8 0	9E21	DC		/9E21	31212780
CAD9 0	FFFF	DC		/FFFF	31212790

SCA WRAP-AROUND TEST

ALPHA MESSAGE FOR DIAG WCRD 1 ERRCR					

CADA 0	329A	ADR2	DC	/329A	31212930
CADB 0	9221	DC		/9221	31212940
OADC 0	21D8	DC		/21D8	31212950
CADC 0	923E	DC		/923E	31212960
OADE 0	9A21	DC		/9A21	31212970
CADF 0	FC92	DC		/FC92	31212980
CAE0 0	3E9A	DC		/3E9A	31212990
CAE1 0	21FC	DC		/21FC	31213000
CAE2 0	9A6C	DC		/9A6C	31213010
OAE3 0	1A21	DC		/1A21	31213020
OAE4 0	5276	DC		/5276	31213030
CAE5 0	BC12	DC		/BC12	31213040
CAE6 0	2112	DC		/2112	31213050
CAE7 0	BC52	DC		/BC52	31213060
CAE8 0	7621	DC		/7621	31213070
CAE9 0	D61E	DC		/D61E	31213080
OAEA 0	36D6	DC		/36D6	31213090
CAEB 0	2132	DC		/2132	31213100
CAEC 0	92FC	DC		/92FC	31213110
CAEC 0	2136	DC		/2136	31213120
CAEE 0	6262	DC		/6262	31213130
CAEF 0	FFFF	TERM2	DC	/FFFF	31213140
OAF0 0	6236	DC		/6236	31213150
OAF1 0	9A36	DC		/9A36	31213160
OAF2 0	9E21	DC		/9E21	31213170
OAF3 0	FFFF	DC		/FFFF	31213180

ALPHA MESSAGE FOR DIAG WCRD 2 ERRCR					

CAF4 0	329A	ADR3	DC	/329A	31213230
CAF5 0	9221	DC		/9221	31213240
CAF6 0	21FC	DC		/21FC	31213250
CAF7 0	923E	DC		/923E	31213260
CAF8 0	9A21	DC		/9A21	31213270
CAF9 0	D892	DC		/D892	31213280
OAF0 0	3E9A	DC		/3E9A	31213290
CAF1 0	21D8	DC		/21D8	31213300
CAF2 0	9A8C	DC		/9A8C	31213310
CAF3 0	1A21	DC		/1A21	31213320
OAFE 0	5276	DC		/5276	31213330
CAFF 0	BC12	DC		/BC12	31213340
CB00 0	2112	DC		/2112	31213350
CB01 0	BC52	DC		/BC52	31213360
CB02 0	7621	DC		/7621	31213370
CB03 0	D61E	DC		/D61E	31213380
CB04 0	36D6	DC		/36D6	31213390
CB05 0	2132	DC		/2132	31213400
CB06 0	92D8	DC		/92D8	31213410
CB07 0	2136	DC		/2136	31213420
CB08 0	6262	DC		/6262	31213430
CB09 0	FFFF	TERM3	DC	/FFFF	31213440
CB0A 0	6236	DC		/6236	31213450
CB0B 0	9A36	DC		/9A36	31213460
CB0C 0	9E21	DC		/9E21	31213470
CB0D 0	FFFF	DC		/FFFF	31213480

ALPHA MESSAGE FOR INTERRUPT ERRCR					

CB0E 0	3292	ADR4	DC	/3292	31213540
CB0F 0	FC21	DC		/FC21	31213550

SCA WRAP-AROUND TEST

OB10 0	2132	DC	/2132	D	31213610
OB11 0	9208	DC	/9208	W2	31213620
OB12 0	2121	DC	/2121		31213630
OB13 0	329A	DC	/329A	DS	31213640
OB14 0	9221	DC	/9221	W	31213650
OB15 0	2152	DC	/2152	Q	31213660
OB16 0	768C	DC	/768C	N/	31213670
OB17 0	1221	DC	/1221	F	31213680
OB18 0	128C	DC	/128C	F/	31213690
OB19 0	5276	DC	/5276	GN	31213700
OB1A 0	2106	DC	/2106	*	31213710
OB1E 0	1E36	DC	/1E36	CE	31213720
OB1C 0	D621	DC	/D621	*	31213730
OB1D 0	2121	DC	/2121		31213740
OB1E 0	2121	DC	/2121		31213750
OB1F 0	2122	DC	/2122	I	31213760
OB20 0	769E	DC	/769E	NT	31213770
OB21 0	2136	DC	/2136	E	31213780
OB22 0	6262	DC	/6262	KR	31213790
OB23 0	FFFF	TERM4 DC	/FFFF	TERMINATOR	31213800
OB24 0	6236	DC	/6236	RE	31213810
OB25 0	9A36	DC	/9A36	SE	31213820
OB26 0	9E21	DC	/9E21	T	31213830
OB27 0	FFFF	DC	/FFFF		31213840

* ALPHA MESSAGE FOR DATA ERROR

OB28 0	923E	ADR5 DC	/923E	WA	31213880
OB29 0	9A21	DC	/9A21	S	31213890
OB2A 0	219A	DC	/219A	S	31213900
OB2B 0	6C1A	DC	/6C1A	/B	31213910
OB2C 0	2121	DC	/2121		31213920
OB2D 0	D61E	DC	/D61E	*C	31213930
OB2E 0	36D6	DC	/36D6	E*	31213940
OB2F 0	2121	DC	/2121		31213950
OB30 0	323E	DC	/323E	DA	31213960
OB31 0	9E3E	DC	/9E3E	TA	31213970
OB32 0	2136	DC	/2136	E	31213980
OB33 0	6262	DC	/6262	RR	31213990
OB34 0	5262	DC	/5262	DR	31214000
OB35 0	FFFF	DC	/FFFF		31214010

* ALPHA MESSAGE FOR SCOPE LCCP

OB36 0	9A1E	ADR6 DC	/9A1E	SC	31214020
OB37 0	5256	DC	/5256	OP	31214030
OB38 0	3621	DC	/3621	E	31214040
OB39 0	5E52	DC	/5E52	LD	31214050
OB3A 0	5256	DC	/5256	OP	31214060
OB3B 0	FFFF	DC	/FFFF	TERMINATOR	31214070

* ALPHA MESSAGE FOR SELECT CHAR.

OB3C 0	1E26	ADR7 DC	/1E26	CH	31214080
OB3D 0	3E62	DC	/3E62	AR	31214090
OB3E 0	219A	DC	/219A	S	31214100
OB3F 0	365E	DC	/365E	EL	31214110
OB40 0	361E	DC	/361E	EC	31214120
OB41 0	9E36	DC	/9E36	TE	31214130
OB42 0	3221	DC	/3221	D	31214140
OB43 0	D662	DC	/D662	*R	31214150
OB44 0	9E76	DC	/9E76	TA	31214160
OB45 0	21FC	DC	/21FC	I	31214170

SCA WRAP-AROUND TEST

OB46 0	D4D6	DC	/D4D6	7*	31214290
OB47 0	FFFF	DC	/FFFF	TERMINATOR	31214300
*****					31214310
* RESTORE MESSAGE					31214320
*****					31214330
* NMSG DC /6236 RE					31214340
OB48 0	6236	DC	/6236	RE	31214350
OB49 0	9A9E	DC	/9A9E	ST	31214360
OB4A 0	5262	DC	/5262	DR	31214370
OB4B 0	3621	DC	/3621	E	31214380
OB4C 0	9AA6	DC	/9AA6	SY	31214390
OB4C C	9A9E	DC	/9A9E	ST	31214400
OB4E 0	3672	DC	/3672	EM	31214410
OB4F 0	219E	DC	/219E	T	31214420
OB50 0	5221	DC	/5221	O	31214430
OB51 0	7652	DC	/7652	NC	31214440
OB52 0	6272	DC	/6272	RM	31214450
OB53 0	3E5E	DC	/3E5E	AL	31214460
OB54 0	FFFF	DC	/FFFF	TERMINATOR	31214470
*****					31214480
OB55 01	6C0008B2	LOPRT STX L	LCCPE	TURN ON LCCP ON ERR	31214490
OB57 01	CC0008C4	XIC L	B2CFF	EXECUTE ALARM CFF	31214500
OB59 01	4C00C87E	BSC L	OFF2	GO TO FCUSE/KEEP RTN	31214510
*****					31214520
* DIAG TRIGGER ANALYSIS					31214530
* COMPARES *WAS* TRIGGERS WITH S/B					31214540
* TRIGGERS AND ANALIZES ANY ERROR					31214550
*****					31214560
* TRGAN DC *--* SAVED RETURN ADDRESS					31214570
OB5B 0	0000	LD L	DIAXX	LD COMEINDED DIAGS	31214580
OB5C 01	C400C943	SRT	16		31214590
OB5E 0	1890	SLA	16		31214600
OB5F 0	1610	SLA	16		31214610
OB60 0	1089	SLT	9		31214620
OB61 0	1007	SLA	7		31214630
OB62 0	D059	STG	DIAX1	STCRE DIAG 1 WAS	31214640
OB63 0	1C1C	SLA	16		31214650
OB64 0	1C86	SLT	6		31214660
OB65 0	100A	SLA	10		31214670
OB66 0	D056	STC	DIAX2	STCRE DIAG 2 WAS	31214680
OB67 01	C40009FD	LD L	TRGWD	LOAD DIA ERRGR BITS	31214690
OB68 0	1890	SRT	16		31214700
OB6A 0	1010	SLA	16		31214710
OB6B 0	1089	SLT	9		31214720
OB6C 0	1007	SLA	7		31214730
OB6D 0	D050	STG	TRWD1	STCRE DIAG WD 1 ERR	31214740
OB6E 0	1010	SLA	16		31214750
OB6F 0	1086	SLT	6		31214760
OB7C 0	100A	SLA	10		31214770
OB71 0	D04D	STC	TRWD2	STCRE DIAG WD 2 ERR	31214780
OB72 0	C048	LD	TRWD1		31214790
OB73 0	4820	BSC	Z	CHECK FOR ERR BIT ON	31214800
OB74 0	702D	MDX	CKIT2	INITIAL ERR BIT CHK	31214810
OB75 0	704C	MDX	WDICK	GO CHK DIAG WD 2	31214820
*****					31214830
* WD1 XIC L BZCN EXECUTE ALARM ON					31214840
OB76 01	CC00C8BC	LDX	3 7		31214850
OB78 0	6307	STX	L3 MSGNC	SET UP MESSAGE ID 7	31214860
OB79 01	6F00CAEF	BSI	L MSGID	CHK WHICH MESSAGE ID	31214870
OB7B 01	4400GA7C	LDX	L3 /007F		31214880
OB7D 00	670C007F	STX	L3 TABLE+2	SET UP MODIFIER ID	31214890
OB7F 01	6F000BB2	LDX	L3 ADR2	LD ALPHA 2 ADDR	31214900
OB81 01	6700CADA	STX	L3 TABLE+3		31214910
OB83 01	6F00CBB3	LD	L TGWD1	LD CA/F ERROR BITS	31214920
OB85 01	C4000BCC	STC	TABLE+9		31214930
OB87 0	DG31	LD	L TGWD2	LD F/CA ERRGR BITS	31214940
OB88 01	C4000BC1	LD	L TGWD2		31214950

SCA WRAP-AROUND TEST

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CB8A 0 D02F      STC   TABLE+10      31214970
QB8E 0 C030      LD    DIAX1      LD DIAG WD 1 S/B      31214980
QB8C 0 D02B      STC   TABLE+8      31214990
CB8D 01 C400C67A LD    L D11AL      LD DIAG WD 1 WAS      31215000
CB8F 0 D027      STC   TABLE+7      31215010
QB90 01 C400067C LD    L D12AL      LD DIAG WD 2 WAS      31215020
QB92 01 E400CC9D AND   L MASK1      MASK OUT CE BITS      31215030
CB94 0 D021      STC   TABLE+6      31215040
CB95 01 C4000688 LD    L CADSW      LOAD DSW WAS          31215050
CB97 0 D01D      STC   TABLE+5      31215060
CB98 00 44800162 BSI   I ERROR      GC PRINT ERROR        31215070
QB9A 1 08B0      DC    TABLE      MESSAGE TABLE        31215080
CB9E 1 0855      DC    LCPRT        LCCF ADDRESS          31215090
CB9C 01 0C0008C4 XIC   L BZCFF      EXECUTE ALARM CFF     31215100
CB9E 0 6300      LDX   3 0          31215110
CB9F 0 6B20      STX   3 TGWD1      31215120
CBAC 0 6B20      STX   3 TGWD2      31215130
GBA1 0 7020      MDX   WDICK        GO CHK DIAG WD 2     31215140
                                     31215150
*                                     31215160
OBA2 01 C4000BBC CKIT2 LD  L DIAX1      LD DIAG WD 1 WAS      31215170
OBA4 01 E4000BBE AND   L TRWD1      AND DIAG WC 1 ERR     31215180
OBA6 01 D4000BC1 STC   L TGWD2      STCRE CN/F ERR BITS  31215190
OBA8 0  C4000BBE LD    L TRWD1      LD DIAG WD 1 ERR      31215200
GBAA 01 F4000BC1 EOR   L TGWD2      EGR DIAG WC 1 CN/F    31215210
GBAC 01 D4000BC0 STO   L TGWD1      STORE DIAG WC 1 F/ON  31215220
GBAE 0 7CC7      MDX   WD1          31215230
*                                     31215240
***** ERROR PRINT TABLE ***** 31215250
*                                     31215260
*****                                     31215270
*                                     31215280
CBBC 0 0000      BSS   E 0          31215290
CBBO 0 0000      TABLE DC  --*    ERROR PRINT TABLE 31215300
QBB1 0 0000      DC    --*          31215310
CB82 0 0000      DC    --*          31215320
QBB3 0 0000      DC    --*          31215330
CB84 0 0000      DC    --*          31215340
QBB5 0 0000      DC    --*          31215350
CB86 0 0000      DC    --*          31215360
CB87 0 0000      DC    --*          31215370
QBB8 0 0000      DC    --*          31215380
CB85 0 0000      DC    --*          31215390
QBBA 0 0000      DC    --*          31215400
CB86 0 0000      DC    --*          31215410
*                                     31215420
***** DIAG TRIGGER STORAGE ***** 31215430
*                                     31215440
*                                     31215450
OBBC 0 0000      DIAX1 DC  --*    DIAG WD 1 WAS      31215460
OBBC 0 0000      DIAX2 DC  --*    DIAG WD 2 WAS      31215470
QBBE 0 0000      TRWD1 DC  --*    DIAG 1 ERROR BITS  31215480
QBBF 0 0000      TRWD2 DC  --*    DIAG 2 ERROR BITS  31215490
QBC0 0 0000      TGWD1 DC  --*    F/CN ERROR WORD    31215500
QBC1 0 0000      TGWD2 DC  --*    CN/F ERROR WORD    31215510
*****                                     31215520
*                                     31215530
WDICK LD    TRWD2      LD DIAG WD 2 ERR      31215540
CB83 0 1690      SRT   16          31215550
CB84 0 1010      SLA   16          31215560
CB85 0 1087      SLT   7          31215570
CB86 0 4620      BSC   Z          CHECK FOR ERR BIT ON 31215580
CB87 0 7002      MDX   WD2        MAKE ANALYSIS        31215590
CB88 01 4C800B5B BSC   I TRGAN     EXIT TC RETURN ACDR. 31215600
*                                     31215610
OB8A 01 C400CBBE *WD2 LD  L DIAX2      LD DIAG WD 2 WAS      31215620
OB8C 01 E4000BBF AND   L TRWD2      AND DIAG WD 2 ERR     31215630
OB8E 01 D4000BC1 STC   L TGWD2      STCRE CN/F ERR        31215640

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SCA WRAP-AROUND TEST

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CB80 01 C4000BBF LD    L TRWD2      LOAD DIAG WC 2 ERR    31215650
QB82 01 F4000BC1 EOR   L TGWD2      ECR CN/F ERR BITS    31215660
QB84 01 D4000BCC STO   L TGWD1      STCRE F/CN ERR        31215670
QB86 01 0C00086C XIC   L BZCN      EXECUTE ALARM CN      31215680
QB88 0 6309      LDX   3 9          31215690
CB89 01 6F000ABF STX   L3 MSGNG     SET LP MESSAGE ID 9   31215700
QB8B 01 44000A7C BSI   L MSGID     CHK WHICH MESSAGE ID 31215710
QB8C 01 6700007F LDX   L3 /CCTF     31215720
CB8D 01 6F000BB2 STX   L3 TABLE+2 SET UP MCDIFIER ID    31215730
QB8E 01 67000AF4 LDX   L3 ADR3      LD ALPHA 3 ACDR       31215740
QB8F 01 6F000BB3 STX   L3 TABLE+3 31215750
QB85 01 C4000BC0 LD    L TGWD1      LD CN/F ERROR BITS    31215760
QB87 0 C0D1      STC   TABLE+9    31215770
QB8E 01 C4000BC1 LD    L TGWD2      LD F/ON ERROR BITS    31215780
QB8A 0 C0CF      STO   TABLE+10   31215790
QB8B 0 C0D1      LD    DIAX2      LD DIAG WD 2 S/B      31215800
QB8C 0 D0CB      STC   TABLE+8    31215810
QB8D 01 C400067C AND   L D12AL      LD DIAG WD 2 WAS      31215820
QB8E 01 E4000C9D AND   L MASK1      MASK OUT CE BITS      31215830
QB8F 0 C0C5      STC   TABLE+7    31215840
QB82 01 C400067A LD    L D11AL      LD DIAG WC 1 WAS      31215850
QB84 0 C0C1      STO   TABLE+6    31215860
QB85 01 C4000688 LD    L CADSW      LOAD DSW WAS          31215870
QB87 0 D08D      STO   TABLE+5    31215880
QB88 00 44800162 BSI   I ERROR      GC PRINT ERROR        31215890
QB8A 1 08B0      DC    TABLE      MESSAGE TABLE        31215900
QB8B 1 0855      DC    LCPRT        LCCF ADDRESS          31215910
QB8C 01 0C0008C4 XIC   L BZCFF      EXECUTE ALARM CFF     31215920
QB8E 0 6300      LDX   3 0          31215930
QB8F 0 6B20      STX   3 TGWD1      31215940
QBC0 0 6B20      STX   3 TGWD2      31215950
QBC1 01 4C800B5B BSC   I TRGAN     EXIT TC RETURN ACDR. 31215960
*                                     31215970
***** INTERRUPT ANALYSIS ***** 31215980
*                                     31215990
* COMPARES SHOULD/BE INTERRUPTS WITH 31216000
* ACTUAL INTERRUPTS AND ANALIZES ANY ERRS 31216010
*****                                     31216020
*                                     31216030
QCO3 0 0000      INTAN DC  --*    SAVED RETURN ACC. 31216040
QCO4 0 630C      LDX   3 0          31216050
QCO5 01 6F000BB8 STX   L3 TABLE+8 RESET INTRPT ERR LOC 31216060
QCO7 01 6F000BB9 STX   L3 TABLE+9 RESET INTRPT ERR LOC 31216070
QCO9 01 C4000688 LD    L CADSW      LD DSW WAS            31216080
QCOB 01 E4000C6A AND   L INTRK      MASK INTRPT BITS     31216090
QCOD 01 D4000C68 STC   L INWAS      STCRE INT EIT WAS     31216100
QCOF 01 C4000942 LD    L DSWXX      LOAD DSW S/B         31216110
CC11 01 E4000C6A AND   L INTRK      MASK INTRPT BITS     31216120
CC13 01 D4000C69 STO   L INSD8      STCRE INT EIT S/B    31216130
CC15 01 F4000C68 EGR   L INWAS      CHECK FOR ERROR       31216140
CC17 0 4820      BSC   Z          31216150
CC18 0 7001      MDX   INERR       GC TC INT ERR ANAL.  31216160
CC19 0 703D      MDX   CKINT       CK FOR NO INTERRUPT  31216170
*                                     31216180
INERR STC   L ERINT  SAVE INT ERR BITS    31216190
LD    L INWAS      LD INTRPT DSW WAS     31216200
AND   L ERINT      AND INT ERROR BITS    31216210
STO   L TABLE+8   LD INT ERR CN/F BITS  31216220
LD    L ERINT      LD INT ERROR BITS     31216230
EOR   L TABLE+8   ECR INT CN/F ERROR    31216240
STC   L TABLE+9   STCRE INT F/CN ERROR  31216250
MDX   CKERR        31216260
CKERR LD    L TABLE+8 31216270
Z          31216280
CC2B 0 4820      MDX   INTER       CHK IF IT IS 0       31216290
CC2C 0 7C05      MDX   INTER       ERR BITS ARE LOADED 31216300
CC2E 01 C4000BB5 LD    L TABLE+9   31216310
CC2F 0 4820      BSC   Z          31216320
CC30 0 7001      MDX   INTER       31216320

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SCA WRAP-AROUND TEST

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OC31 0 7C25          MDX  CKINT  NC ERR BITS LOADED      31216330
OC32 01 CC00C8BC    INTER XIC  L  BZCN  EXECUTE ALARM CN      31216340
OC34 0 6300          LDX  3 0          31216350
OC35 01 6F000ABF    STX  L3 MSGCN          31216360
OC37 01 6C00CABE    STX  L  INTSW          TURN ON INT MSG SW      31216370
OC39 01 4400A7C     BSI  L  MSGID          CHK WHICH MSG ID       31216380
OC3B 0 635F          LDX  3 /005F          31216390
OC3C 01 6F00CBB2    STX  L3 TABLE+2      SET LP MODIFIER IC      31216400
OC3E 01 6700B0E     LDX  L3 ADR4          LCAC ALPHA 4 ADDRESS    31216410
OC40 01 6F00BB3     STX  L3 TABLE+3      SET LP ALPHA MSG        31216420
OC42 01 C400067C    LD  L  D12AL          LD DIAG WD 2 WAS        31216430
OC44 0  EC56         AND  MASK1           MASK CLT CE BITS        31216440
OC45 01 D4000BB6    STD  L  TABLE+6      31216450
OC47 01 C400067A    LD  L  D11AL          LD DIAG WD 1 WAS        31216460
OC49 01 C4000BB5    STC  L  TABLE+5      31216470
OC4B 01 C400068E    LD  L  CADSW          LCAC DSW WAS            31216480
OC4D 01 D4000BB7    STD  L  TABLE+7      31216490
OC4F 00 44800162    BSI  I  ERROR          GC PRINT ERROR          31216500
OC51 1  0B80         DC  TABLE           MESSAGE TABLE          31216510
OC52 1  0B55         DC  LCPRT            LCGP ADDRESS            31216520
OC53 01 CC0008C4    XIC  L  BZCFF          EXECUTE ALARM CFF       31216530
OC55 01 4C80CC03    BSC  I  INTAN          EXIT TC RETURN          31216540
                                     31216550
OC57 01 C4000685    * CKINT LD  L  LCPSW    CHK IF INTERRUPT S/B    31216560
OC59 0  4820         BSC  Z                31216570
OC5A 0  7002         MDX  INTGK            YES, EXIT TC RETURN     31216580
OC5E 01 4C80CC03    BSC  I  INTAN          31216590
                                     YES, EXIT TC RETURN     31216600
OC5D 01 C400068A    * INTOK LD  L  PRTPN    CHK IF INTERRUPT WAS    31216610
OC5F 0  4820         BSC  Z                31216620
OC60 0  70FA         MDX  CKINT+4          YES, EXIT TC RETURN     31216630
OC61 0  6306         LDX  3 11             NO, SET MSG ID B        31216640
OC62 01 6F00CBB0    STX  L3 TABLE          31216650
OC64 01 CC0008BC    XIC  L  BZCN          EXECUTE ALARM CN        31216660
OC66 0  70D4         MDX  INTER+9          GO SET LP ERR TABLE    31216670
                                     31216680
OC67 0  0070         ERINT DC  **          INTERRUPT ERR BITS      31216690
OC68 0  0000         INWAS DC  **          DSW INT BITS WAS        31216700
OC69 0  0000         INSD8 DC  **          DSW INT BITS S/E        31216710
OC6A 0  D800         INTMK DC  /D800        31216720
                                     31216730
OC6B 0  C000         * DATCK DC  **          31216740
OC6C 01 C4000684    LD  L  RCVED          LCAC BUFFER DATA        31216750
OC6E 01 EC00068F    OR  L  INBUF          SAVE BUFFER DATA        31216760
OC7C 01 D4000684    STC  L  RCVED          31216770
OC72 01 F400068C    EOR  L  RDCPK          CK FOR DATA ERRCR      31216780
OC74 0  4820         BSC  Z                31216790
OC75 0  7002         MDX  DATEK            GO TC DATA ERR RTN      31216800
OC76 01 4C80CC6B    BSC  I  DATCK          EXIT TC RETURN          31216810
                                     31216820
OC78 01 C4000679    * DATER LD  L  DIAWD+1  SET UP C.E. BIT CHK     31216830
OC7A 0  1890         SRT  16                31216840
OC7E 0  1010         SLA  16                31216850
OC7C 0  1083         SLT  3                 31216860
OC7D 0  1007         SLA  7                 31216870
OC7E 01 D400CBB7    STC  L  TABLE+7      31216880
OC80 01 CC0008BC    XIC  L  BZCN          EXECUTE ALARM CN        31216890
OC82 0  630C         LDX  3 12             SET UP MESSAGE ID C      31216900
OC83 01 6F00CBB0    STX  L3 TABLE          31216910
OC85 0  6307         LDX  3 /0007          31216920
OC86 01 6F00BB2     STX  L3 TABLE+2      31216930
OC88 01 6700B2E     LDX  L3 ADR5          31216940
OC8A 01 6F00BB3     STX  L3 TABLE+3      31216950
OC8C 01 C4000684    LD  L  RCVED          LCAC DATA WAS          3121696C
OC8E 01 D4000BB5    STC  L  TABLE+5      31216970
OC90 01 C400068C    LD  L  RDCPR          LCAC DATA S/E          31216980
OC92 01 D400CBB6    STD  L  TABLE+6      31216990
OC94 00 44800162    BSI  I  ERROR          GC PRINT ERROR          31217000

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SCA WRAP-AROUND TEST

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CC96 1 0B80          DC  TABLE           31217010
CC97 1 0B55          DC  LCPRT            31217020
CC98 01 0C00C8C4    XIC  L  BZCFF          EXECUTE ALARM CFF      31217030
CC9A 01 4C800C6B    BSC  I  DATCK          EXIT TC RETURN         31217040
                                     31217050
                                     *
                                     *****
                                     *          CONSTANTS
                                     *****
                                     *
CC9C 0  1000         K1000 DC  /1000      CONSTANT 1000          31217100
CC9D 0  FC00         MASK1 DC  /FC00      CE BIT MASK            31217110
                                     *****
CC9E 0  05F6         END  BEGN            LAST STATEMENT         31217120
                                     31217130

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SCA WRAP-AROUND TEST

CROSS REFERENCE LISTING

SYMBOL	VALUE	REFERENCES
ADR1	0A00	0A54
ADR2	0ACA	0B81
ADR3	0AF4	0BE1
ADR4	0B0E	0C3E
ADR5	0B28	0C88
ADR6	0B36	0742
ADR7	0B3C	07F5
ALT	0630	0676,07E0,C8A1,09CF
ANAL1	0A0C	0A03,0A17
ANAL2	0A19	0A08,0A2A
ANL01	0A15	0A13,0A14
ANL02	0A26	0A26,0A27
AOL00	0833	080D
A1LCC	0E39	0817
BEGIN	0160	05DC,05F6
BEGN	05F6	0C9E
BZOFF	08C4	06DF,0751,0794,0808,0E6D,0A76,C857,0B5C,0BFC,0C53,0C98
BZON	08BC	06DA,074C,078F,0803,0A49,0E76,C8D6,0C32,0C64,0C80
BILCC	083D	0821
CADSW	0688	0697,0A0D,CA3D,0A64,0E55,0EF5,0C09,0C4B
CHPNT	07E9	07CE,07D4
CHRTA	07D5	080A
CKERR	0C29	0C28
CKINT	0C57	0C19,0C31,0C60
CKIT2	0BA2	0B74
CKIT3	0A20	0A31,0A37
CKOPT	0701	06C7,0733,079D,08A3
CLLOG	0797	079C
CNTIA	068E	0630,0633
CNTRC	0644	063D
CNTRL	06A5	08B0
CN20	06B4	06A8
CN3C	06BB	06AD
CONAN	05F3	06A3
CILCC	0841	082B
EATCK	0C6B	069F,0C76,0C9A
CATER	0C78	0C75
DELY3	05FA	05FB,0723,0764,07C6,0E59
DIACK	063C	062F
DIAG	08C2	05E8,09EA,09FC
DIAGG	07EE	09F2
DIAMD	079F	070A,07A8,07AA
DIASW	0650	062D,09BD,09DA
DIANC	0678	069E,0A20,CA2C,0A2F,0A32,0A35,CA7C,0C78
DIAN1	0686	060A,067A,067C
DIAN2	0687	060D,0627,0672
DIAXX	0543	088D,09FA,CA22,0B5C
DIAX1	08BC	0878,0B62,C88B,0BA2
DIAX2	08BD	0666,C8CA,C8EB
DIAG1	0537	
DIAG2	0539	
DIAG3	053B	
DIAG4	053D	
DIAG5	053F	
DIAG6	0541	
DI1AL	067A	060B,060C,062E,062C,0E9D,0E8F,CA6E,0B8D,0EF2,0C47
DI2AL	067C	060E,060F,062E,0629,0E98,0891,CA68,0B90,0BEC,0C42
CLYIM	08B3	061C,08B9
CMRTA	09E0	08DB,08E9,0952,0962,05C5
DSWAL	0685	0606,0607,0622,0623,0E56
DSWAA	0A3C	0A15,CA78
DSWRE	05FF	0A11,0A3F,CA43
DSWSE	066A	0605
DSWSN	066E	0619,0620

SCA WRAP-AROUND TEST

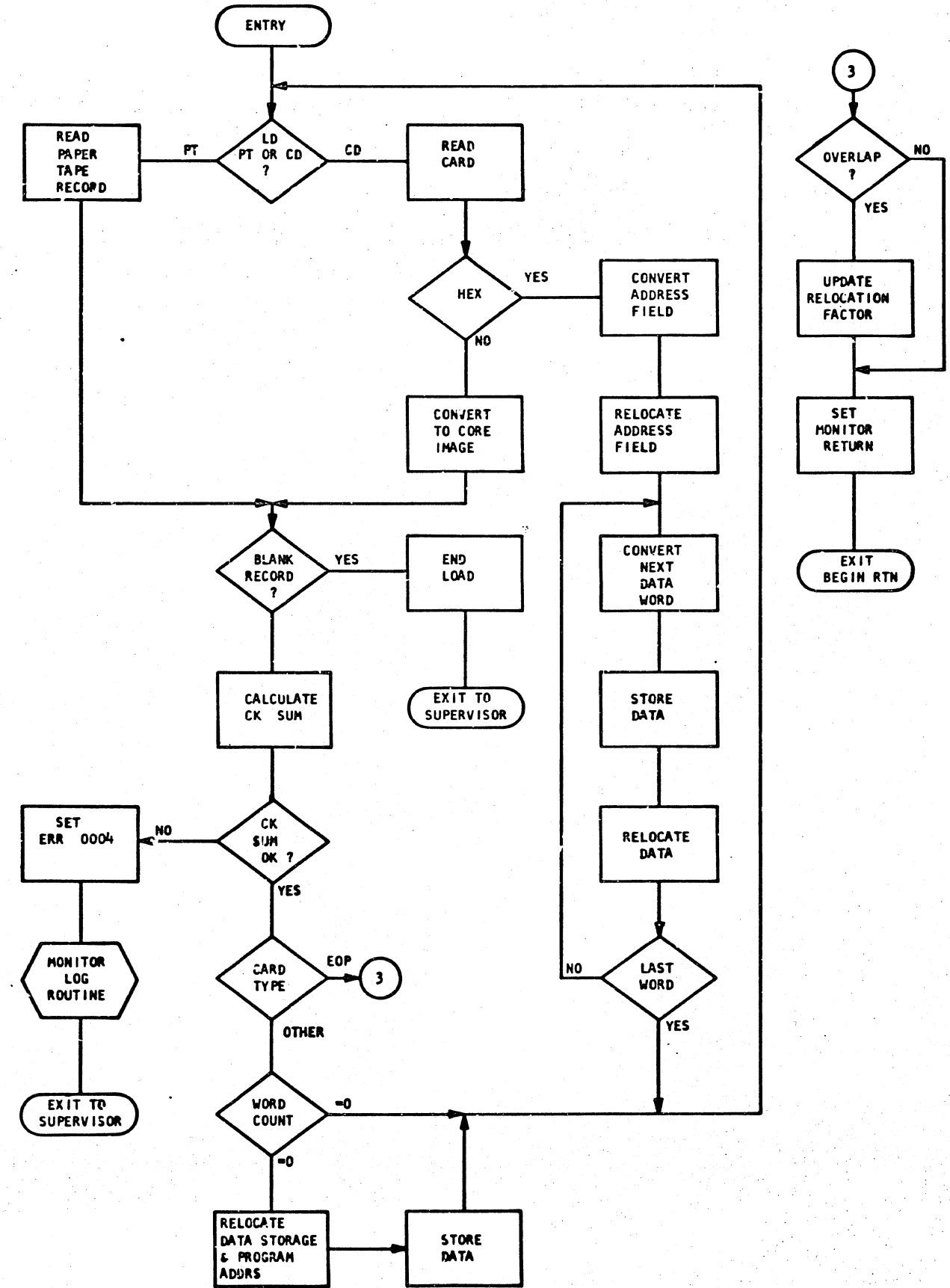
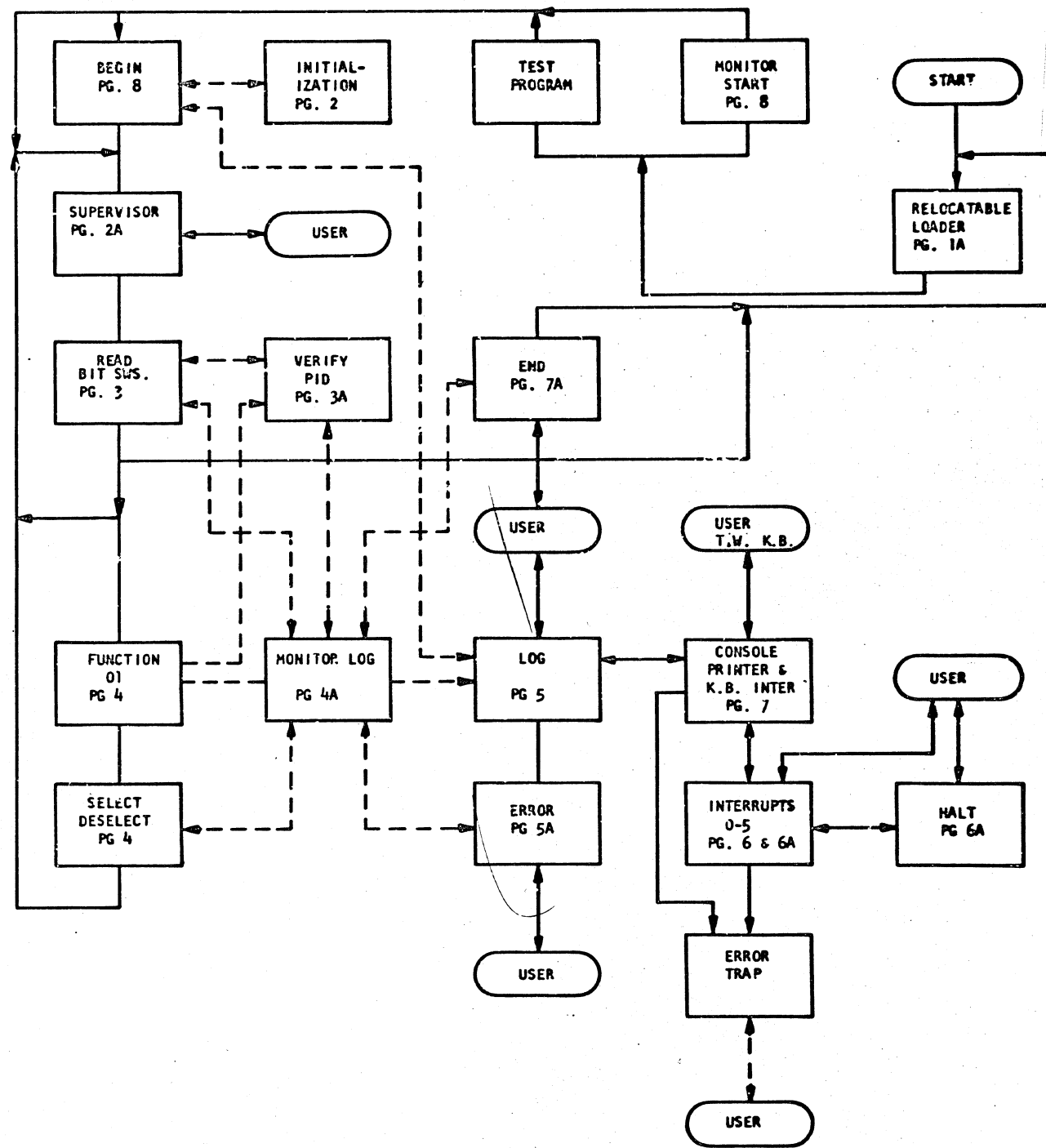
DSW1	0A7A	0882,0A41,CA45,0A58
DSW2	CA7B	0A47,CA5C
DSWXX	0942	088B,C8CA,C8D8,08E6,0533,094C,095C,096D,0578,05E1,09C3,09DE,09F3,0A0F,0A60,0C0F
CSWC1	0536	08C9
CSWC2	0538	08D7,094C
CSWC3	053A	08E5,0932,055C,096C
CSWC4	053C	0577,09AF
CSWC5	053E	09C1
CSWC6	0540	09DC
CTMK2	0A2C	0A1C
DTMK2	CA32	0A1F
CILCC	0E46	
END	0164	05DC,05DC,05DC,05DC,06E1
ENDCP	0674	0637
ENDRT	0627	0666
ENMSG	06CB	06B9,06D0
ERINT	0C67	0C1A,0C1E,0C22
ERLCK	0166	05EA
ERRGF	0162	05DC,CA72,C89E,08F8,0C4F,0C94
FAST1	0787	071F,07C2,07C8
FAST2	07C4	07C1,07CA
FSTCK	0700	0758,0759,07BC,07BE
GETCN	0662	0635,0641,0643,064E,0658,0661
ICHAR	06C6	08C0
IDRTN	0AA2	0A93,0AB2,CAB4,0AB6
ILO	017A	05DC
IL1	018A	05DC,0868
IL2	019A	05DC
IL3	01AA	05DC
IL4	01BA	05DC
INBUF	068F	0651,0652,0C6E
INERR	0C1A	0C18
INSCB	0C69	0C13
INTAN	0C03	06A1,0AA0,0C55,0C5B
INTER	0C32	0C2C,0C30,0C66
INTIC	0ABD	0A9E,0AA2,0AA5,0AAS
INTMK	0C6A	0C0B,0C11
INTCK	0C5D	0C5A
INTR	061F	063A,065D,0C66,0866
INTSW	0ABE	0A92,0A9F,0AAA,0C37
INWAS	0C68	0C0D,0C15,0C1C
K0001	06FF	0757,07BA
K00C6	0682	0647
K00G5	0683	064C
K100C	0C9C	07AD
LDSYN	0659	064A
LOCK	05E7	05ED,05F3,08A5
LOG	0163	05DC,06DC,074E,0791,08C5
LOGEY	0167	
LOGG1	0785	
LOGG2	073C	073A,0741
LOGG3	07EF	07ED,07F4
LOG1	0845	0785,0799,080F
LOG1A	0850	0819
LOG1E	0E55	0797,0823
LOG1C	085A	082D
LOG1C	085F	
LOG1C	0E0B	0770,0812,0813
LOG12	0815	0775,081C,081D
LOG13	081F	077A,0826,0827
LOG14	0825	077F,0830,0E31
LOOP	05F5	05EF,05F1,06C1,08AA
LOOPE	06B2	08A7,08A9,0B55
L0PRT	0B55	0A75,0898,08FE,0C52,0C97
L0PSW	0685	09E2,0A95,0C57
LRTN	06F9	06FA
MASK1	0C9D	0A6A,0B92,C8EF,0C44

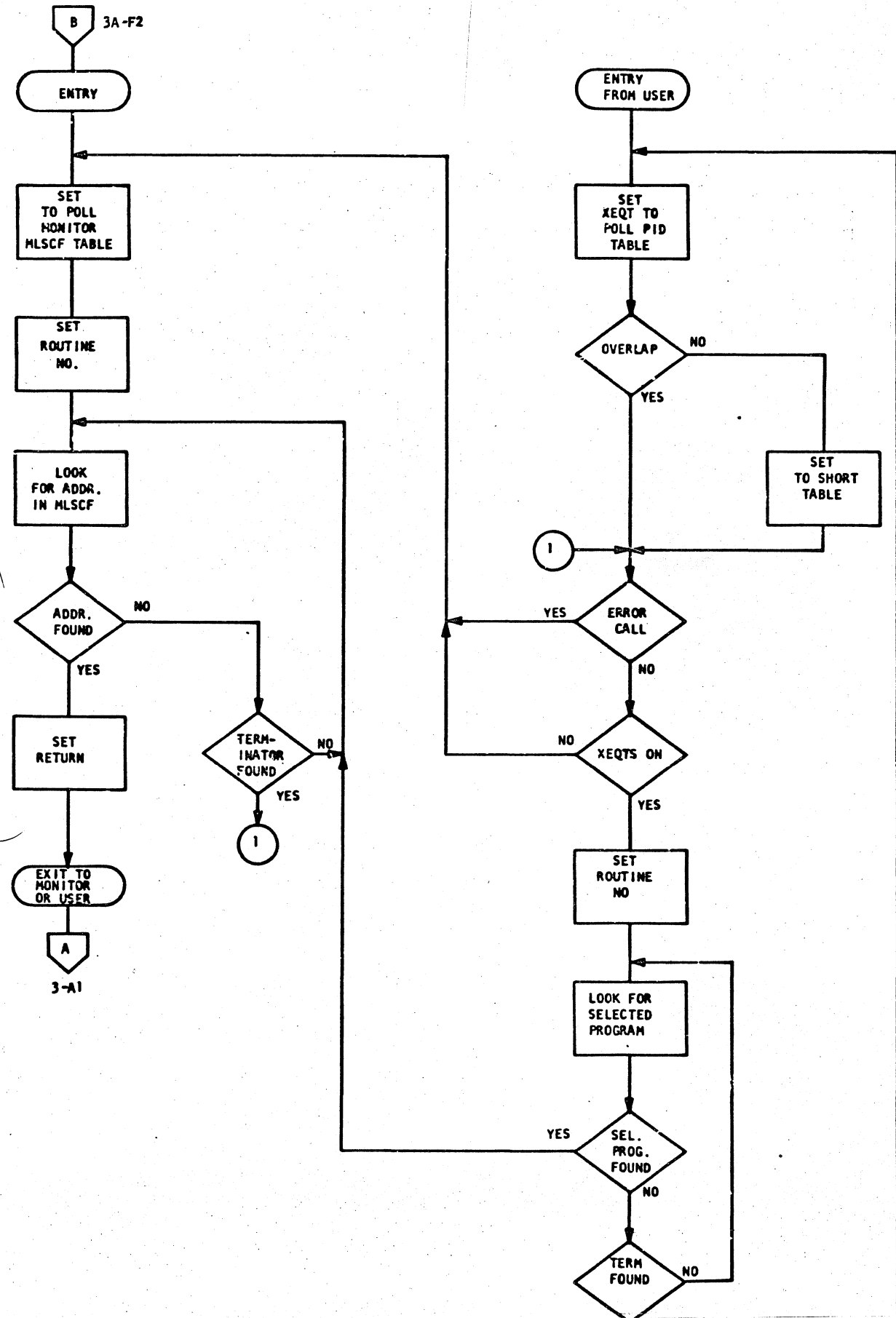
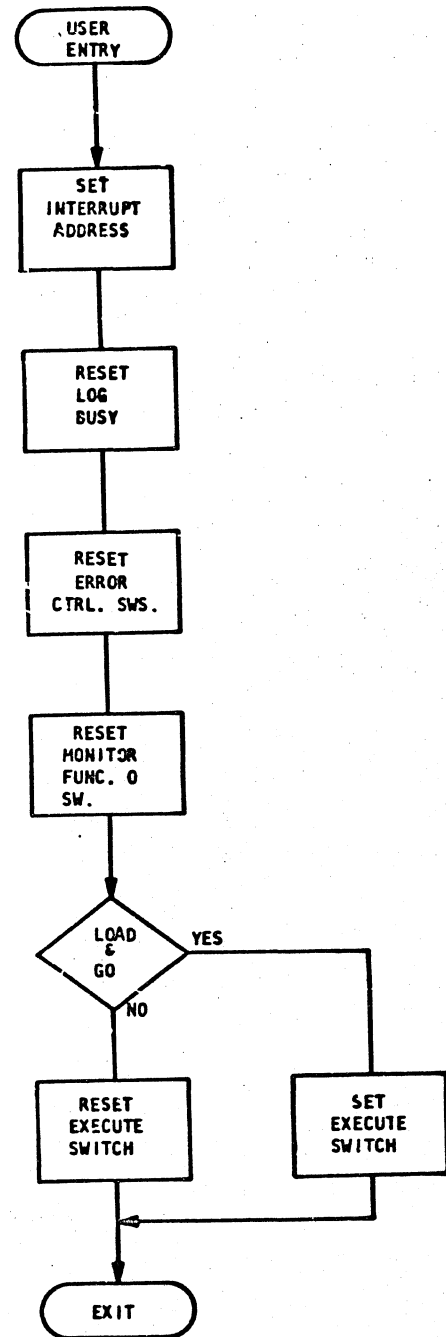
SCA WRAP-AROUND TEST

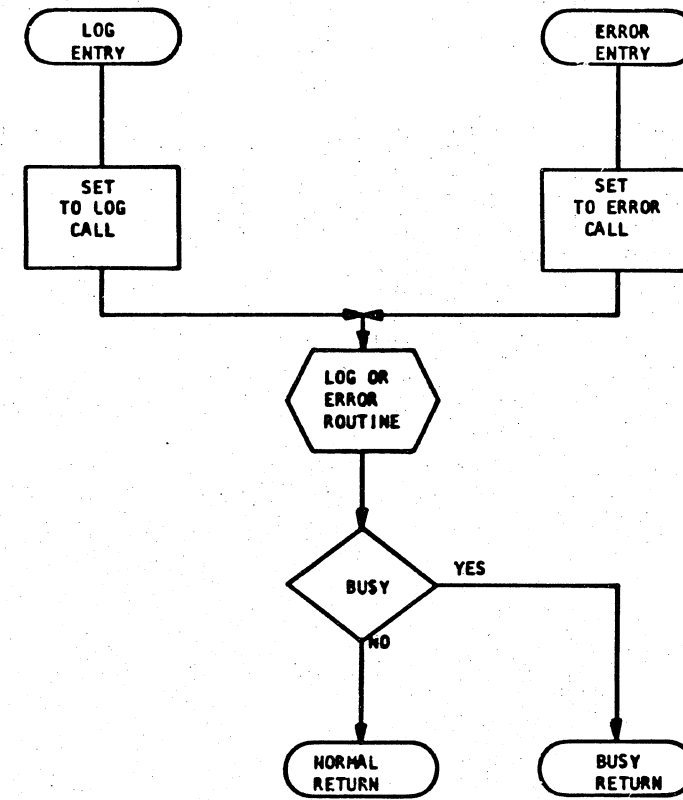
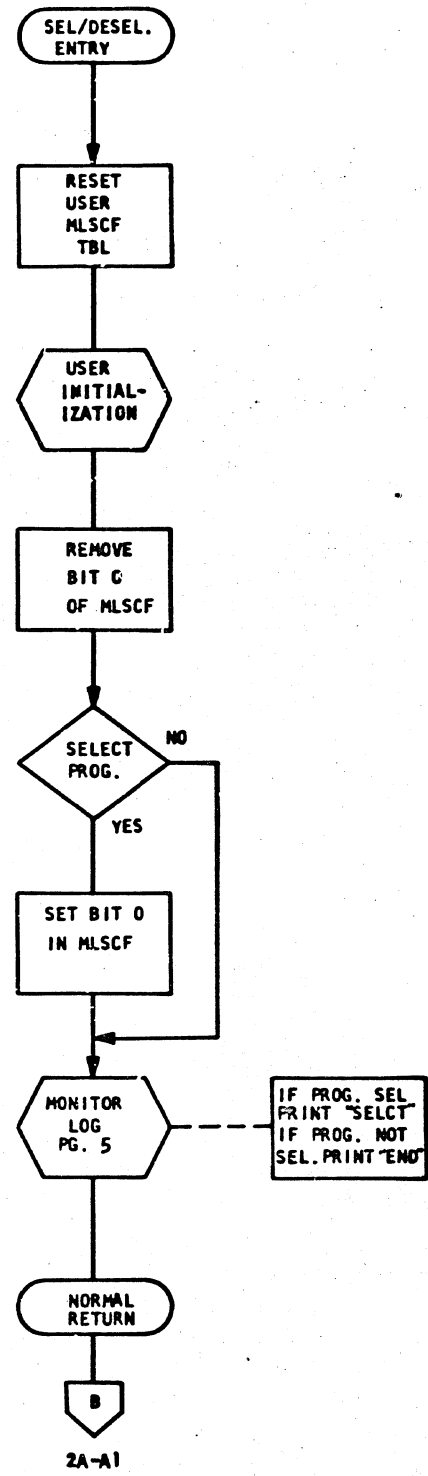
MASK2	0A39	0A2E
MASK3	CA38	0A34
MID1	0A83	0A80
MID3	CA85	0A9C
MKS#1	0A38	0893,08D9,C8E7,0950,0960,0A1A
MKS#2	0A3A	07E2,C895,CA1D
MLSCF	C5E5	0612,06C3
MSGGC	0A8A	0ABA
MSGIC	0A7C	0A4E,CAAB,CB7E,0BCB,0C39
MSGNG	0ABF	0A4C,0AA4,CAB7,CB79,0BC9,0C35
MSG	0B48	06D3
NDPNT	072F	C73B,C753
NDRS1	C7AC	0714,C7B3,C7B5
NDRM	0AB7	0A88
OFF	086C	09F8,0A0A
OFF1	0874	0E6A,C870
OFF2	0876	0873,CB59
OFF3	0878	087B
OFF4	087D	0880
OFF5	0882	0885
OFF6	0887	088A
OPRTN	0733	0728,C75D,C762,0766
PASS	068D	0656,C664
PASS1	07E8	07CC,C7E5
PID	05DC	05F8
PNDCK	0A4D	0A98
PNTCF	C7EE	C7EC,C7FF
PRTPN	068A	0639,0653,0663,CA59,0AAD,0C5D
RAC	05DE	C6BF,0755,C7B8
RCVEC	0684	0650,066C,C87C,0C6C,0C70,0C8C
RDCAT	066C	0624,064F
RDCNT	C693	0644,0646,C64B,0655
RDCPR	068C	07DA,C8D2,C8E0,08EE,0EF7,0900,C905,0912,091E,0924, 092D,0947,0957,0967,0972,097D,C986,098F,099E,09A1, 09AA,C9B7,C9C9,0C72,0C90
RDIA1	0670	0608,0625
RDIA2	0672	0609,0626
RDWRT	0695	0621,063C
RD9TH	064B	0649
RESET	08BE	061A,08C8
RID	05DD	06AA,06B2,06E4,06B6,06BB,06D1,C864
RIDCK	06FA	06AC,06BB
RLCF	0188	
RQKE	018C	05DC
RQTY	018B	05DC
RSOFF	0671	0717,C71A,C7AF,07B1
RSTS#	0868	06FD,086E,C871,0875,0EAC,08CB,CA85
RTNS#	0165	06C5
RTIBL	06E3	06BD,06FA
RTUA	C922	06EC
RTOE	092B	06ED
RTOC	C945	06EE
RTOD	0955	06EF,096E,C975,09B3
RTOE	C965	06F0
RTOF	C970	06F1,0982,C98E,C994,099D,09A6
RTO1	C8C8	06E3,08AE
RTO2	C8D0	06E4
RTO3	C8DE	06E5,08F3,C8FC,0905,09CE,0917,C920,0929,0934
RTU4	08EC	06E6
RTO5	C8F5	06E7
RTO6	08FE	06E8
RTO7	0907	06E9
RTO8	C910	06EA
RTO9	0919	06EB
RT10	097B	06F2
RT11	0984	06F3
RT12	098D	06F4
RT13	C996	06F5

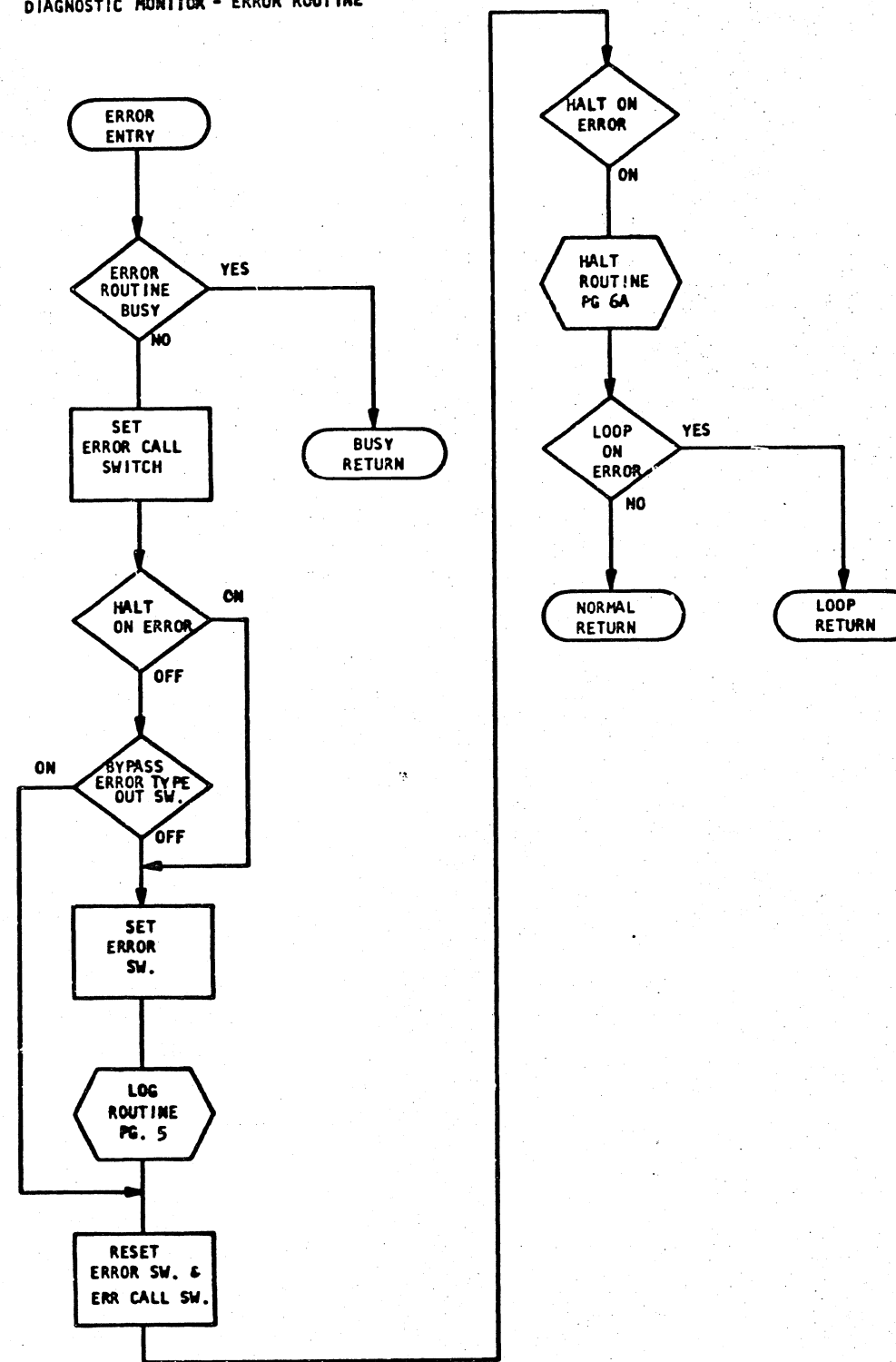
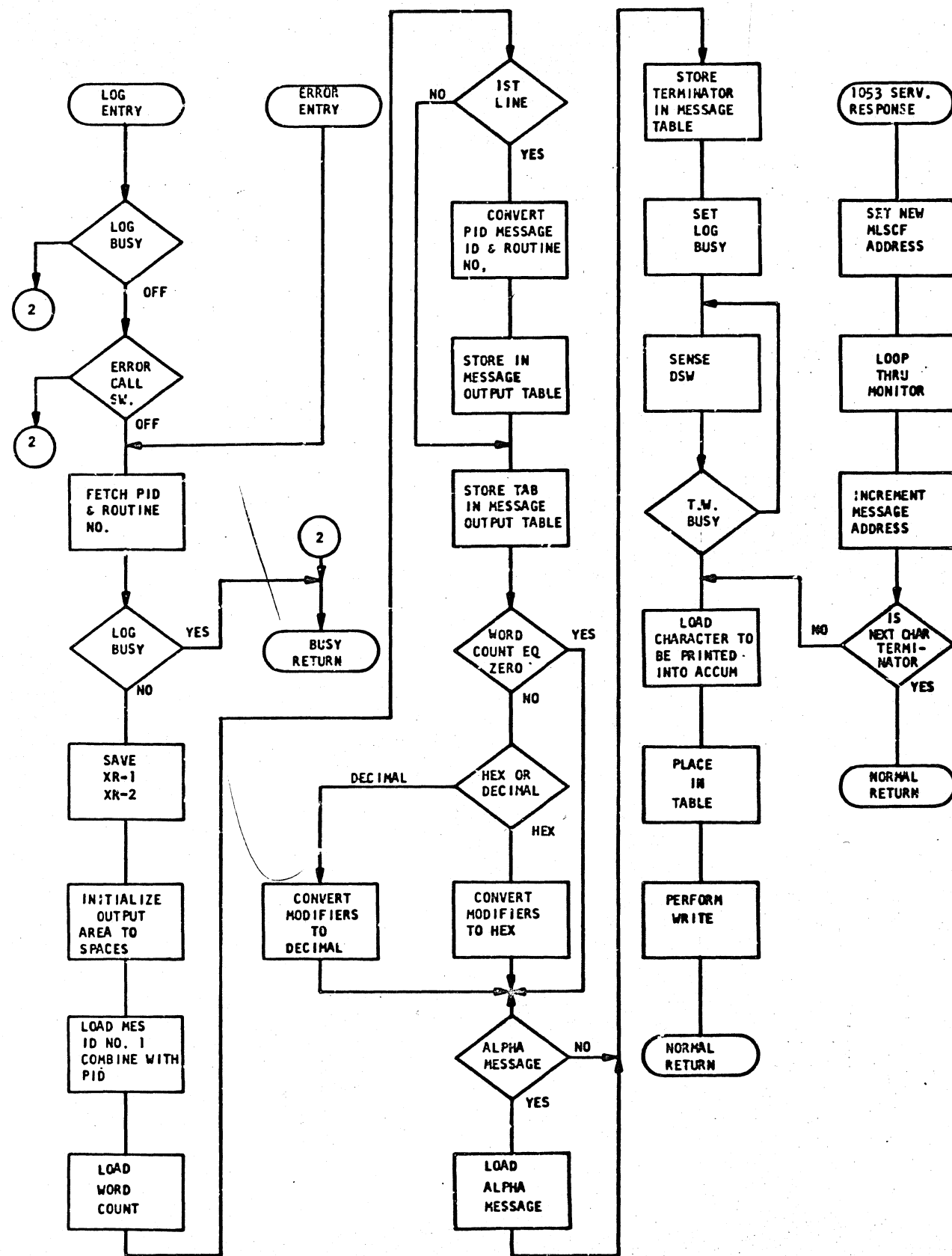
SCA WRAP-AROUND TEST

RT14	C95F	06F6
RT15	C9A8	06F7
RT16	09B5	06F8,070D,C7A4
RT17	C9C7	C6F9,070F,C7A6,07DE,0E9B
SAVE1	06FD	0716,C719
SCOPE	C755	0732
SCOPI	075E	075C
SCOP2	C763	0761
SELCT	07CB	07E6,C9D4
SEND	0681	067E,C7DC,C89C,09CB
SIREG	08C0	C65F,C9E0
SNDS#	0696	061E
SPACE	0A8C	0A89
START	0161	05DC,C614,C6C9
STOCT	0691	0634,C8D5,C8E3,08F1,0EFA,0903,C90C,0915,091E,C927, 0931,C94A,C95A,096A,0975,0980,0989,0992,099B,09A4, 09AD
STOPS	C692	0665,C9BB,C9DE
STOR1	0A01	09F5,0A05
STOR2	0A07	09FC
STRT	0663	05E3,C5E4,C5E5
STWD	09FE	0A02,0A07
SVKB	018D	
SHDIA	0A00	07A0,C9F7
SW0	050F	05E8
SW1	05E0	06A6,C6B0
SW2	05E1	05FD,0703,0707,0711,071C,C725,C76C,0772,0777,077C, 0781,09D1
SW3	05E2	072B,C72F,C735,075E,07D1,07D5,C7E9
SYALT	0676	0662
SYCMC	068B	0659,C94E,C95E,C9BF,C9E4
SYCON	065F	065B
SYNCC	C9F0	09E7
S2PNT	C767	0706,C76C
S2WAS	06FB	0702,C783
S3PNT	C735	072E
S3WAS	06FC	072A,0737,C7CF,07D7
TABLE	0B80	06CD,C6D5,C6D8,06DE,073E,C744,C747,074A,075C,C769, 0787,C78A,C78D,0793,07F1,07F7,C7FA,07FC,0801,C807, CA52,CA56,CA5A,0A5E,0A62,CA66,CA6C,0A70,0A74,0A83, 0AA6,0B7F,0B83,0B87,0E8A,0B8C,C88F,0E94,0E97,C95A, 0BDF,0BE3,0BE7,0BEA,0EEC,0EF1,C8F4,0BF7,0EFA,0CC5, 0C07,0C20,0C24,0C26,0C29,0C2D,0C3C,0C40,0C45,0C49, 0C4D,0C51,0C62,0C7E,0C83,0C86,C88A,0C8E,0C92,0C96
TERM	CAB8	0A89
TERM1	0AC5	0A8A
TERM2	0AEF	0A8C
TERM3	0BC9	0A8E
TERM4	0B23	0A90
TGWD1	0BC0	0385,0B9F,0BAC,0BD4,0EE5,0BFF
TGWC2	0BC1	C888,C8A0,C8A6,C8AA,0ECE,0BD2,C8E8,0CC0
TMDLY	C86B	0885,C886
TRGAN	0B5B	0A28,C8C8,CC01
TRGWC	C9FD	0E87,CA24,CB67
TRWC1	0BBE	0B6D,0B72,C8A4,C8A8
TRWC2	0BBF	0B71,C8C2,C8CC,0BDD
WAITB	0616	0610
WAIT1	C605	0601,0618
WAIT2	05FB	08CD,09EE
WCNT	05F9	05FC,C602,0604,0616
WD1	C876	0BAE
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WD2	0BCA	0BC7
WRCAT	C67E	065C,09EC
WRCNT	0654	063F,0642

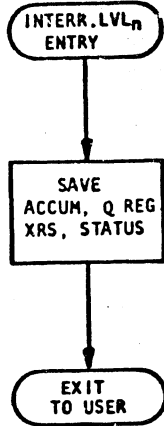






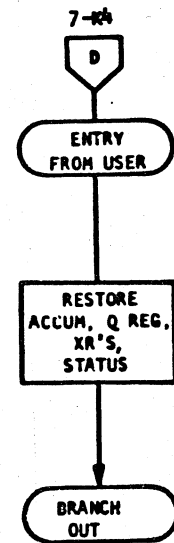
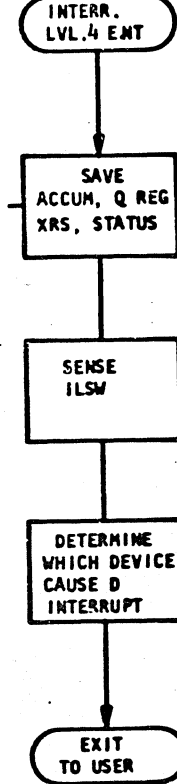


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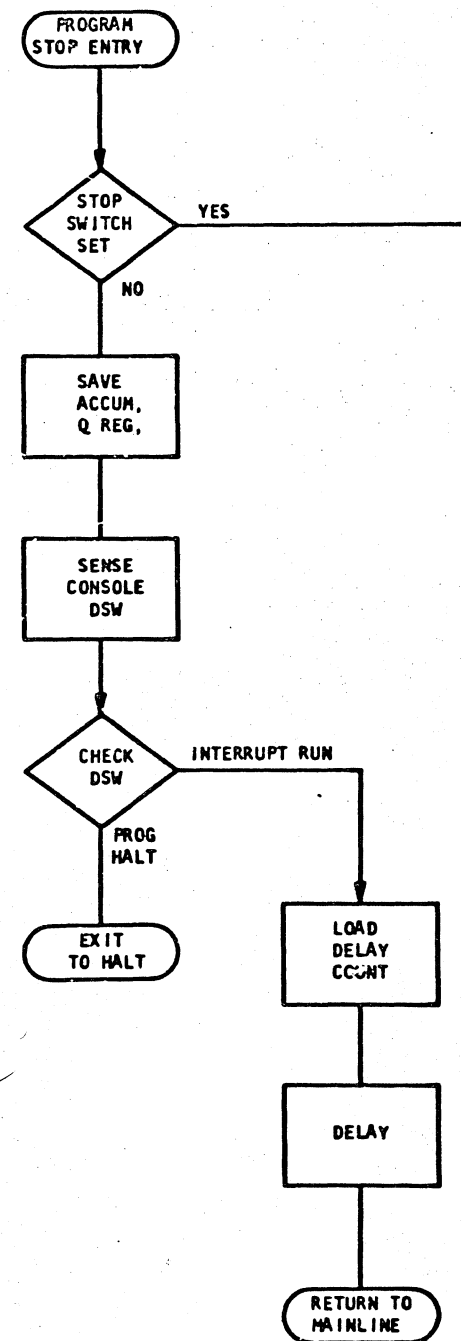


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 ILSW LVL_n
 XFER VECTOR
 XR2=INT
 LVL_n

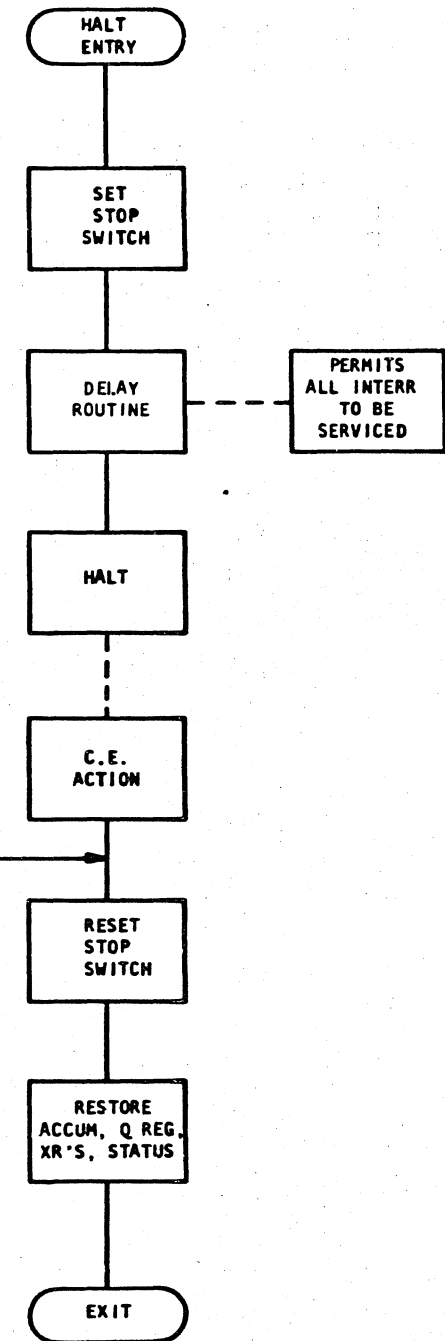
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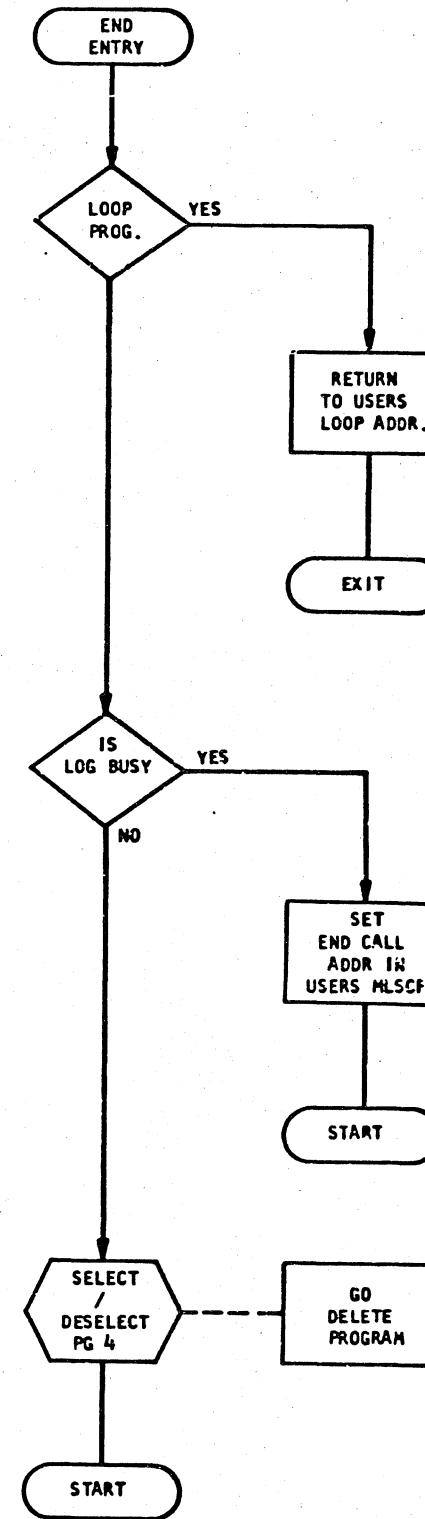
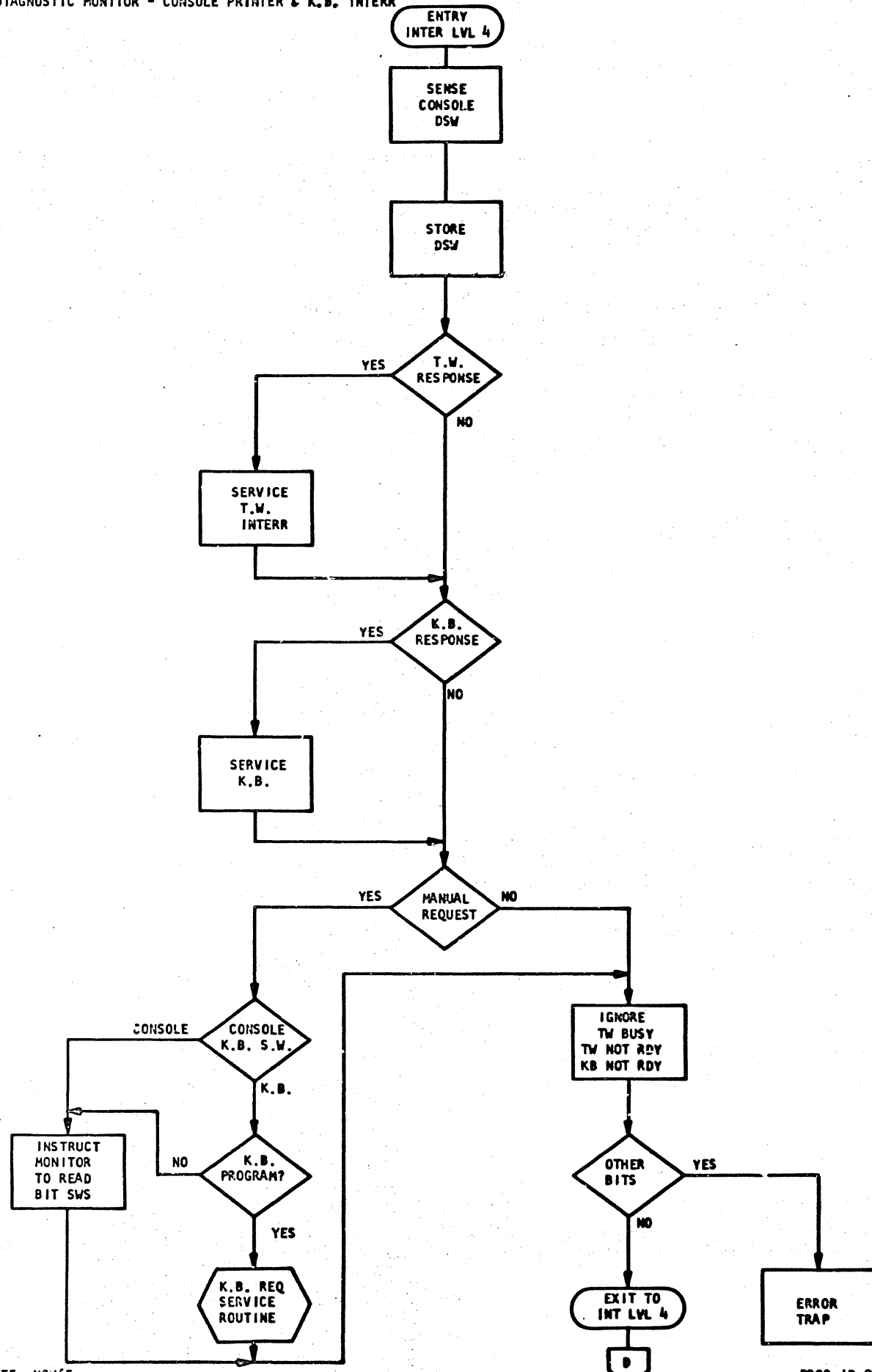


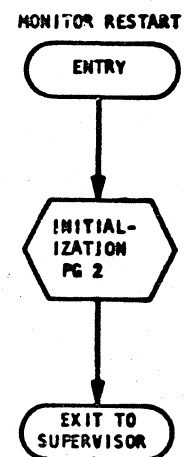
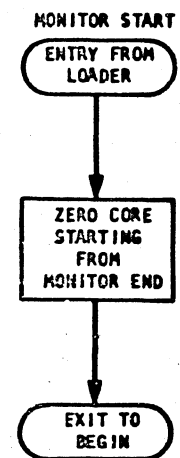
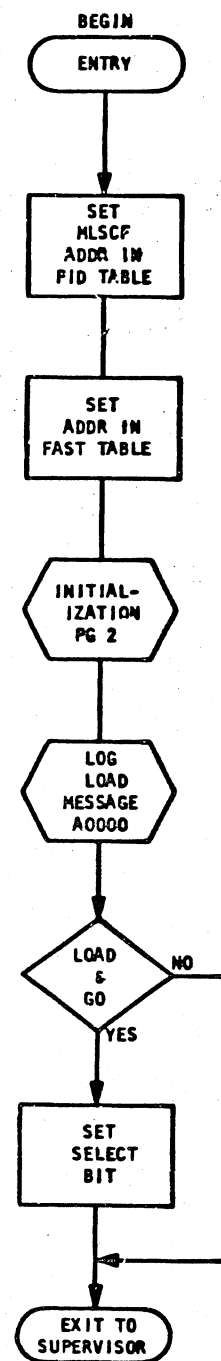
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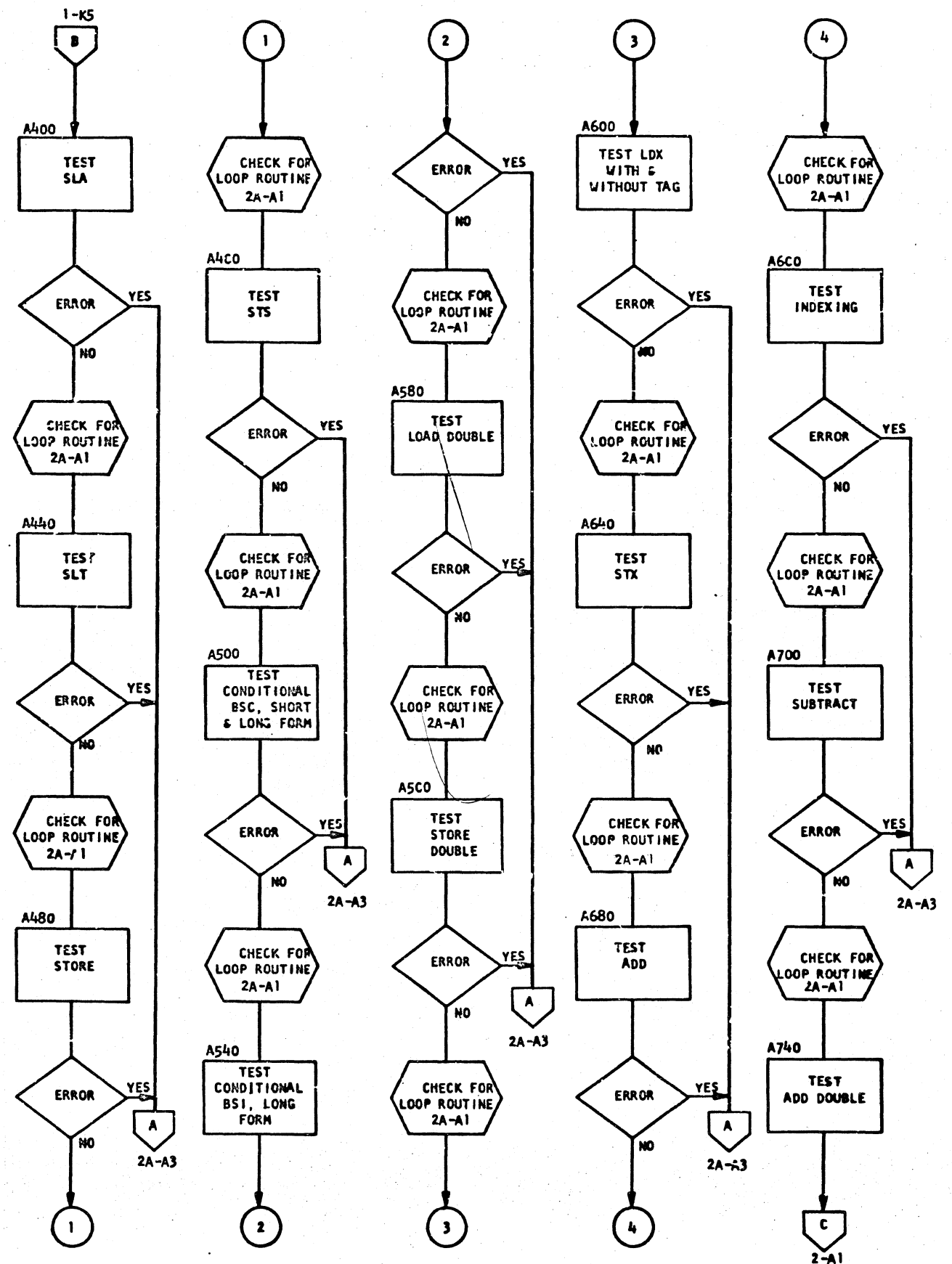
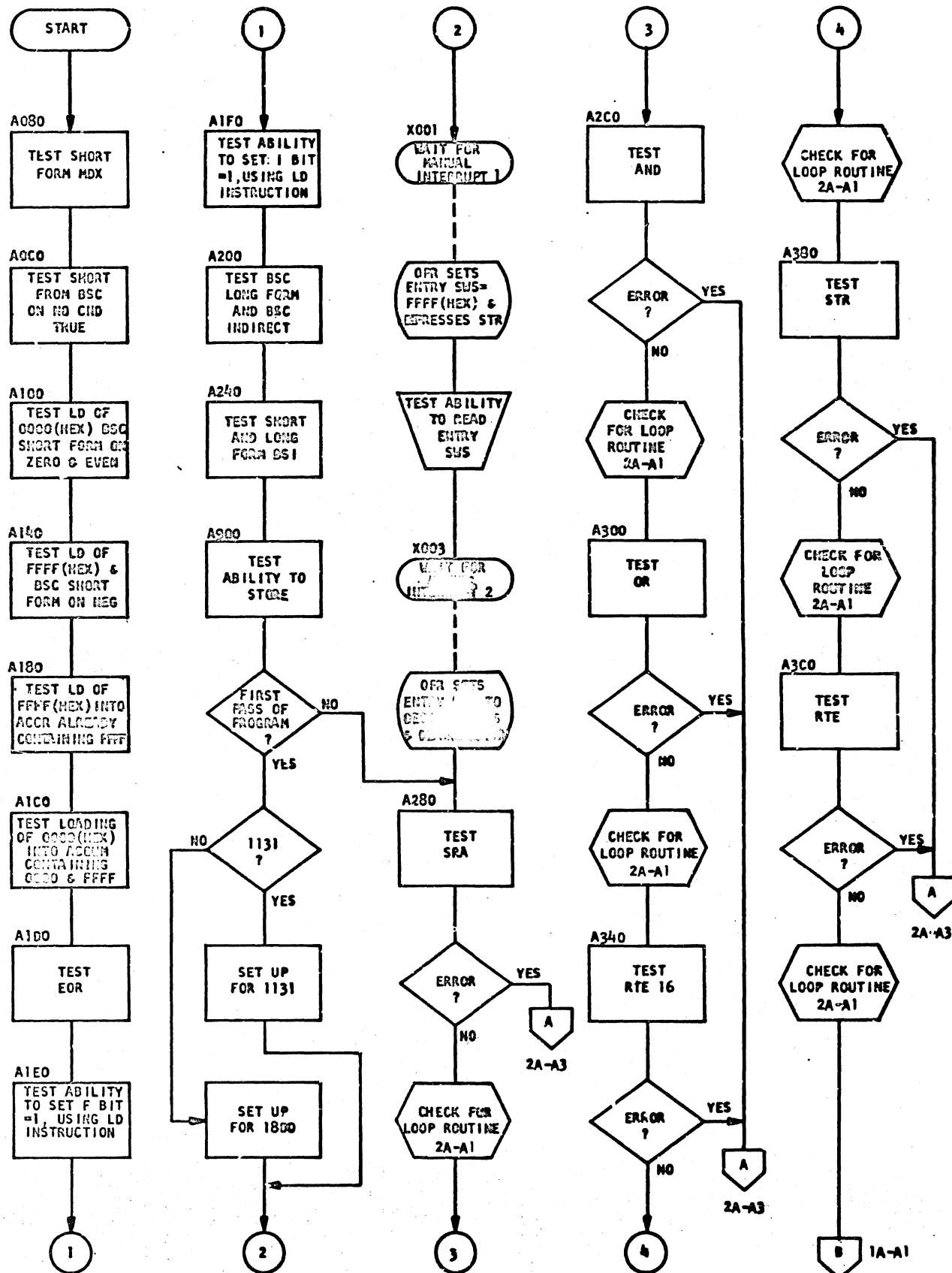


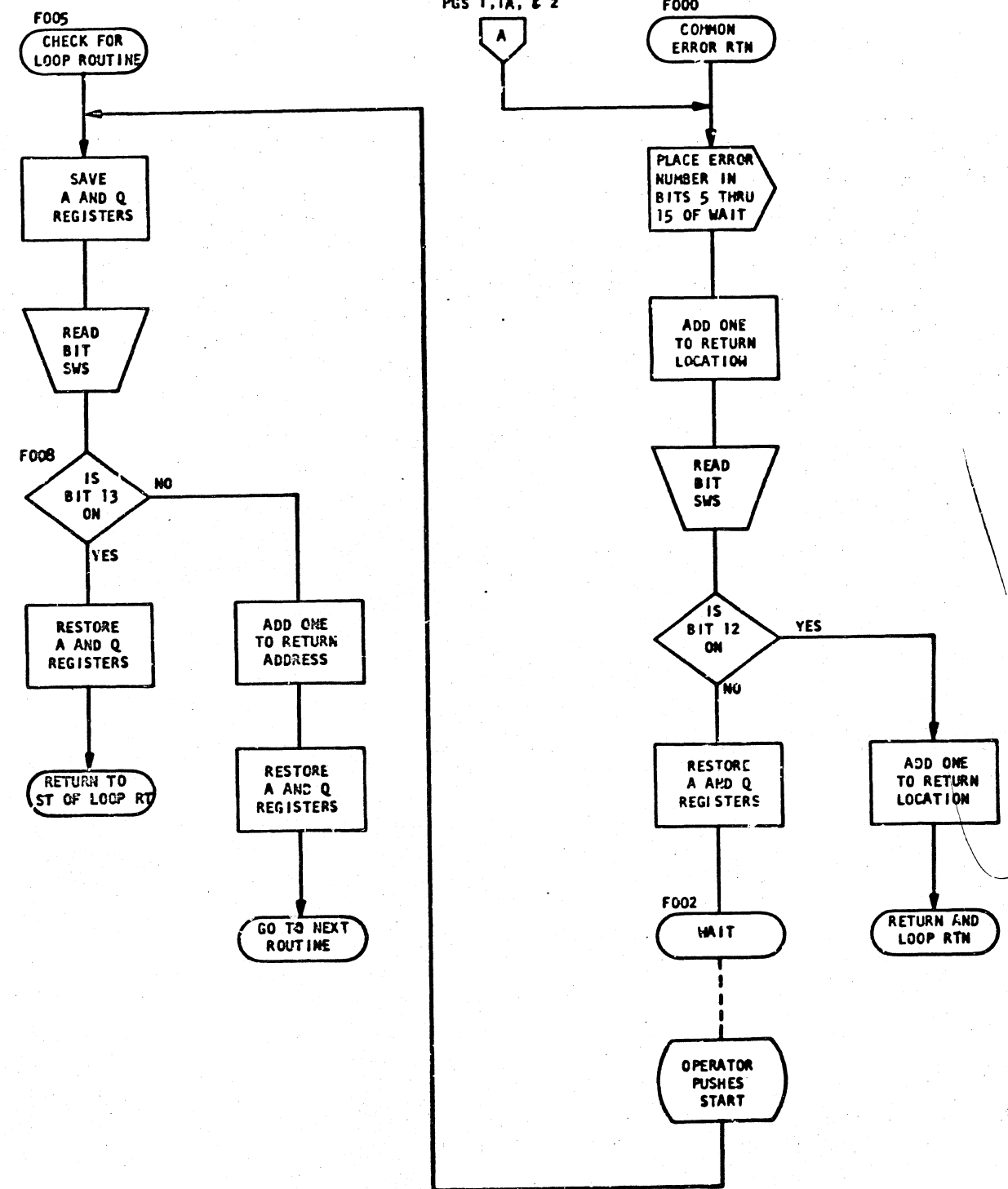
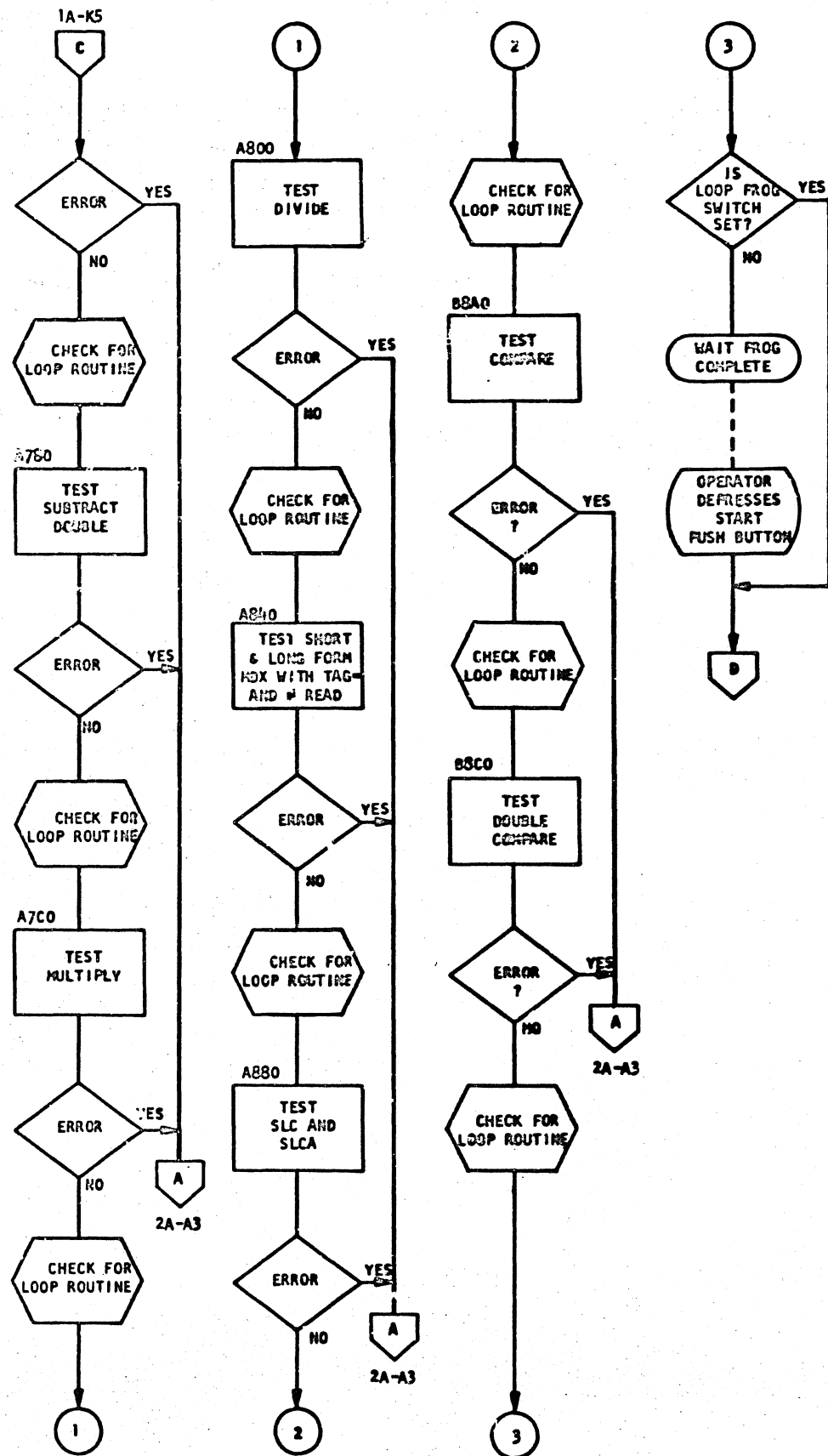
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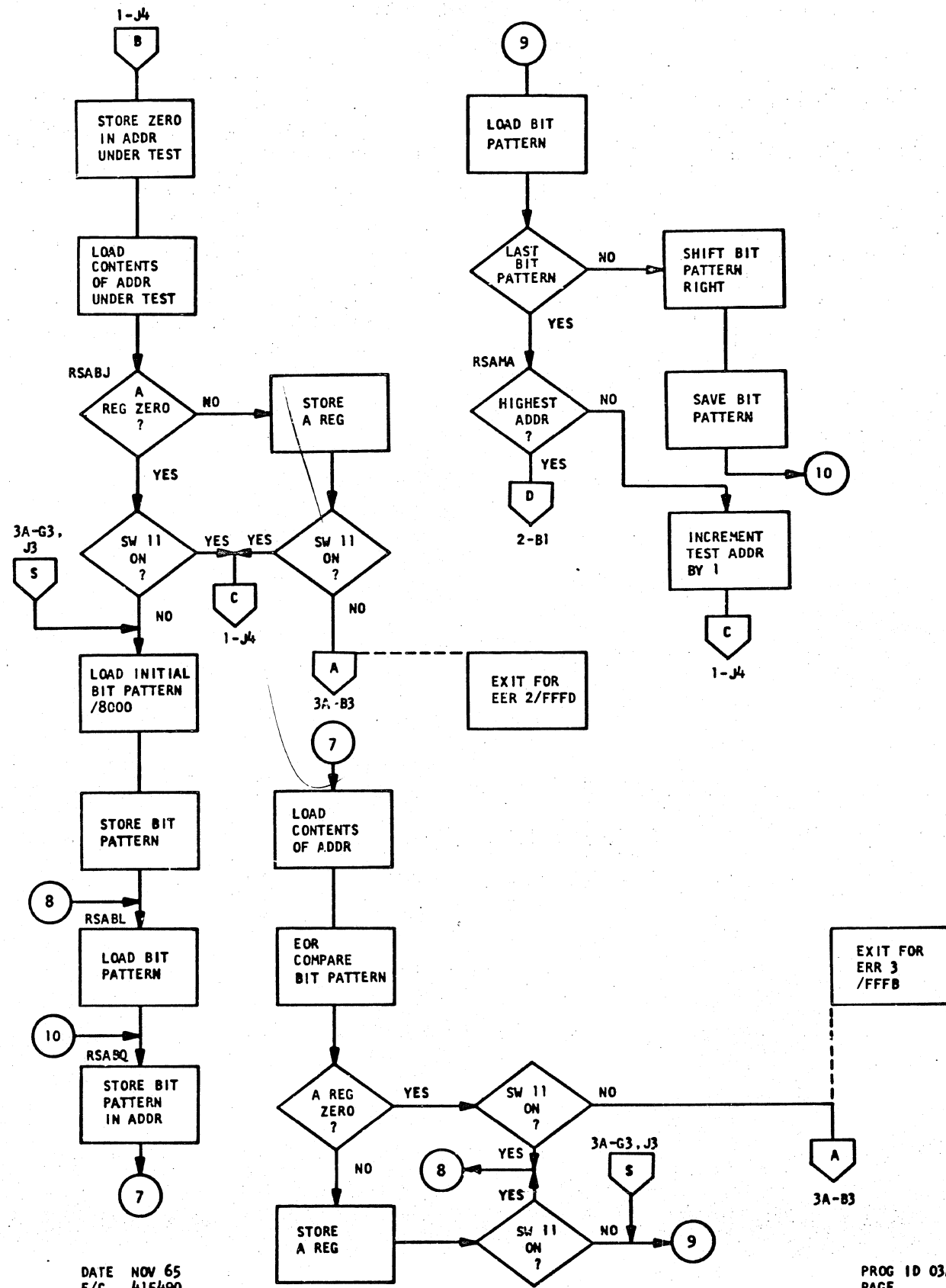
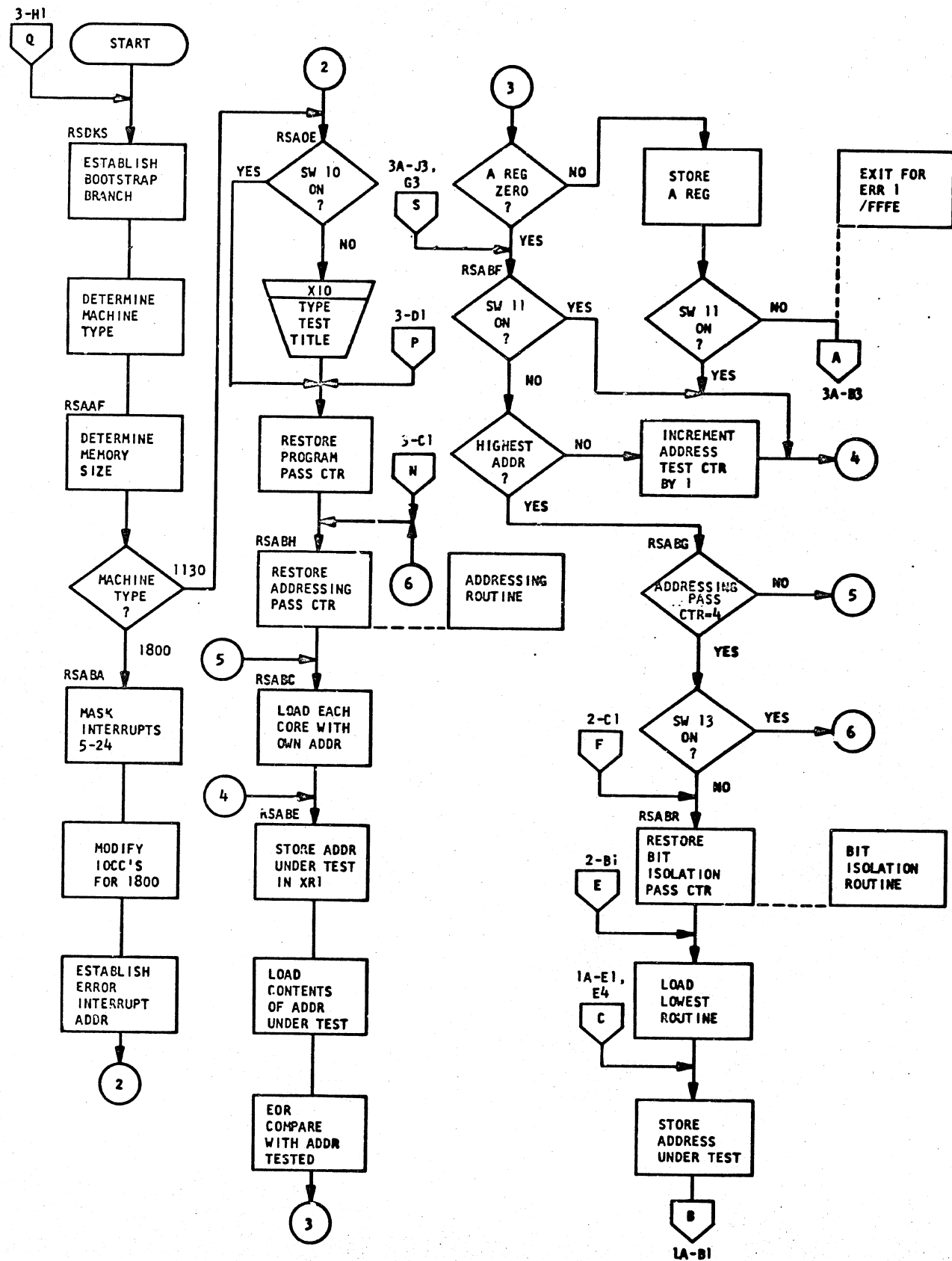


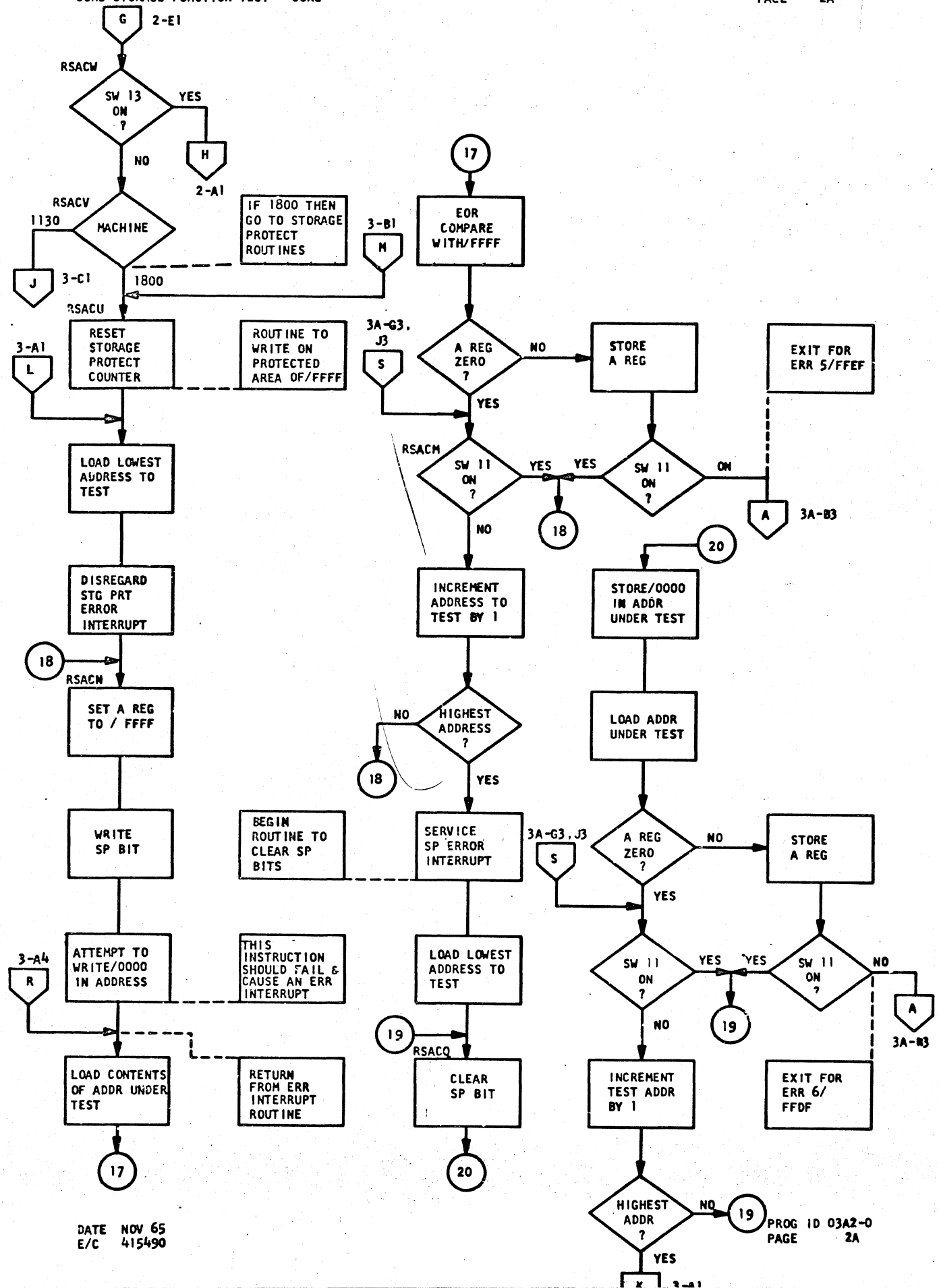
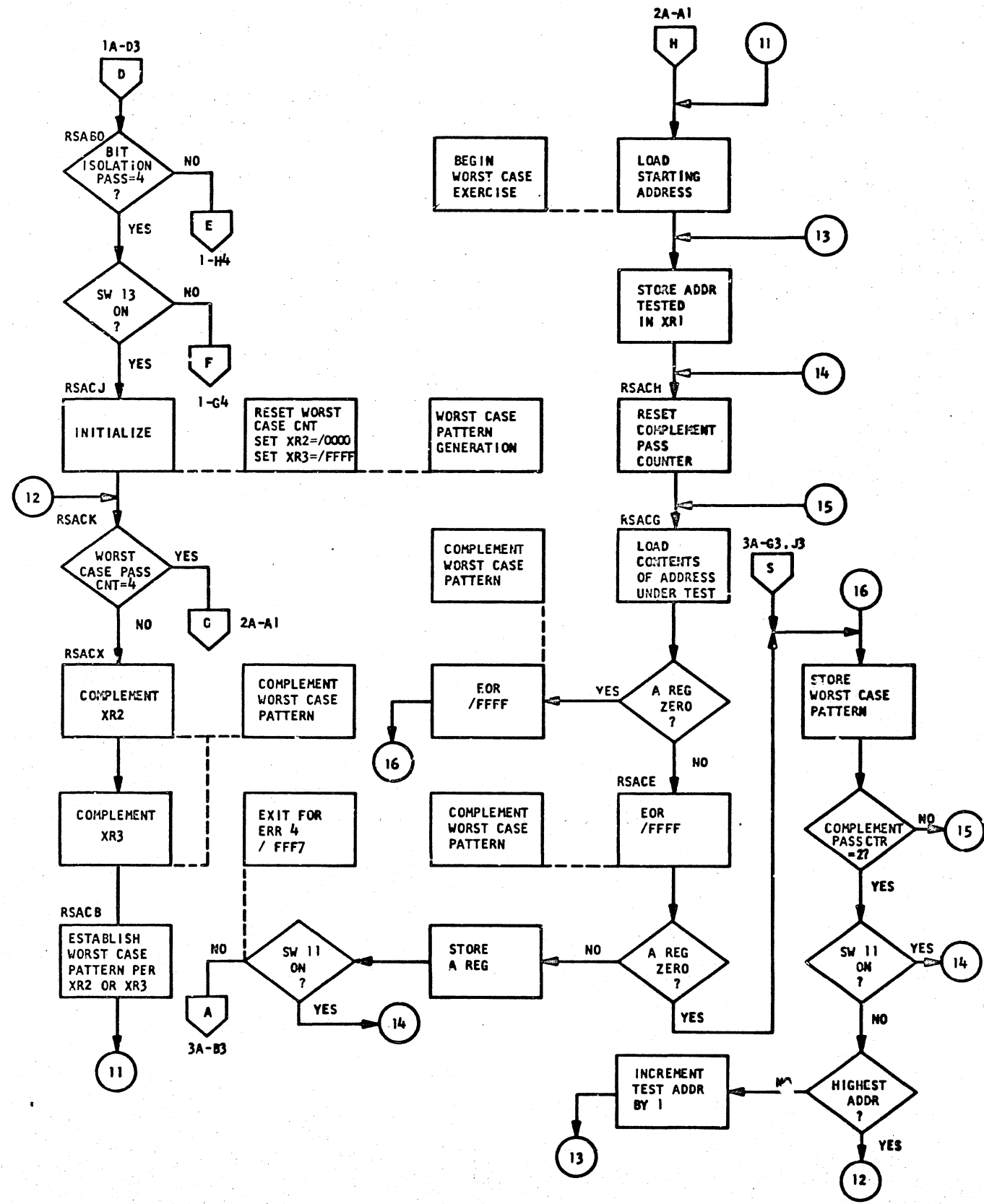


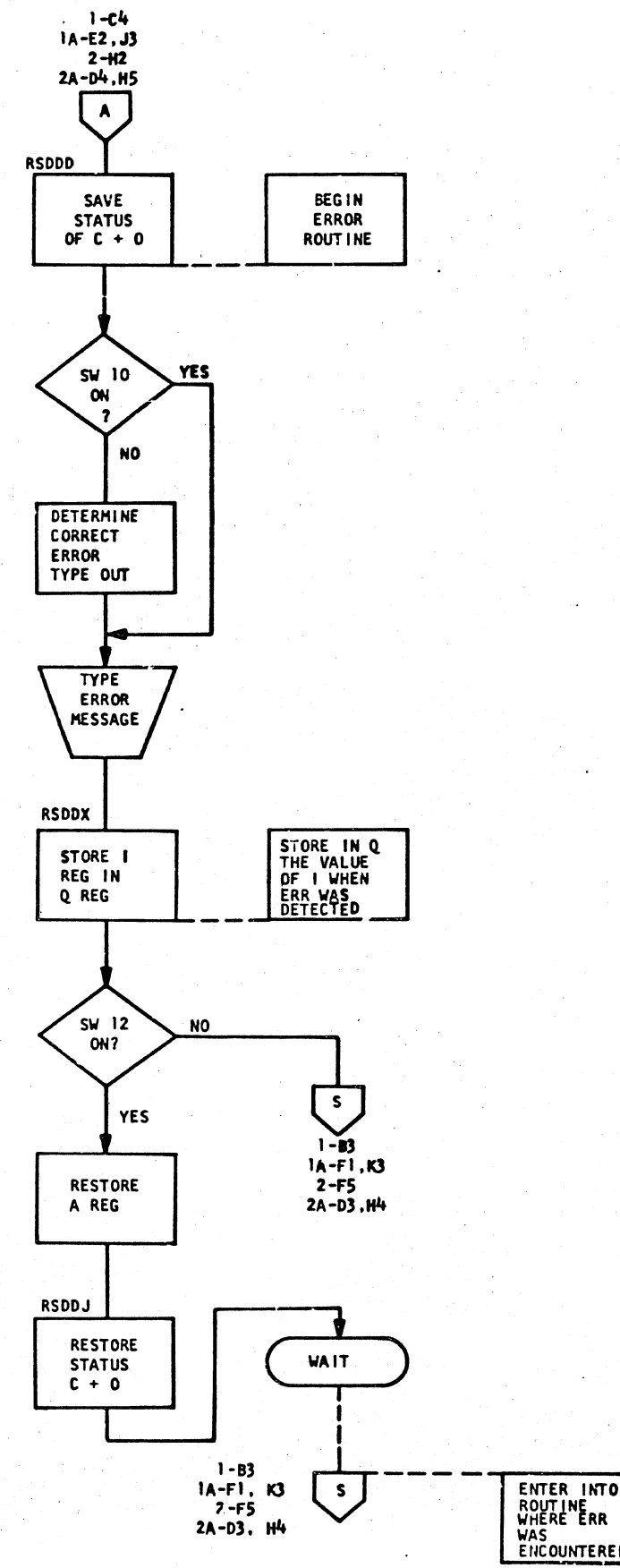
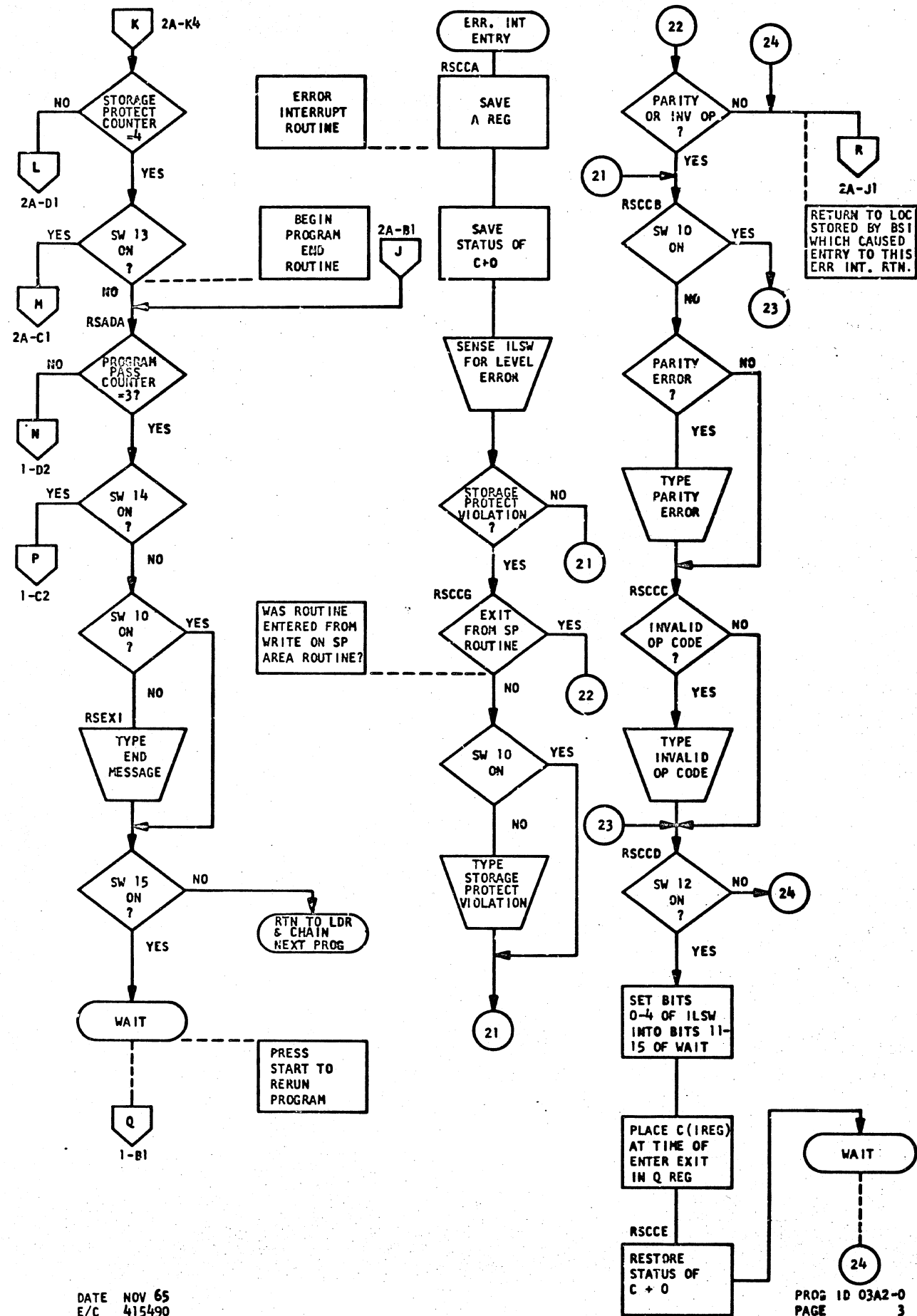


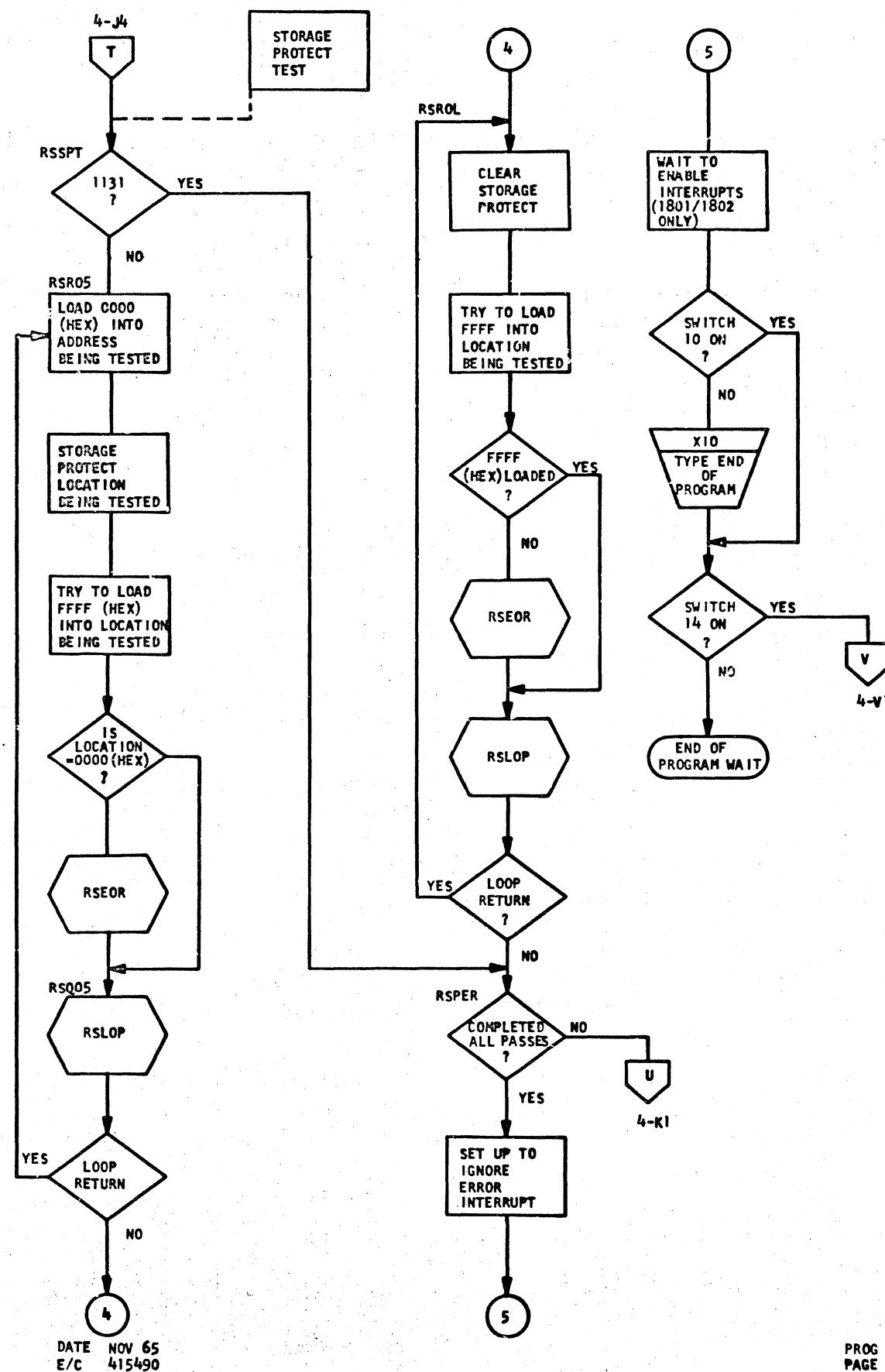
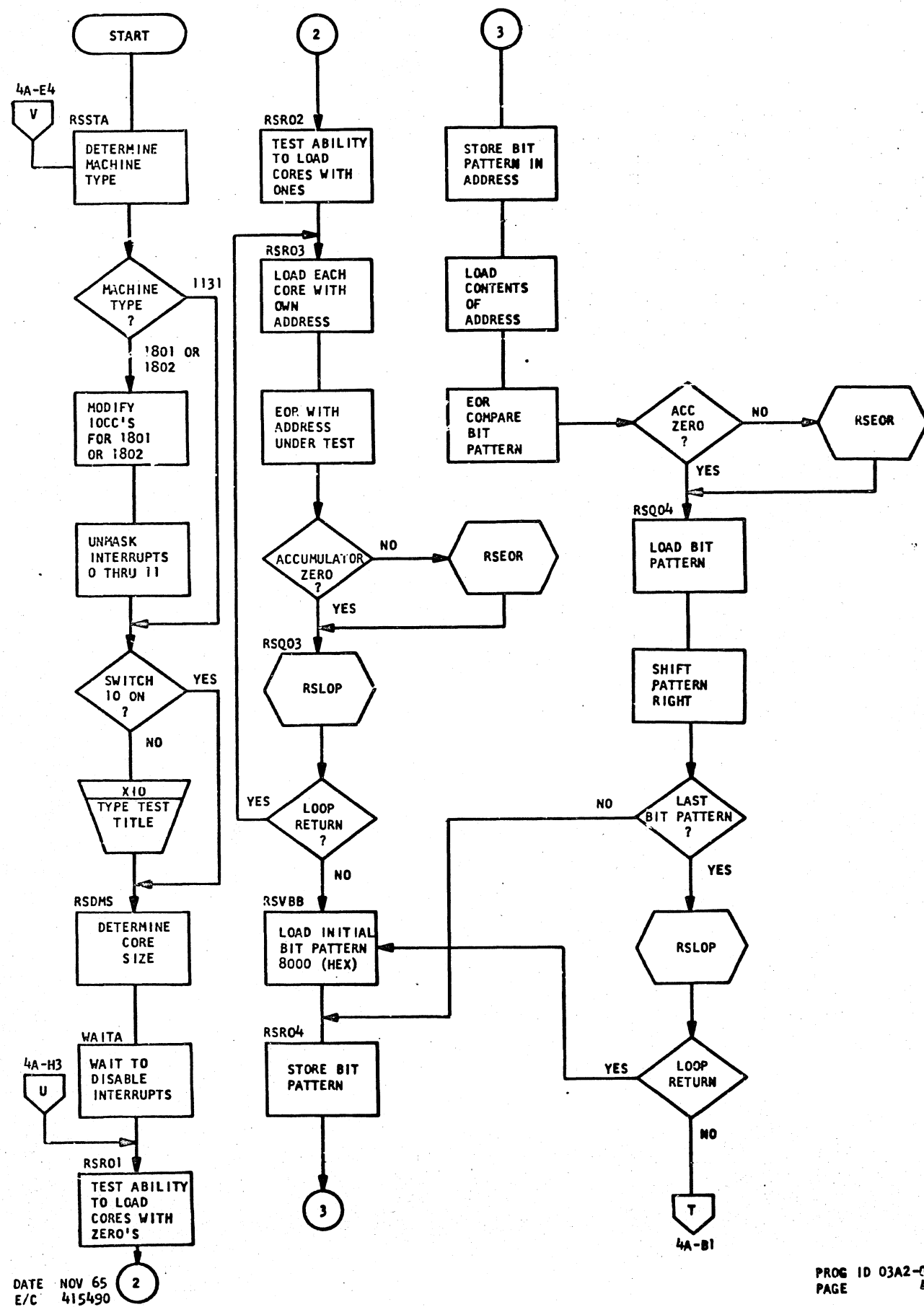


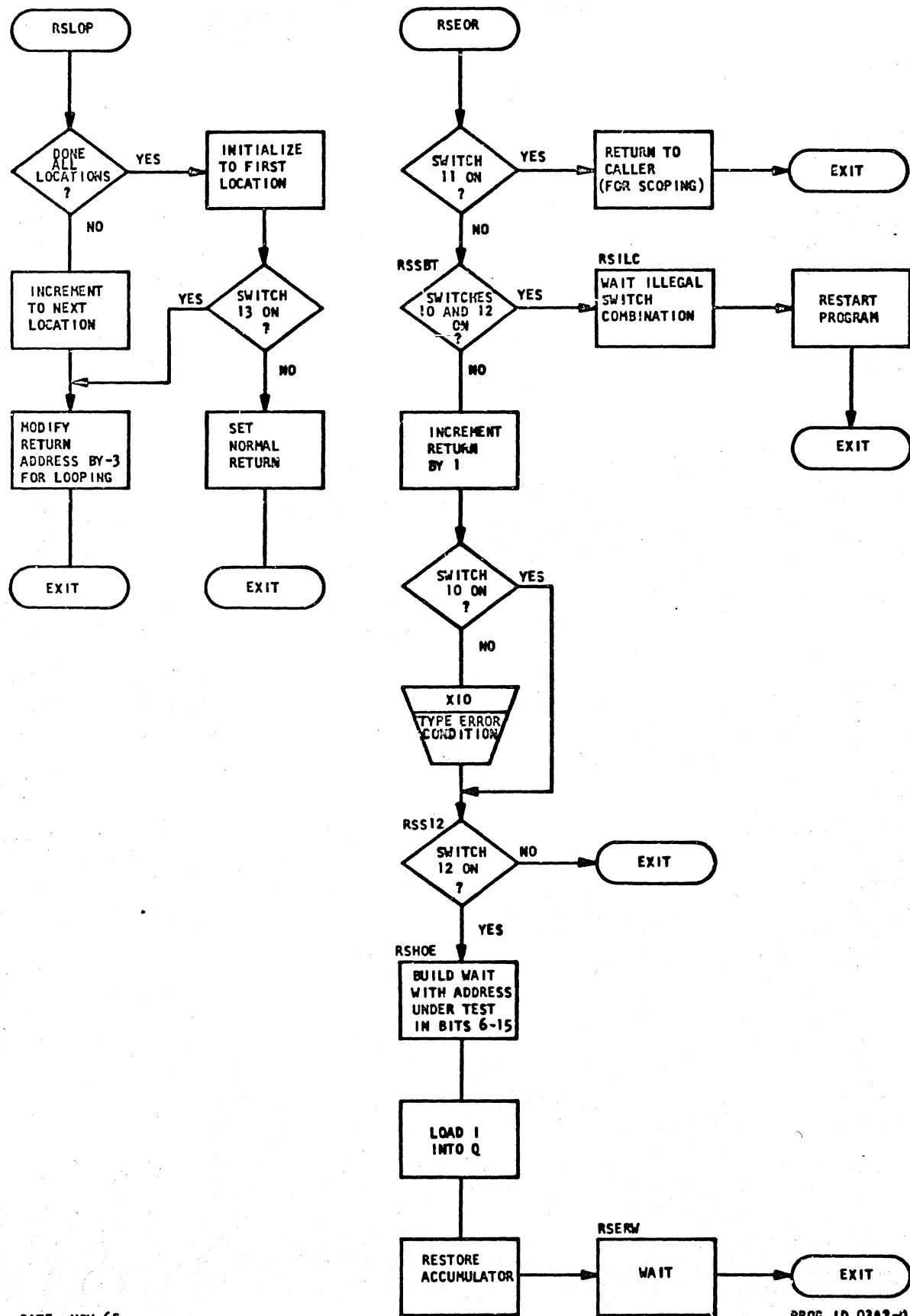


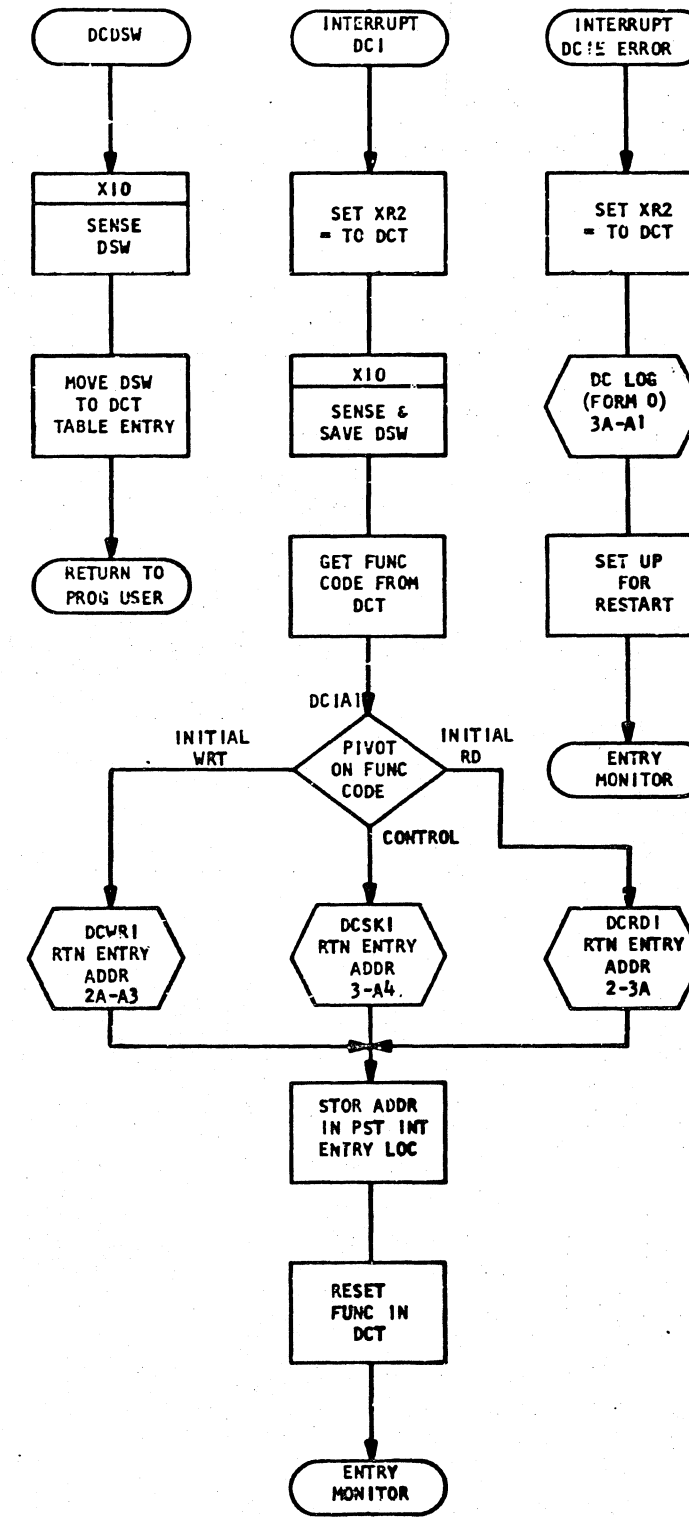
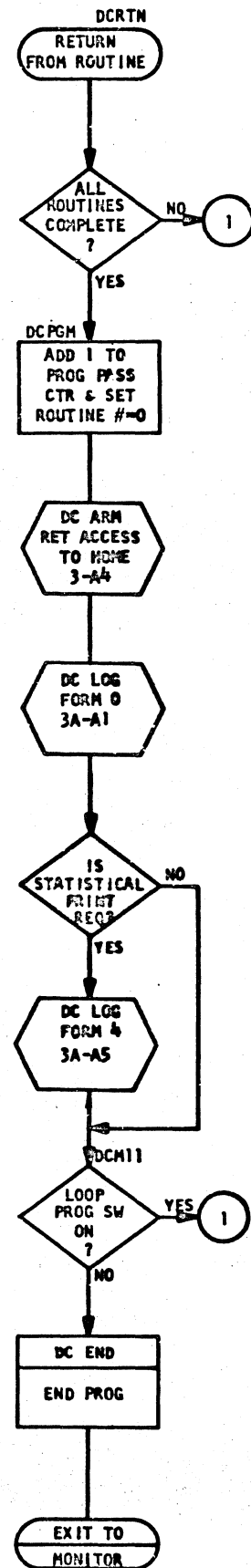
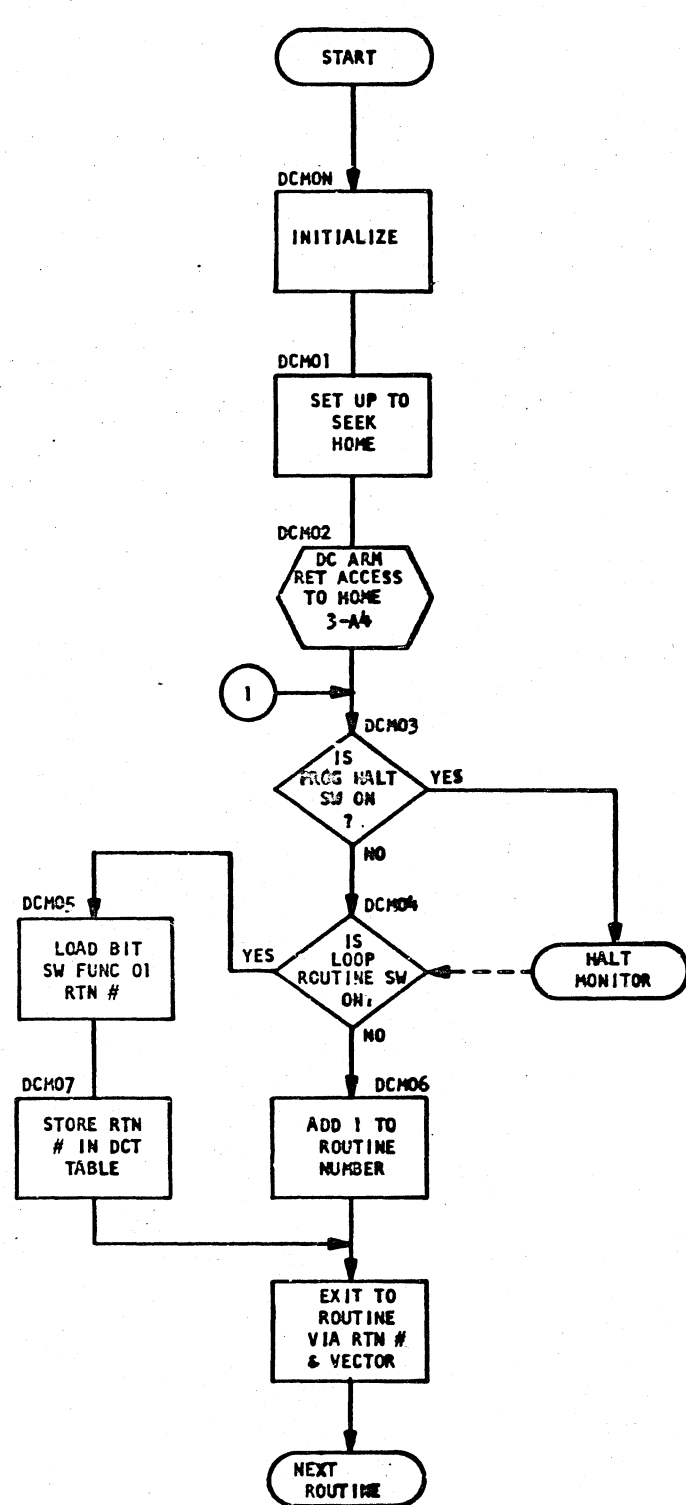


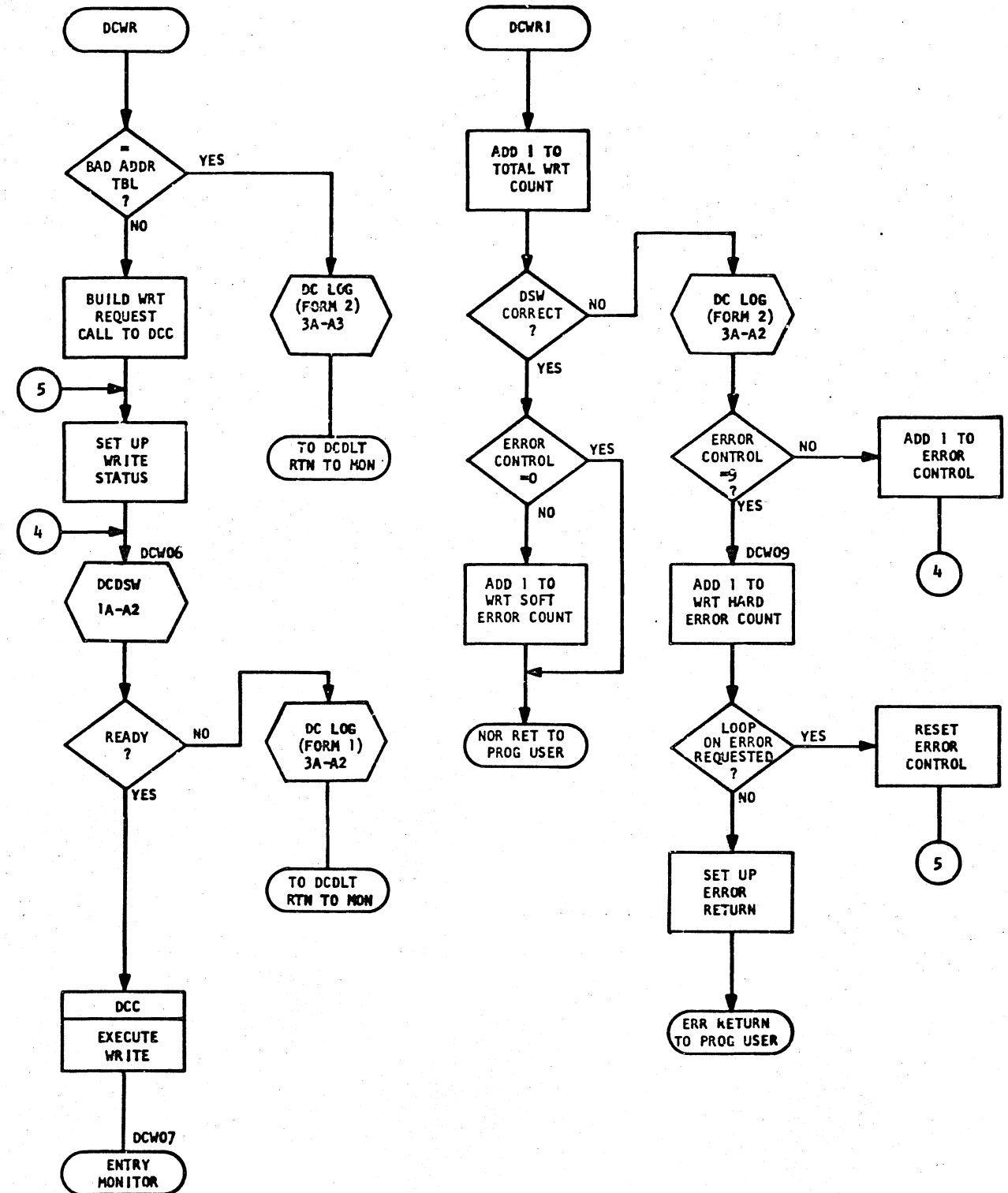
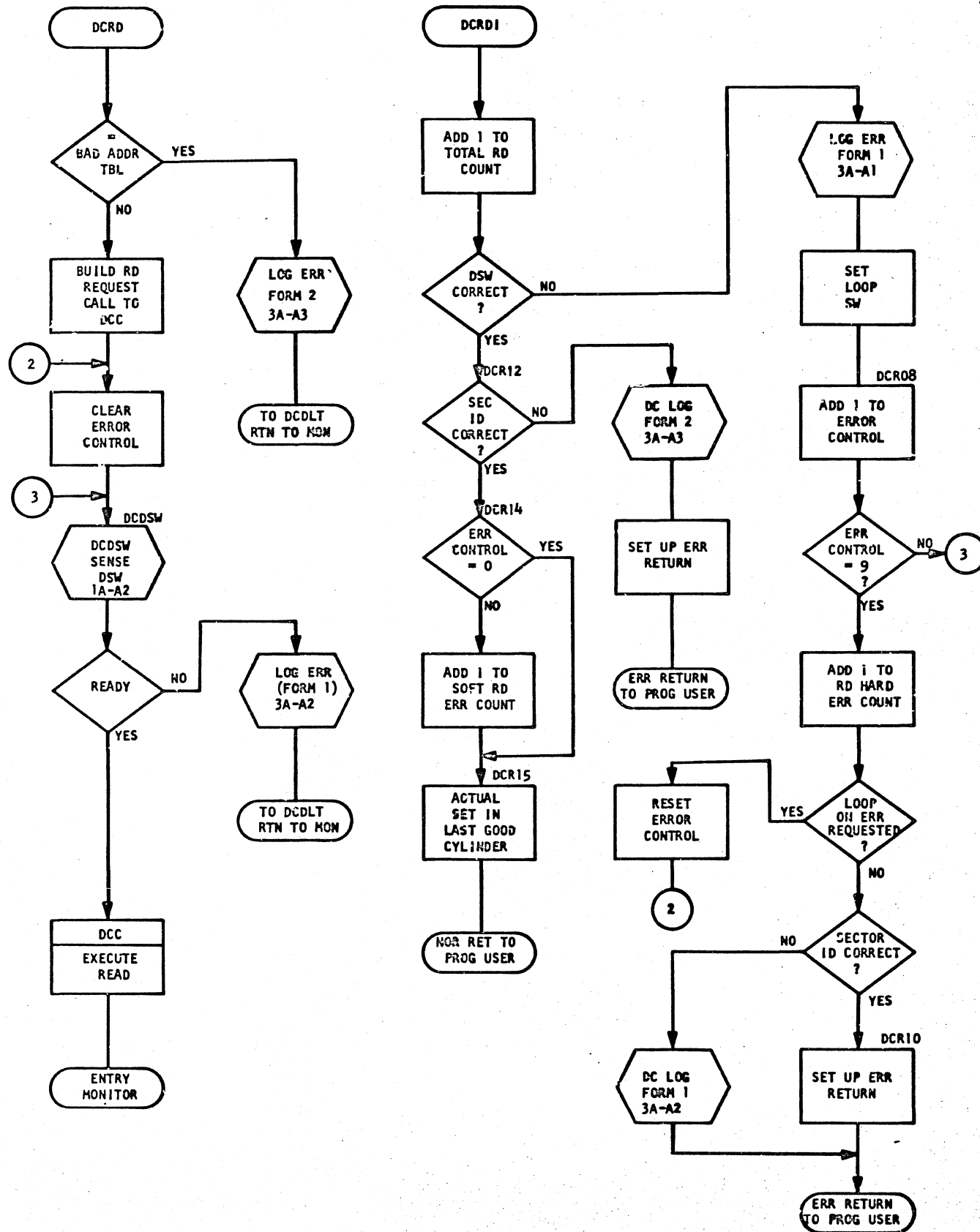


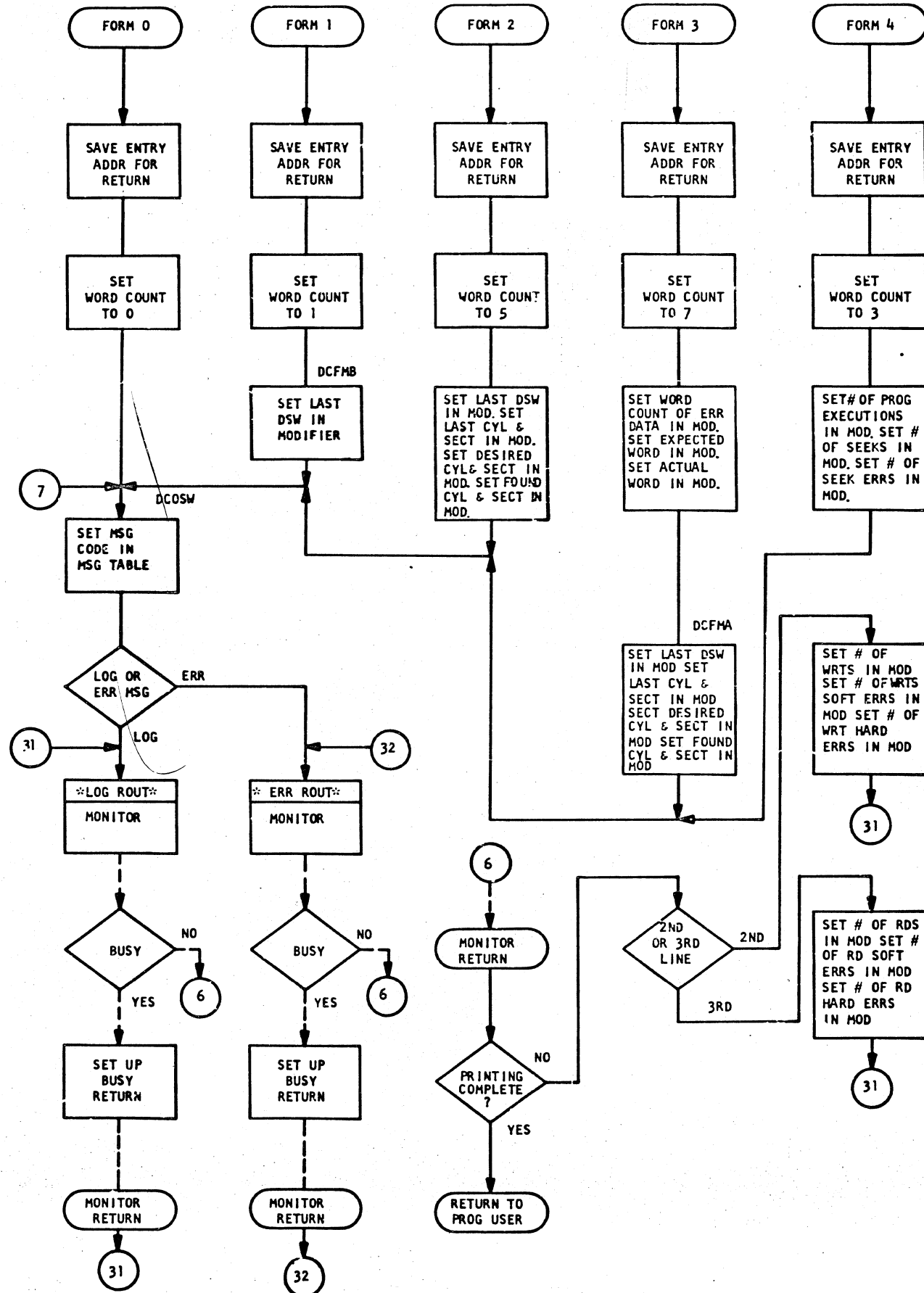
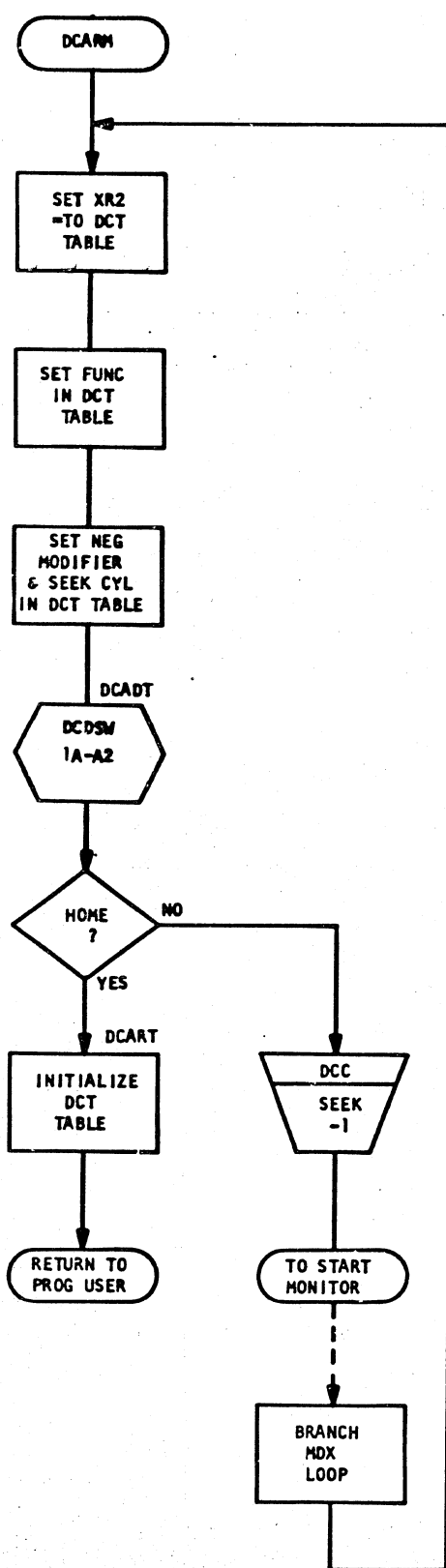
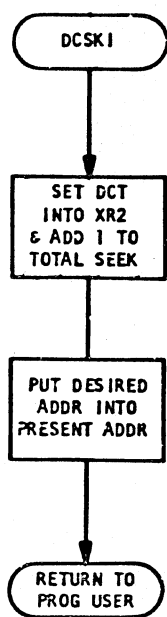
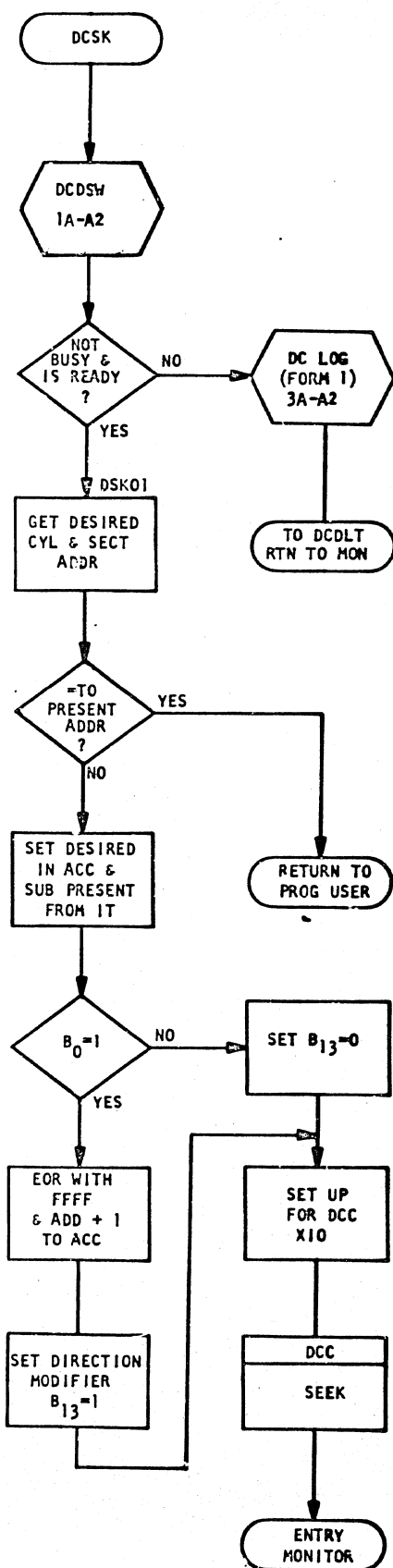


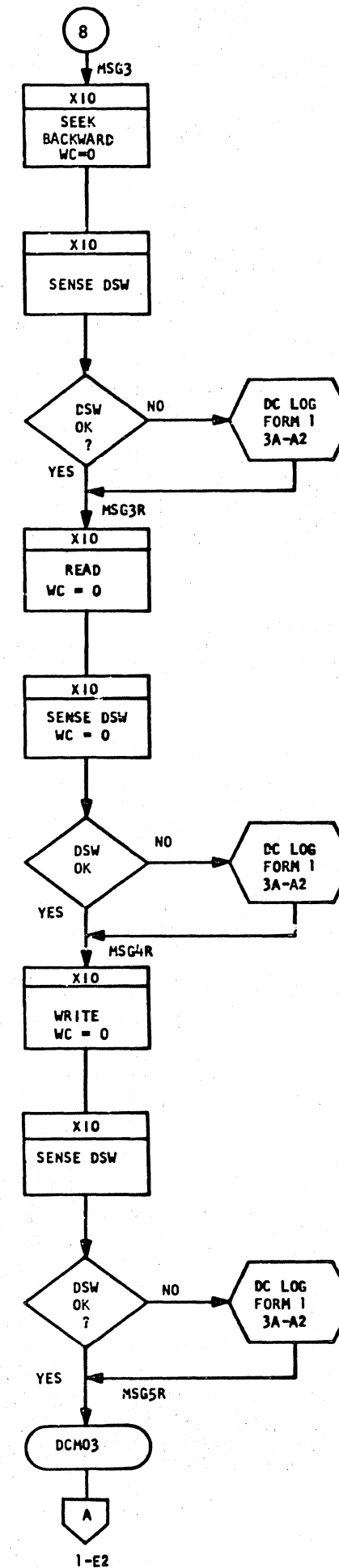
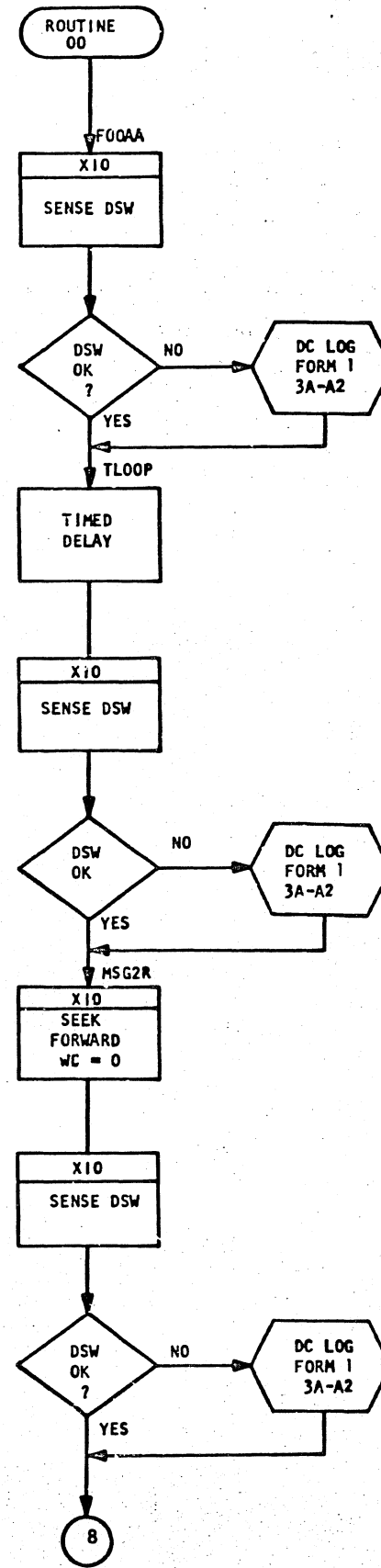
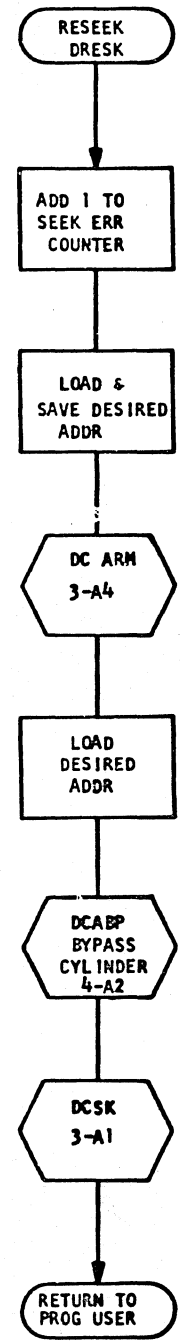
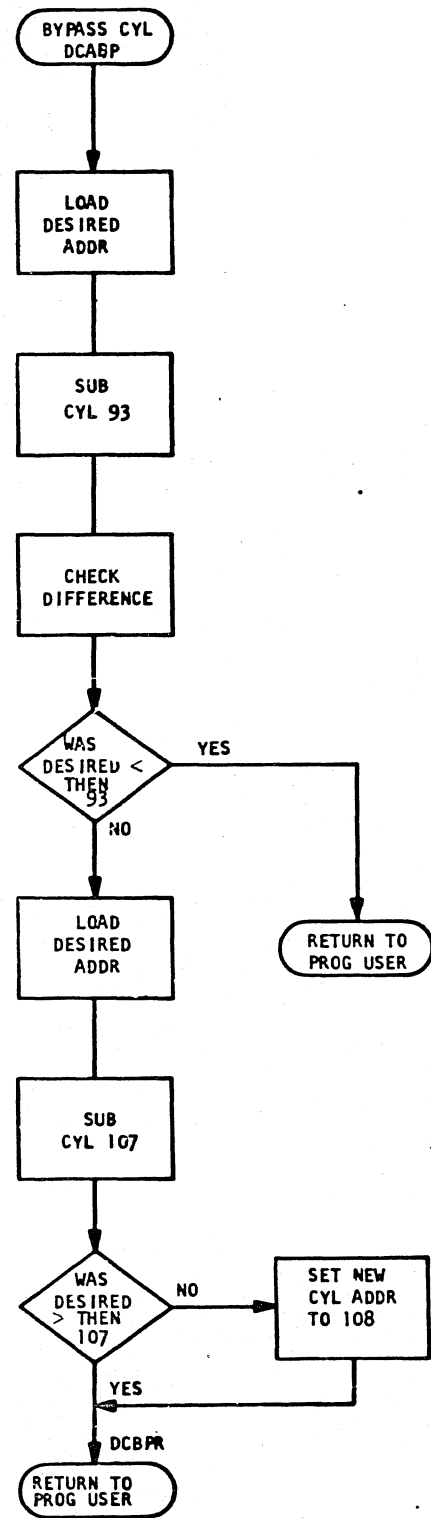


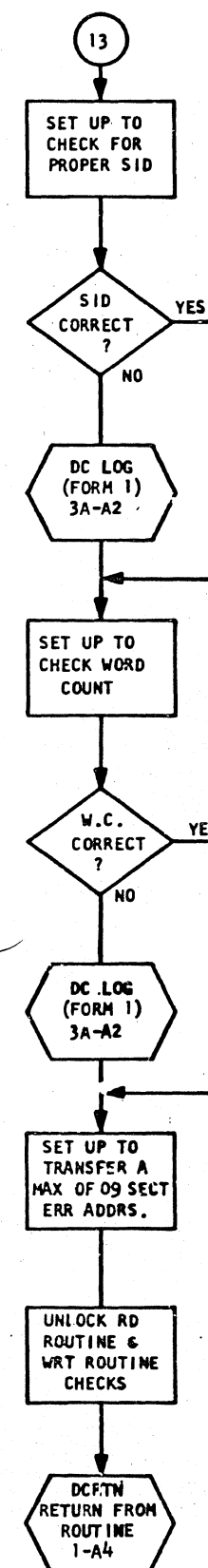
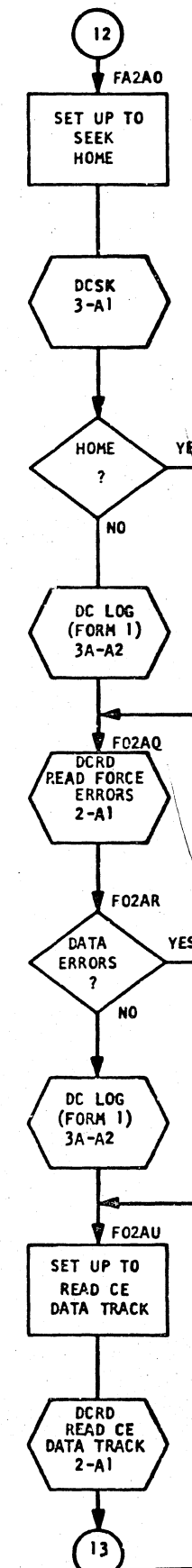
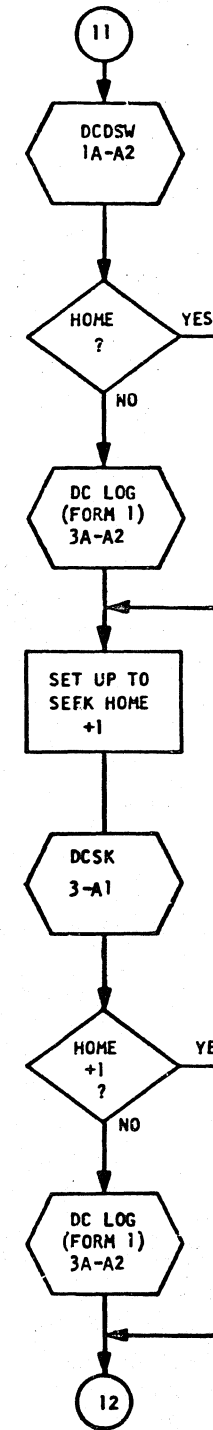
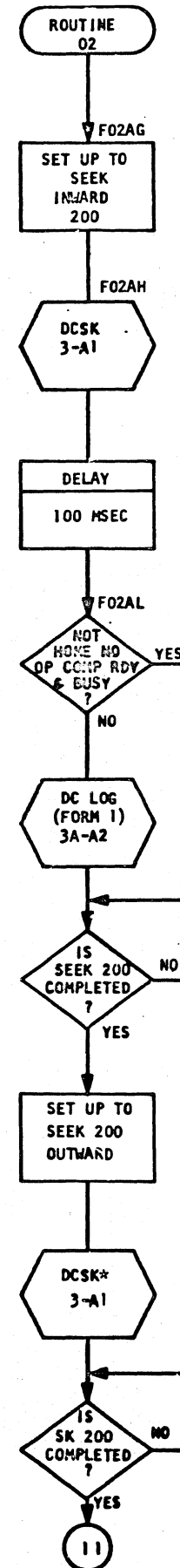
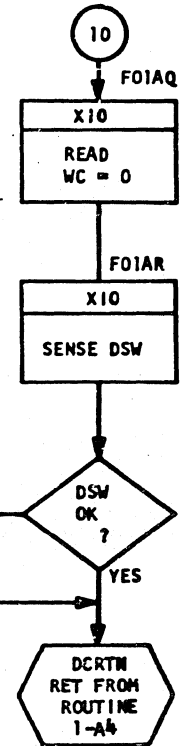
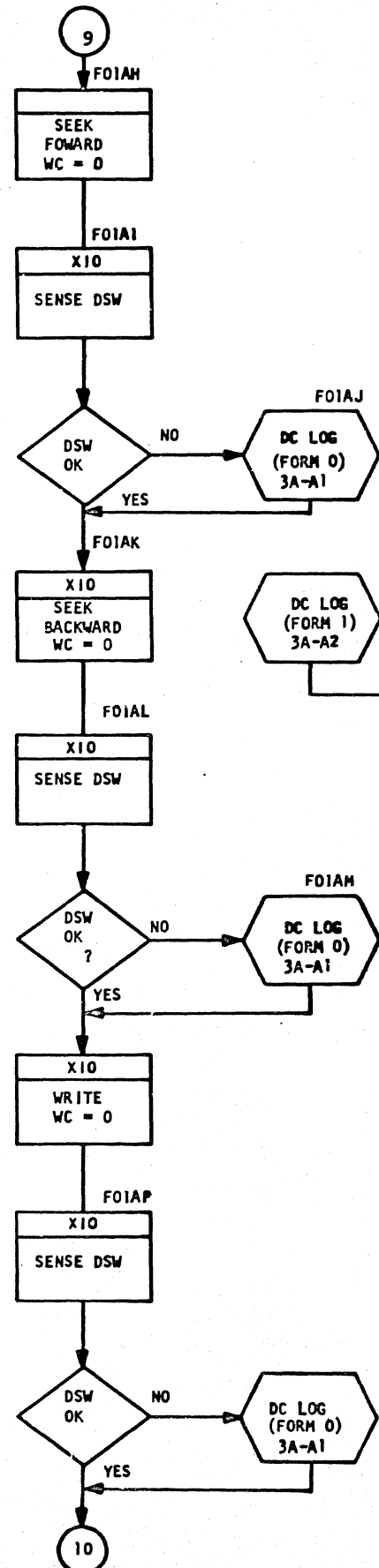
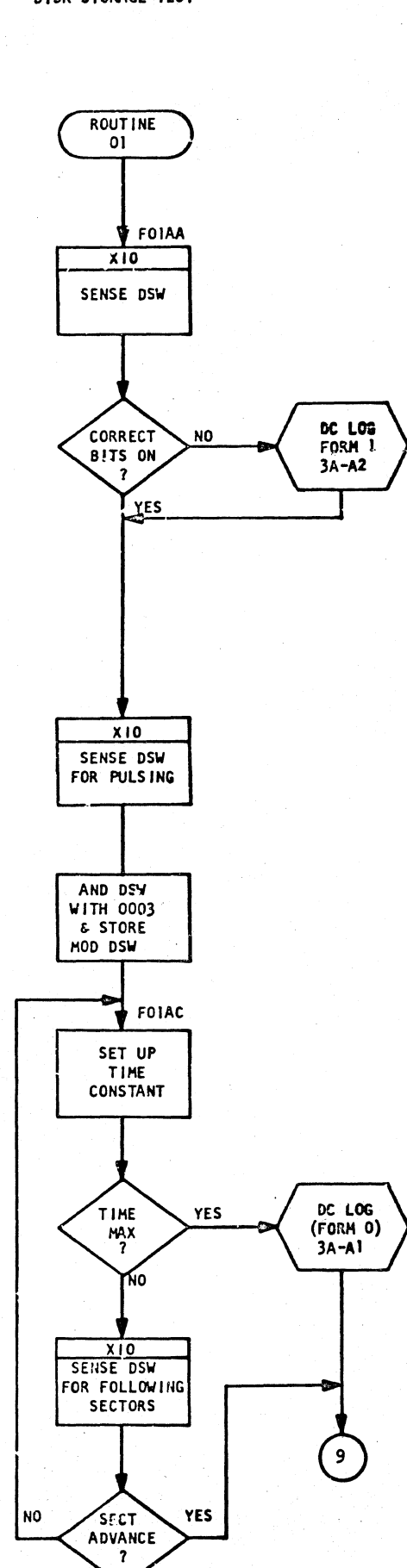


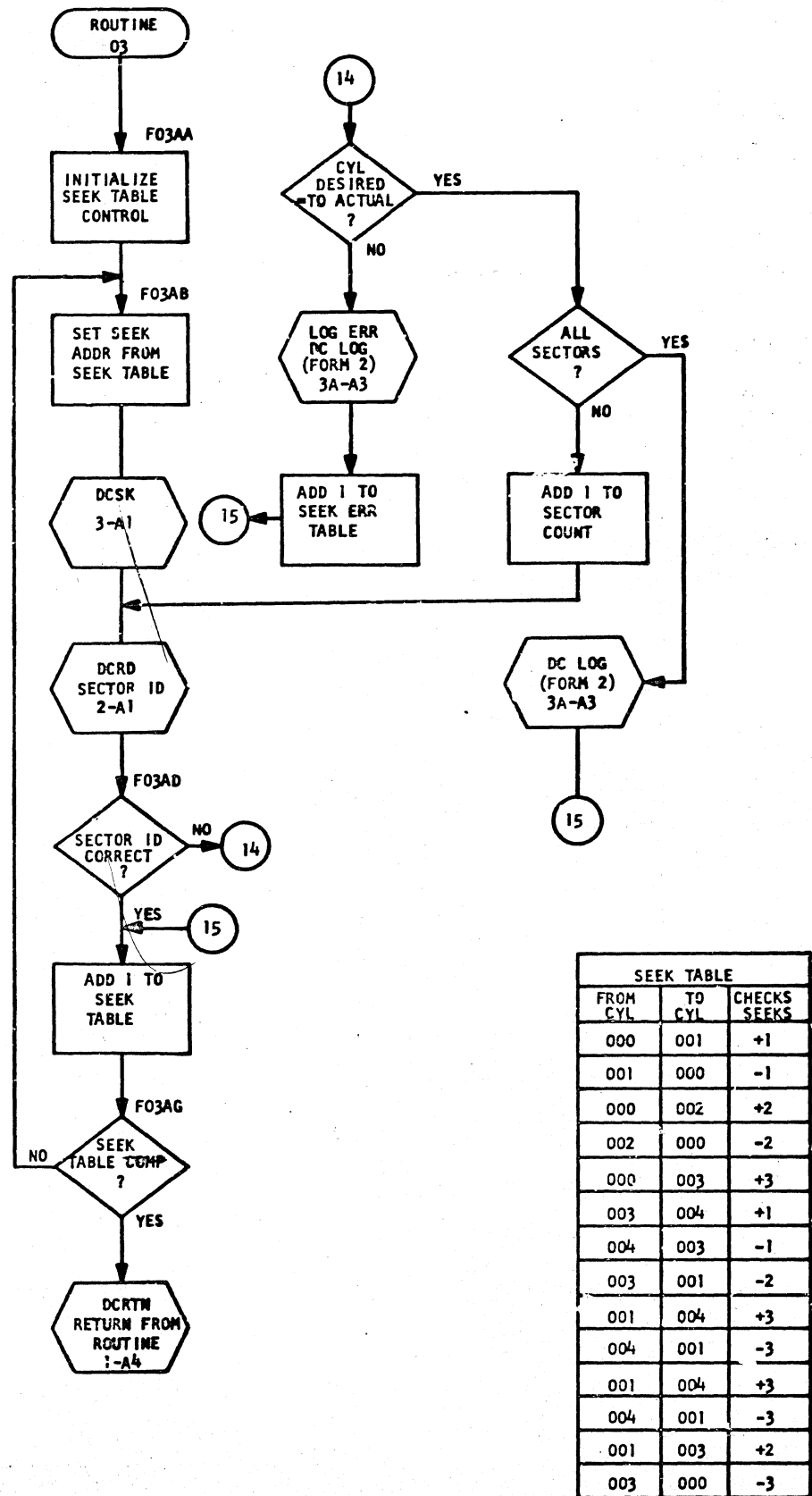




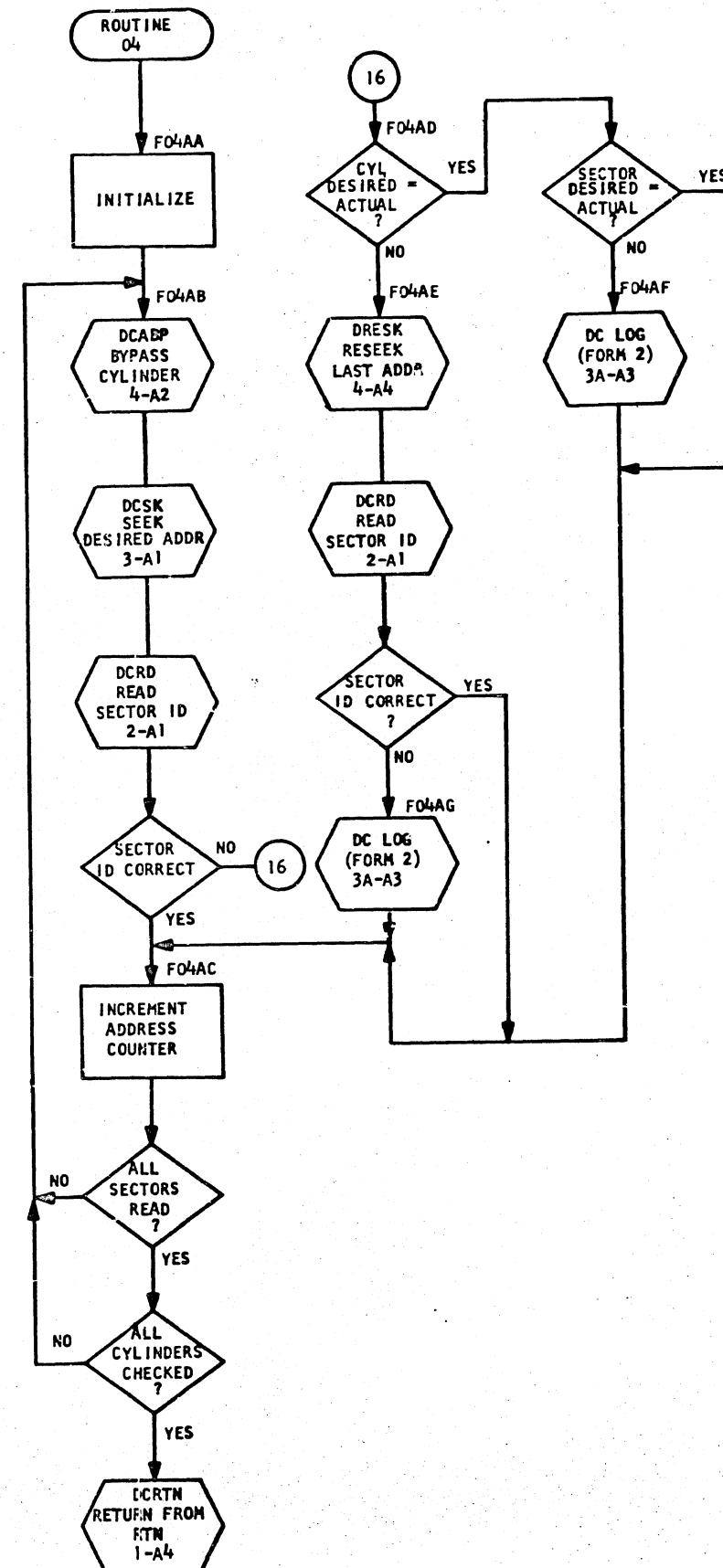


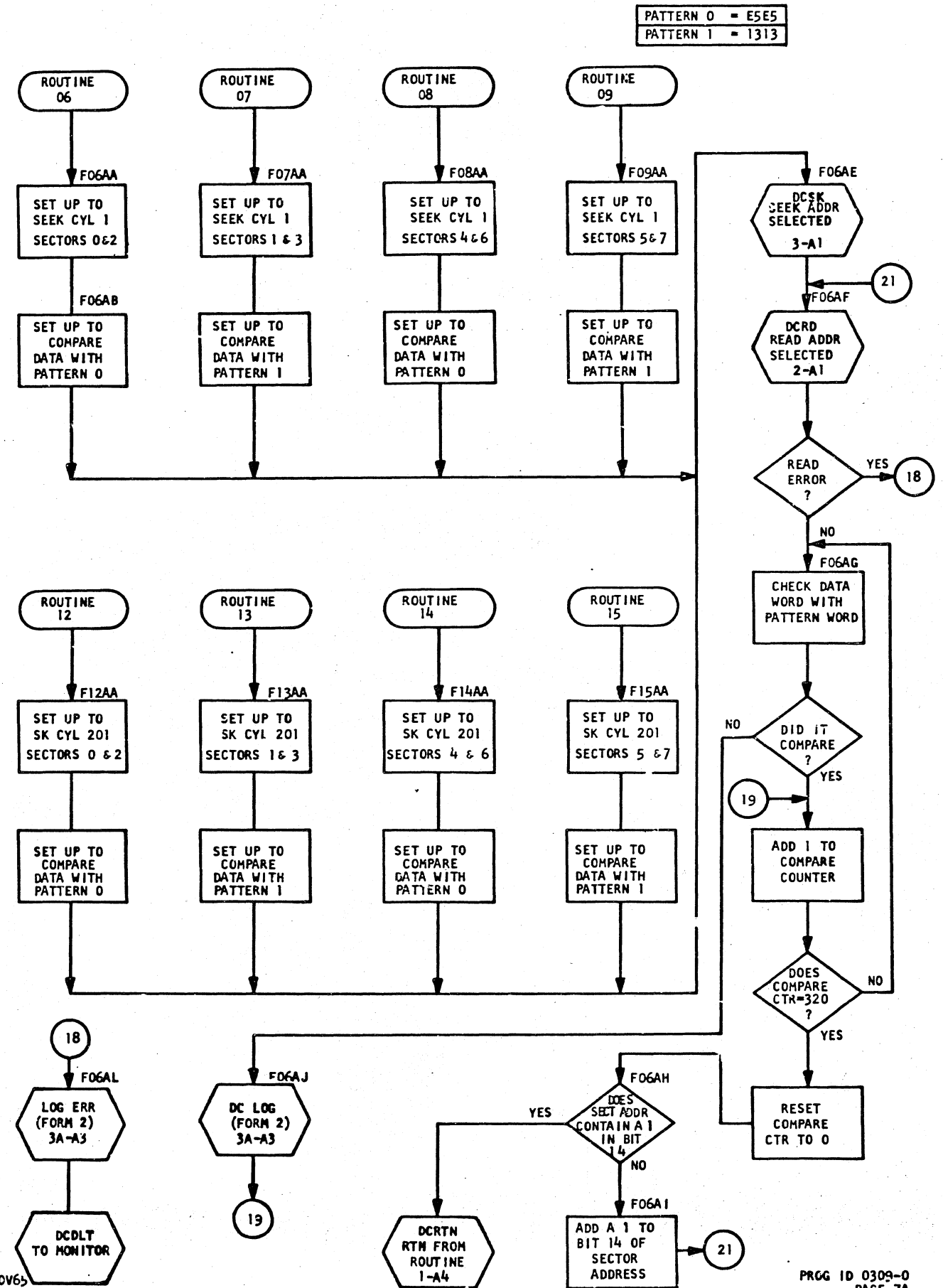
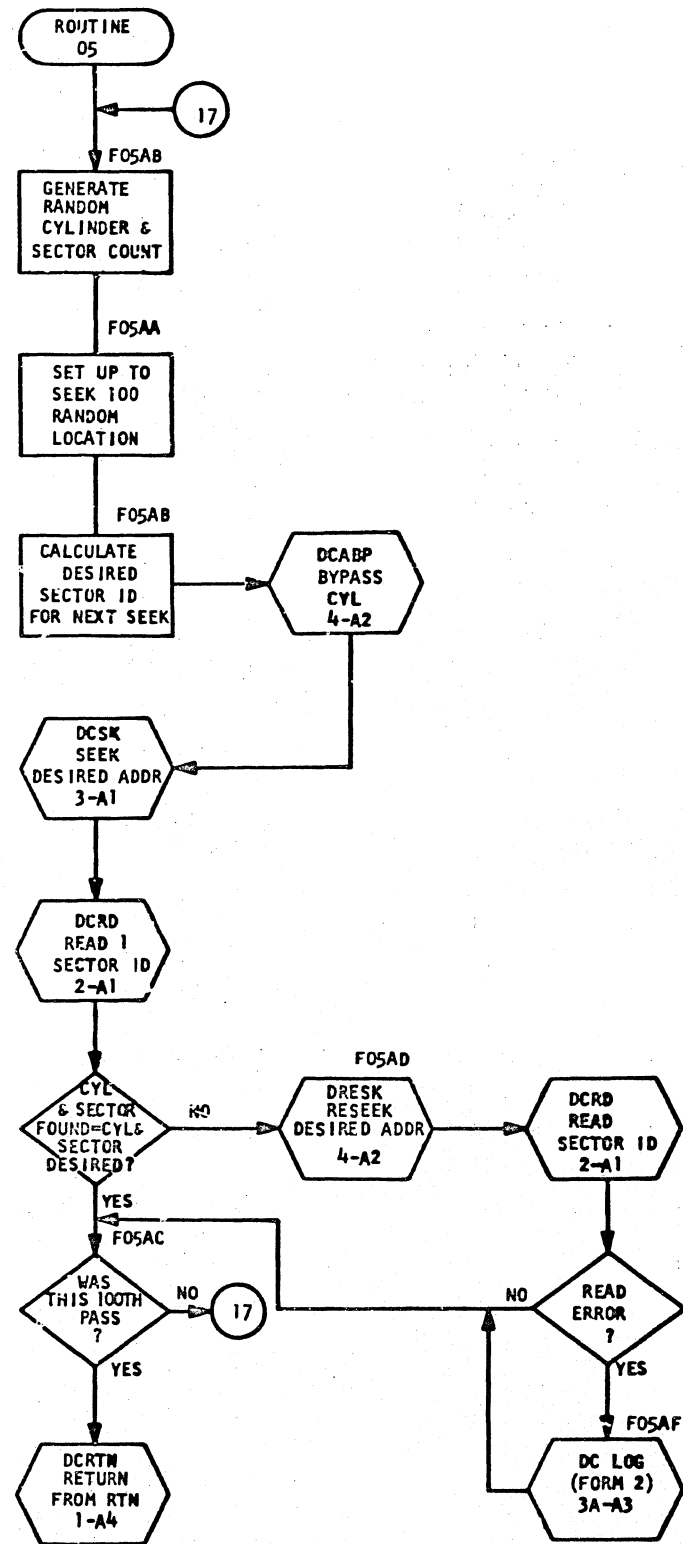




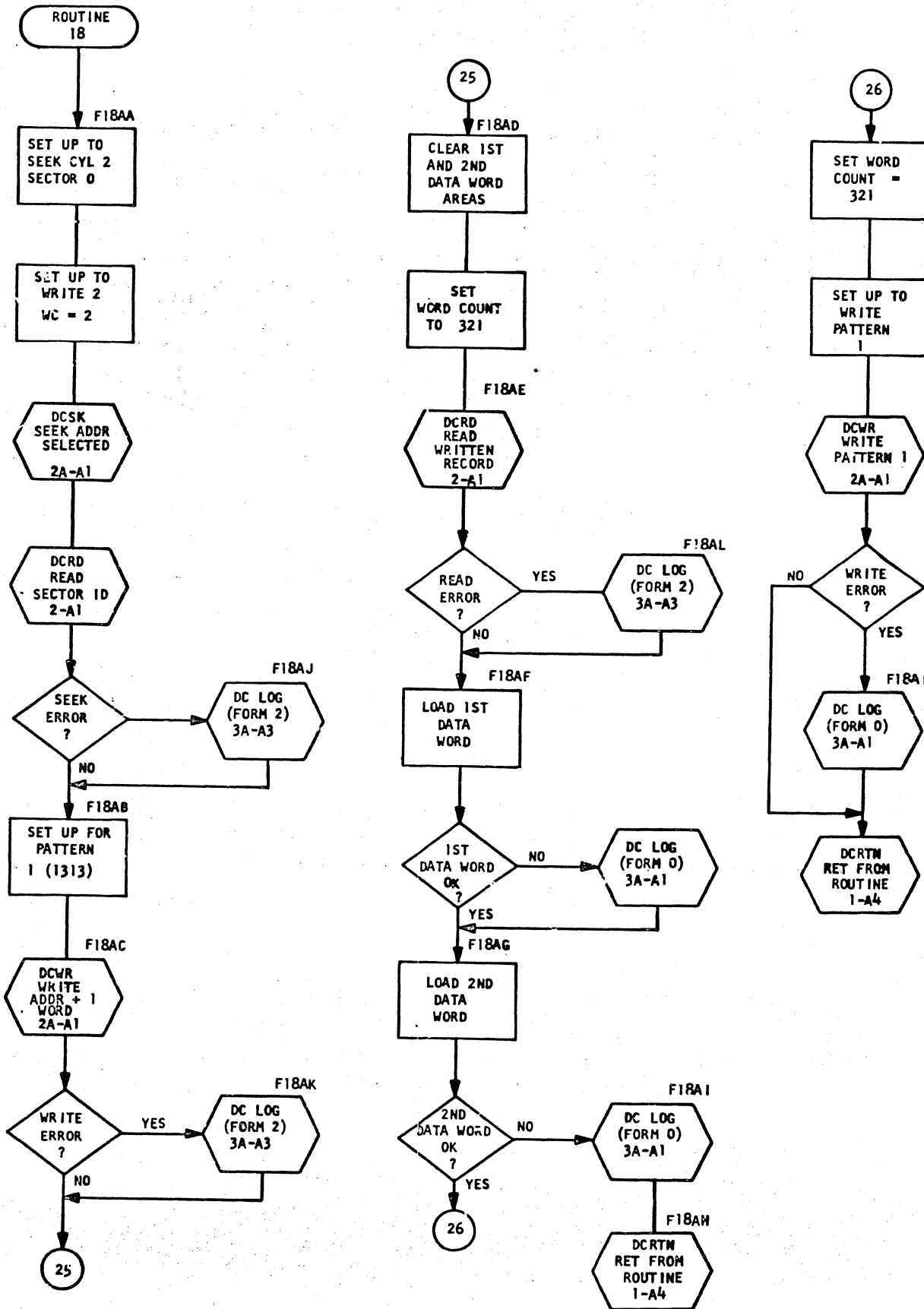
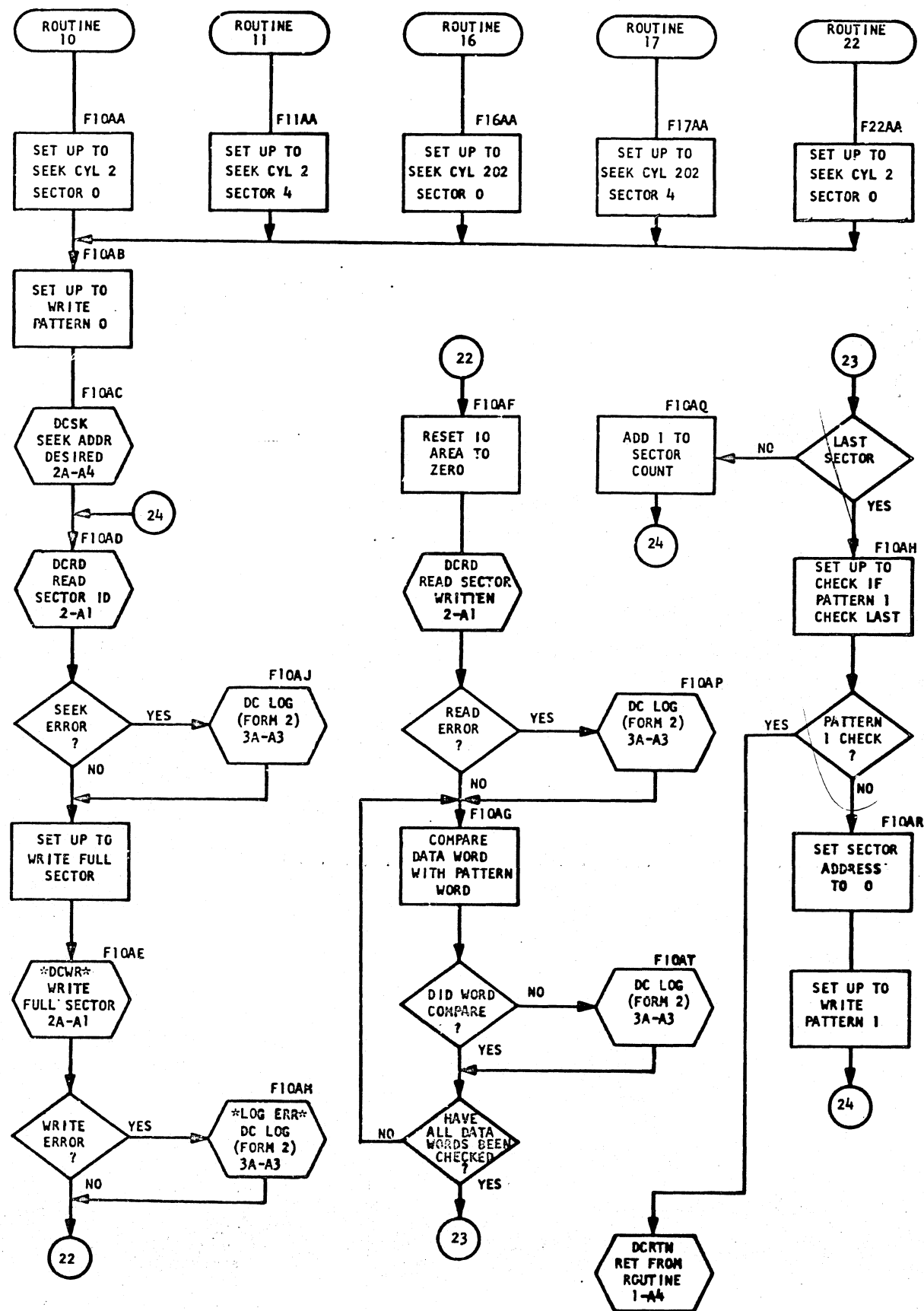


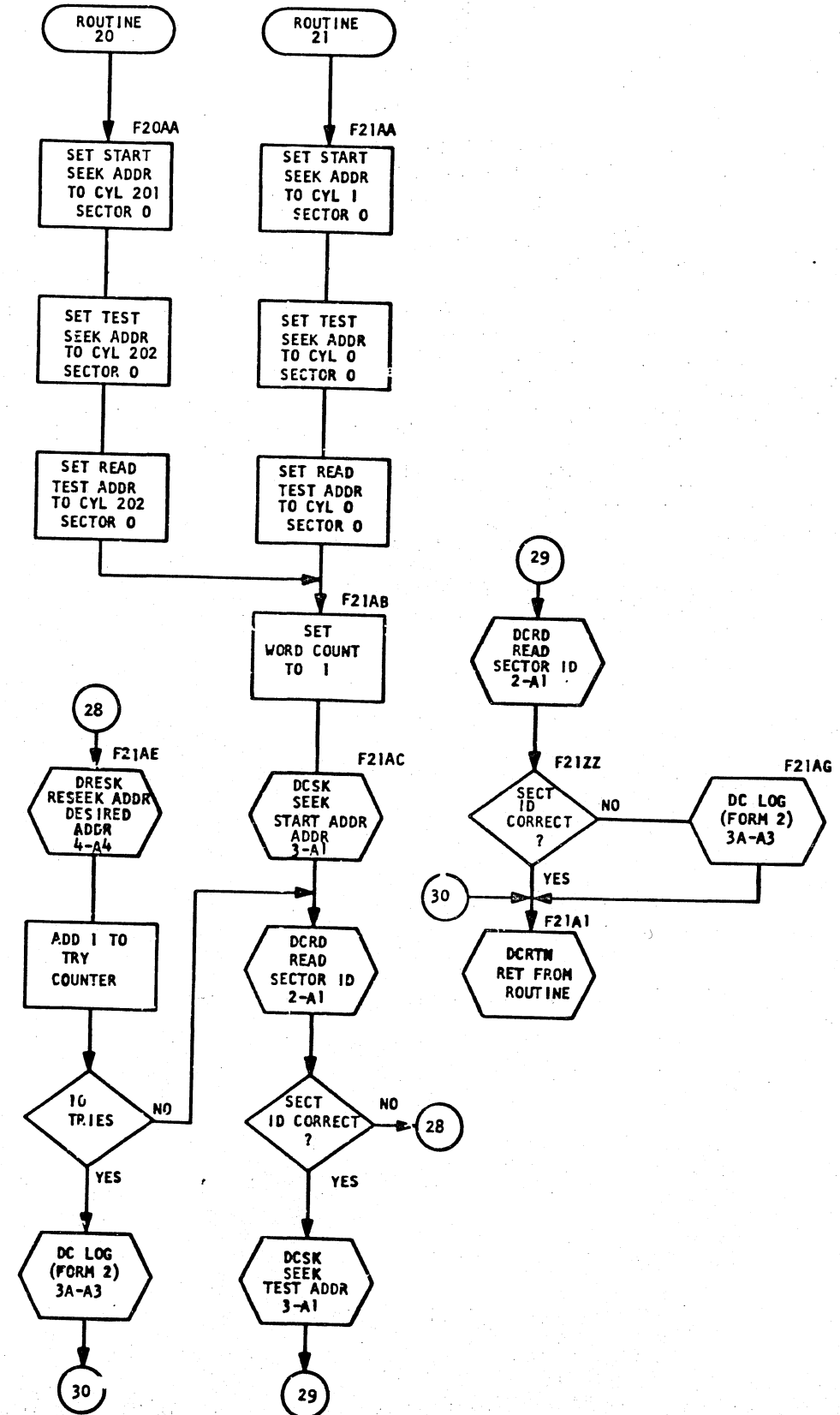
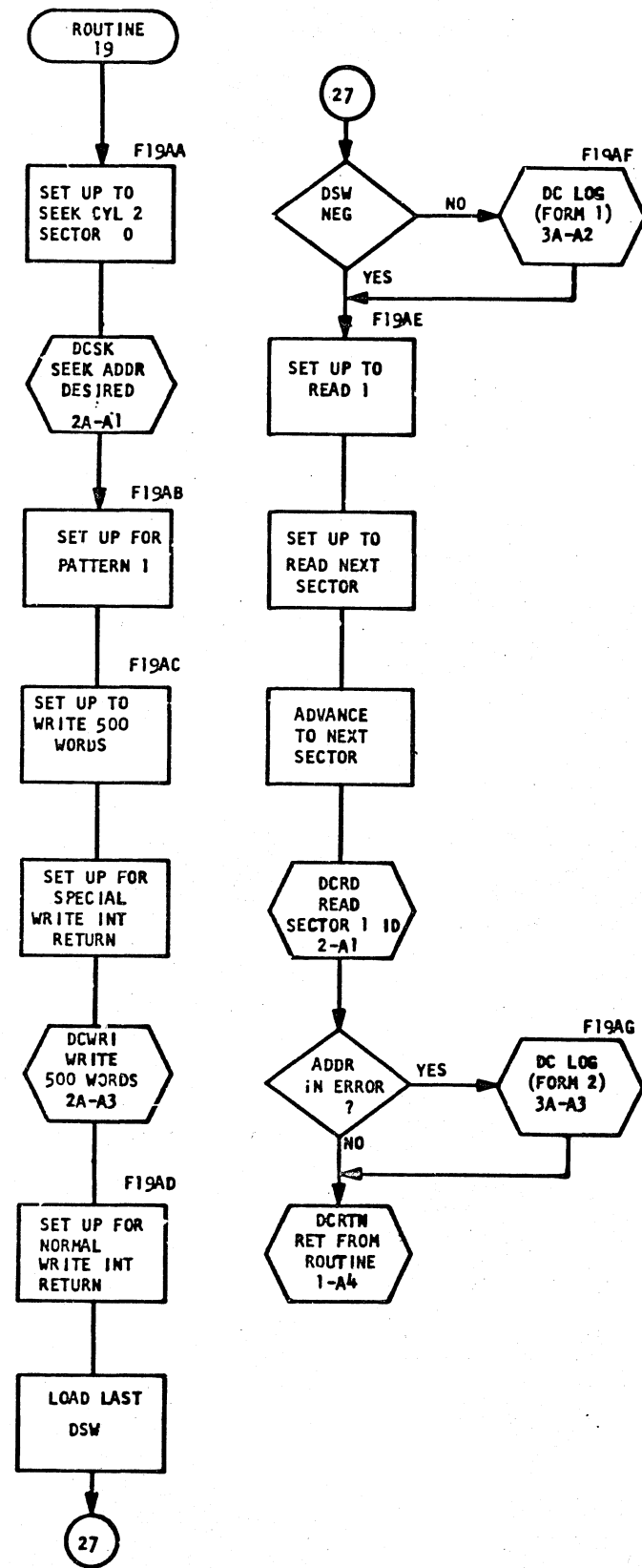
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FROM CYL	TO CYL	CHECKS SEEKS
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001	000	-1
000	002	+2
002	000	-2
000	003	+3
003	004	+1
004	003	-1
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001	004	+3
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001	003	+2
003	000	-3

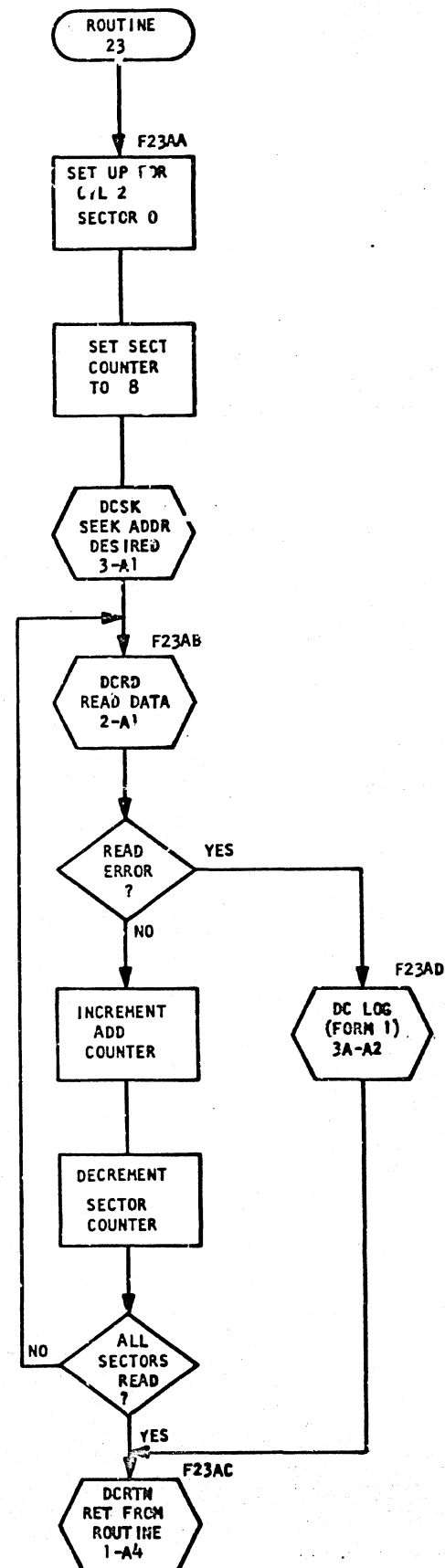


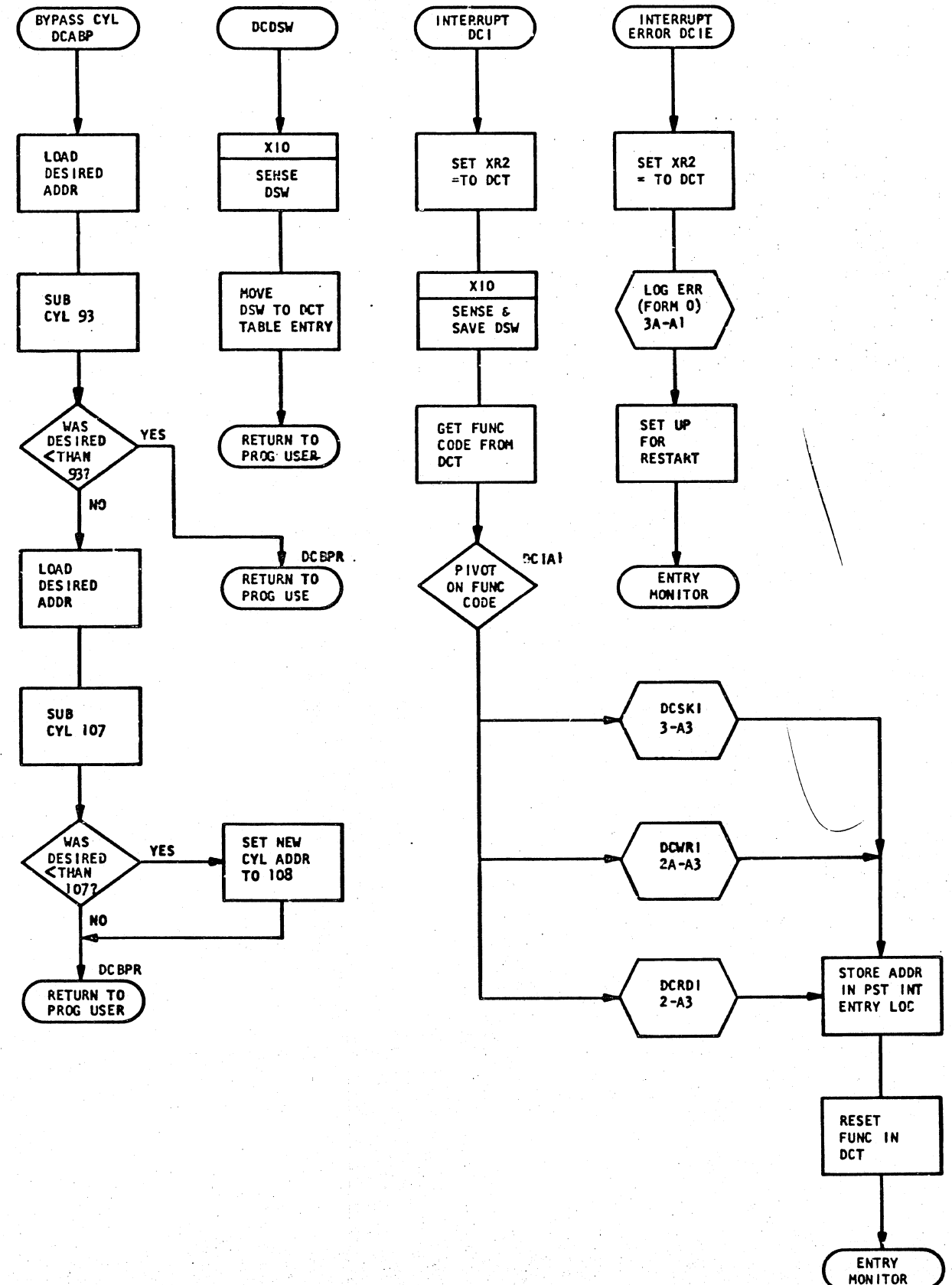
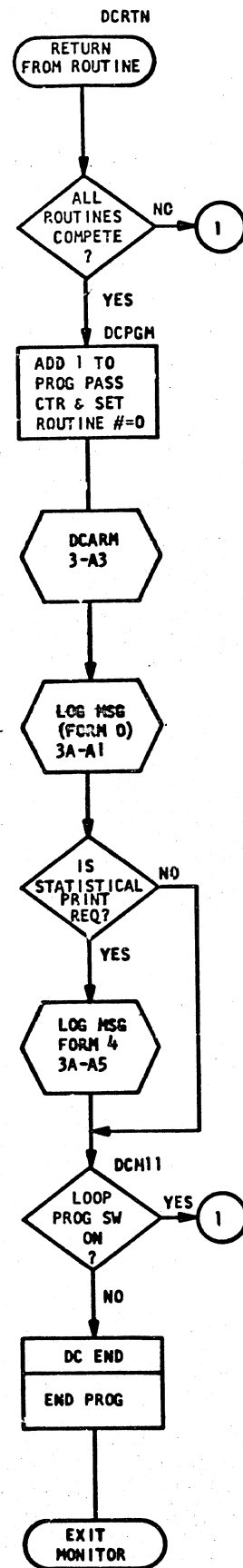
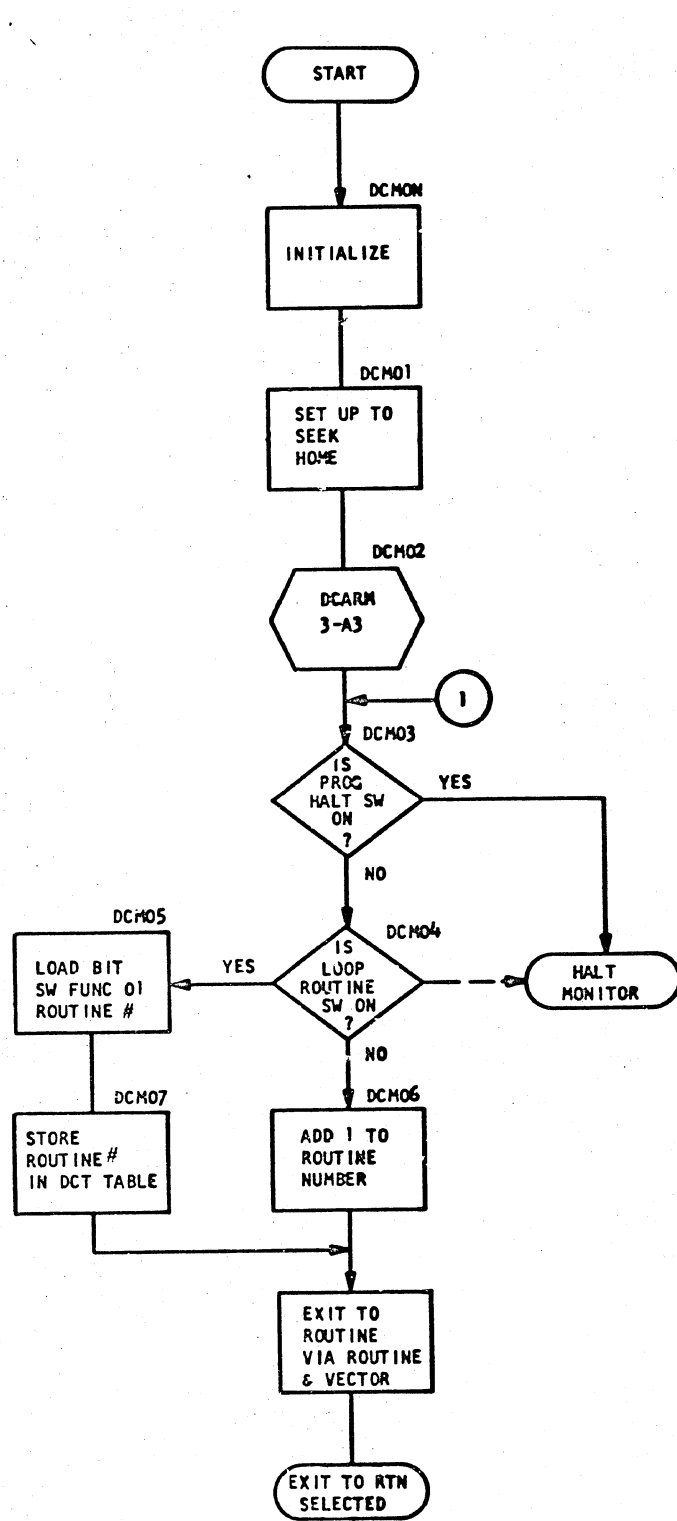


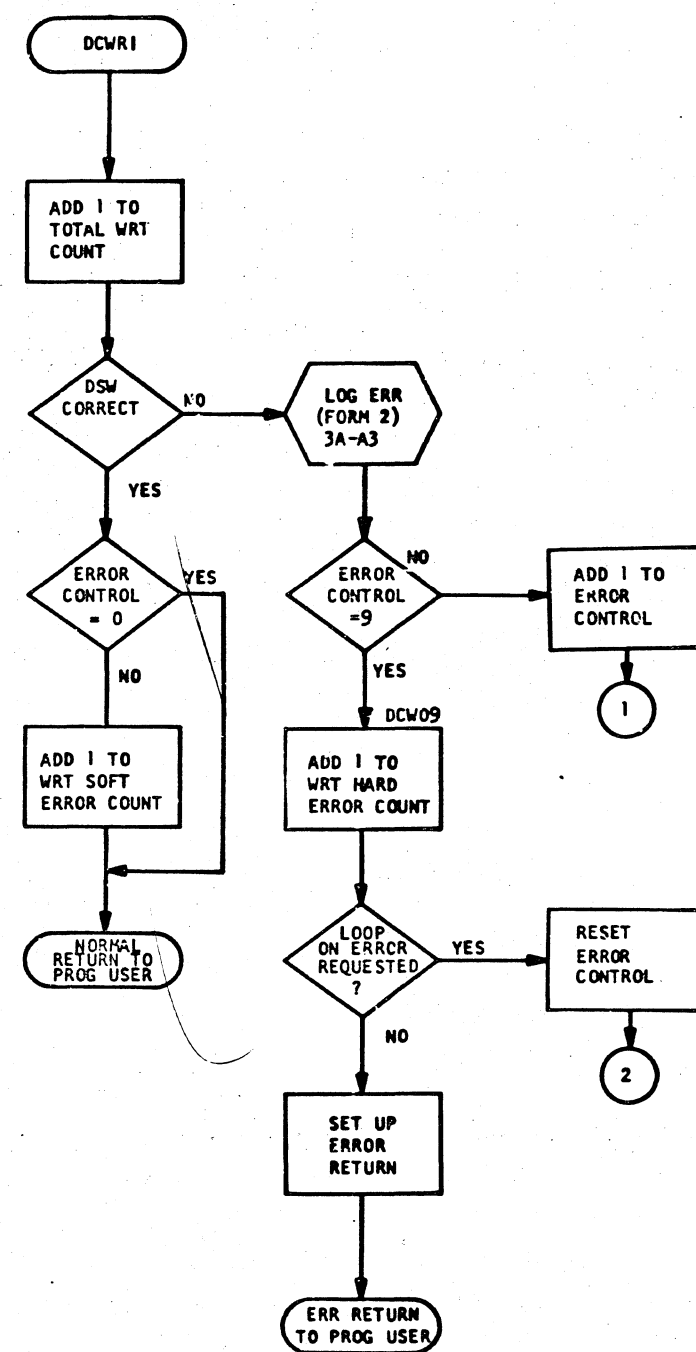
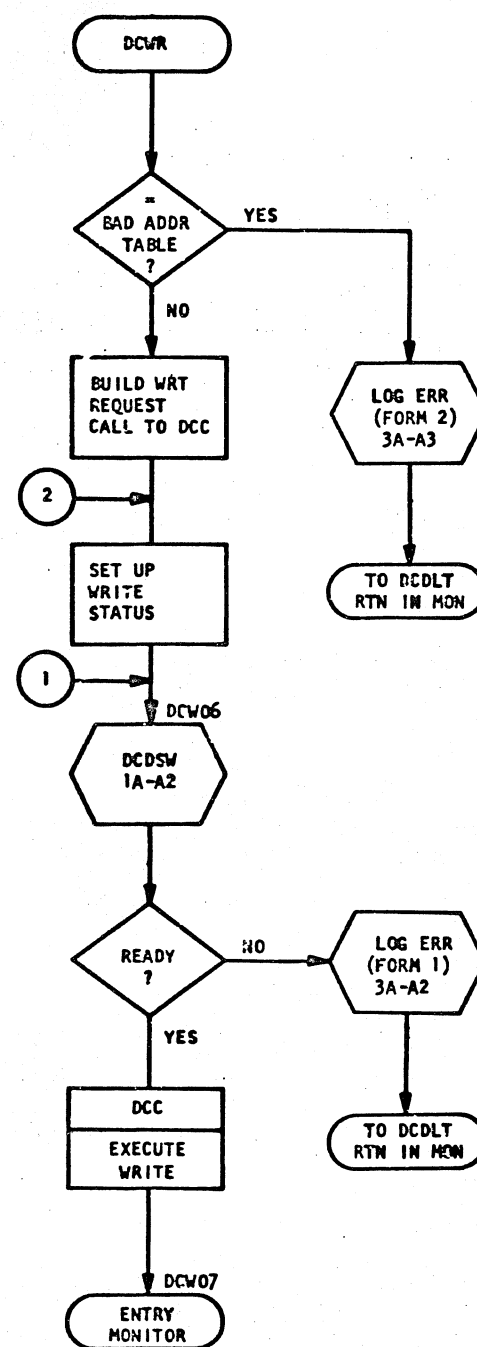
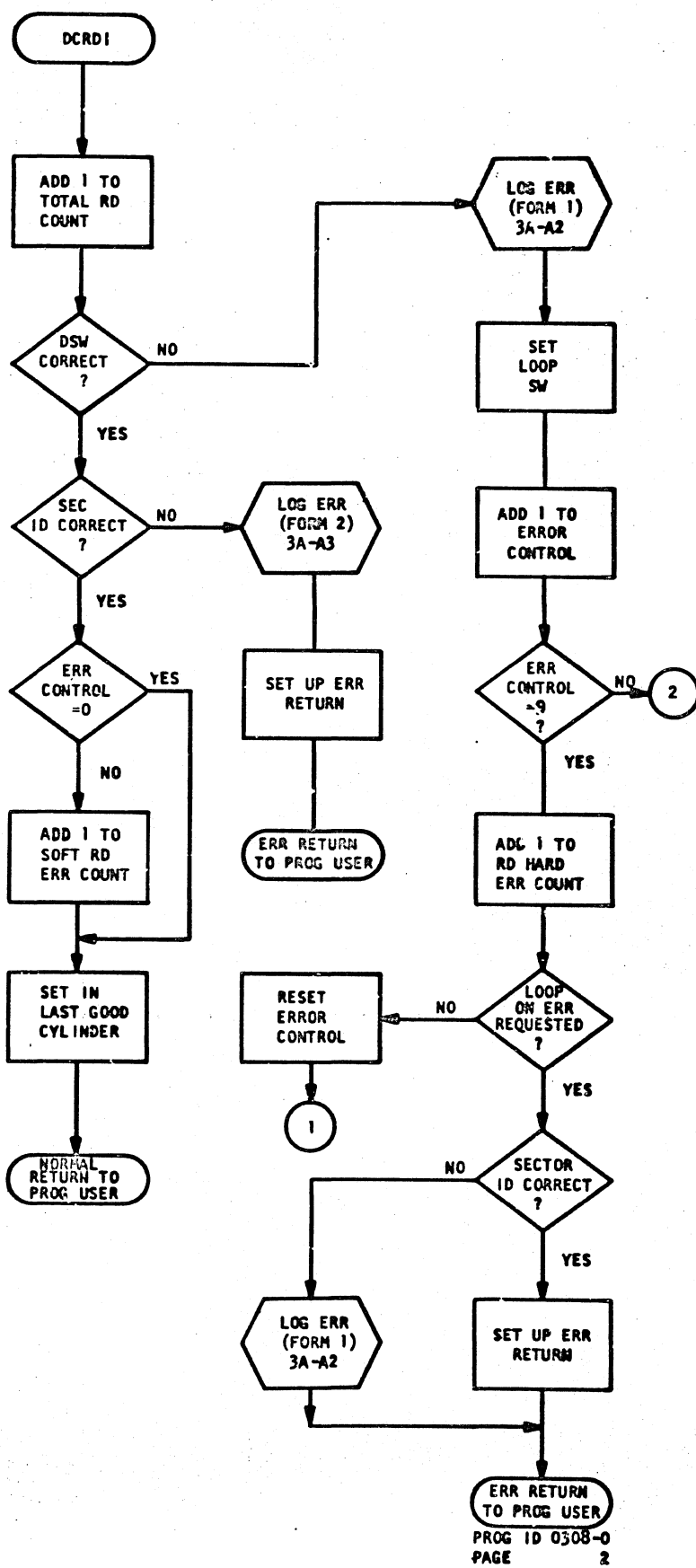
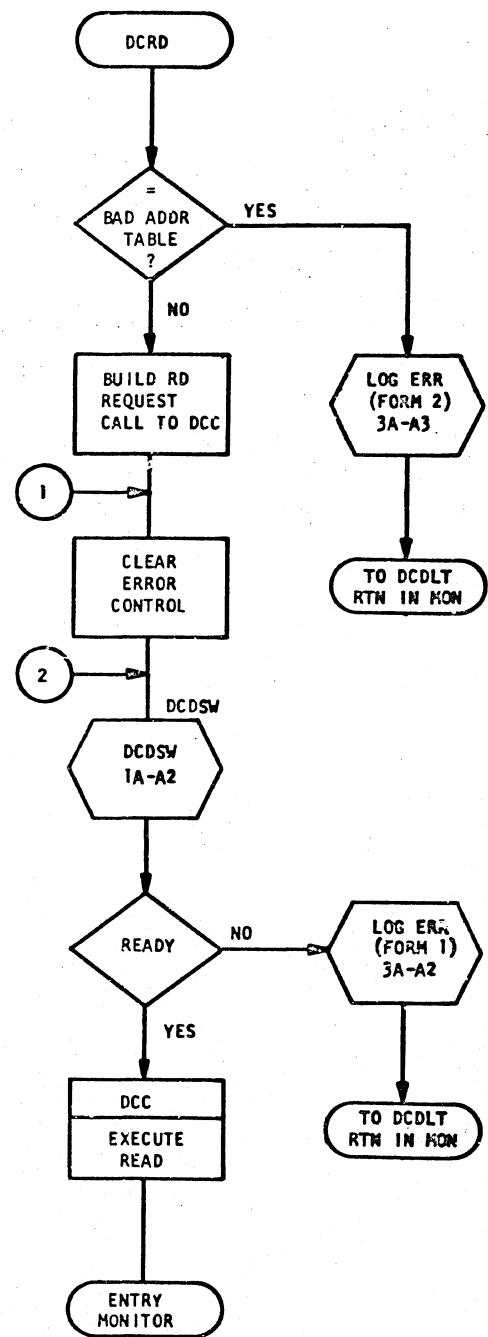
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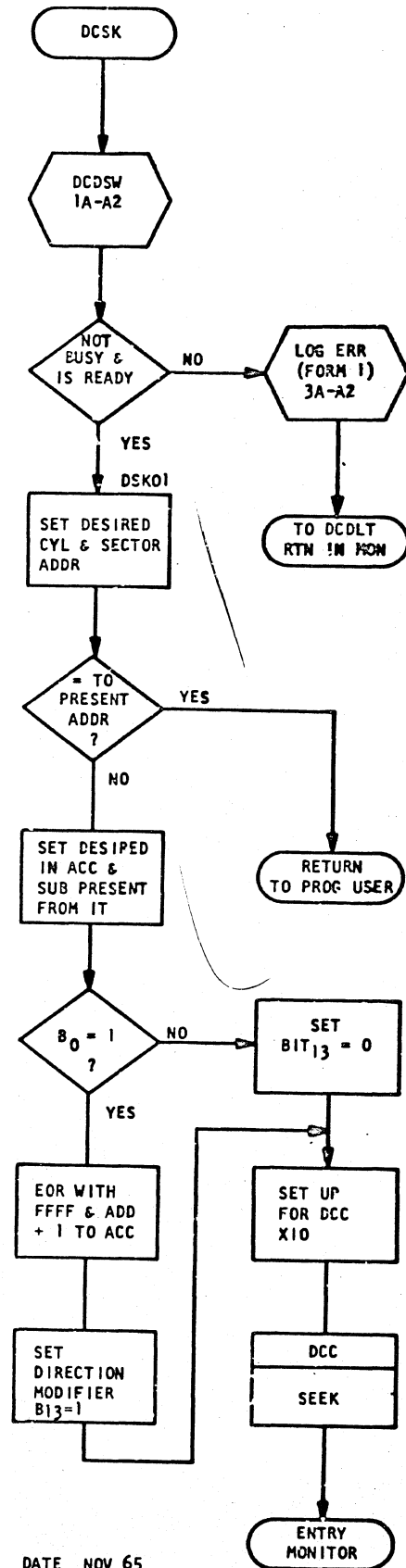




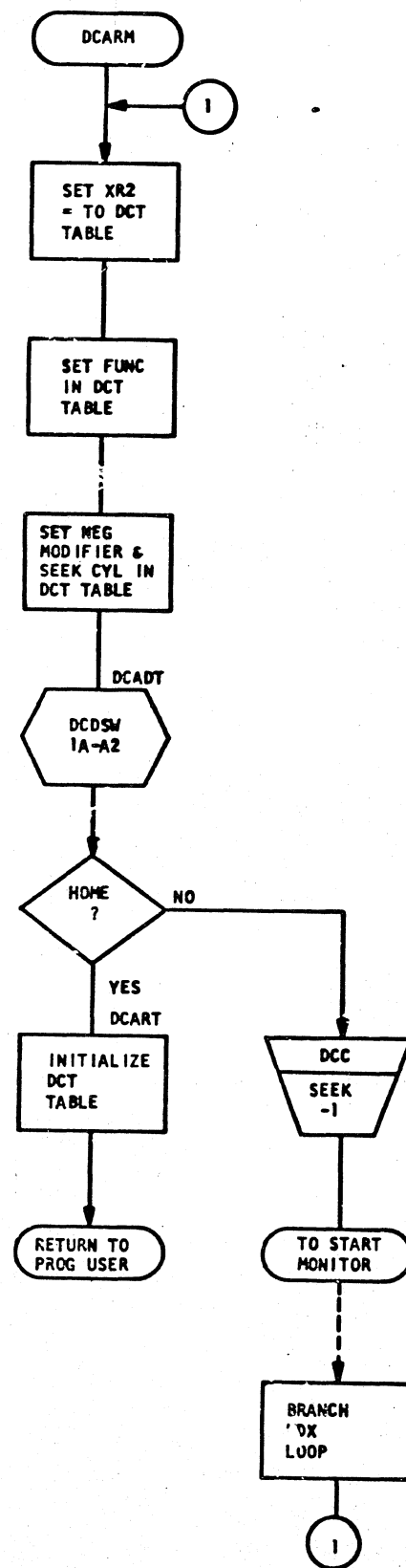
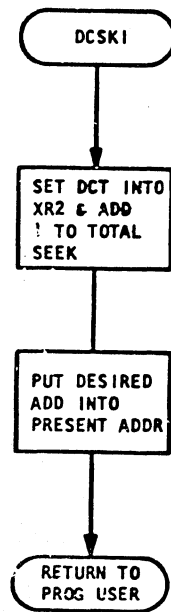




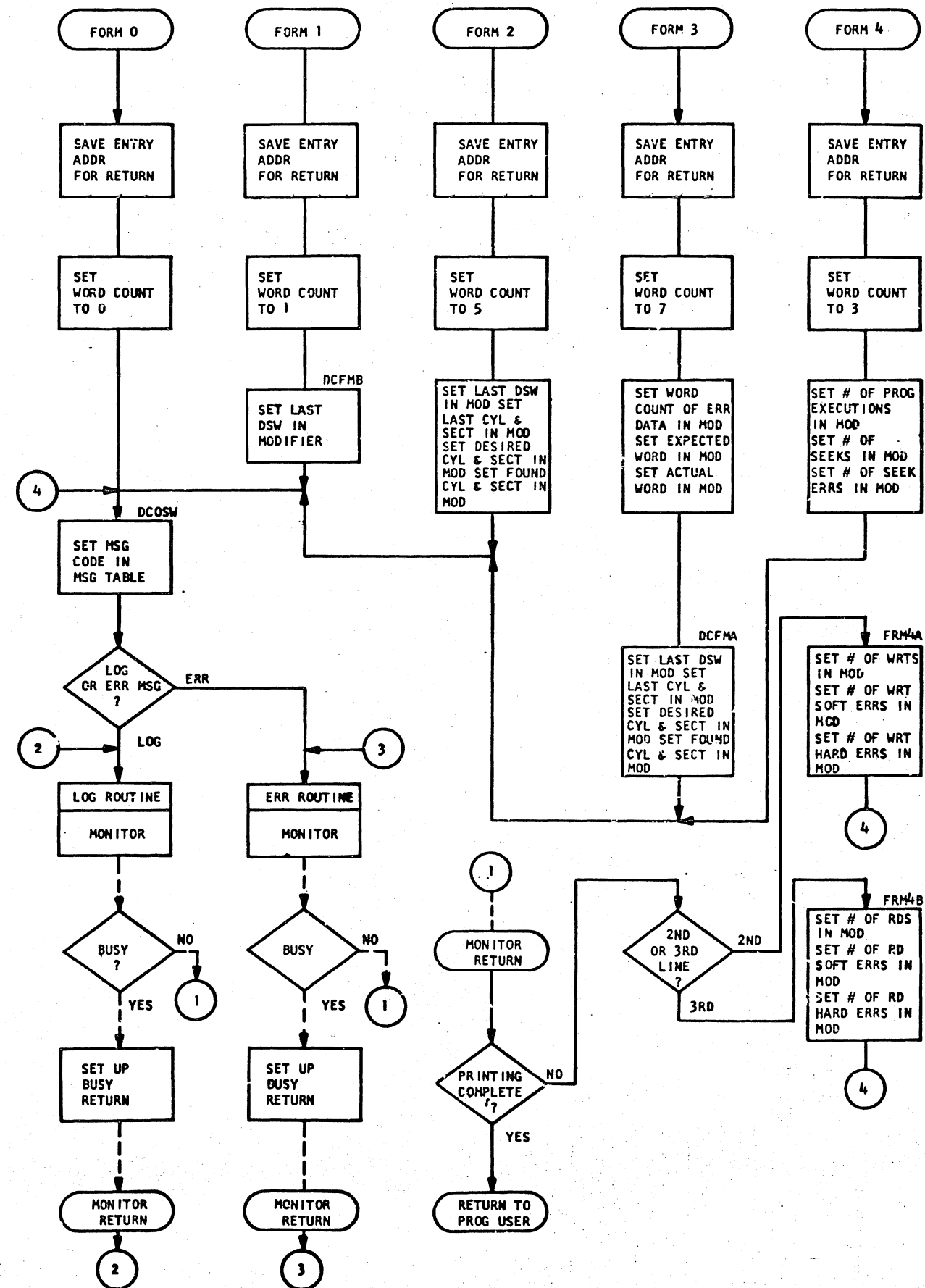


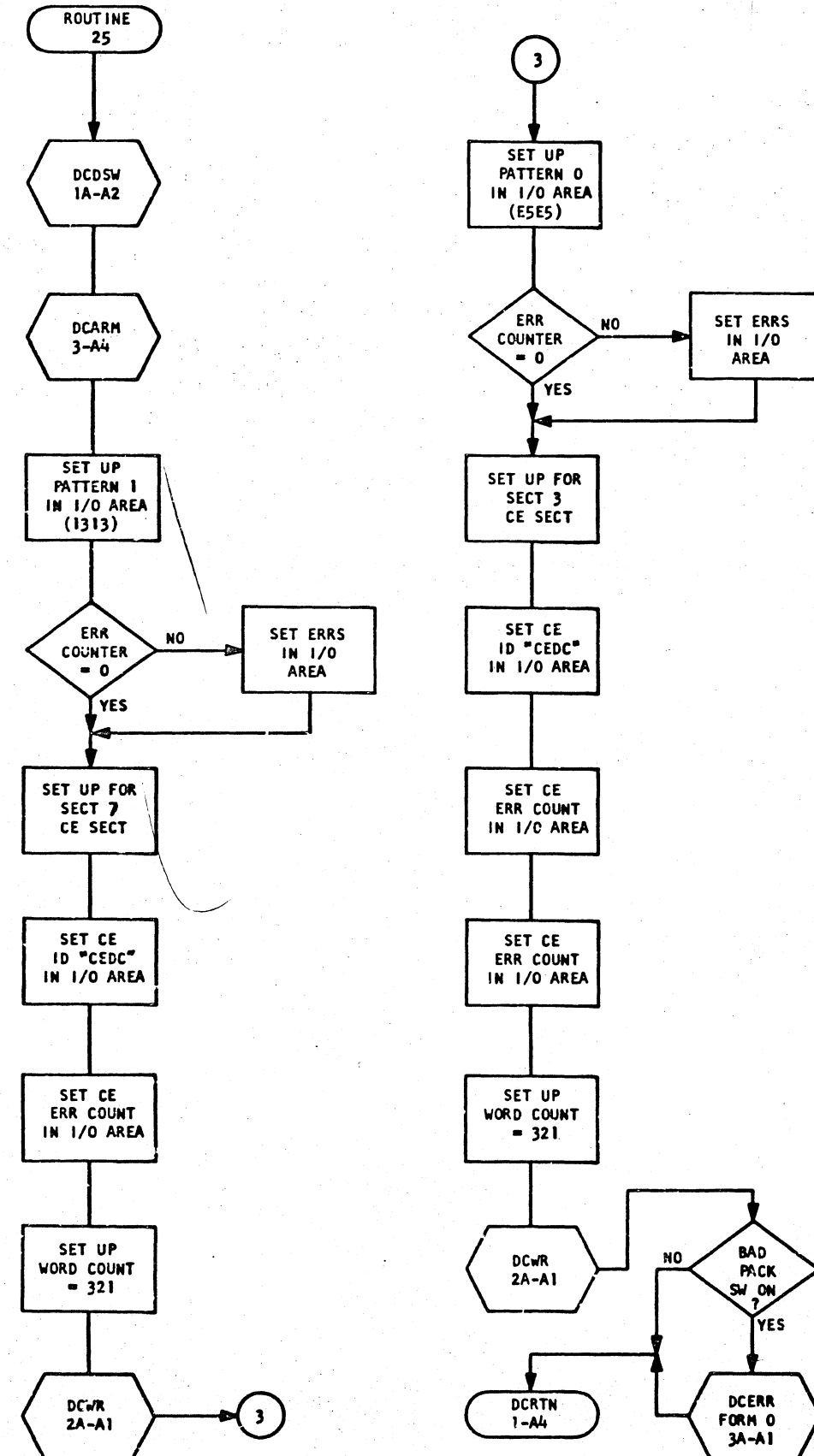
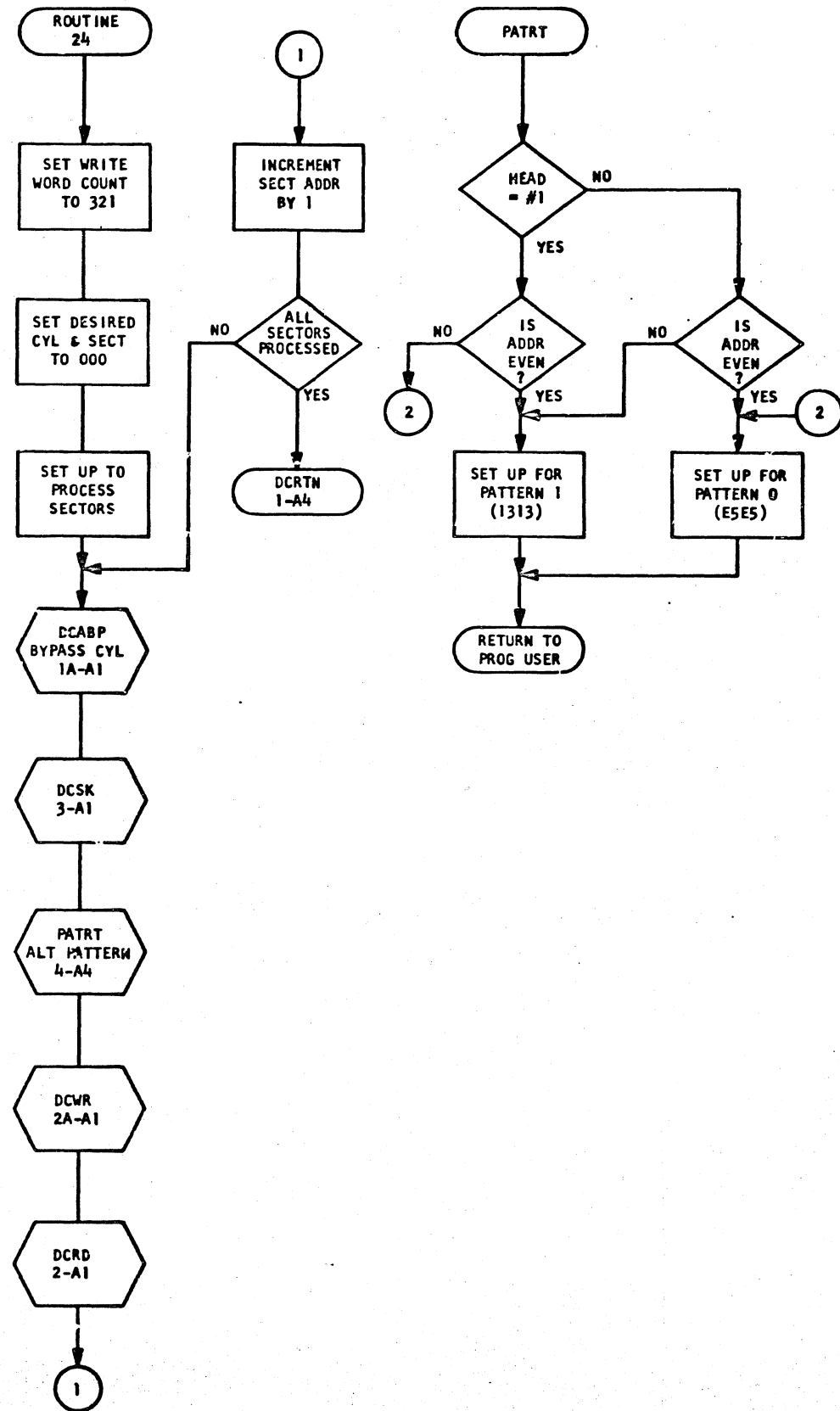


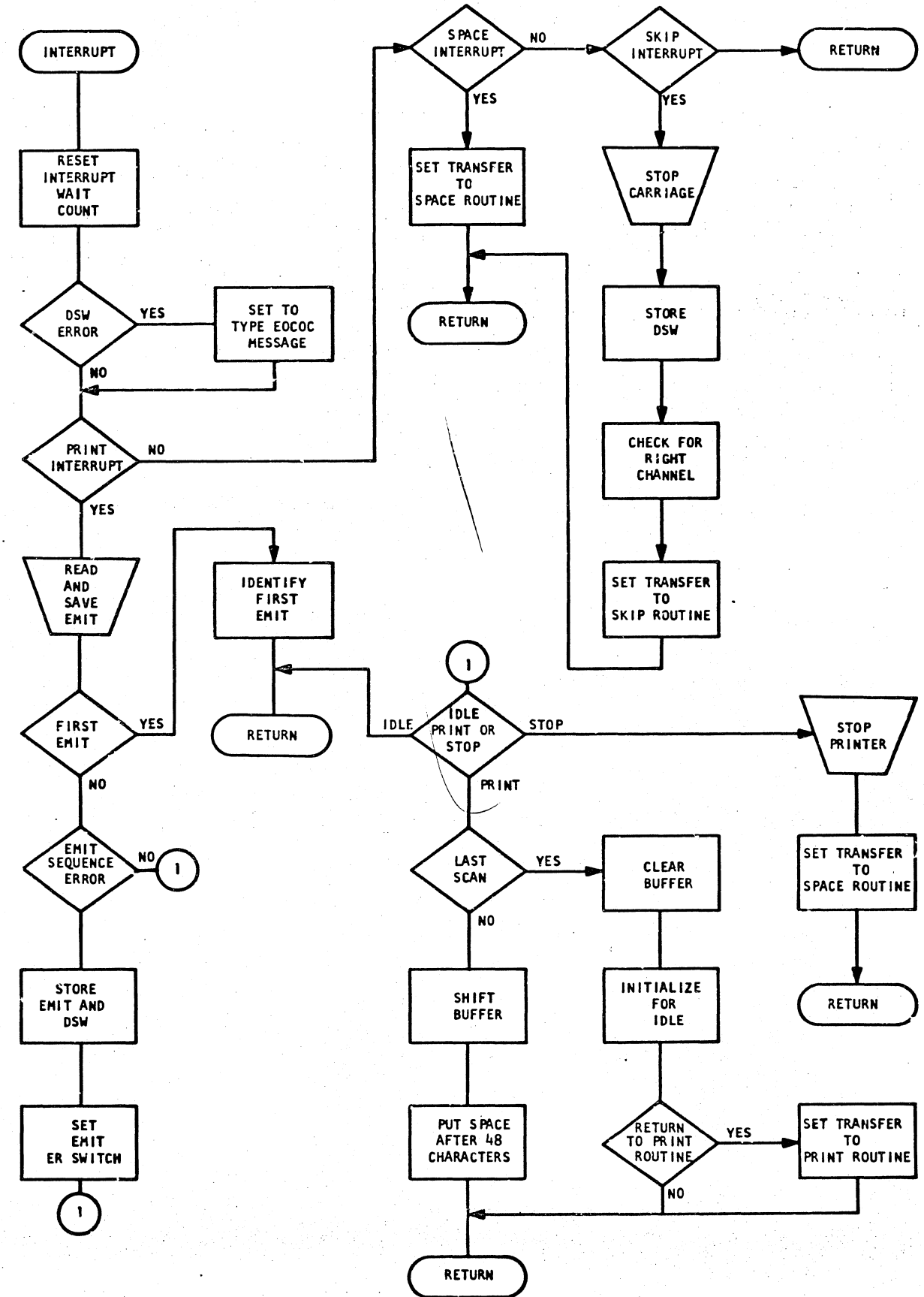
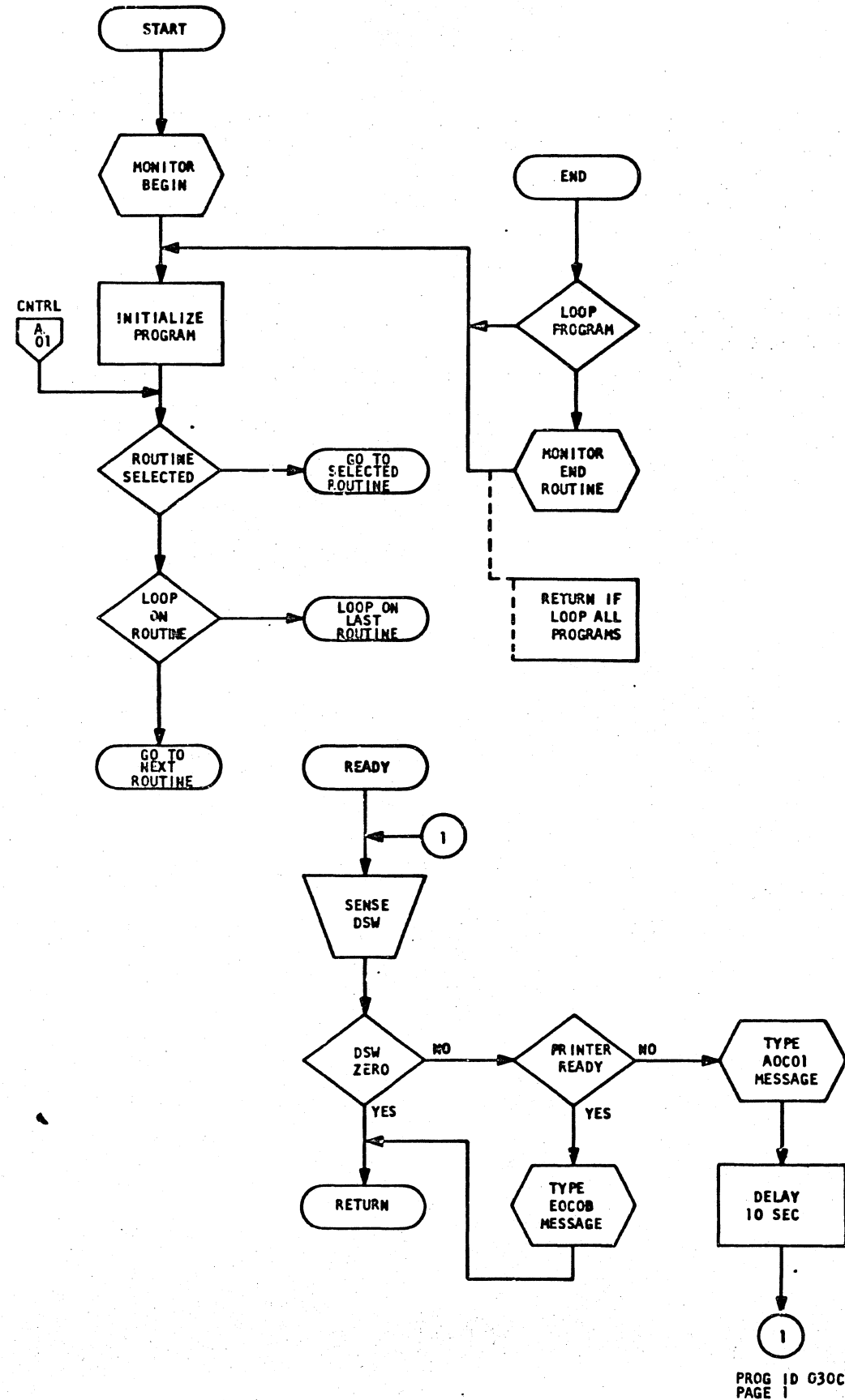
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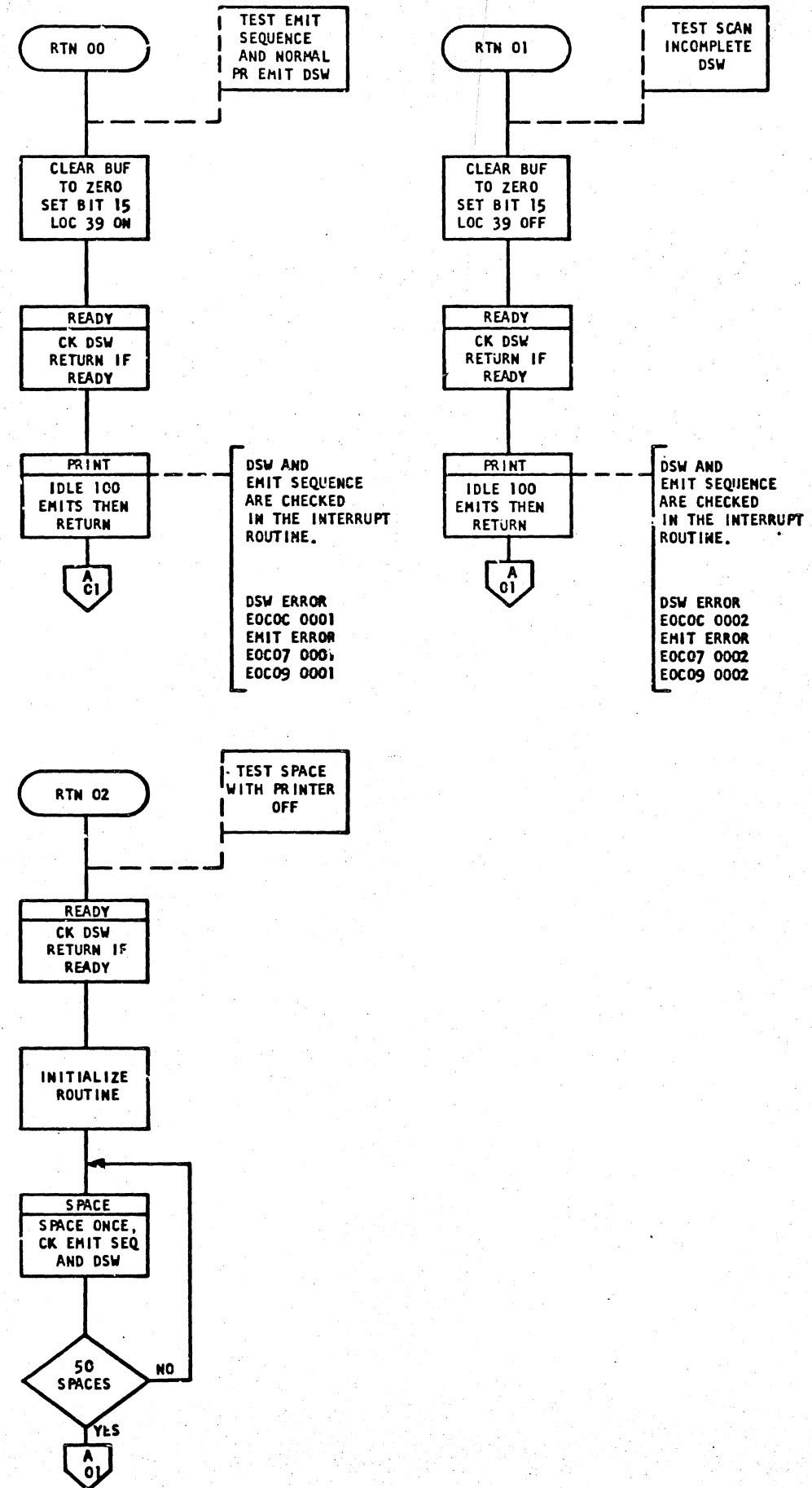
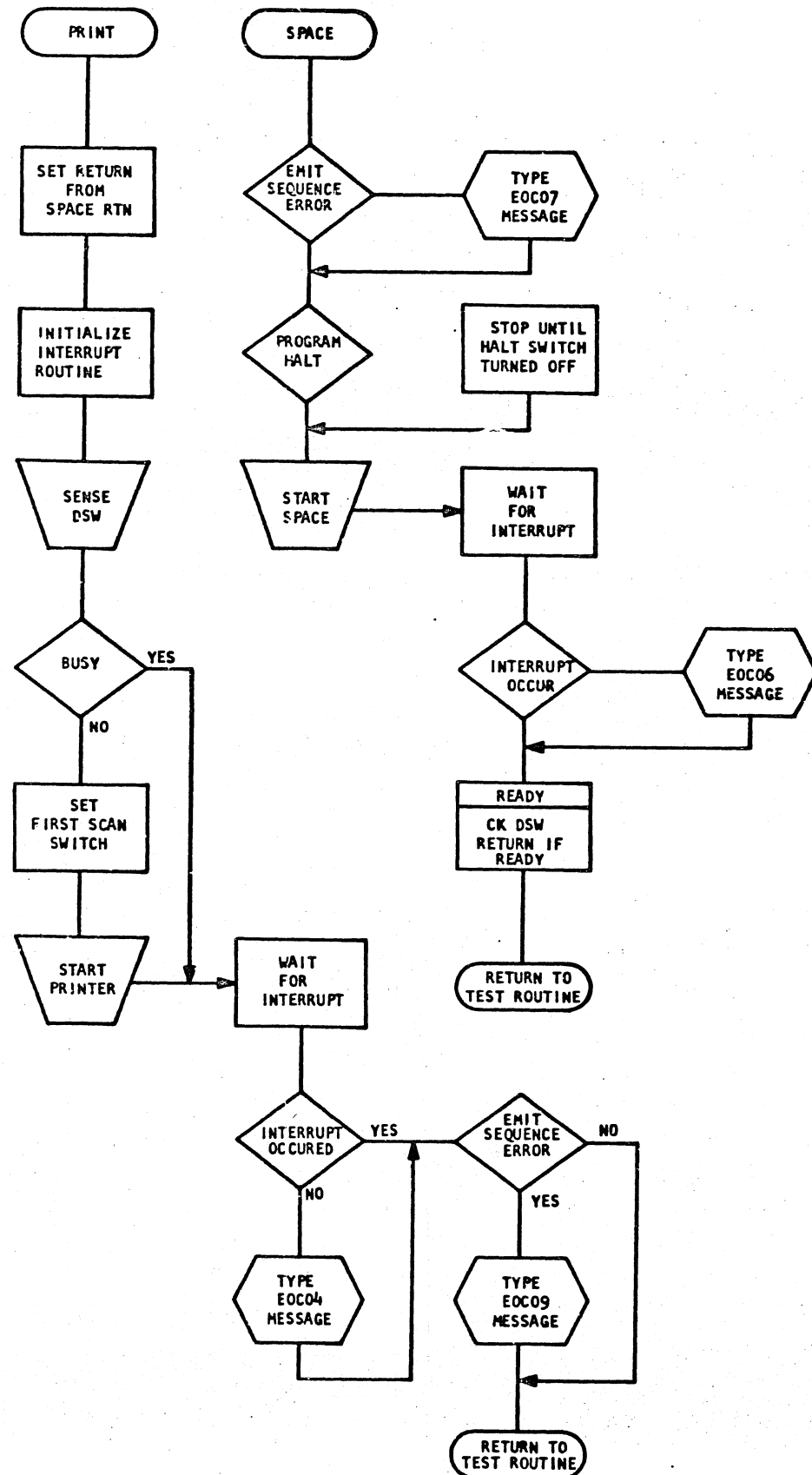


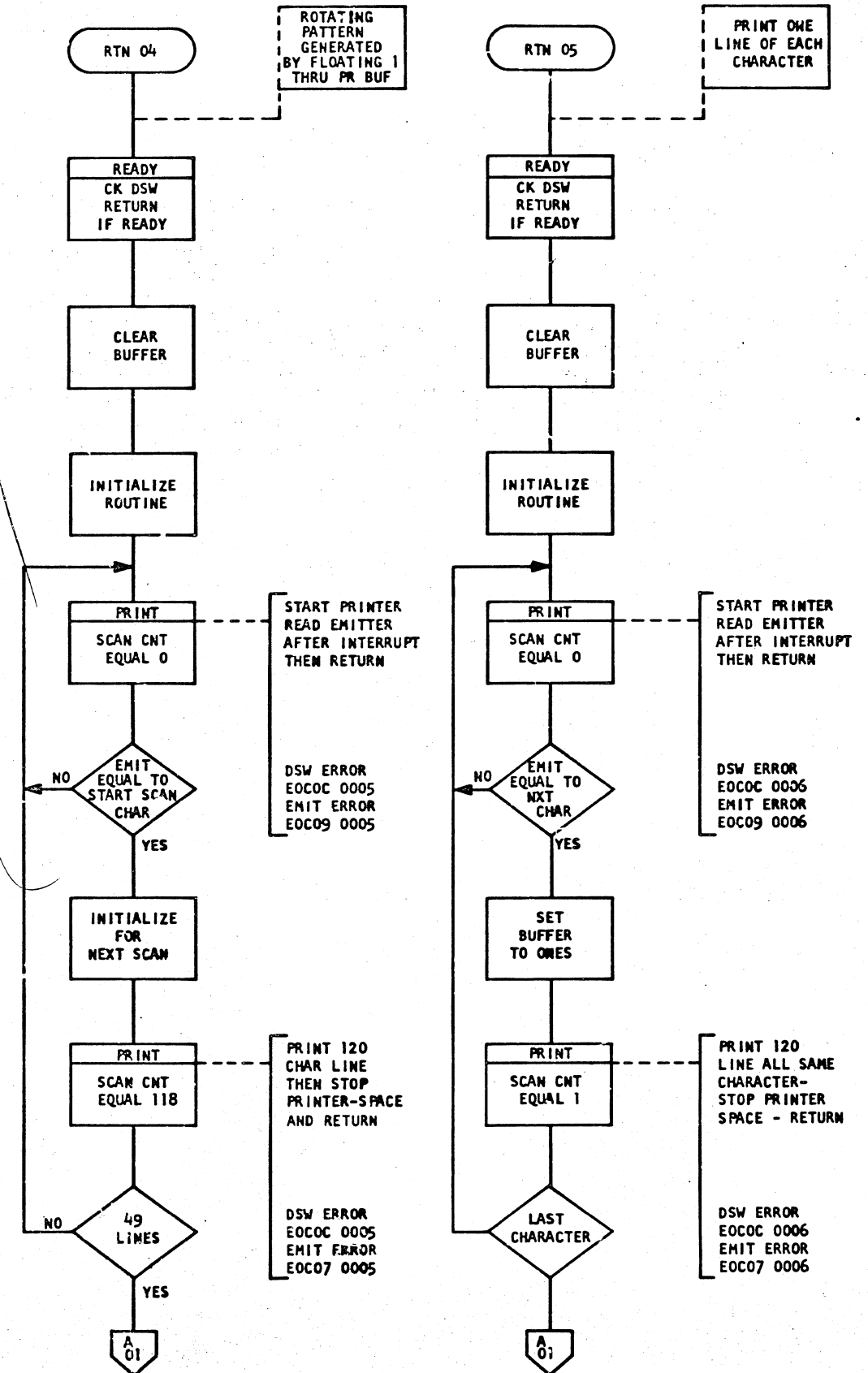
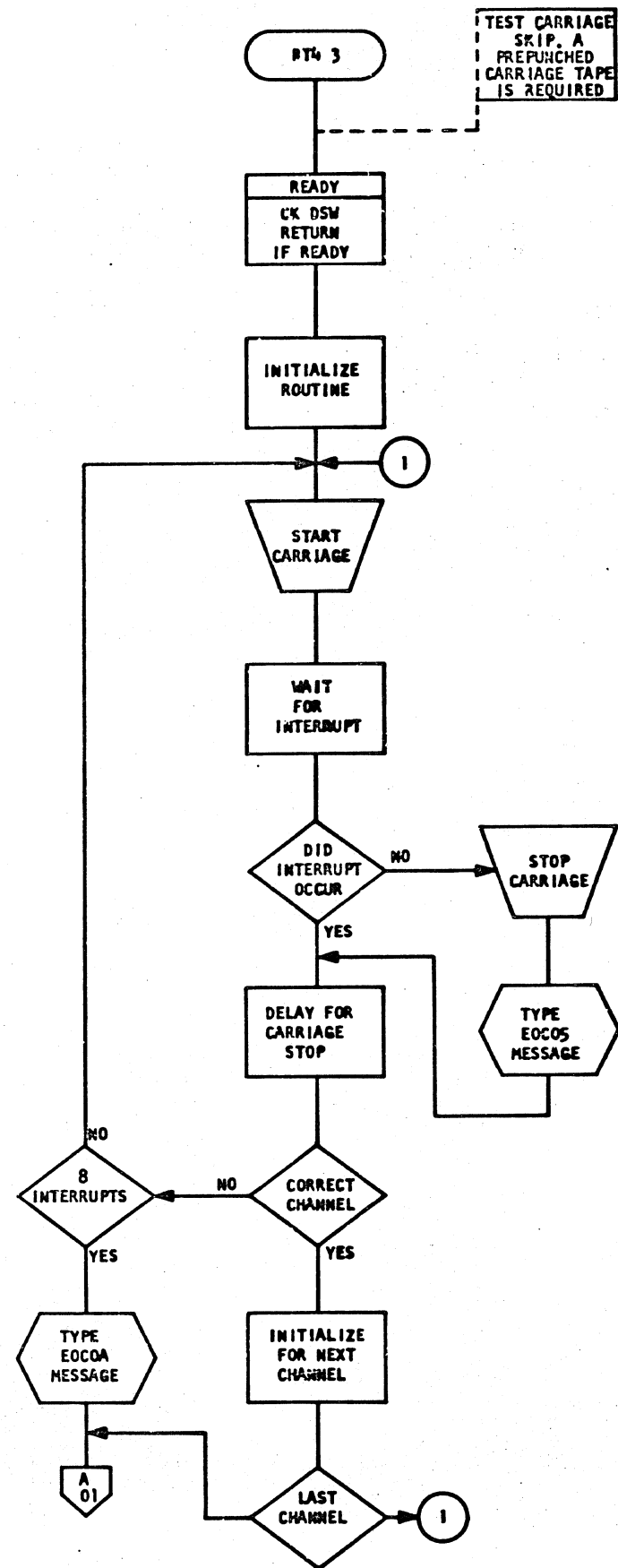
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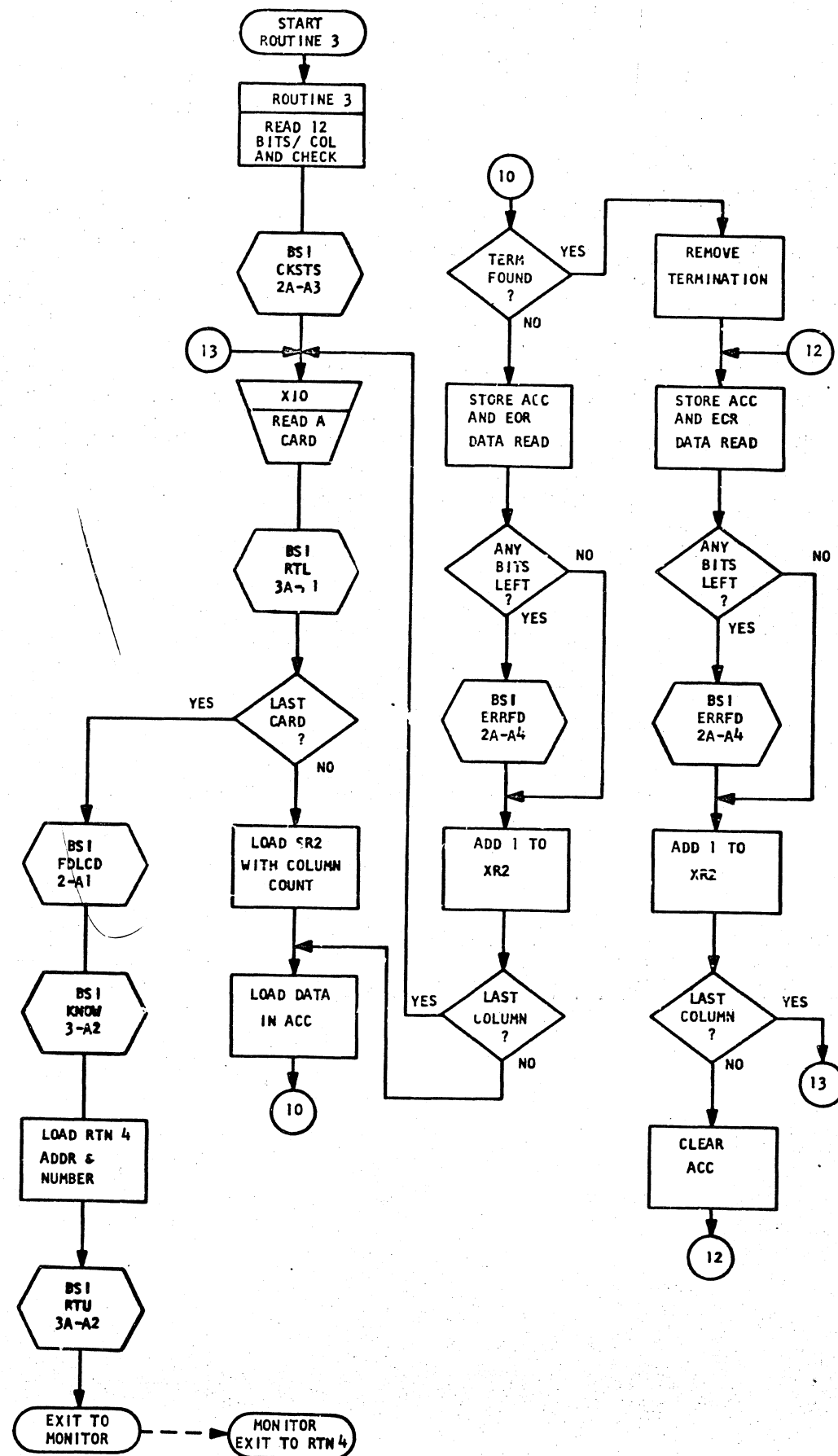
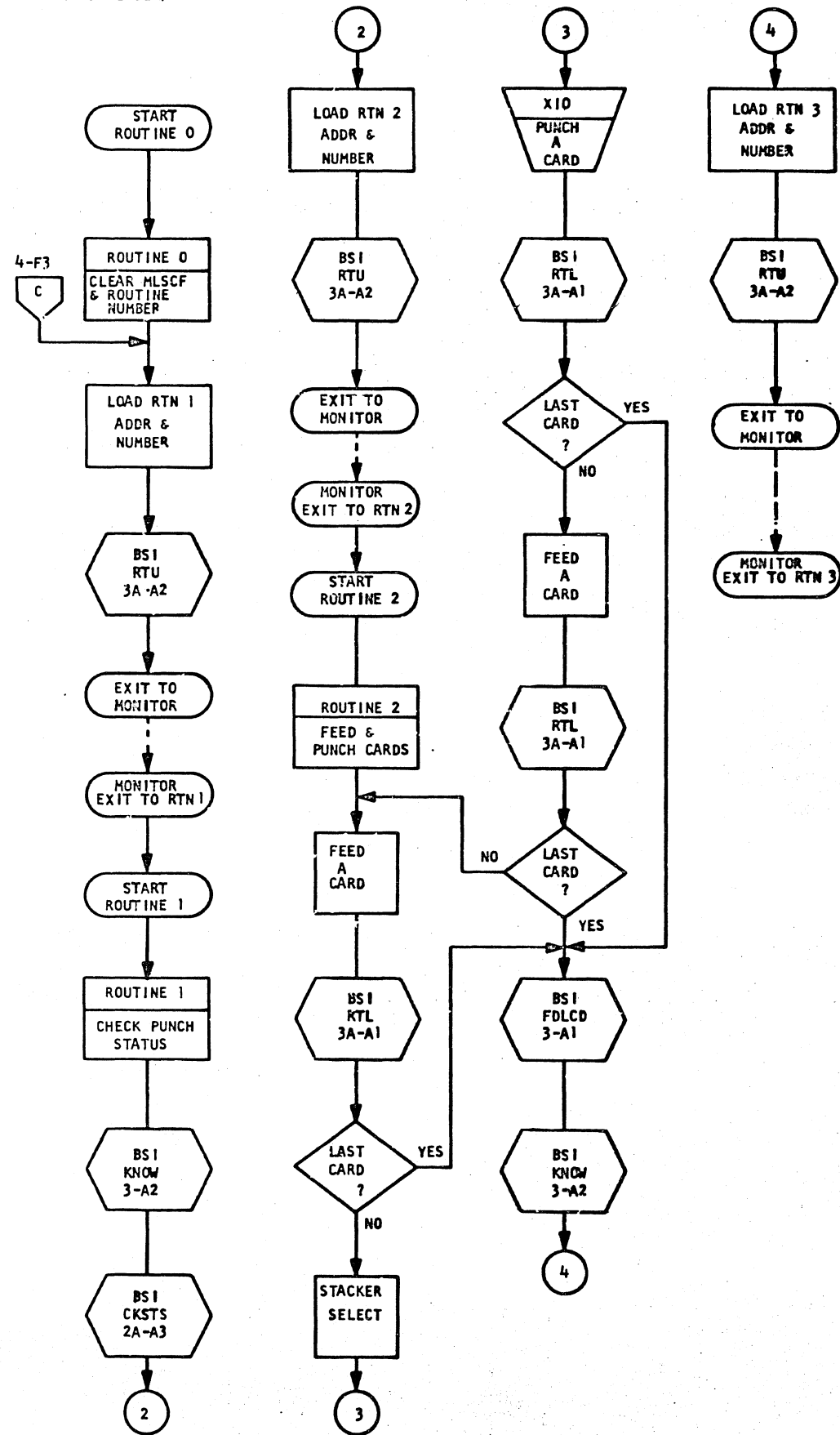


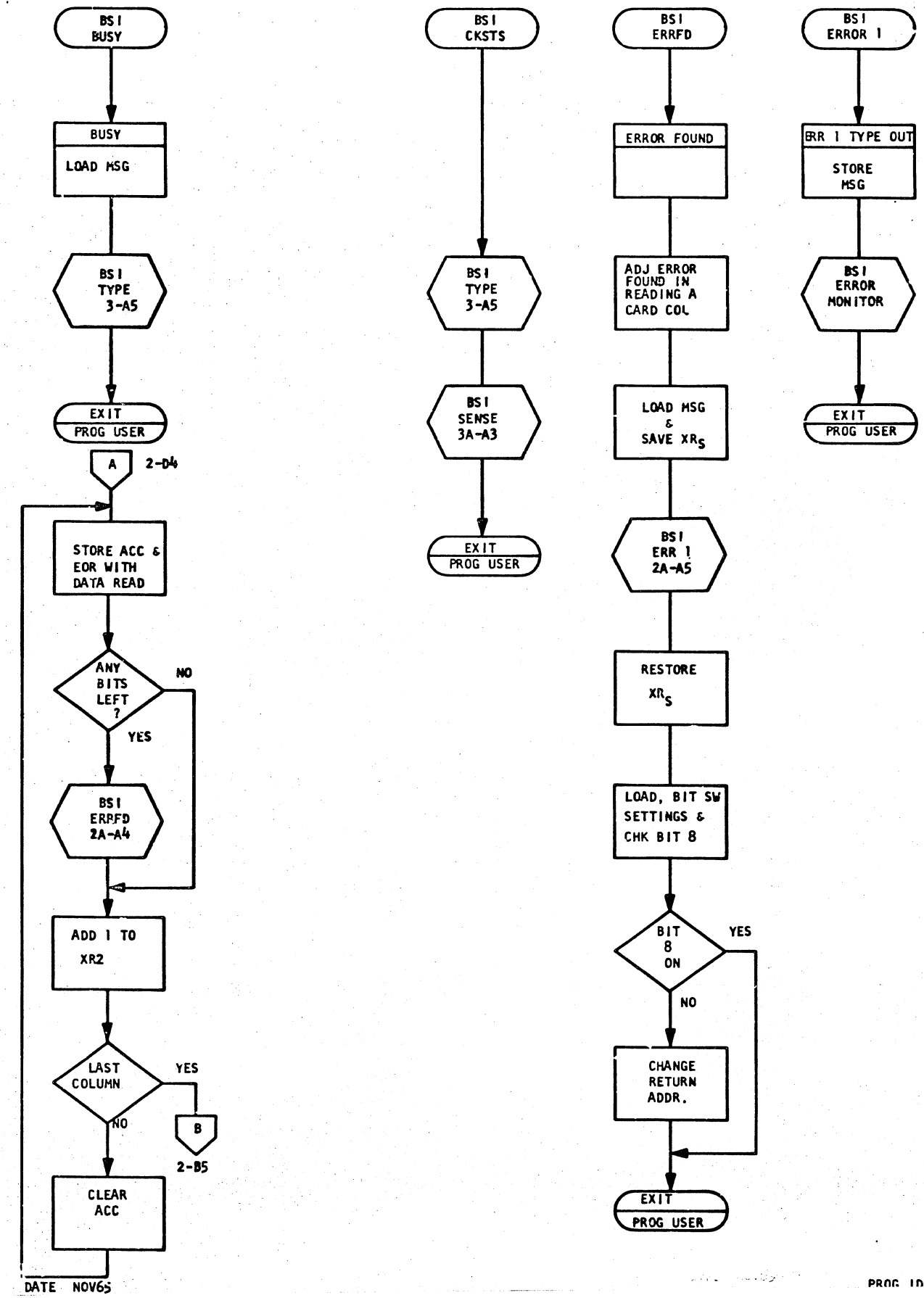
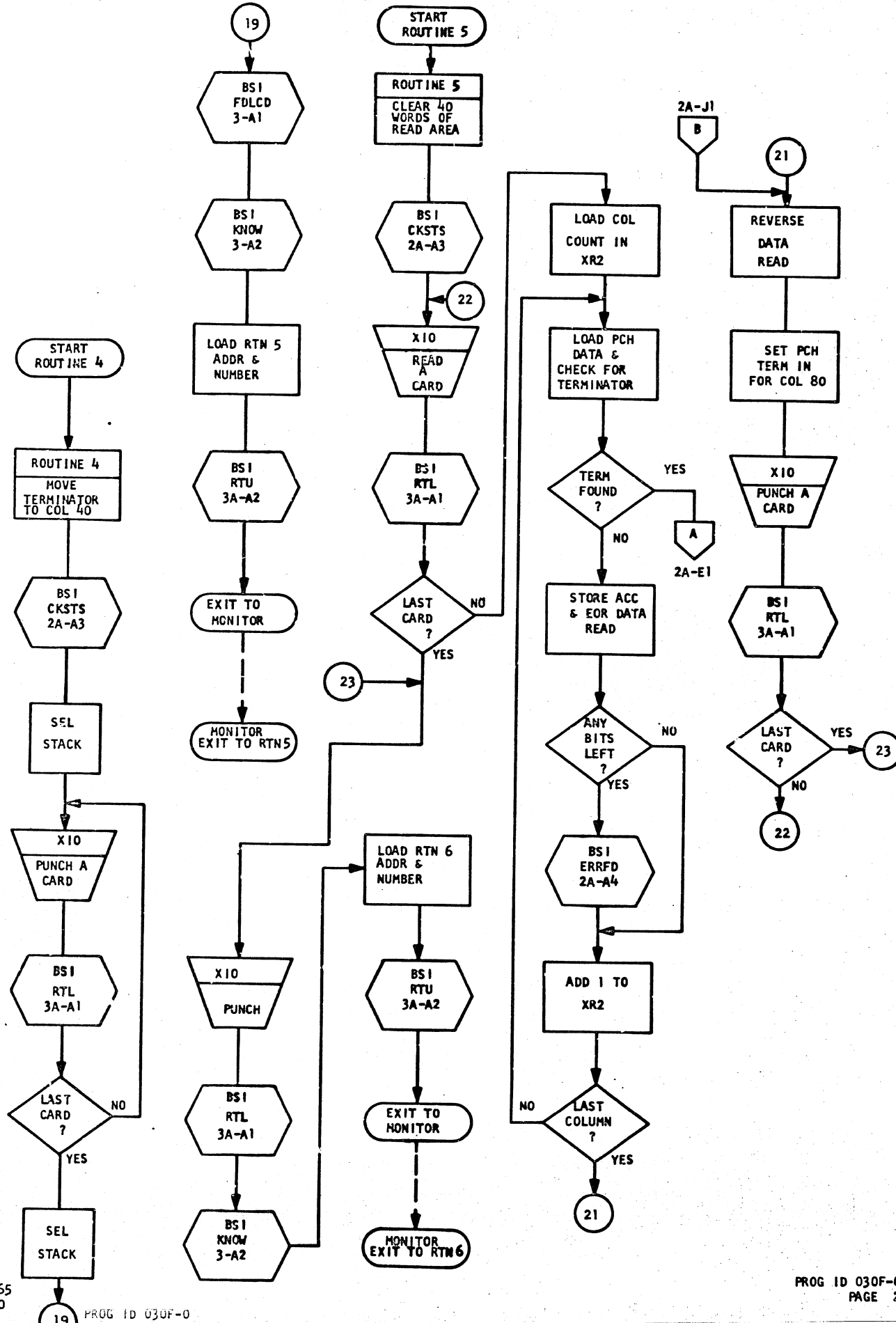


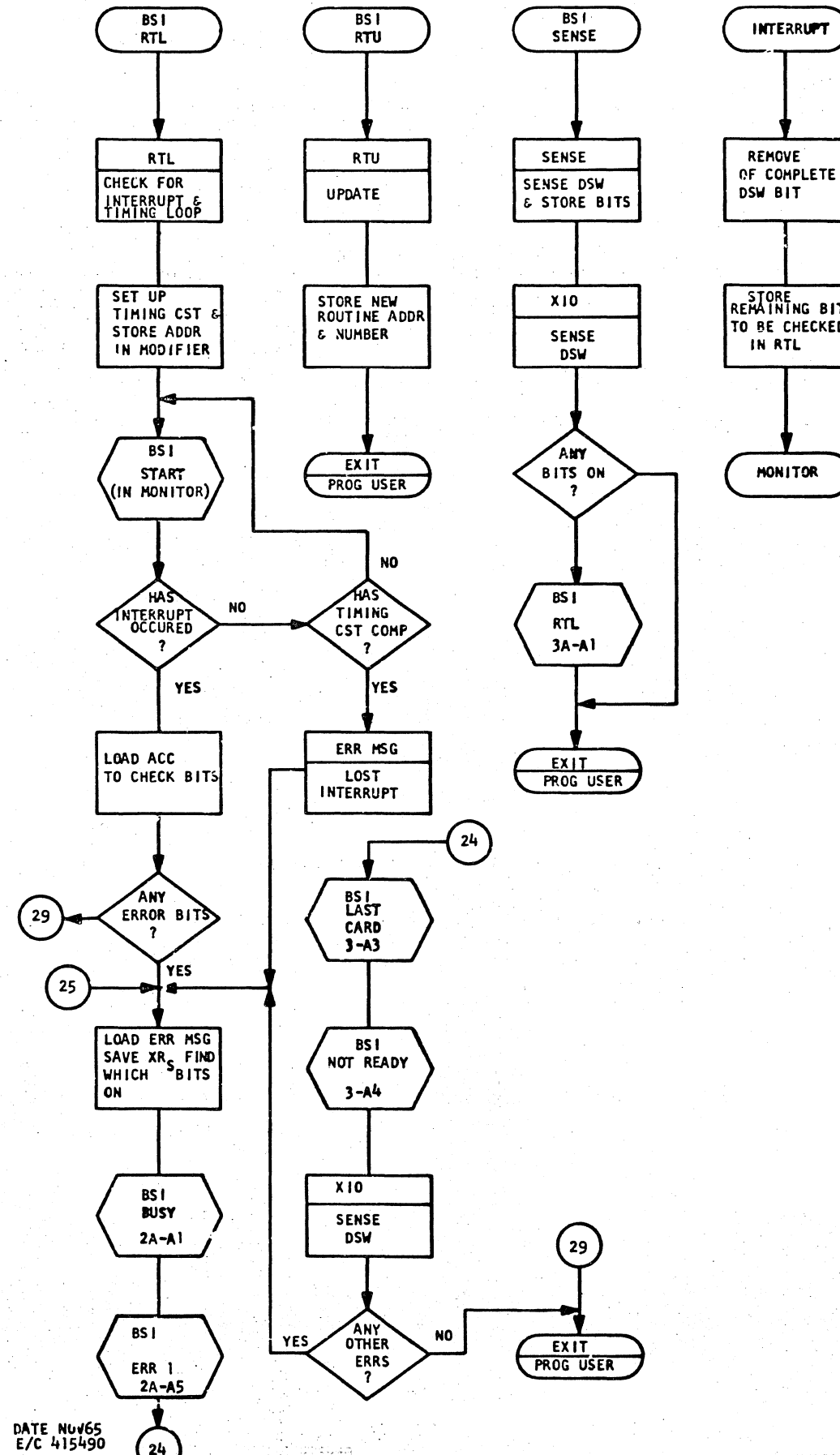
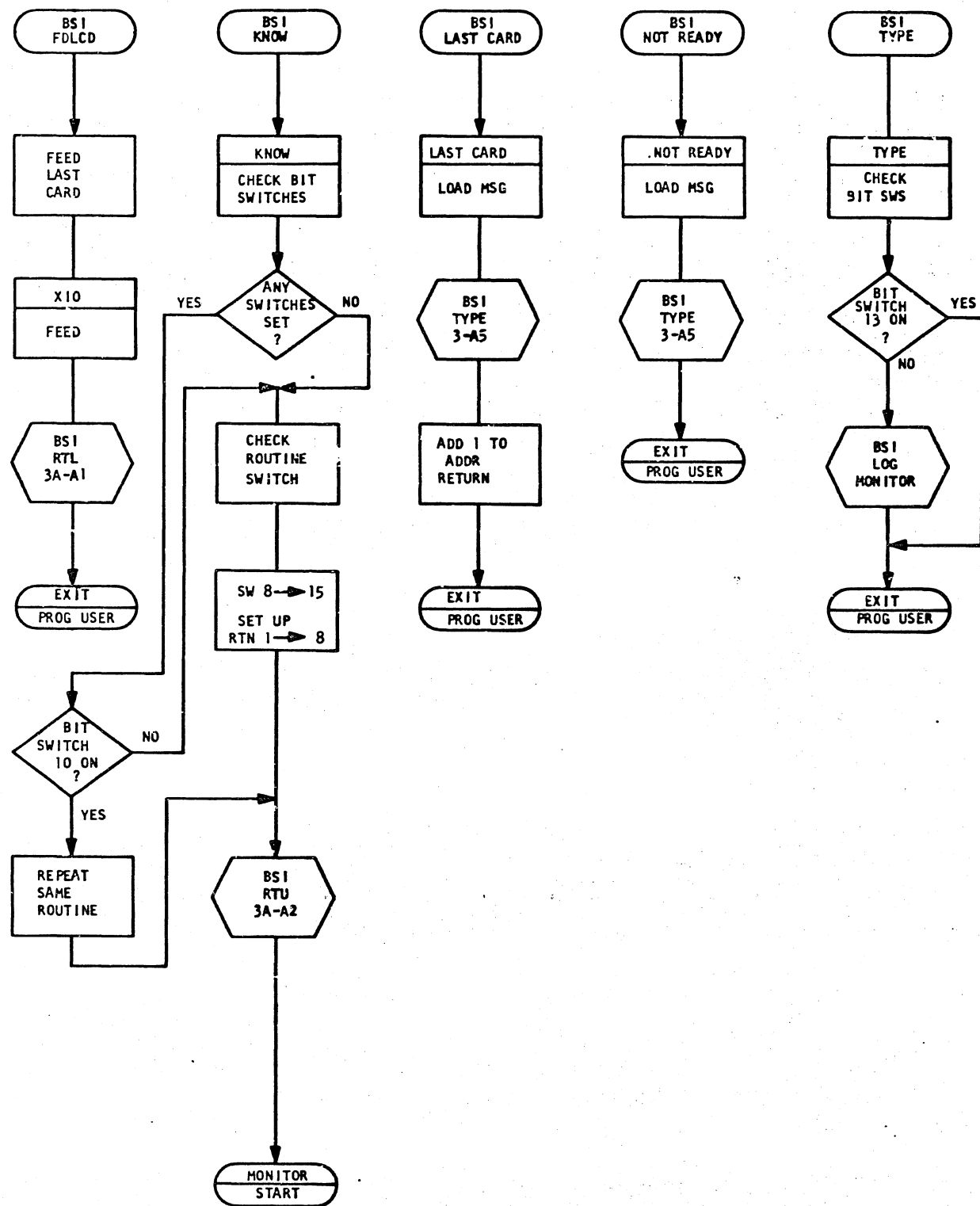


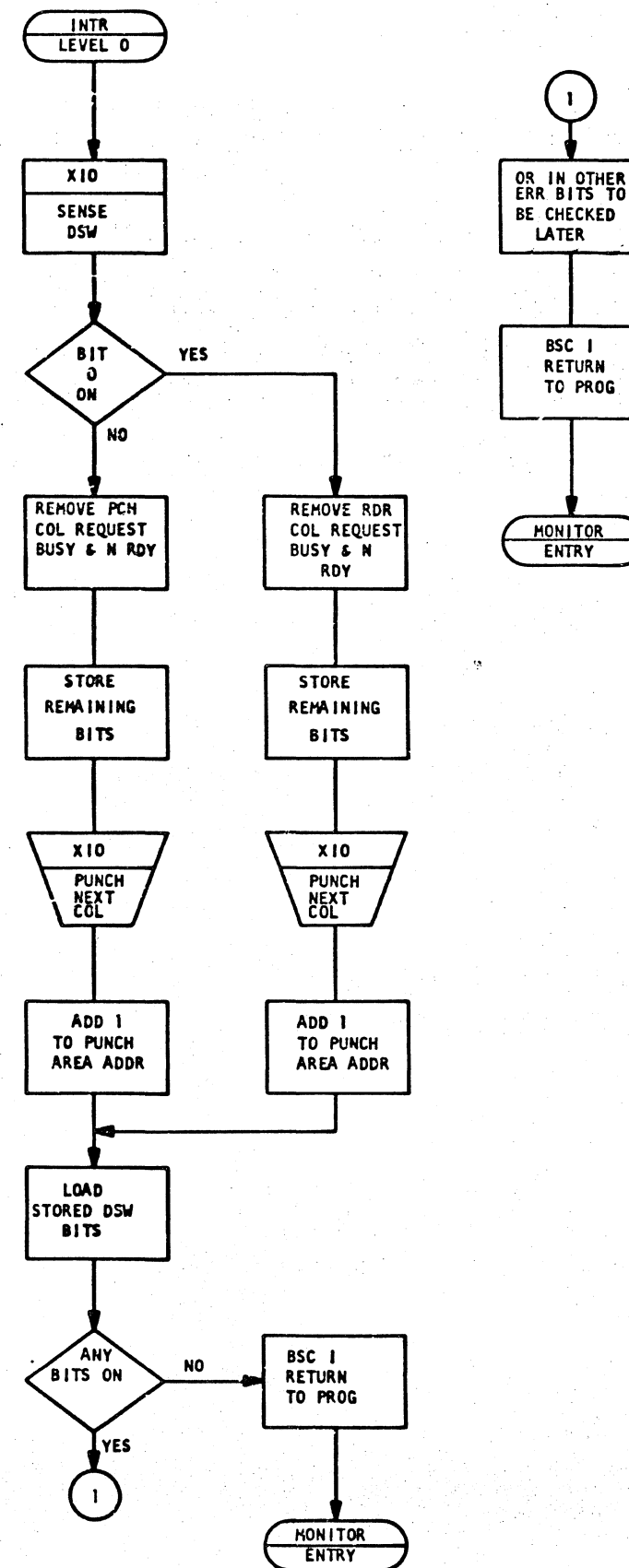
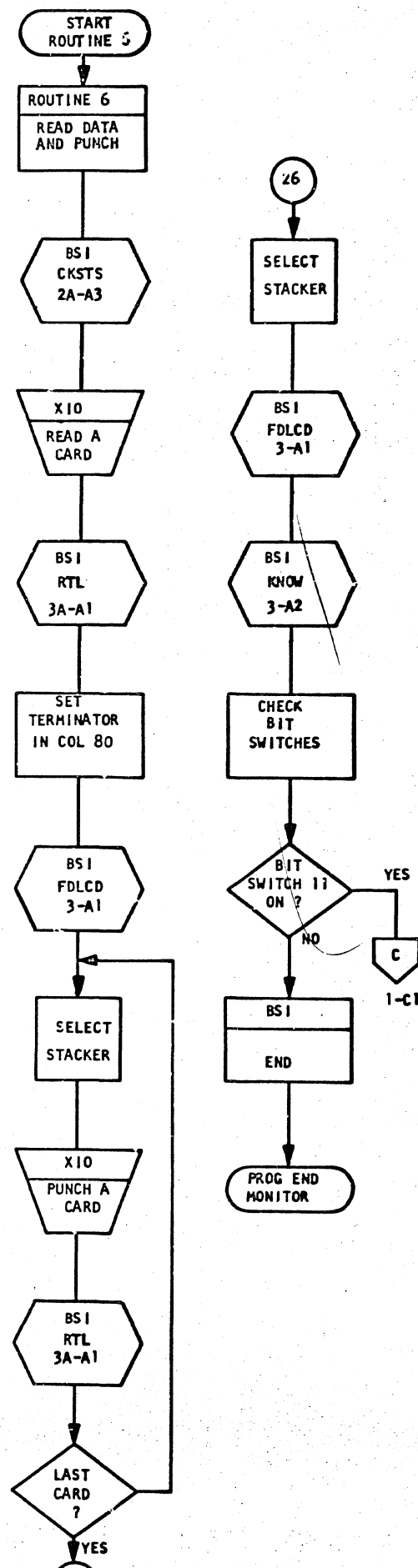


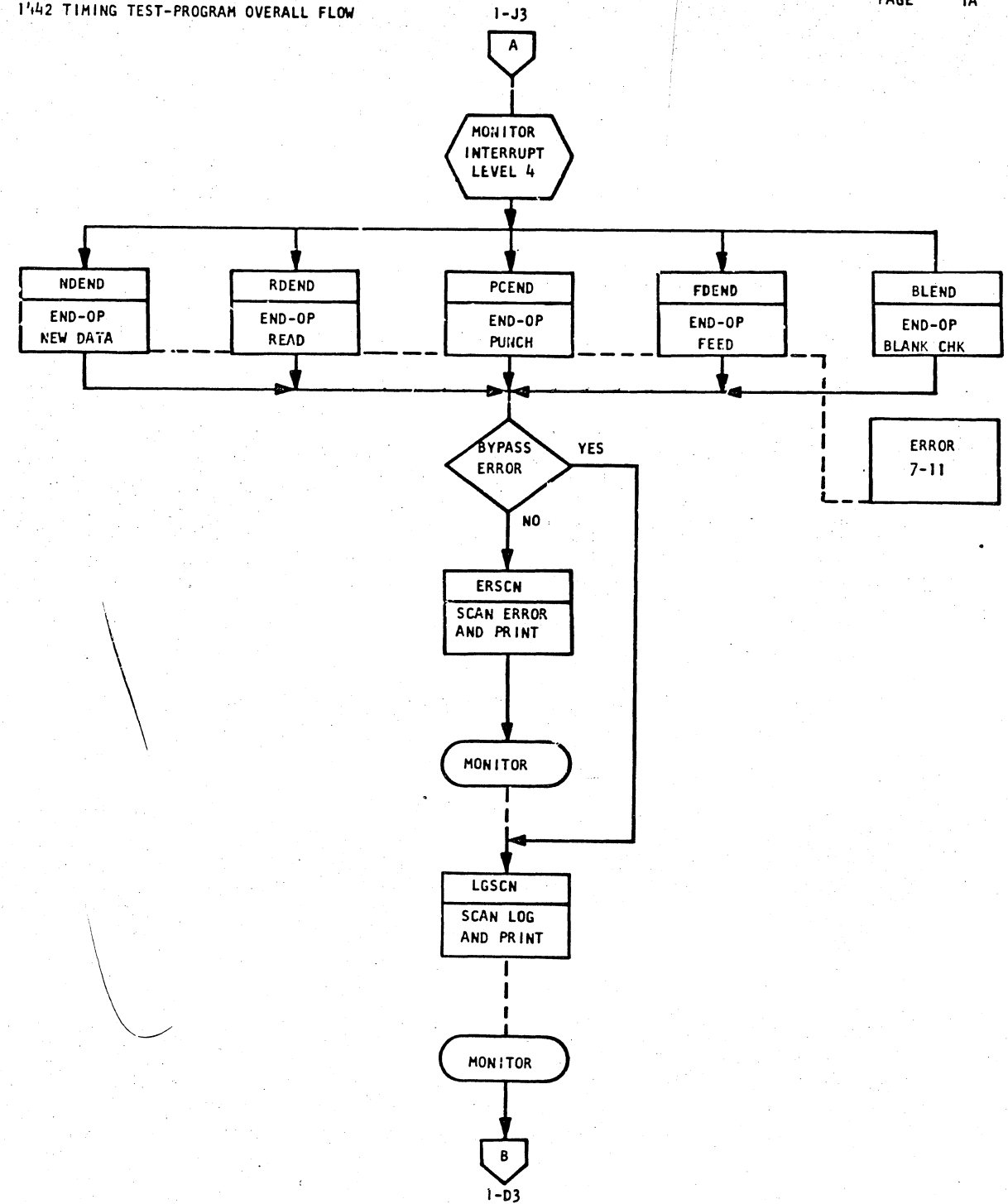
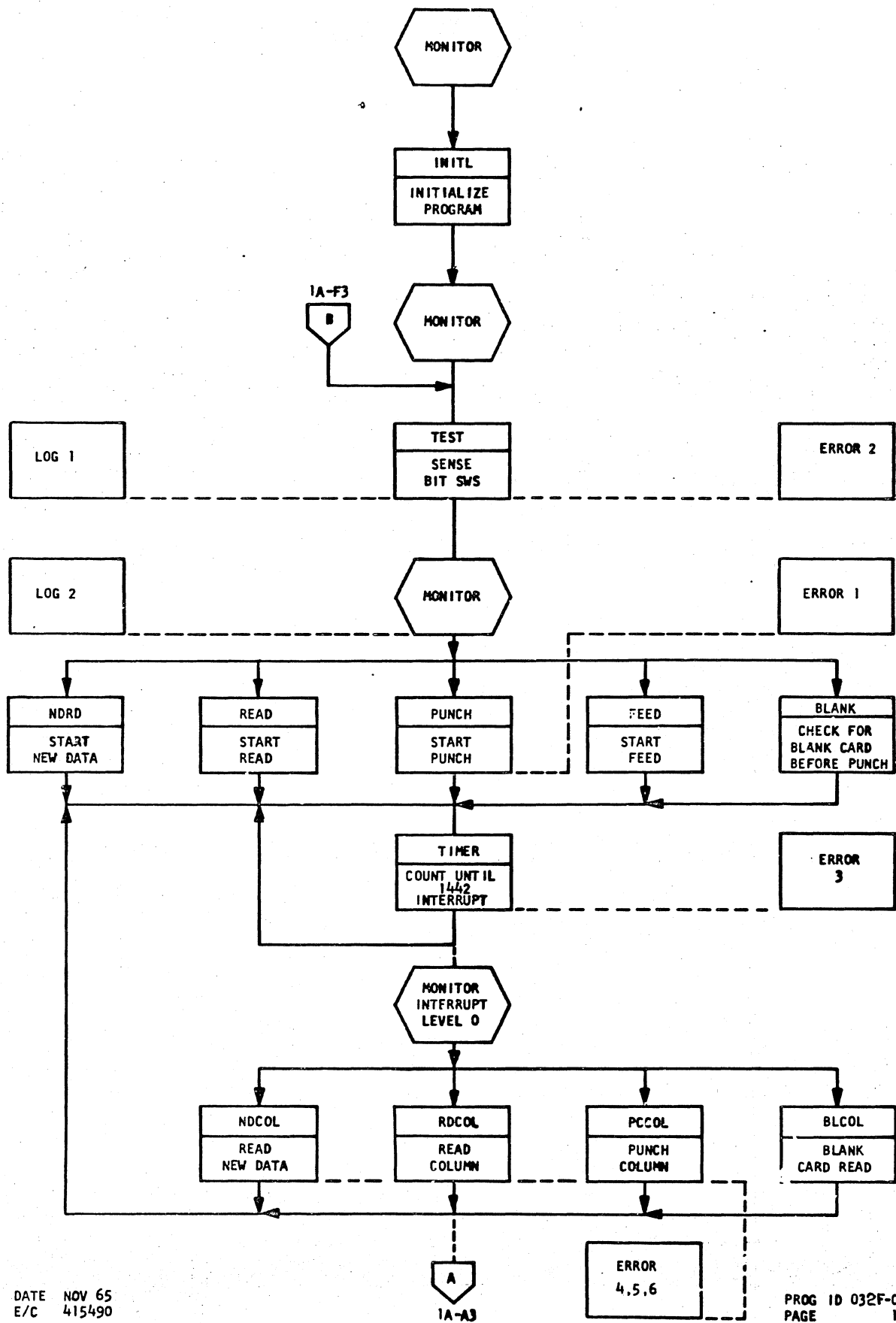


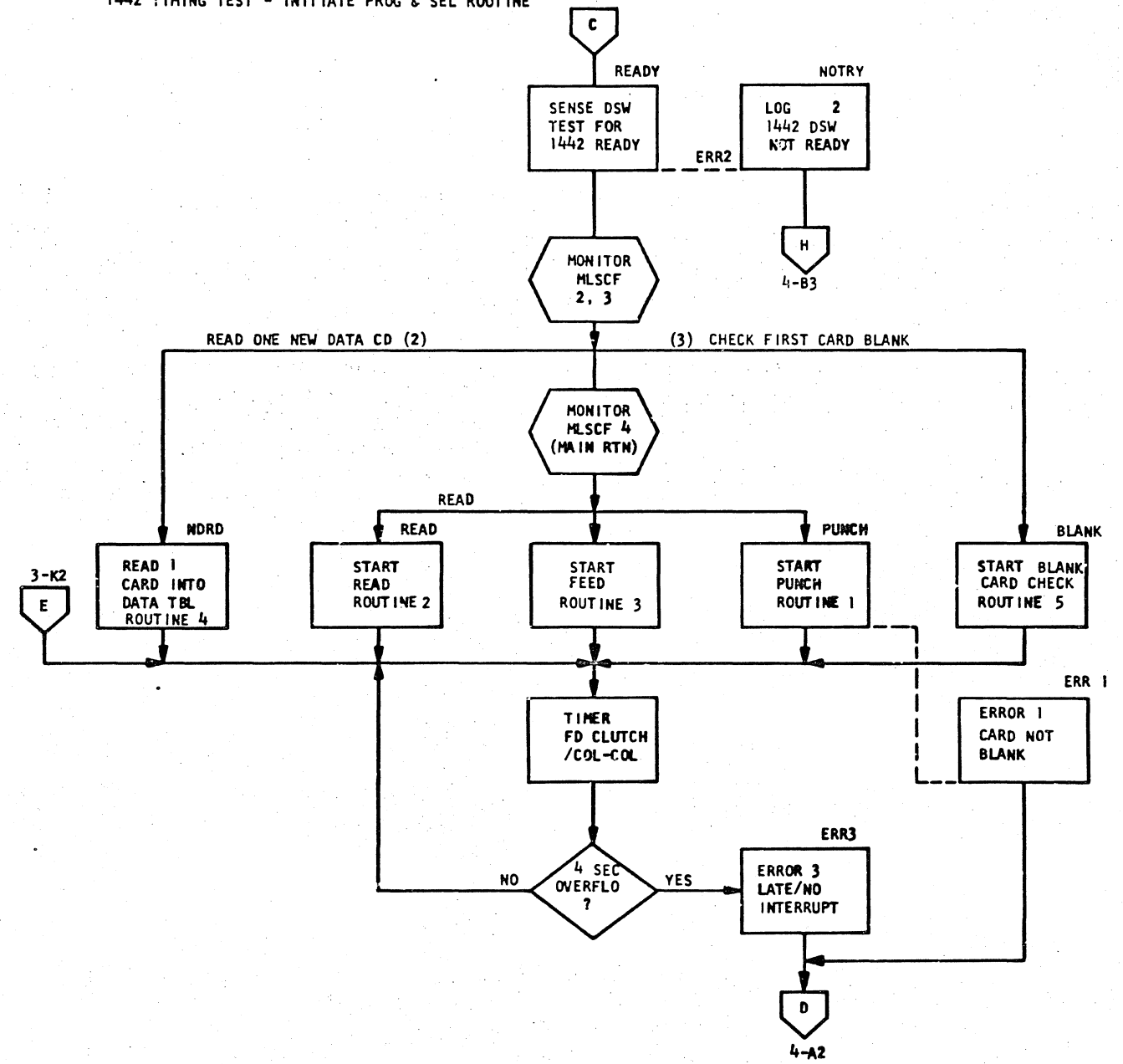
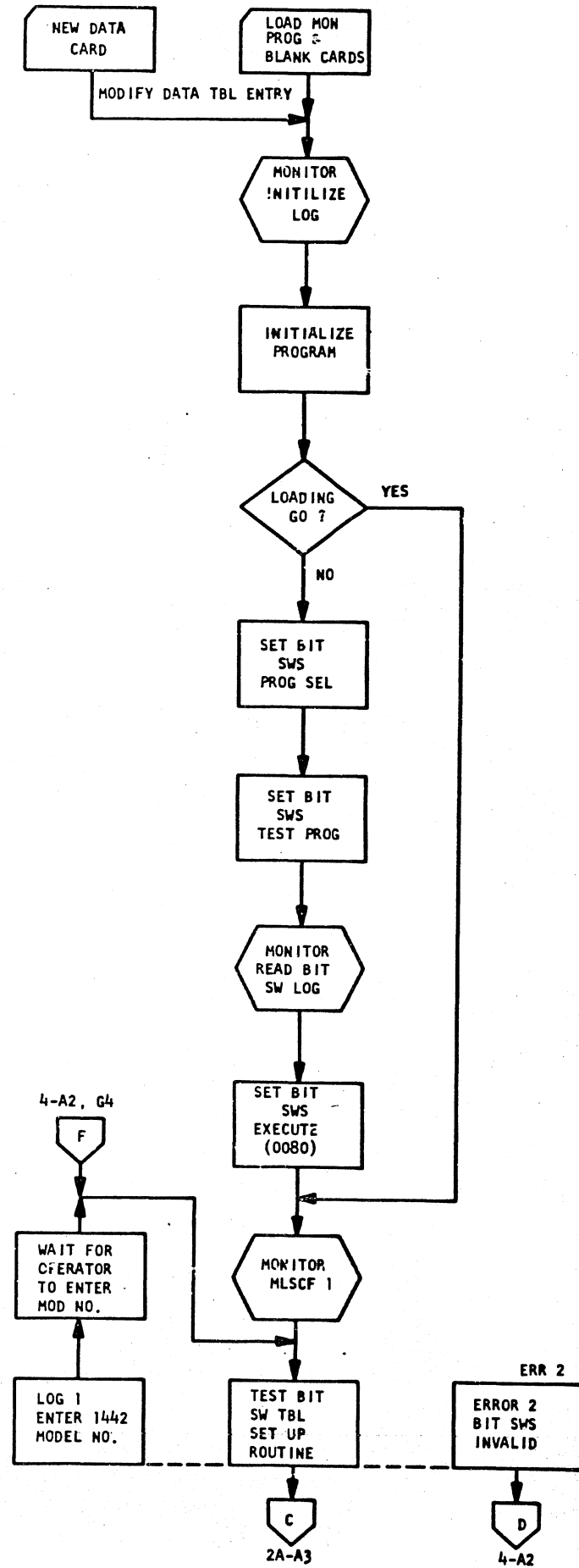


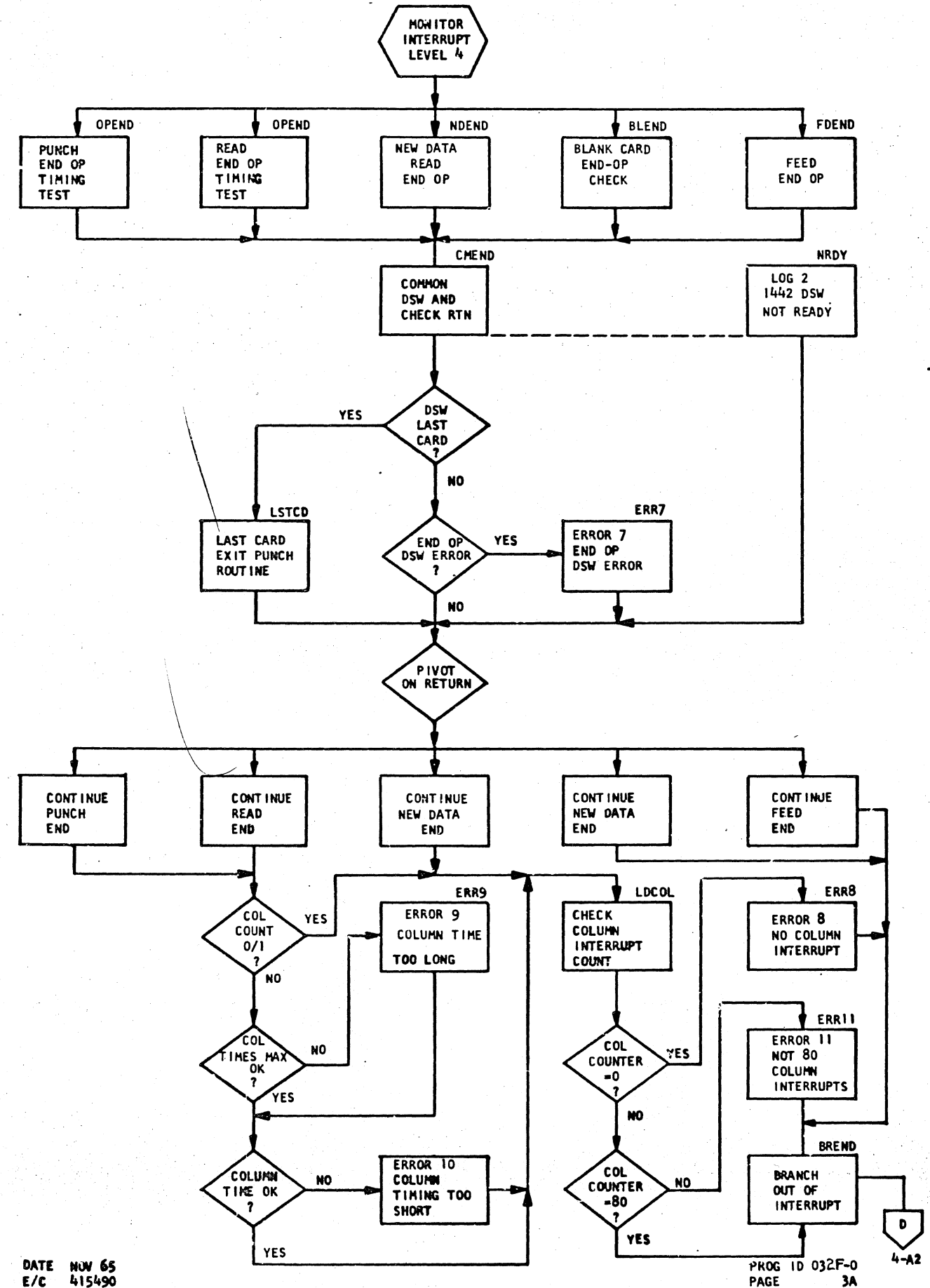
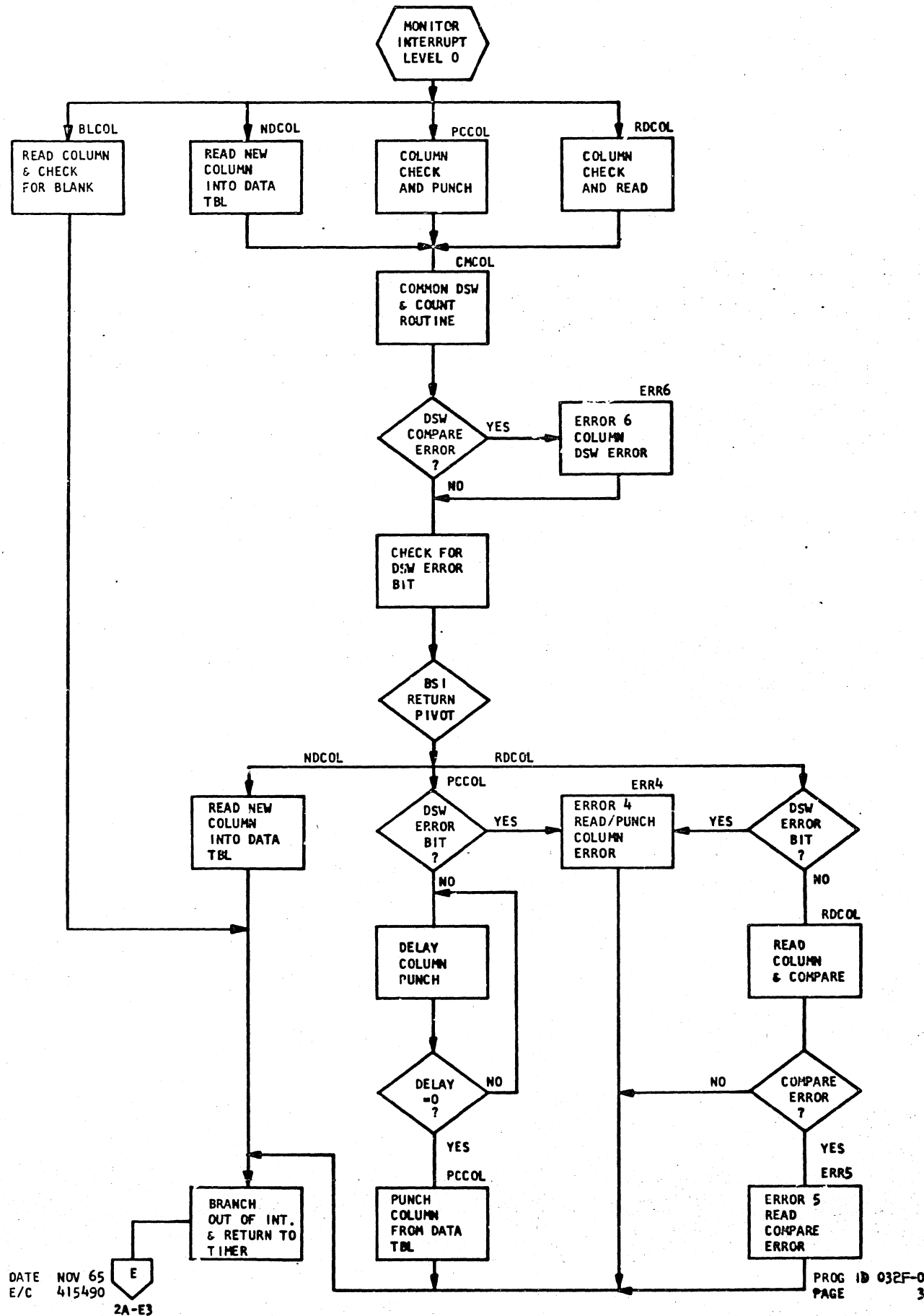


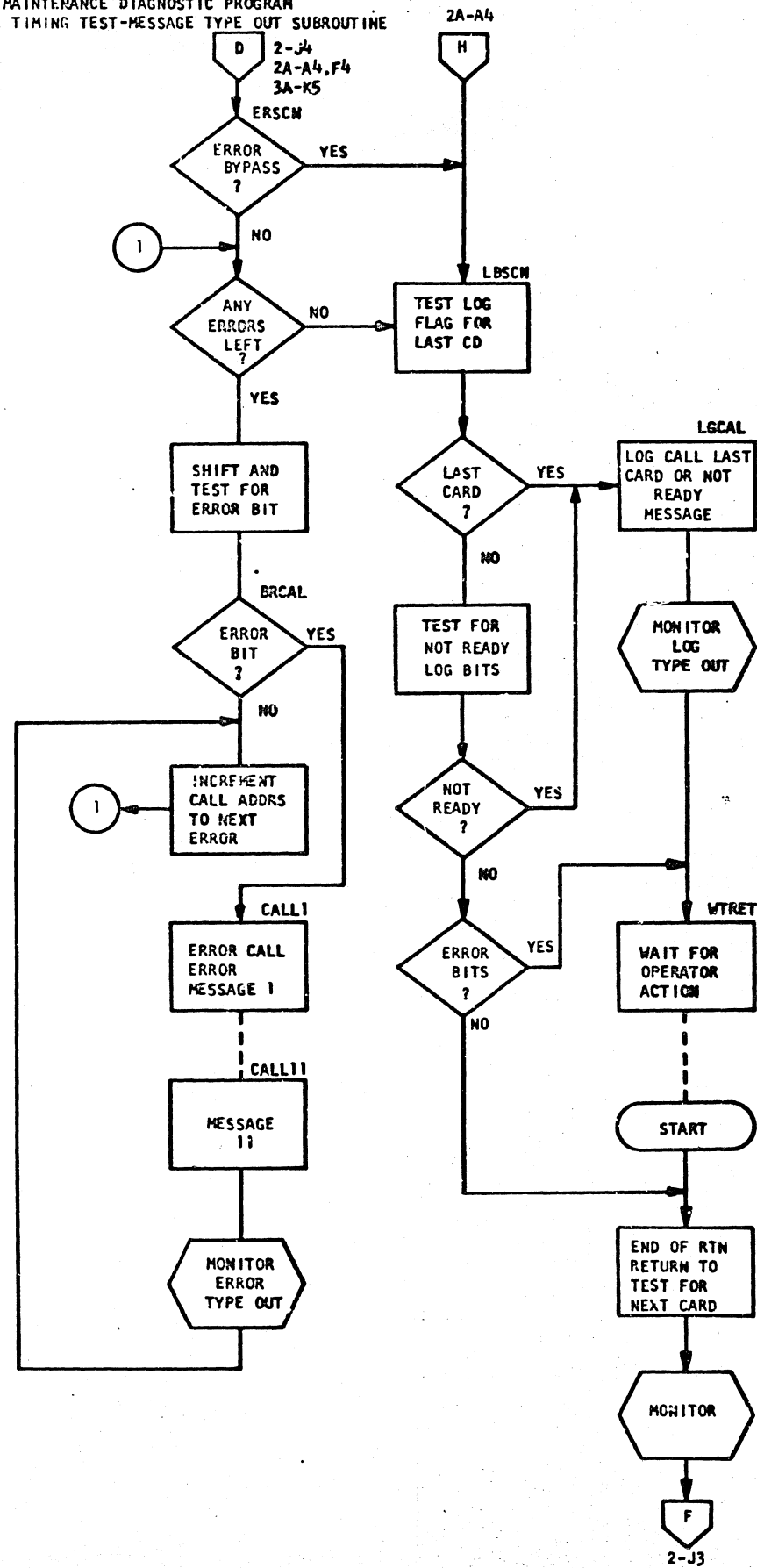


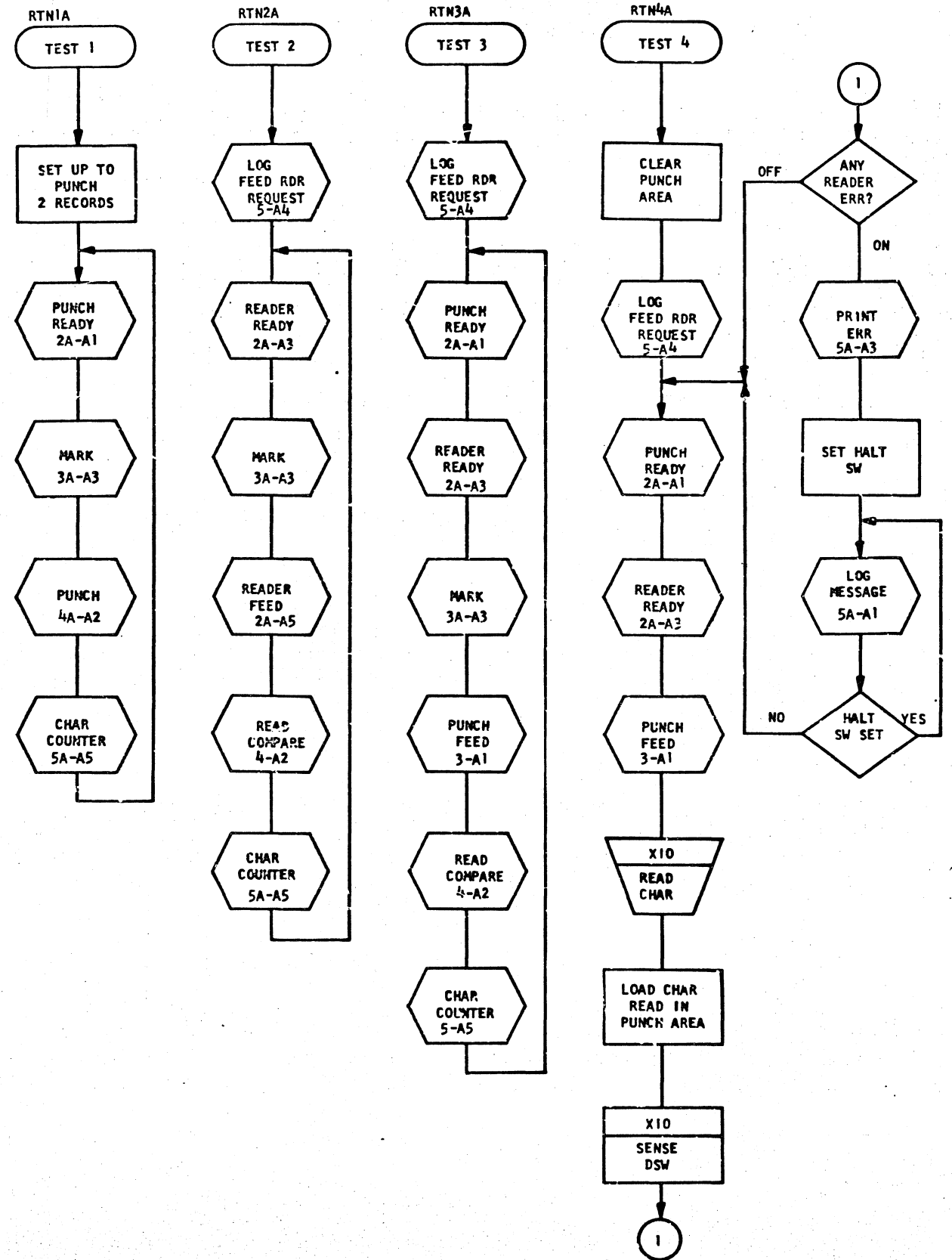
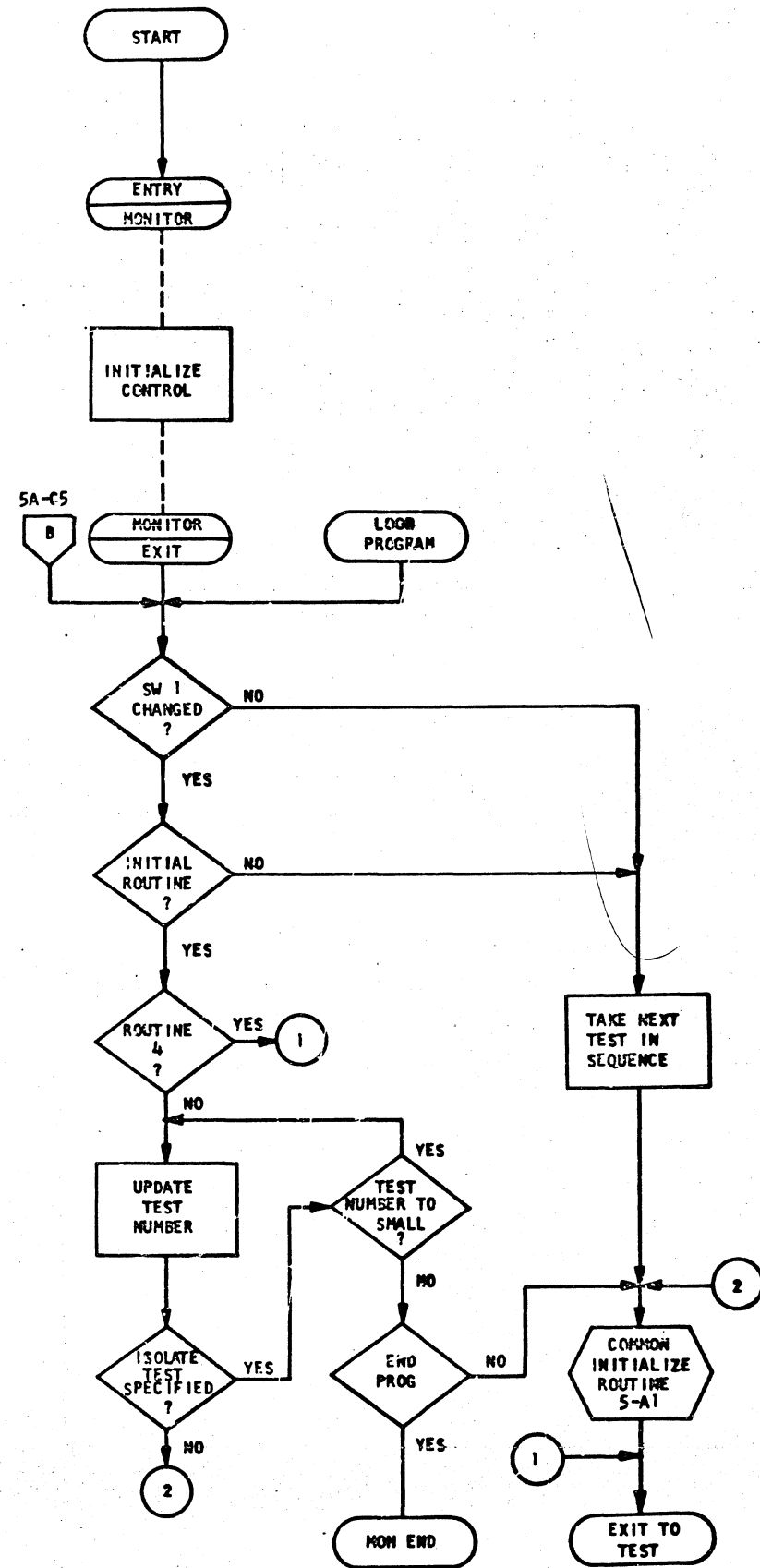


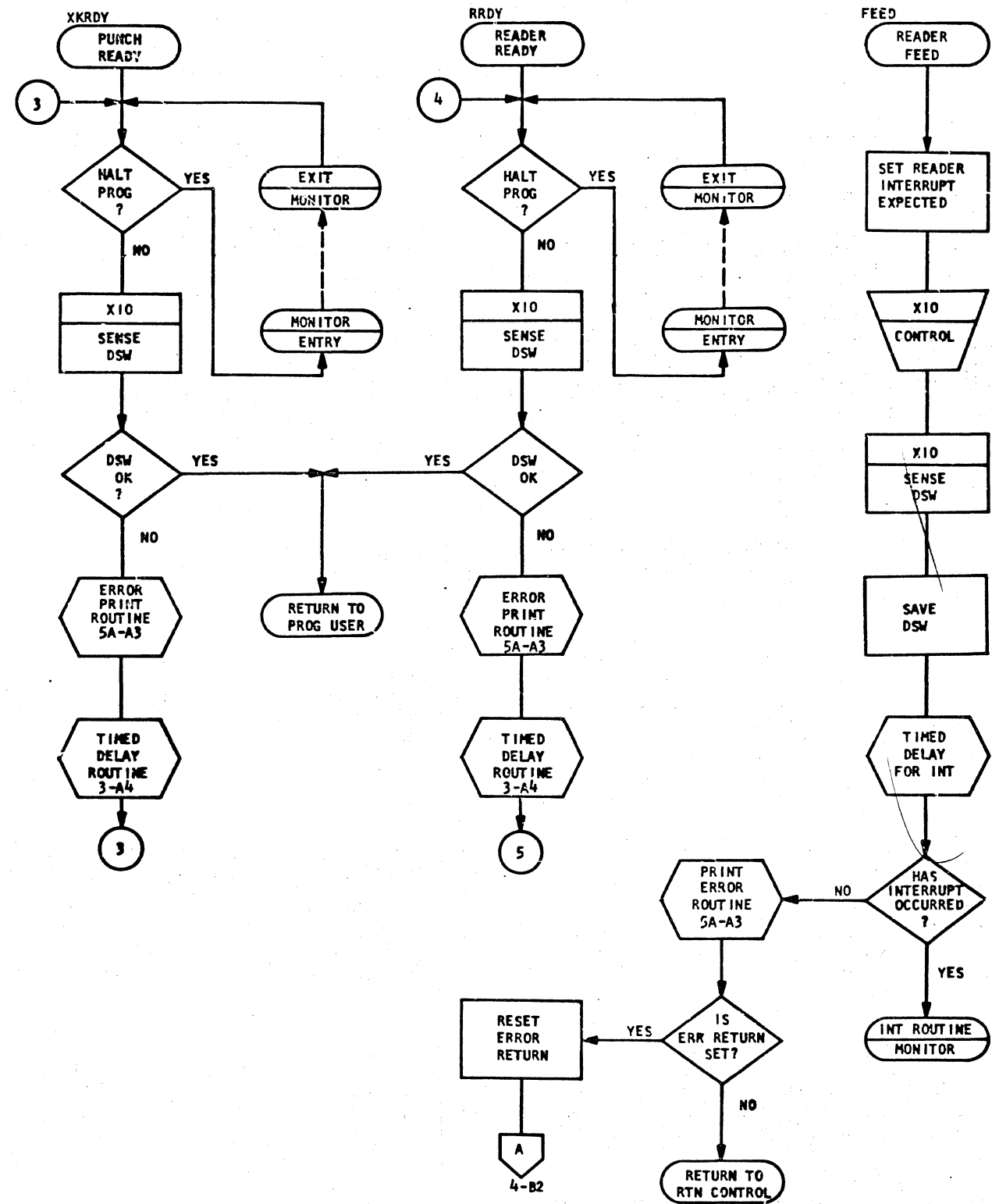
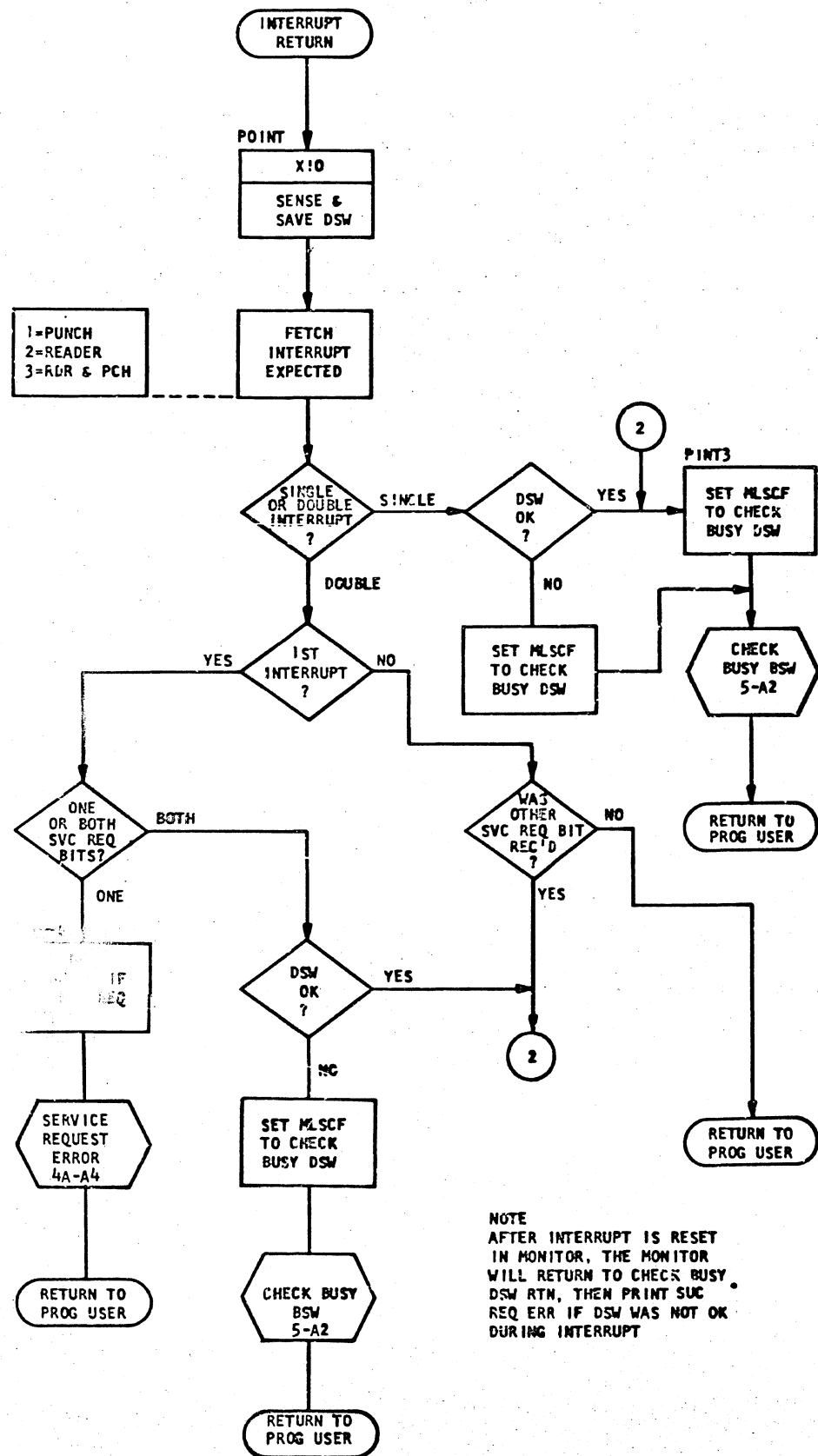


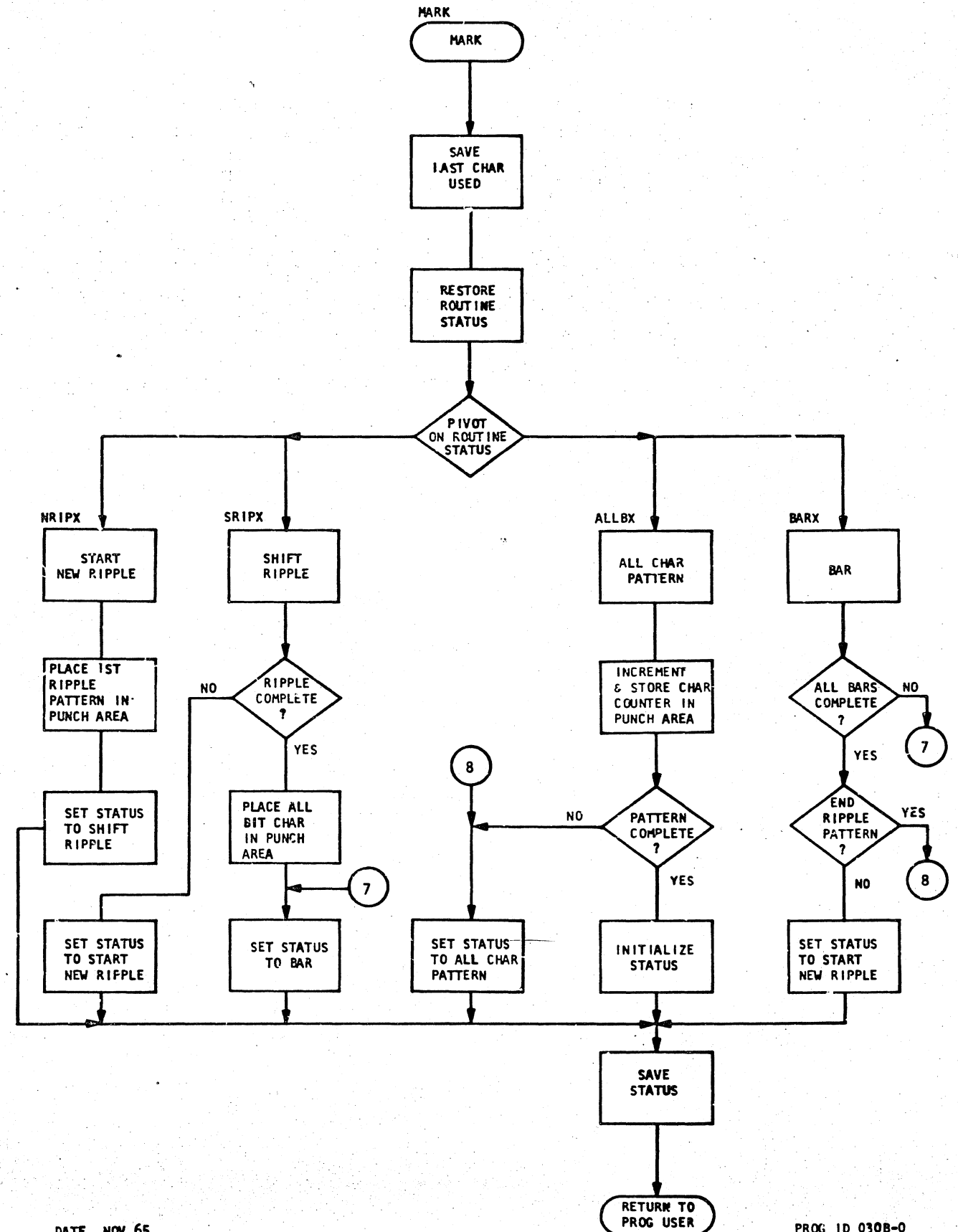
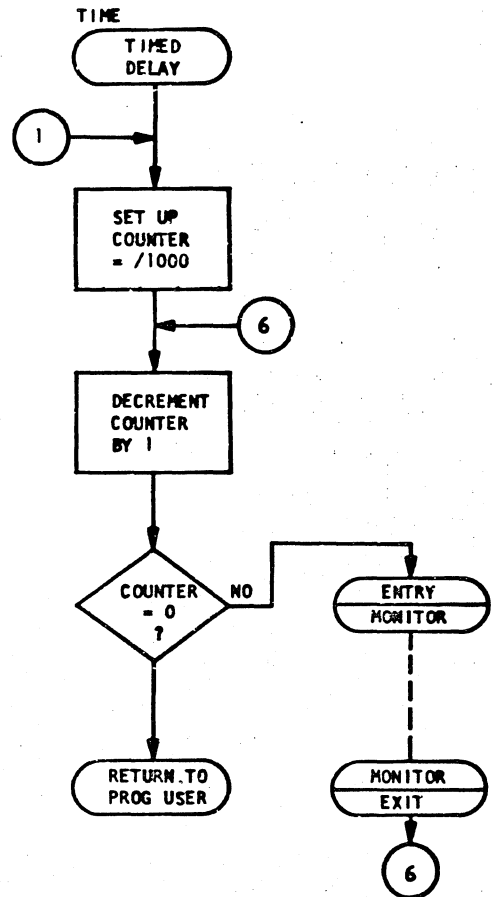
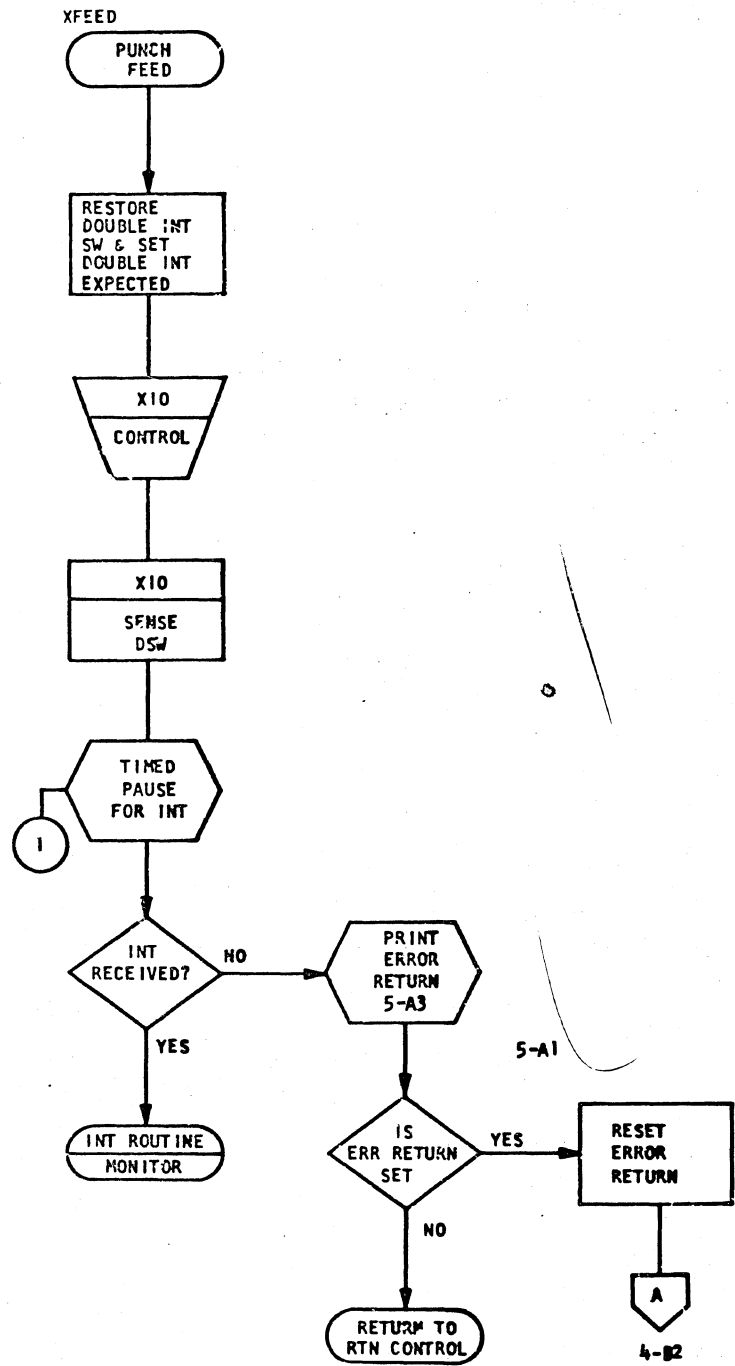


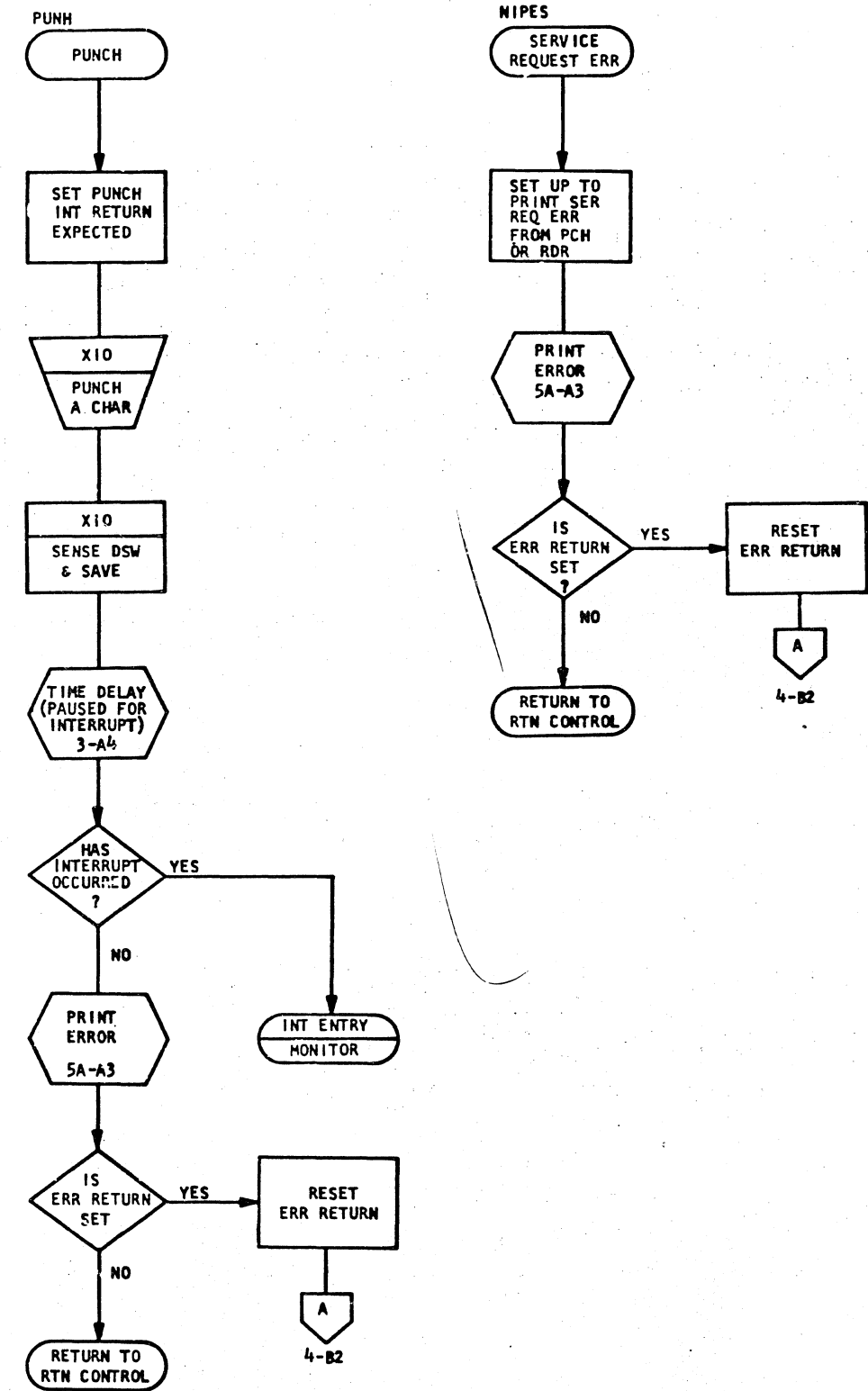
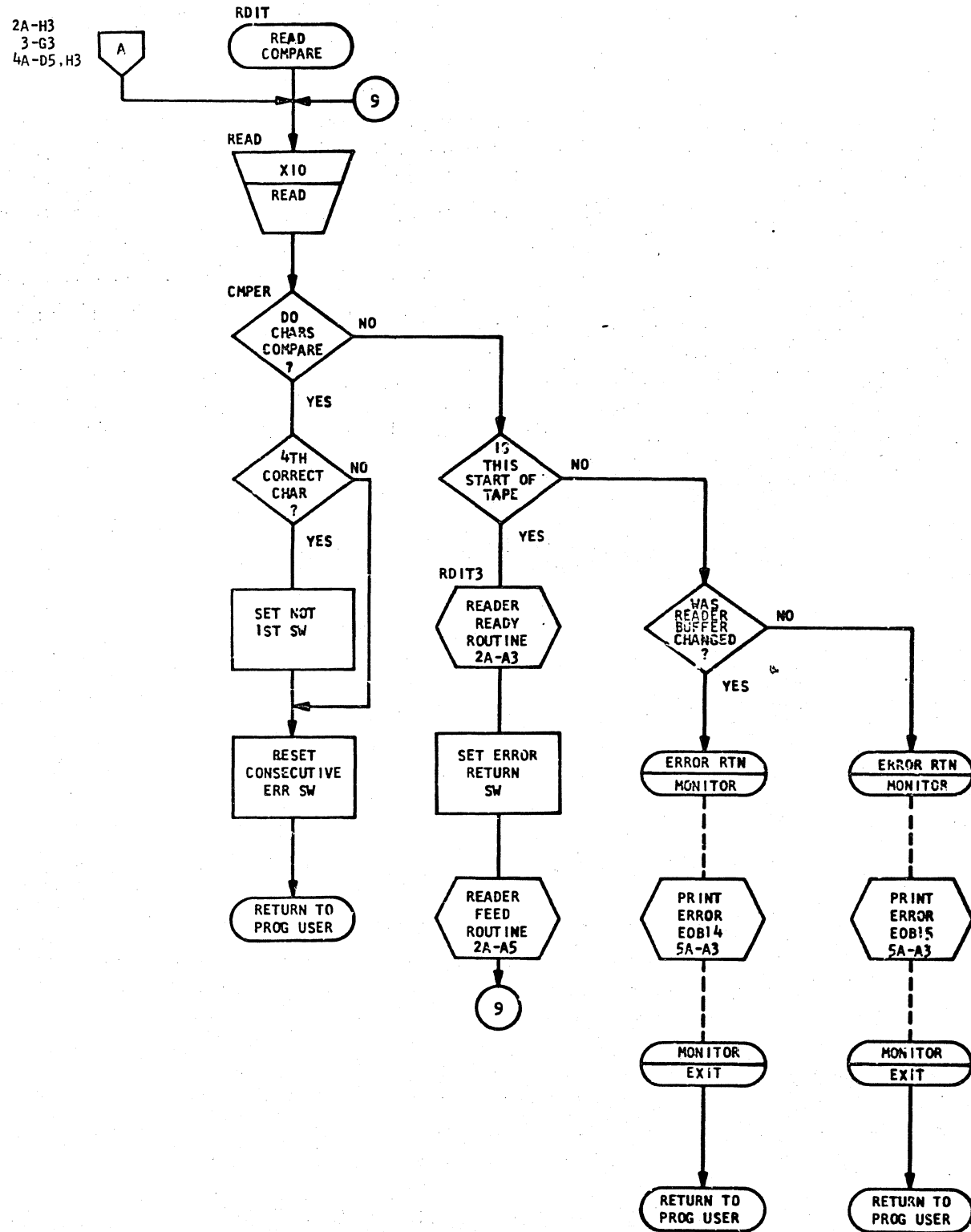


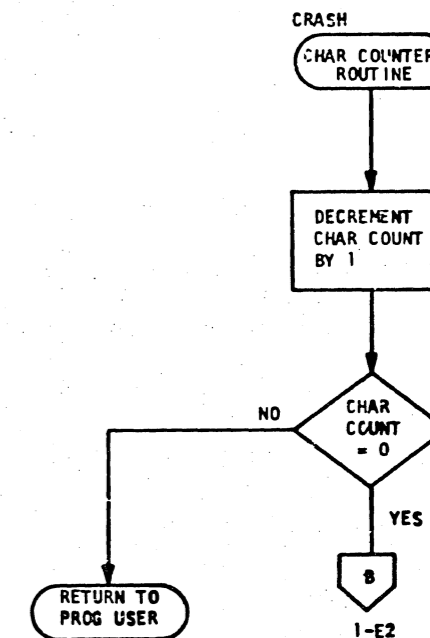
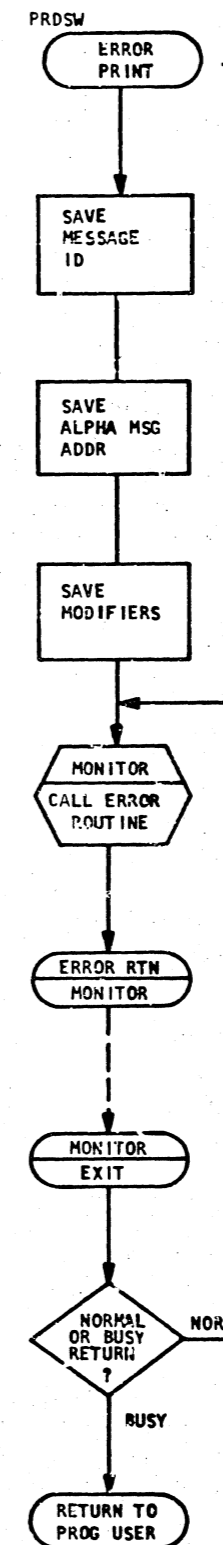
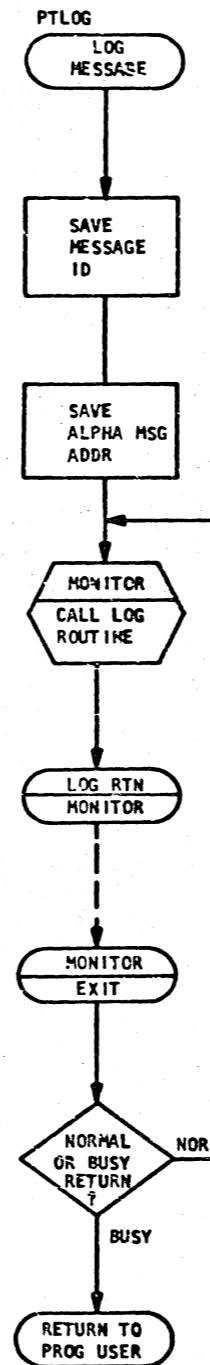
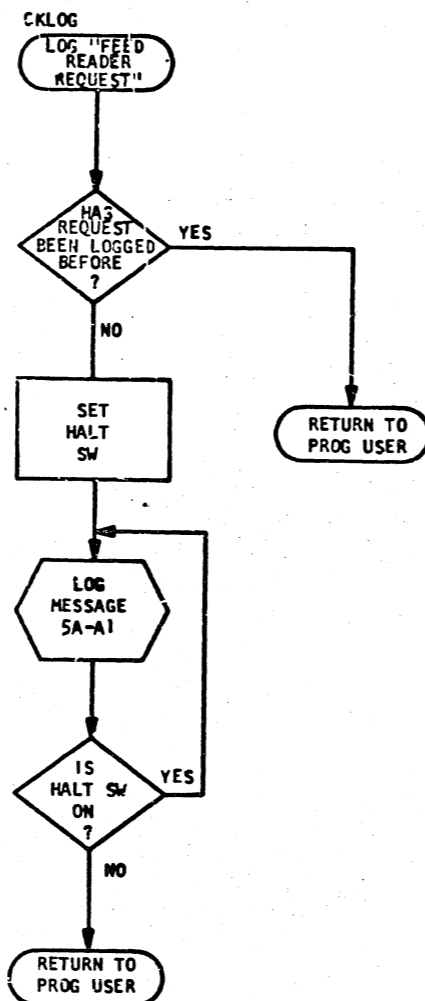
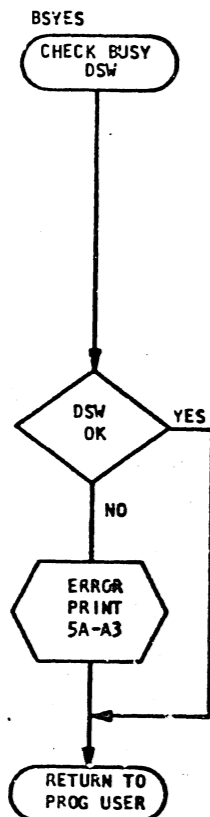
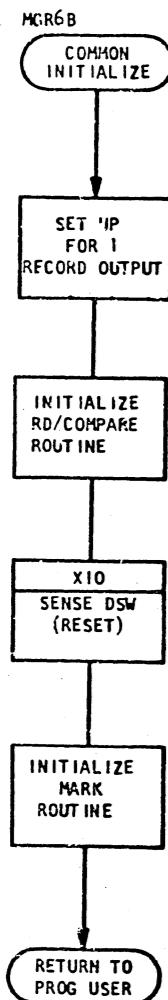


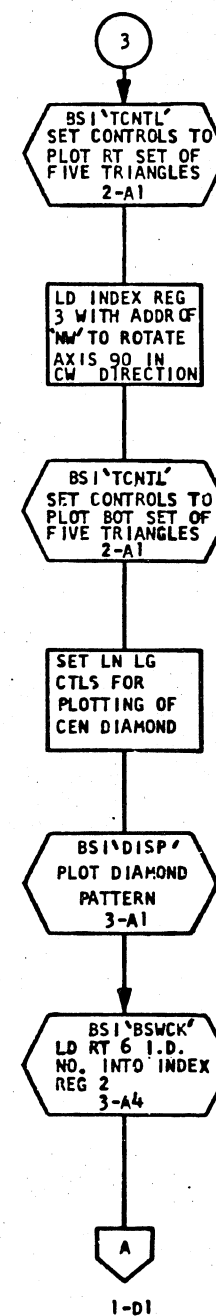
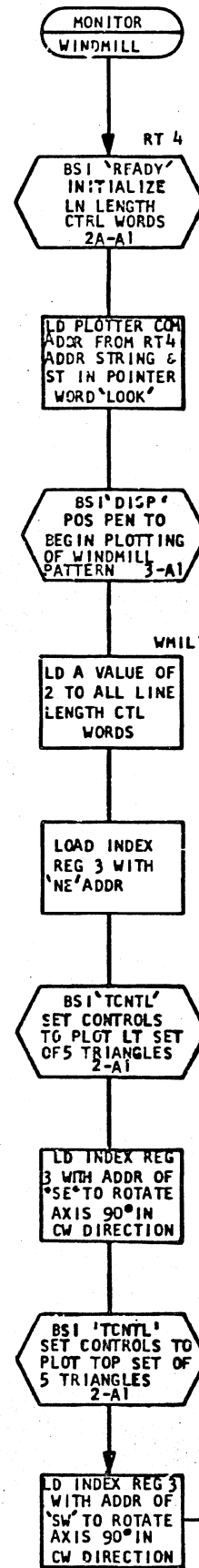
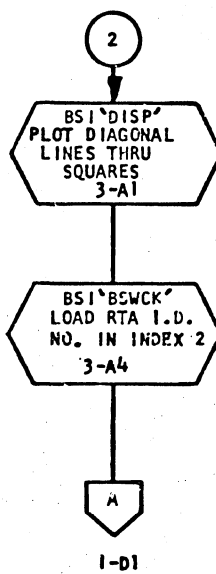
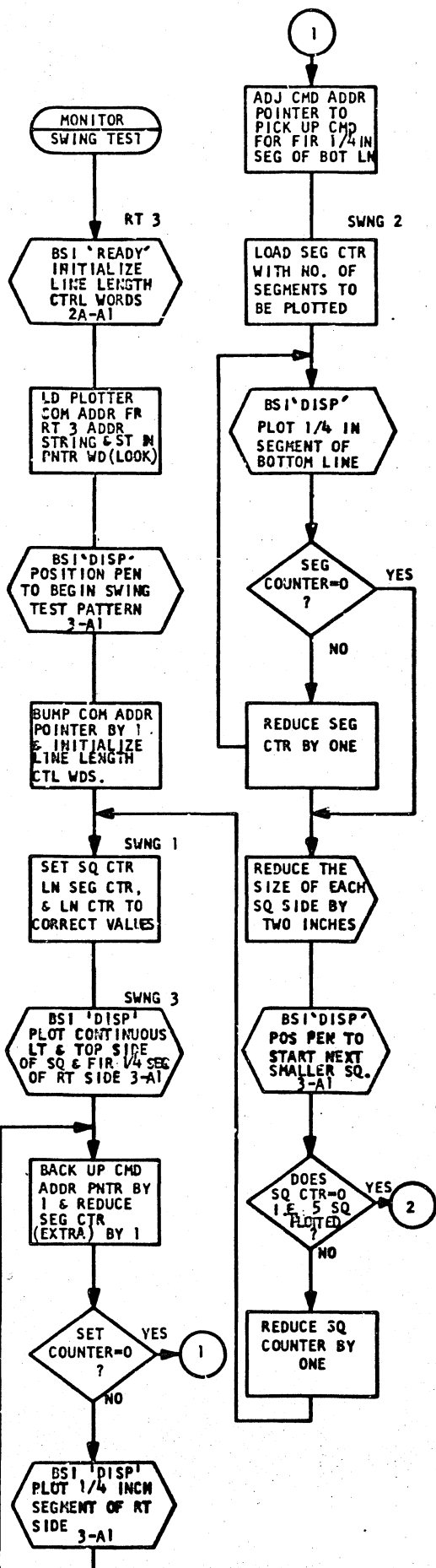
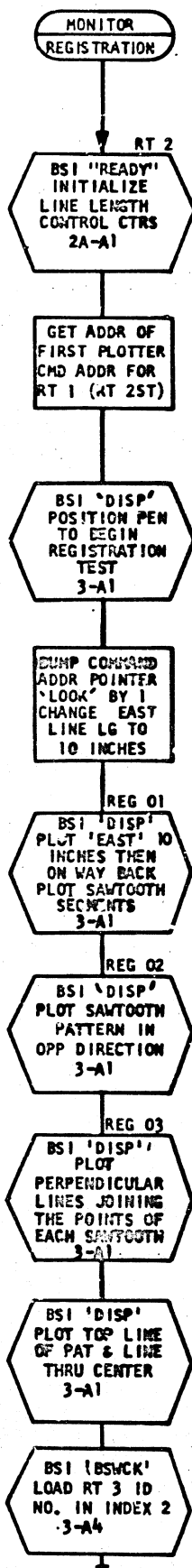
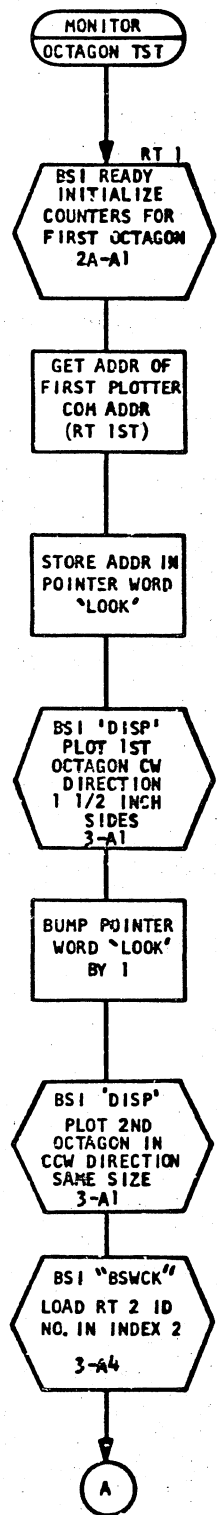
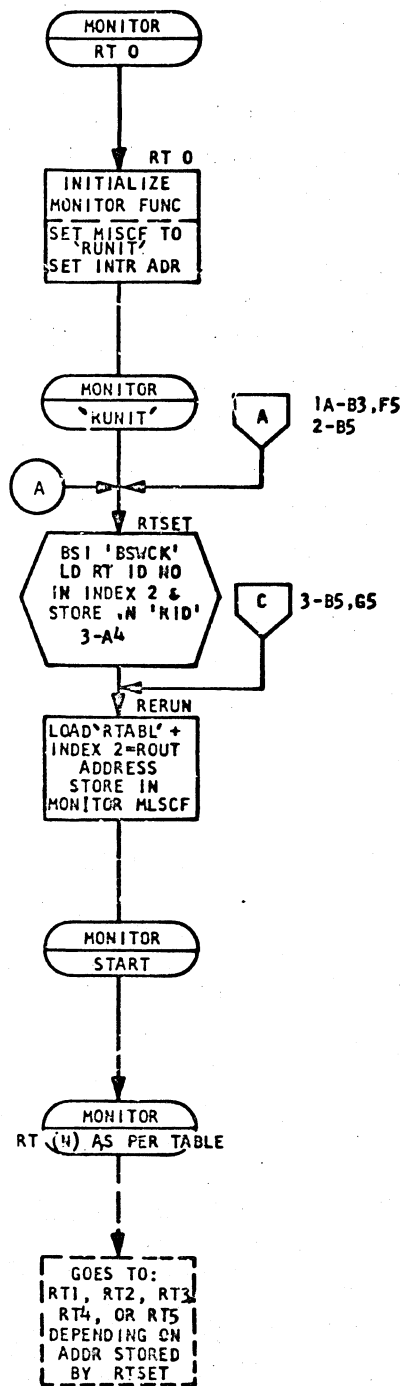


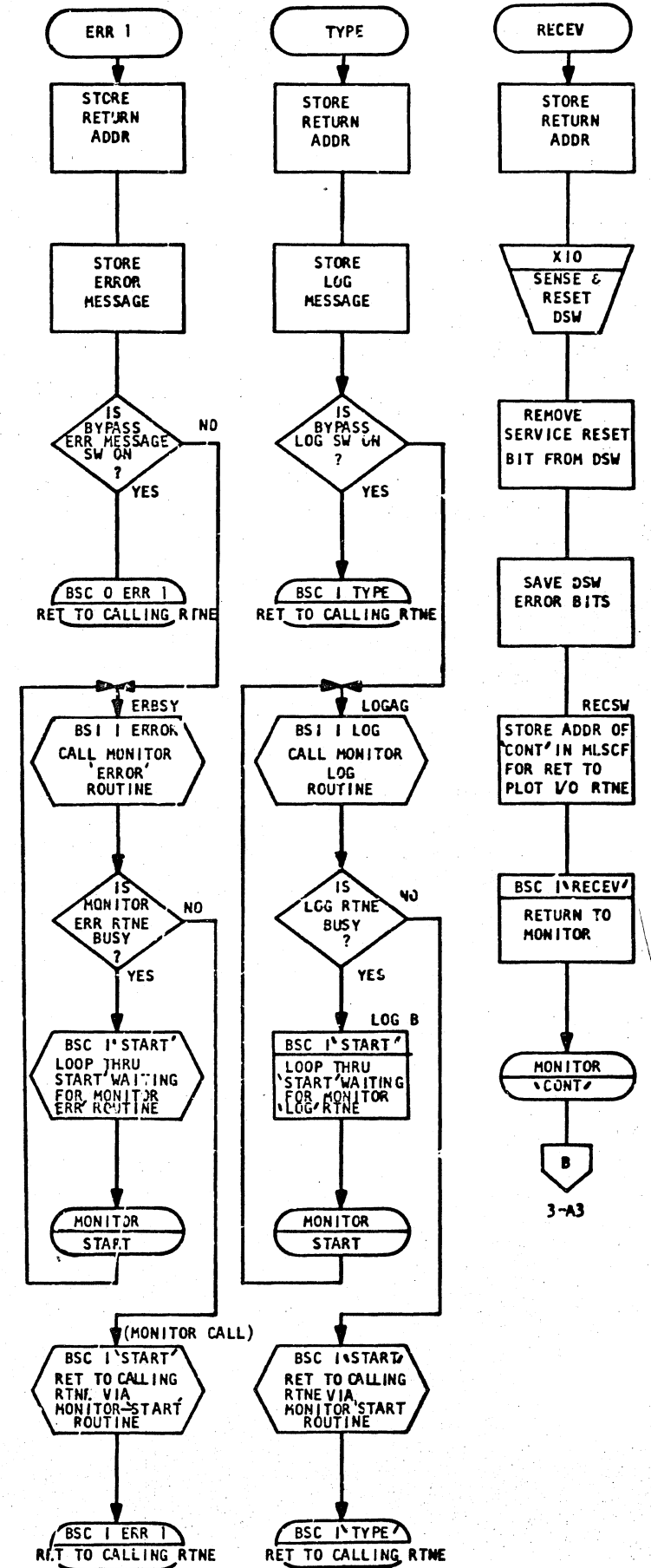
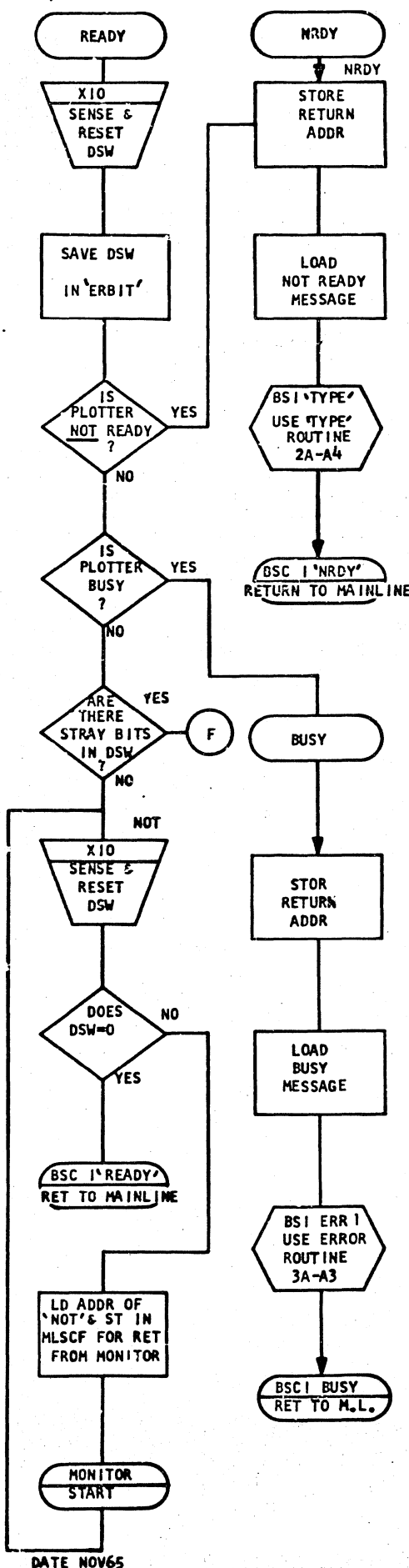
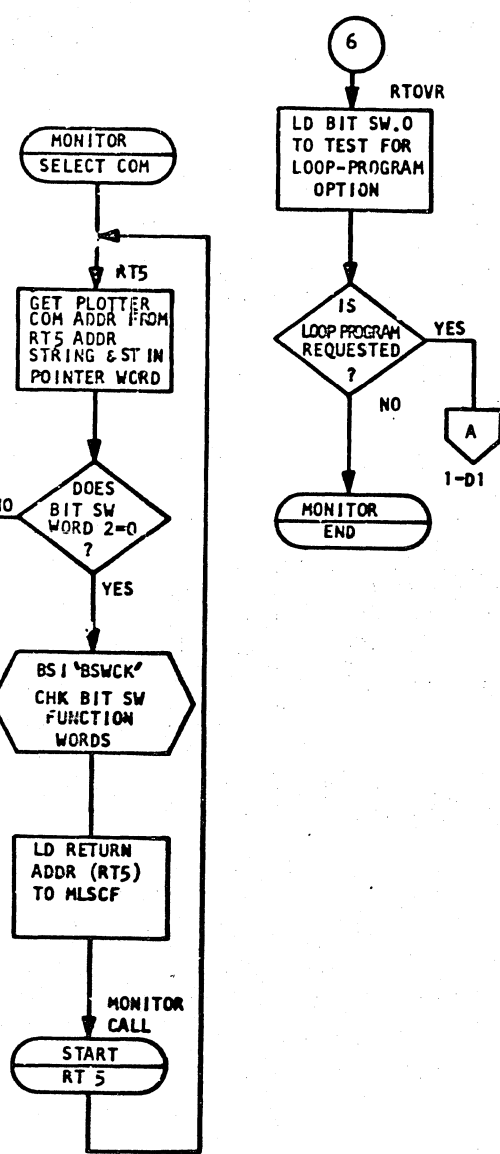
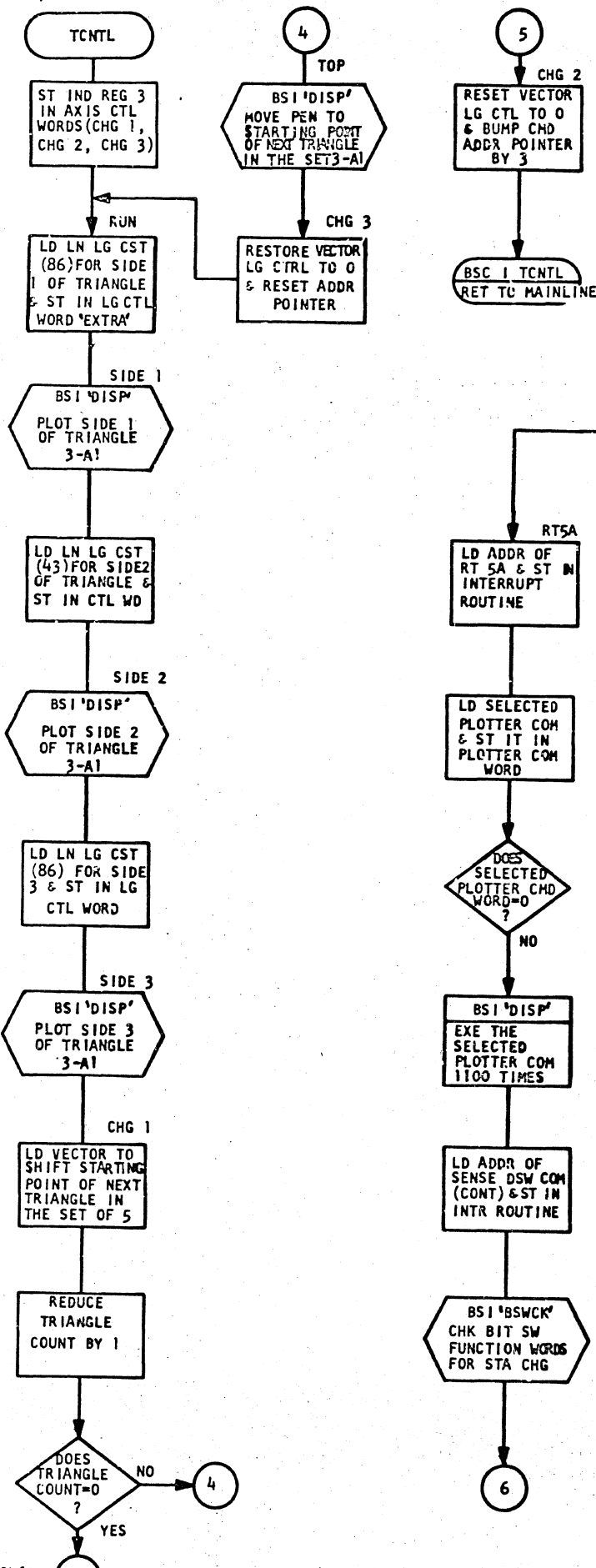




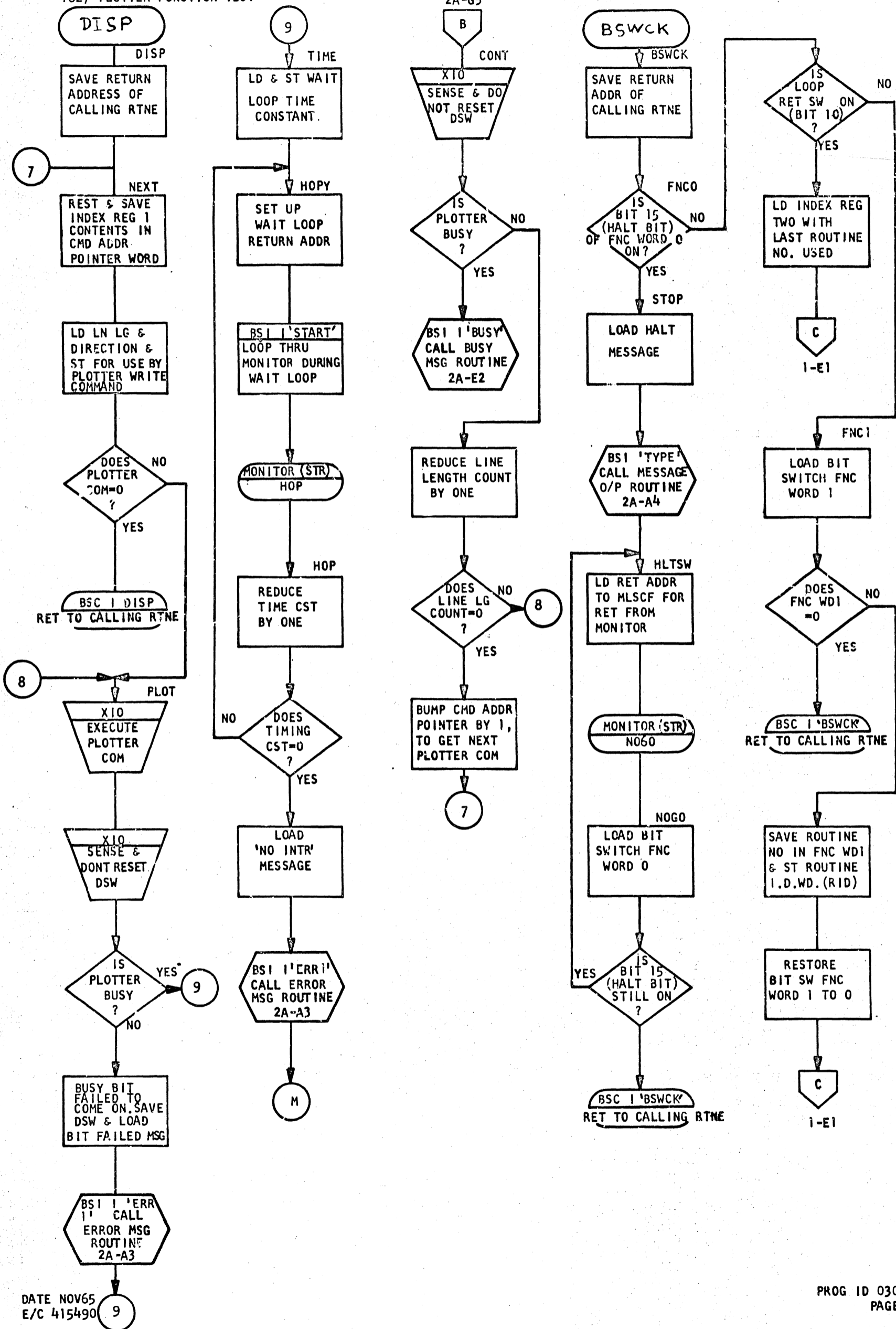


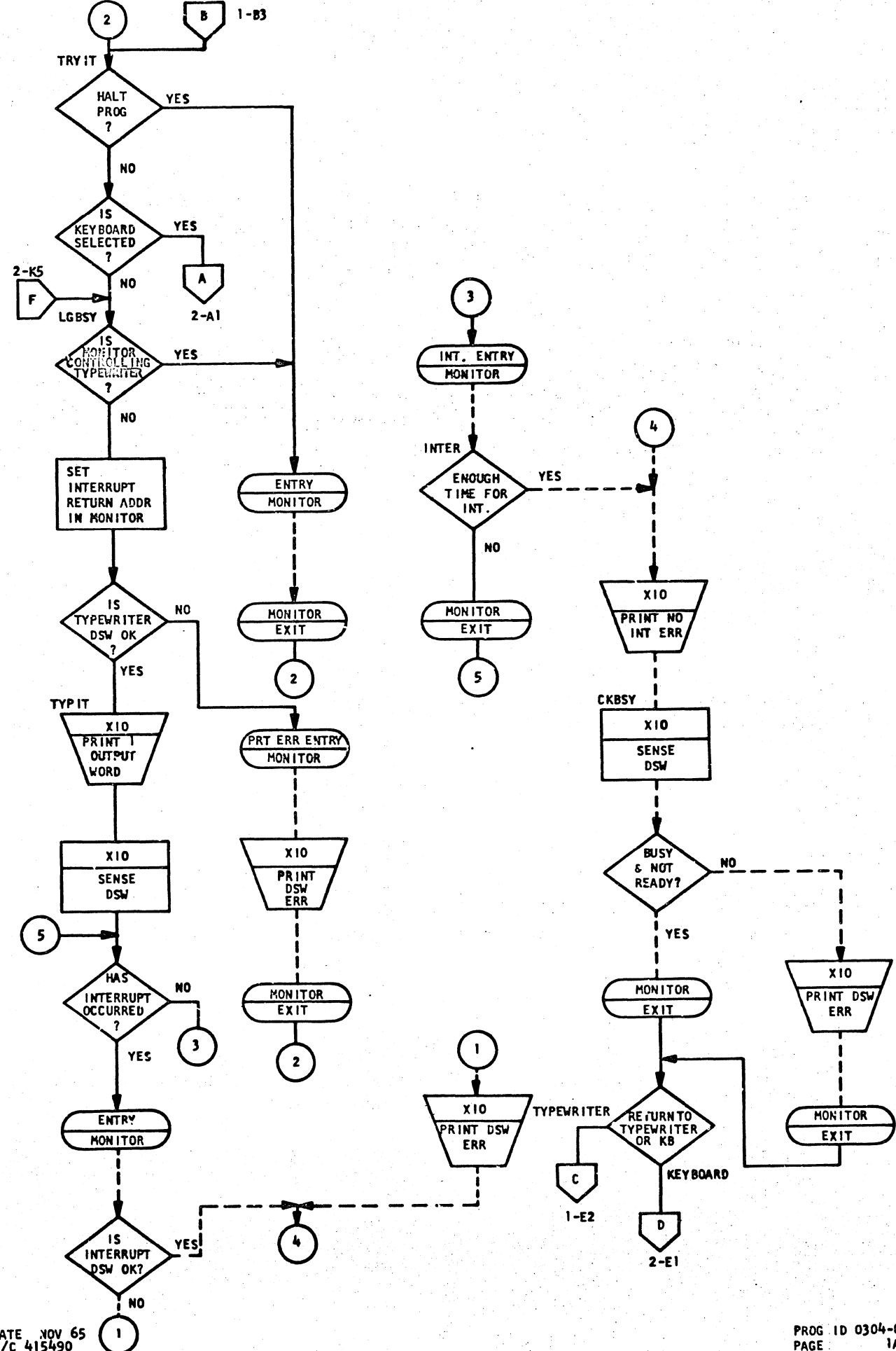
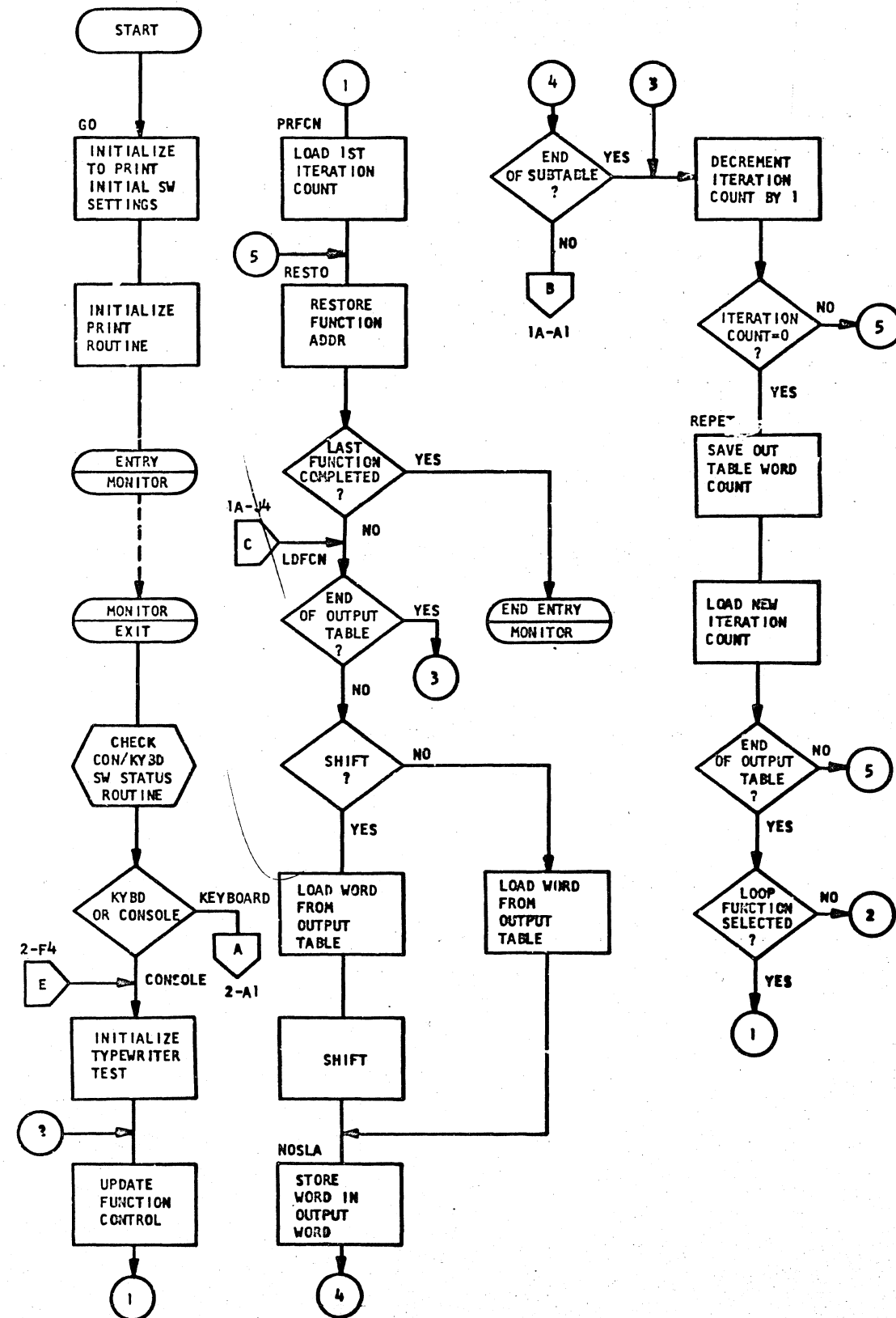


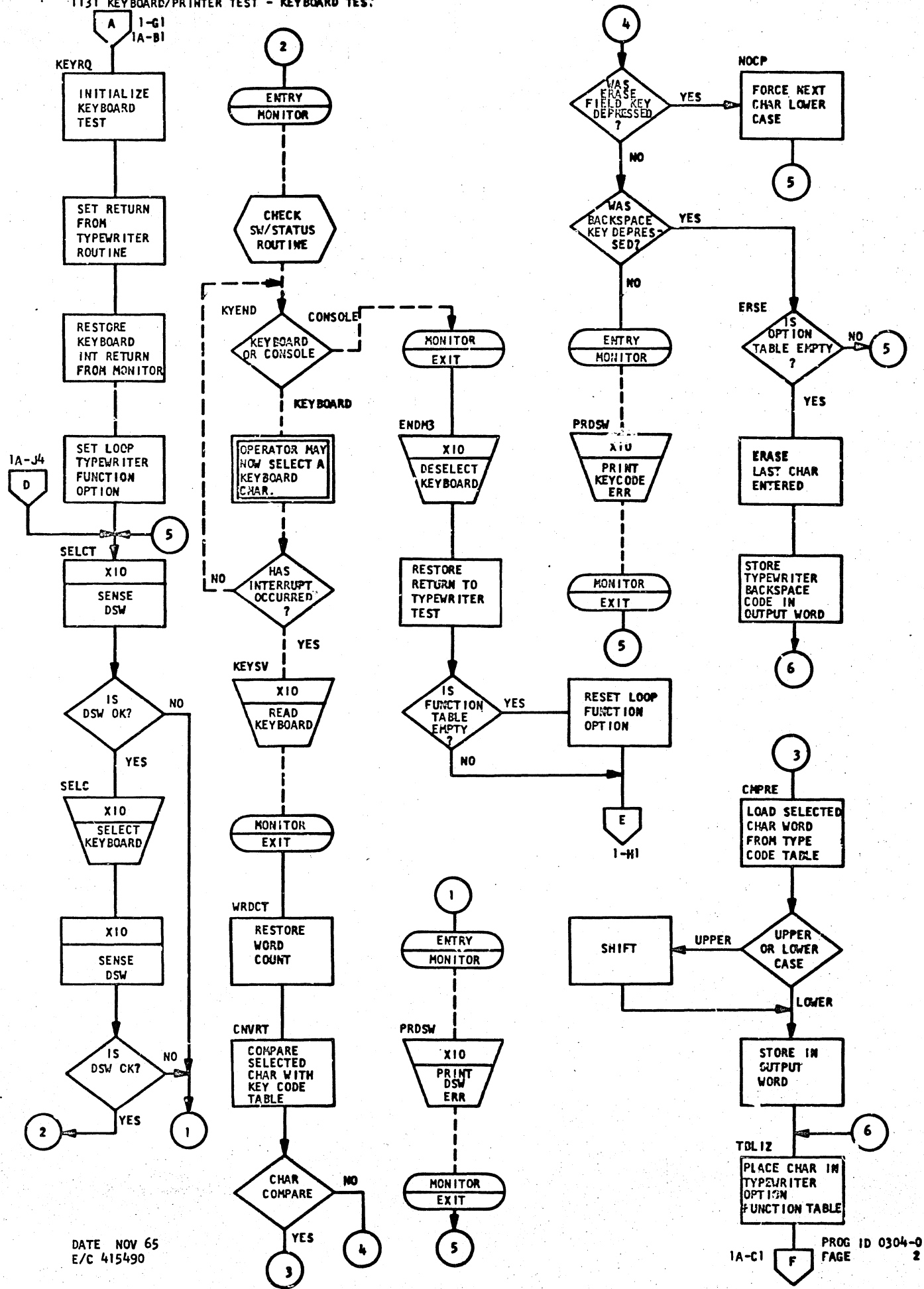




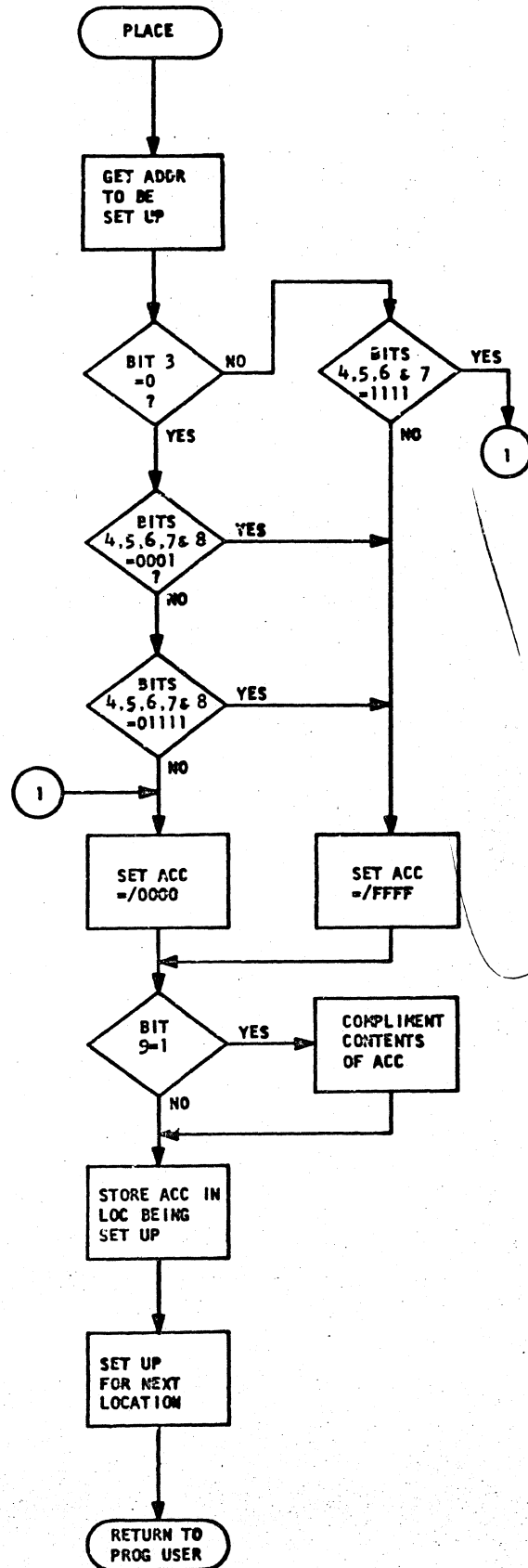
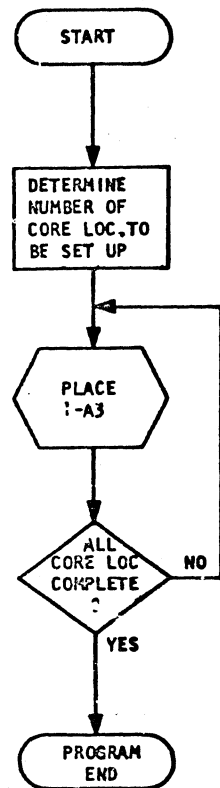
1627 PLOTTER FUNCTION TEST



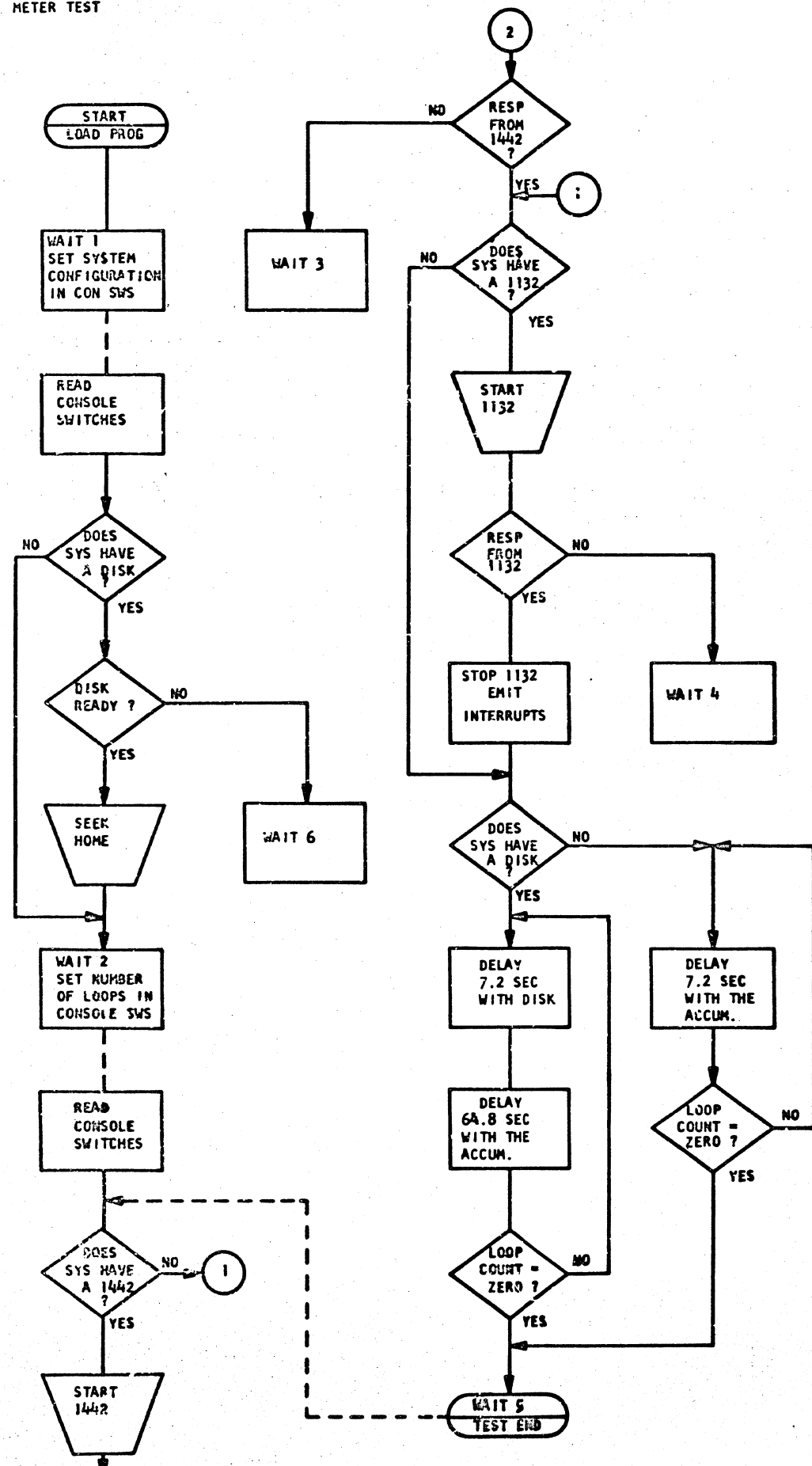


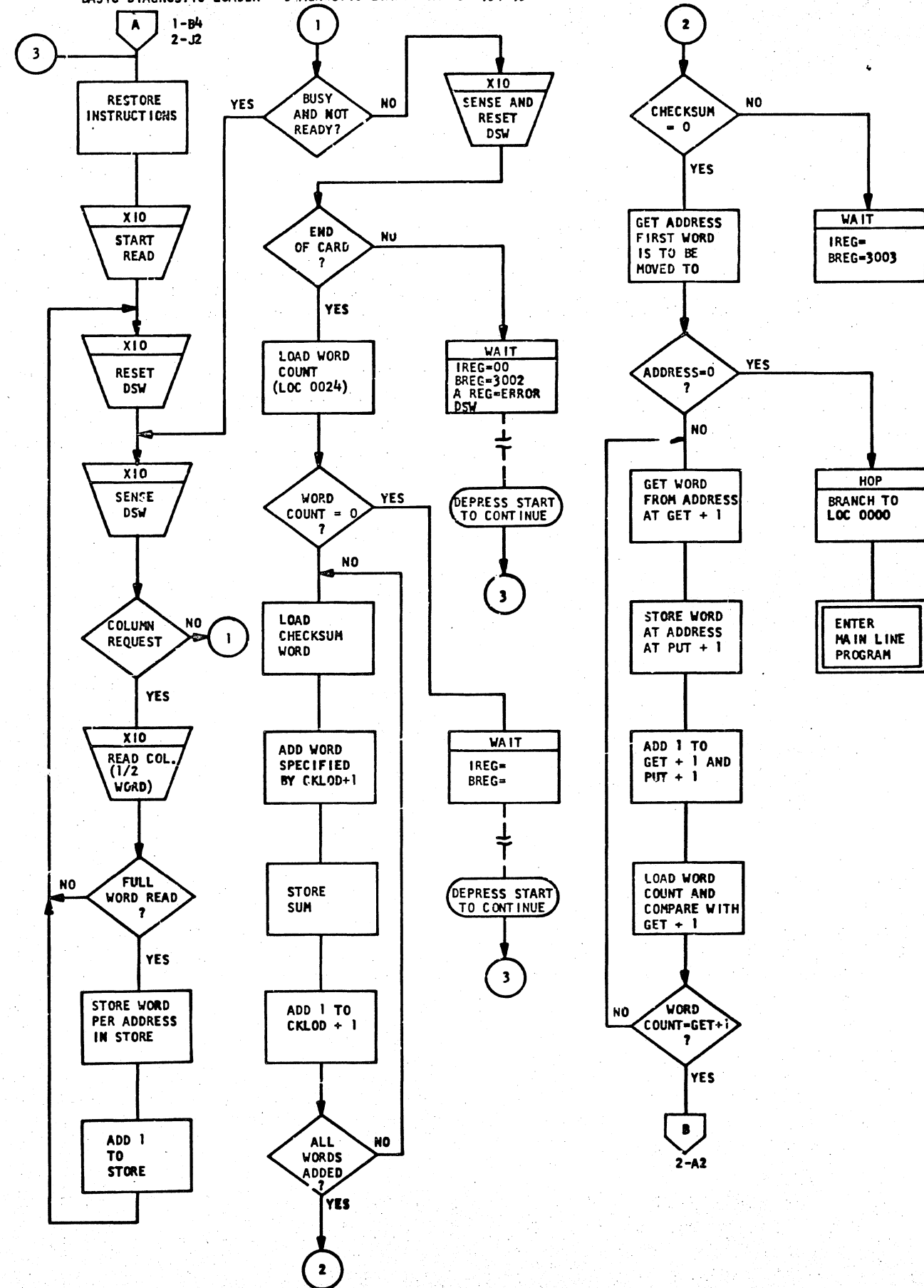
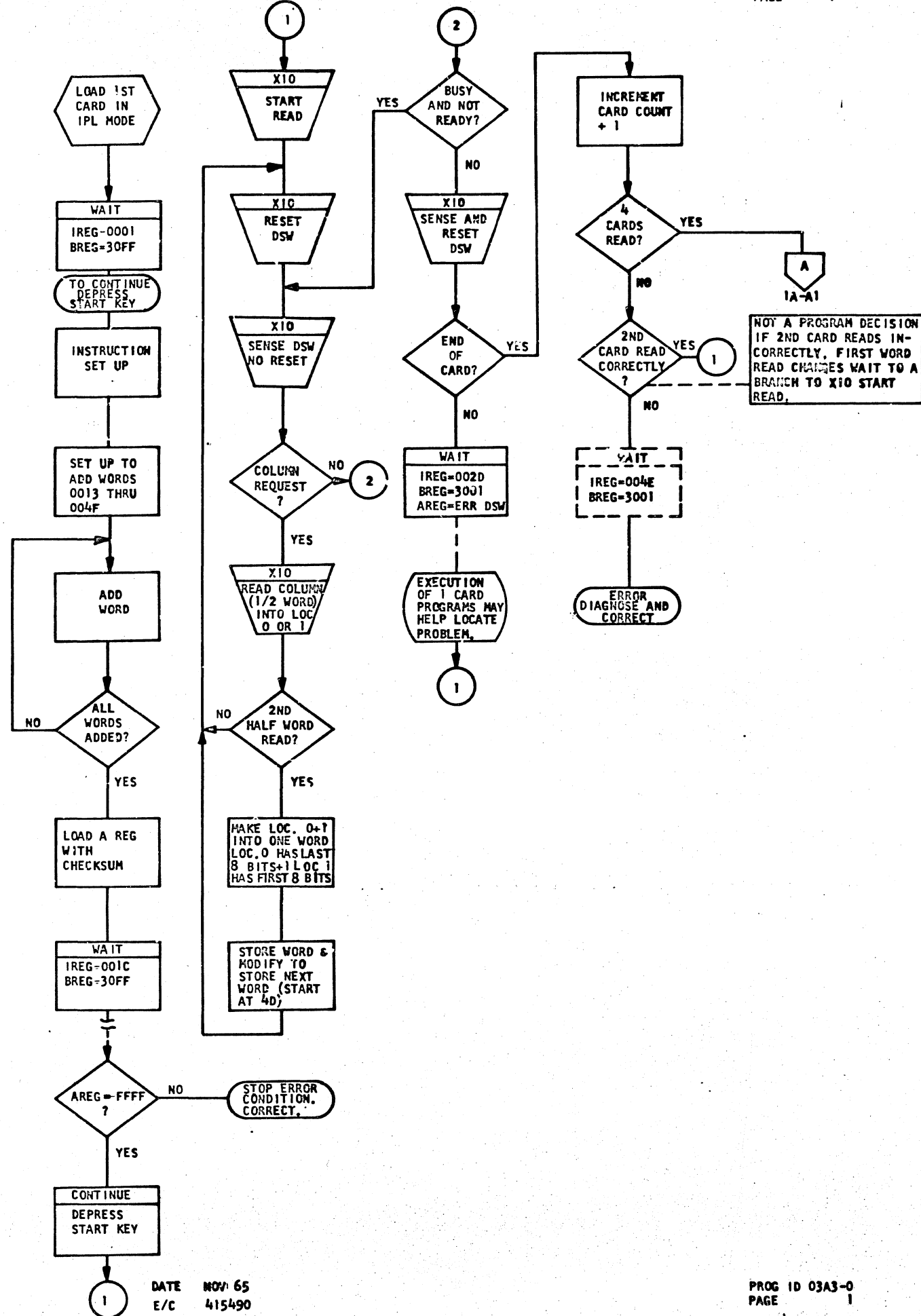


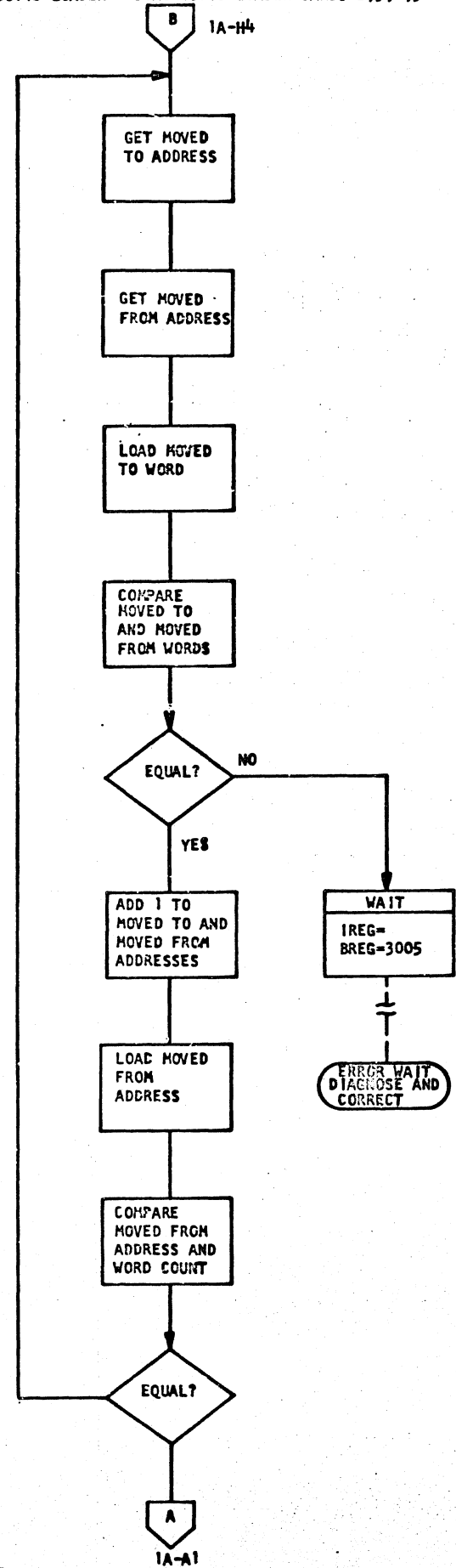
CORE ADJUSTMENT

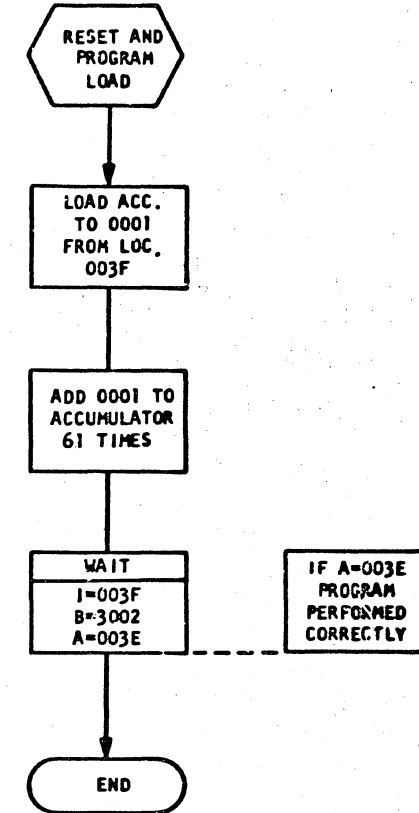
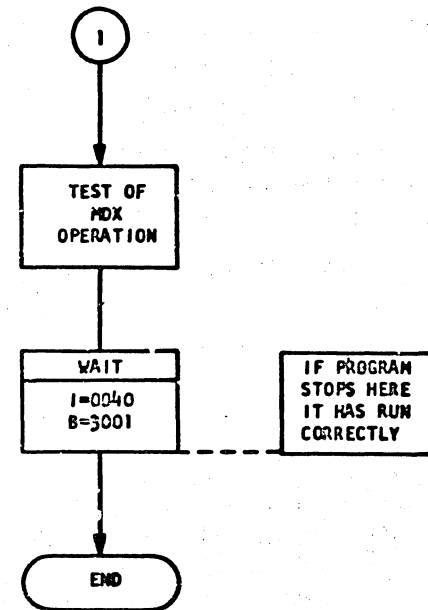
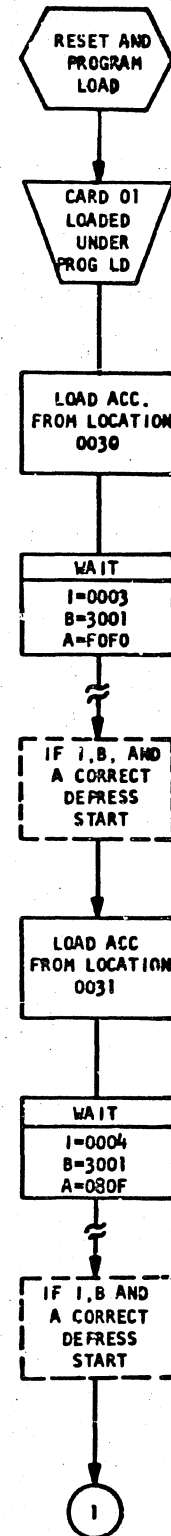


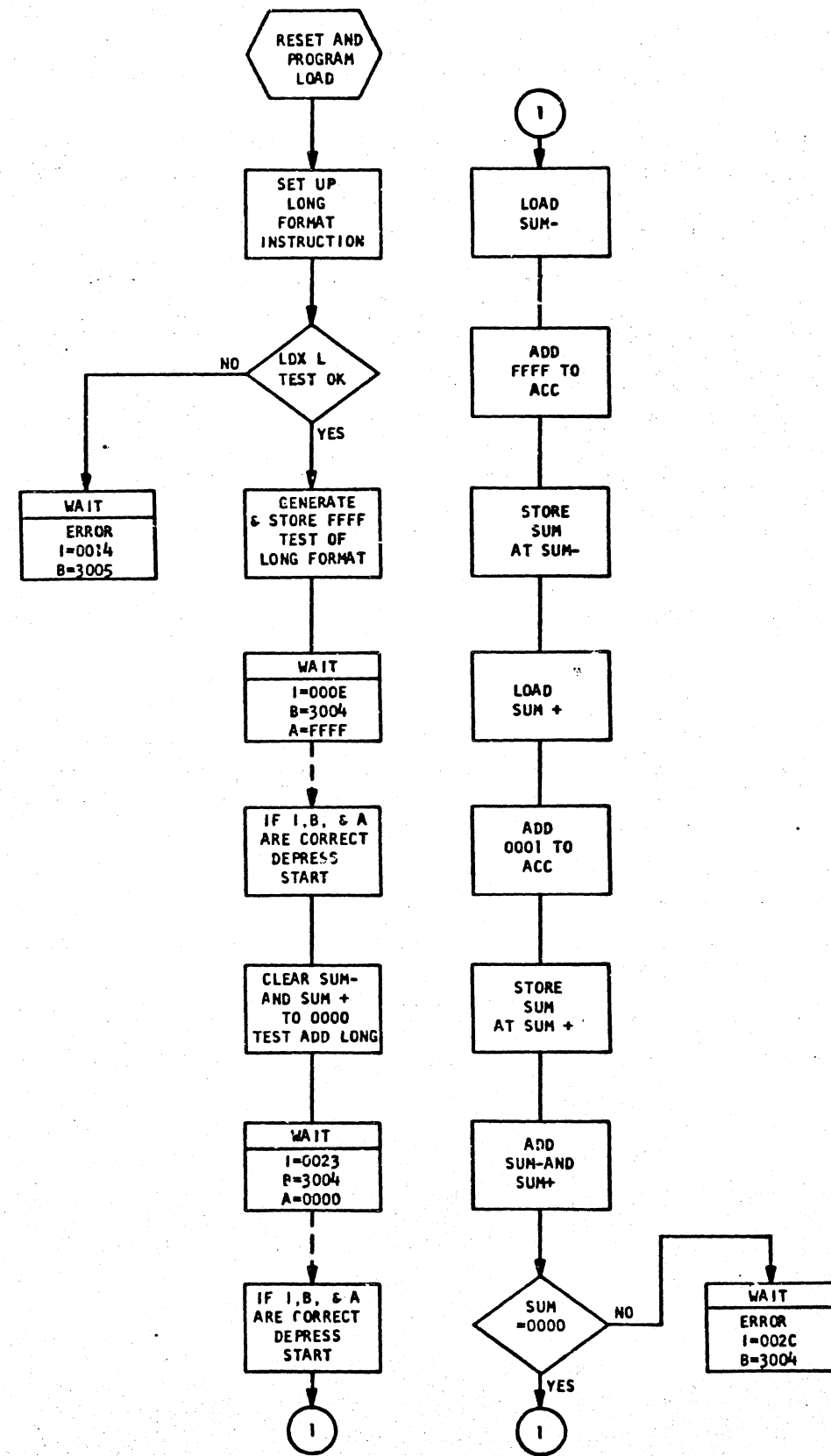
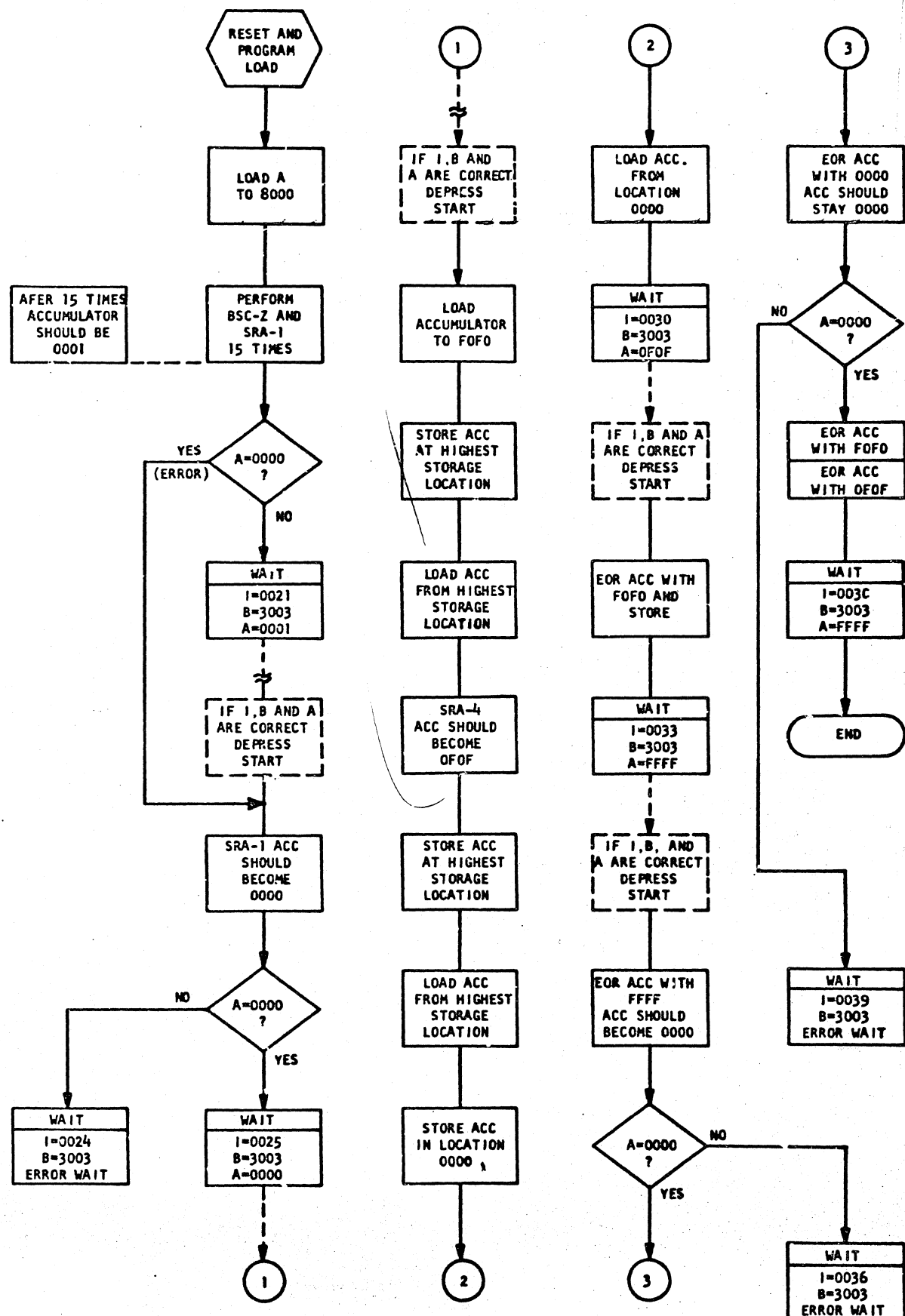
METER TEST

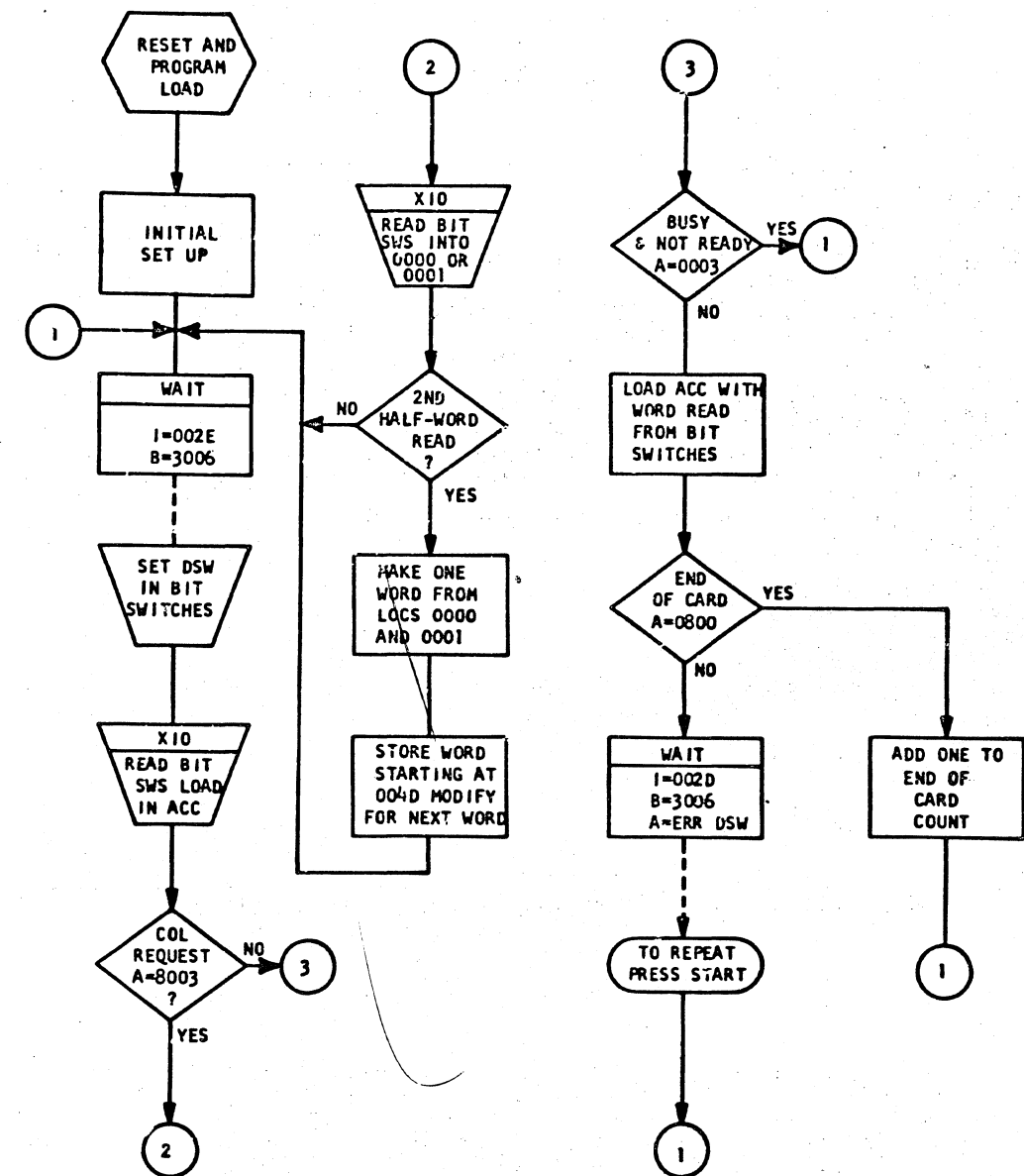
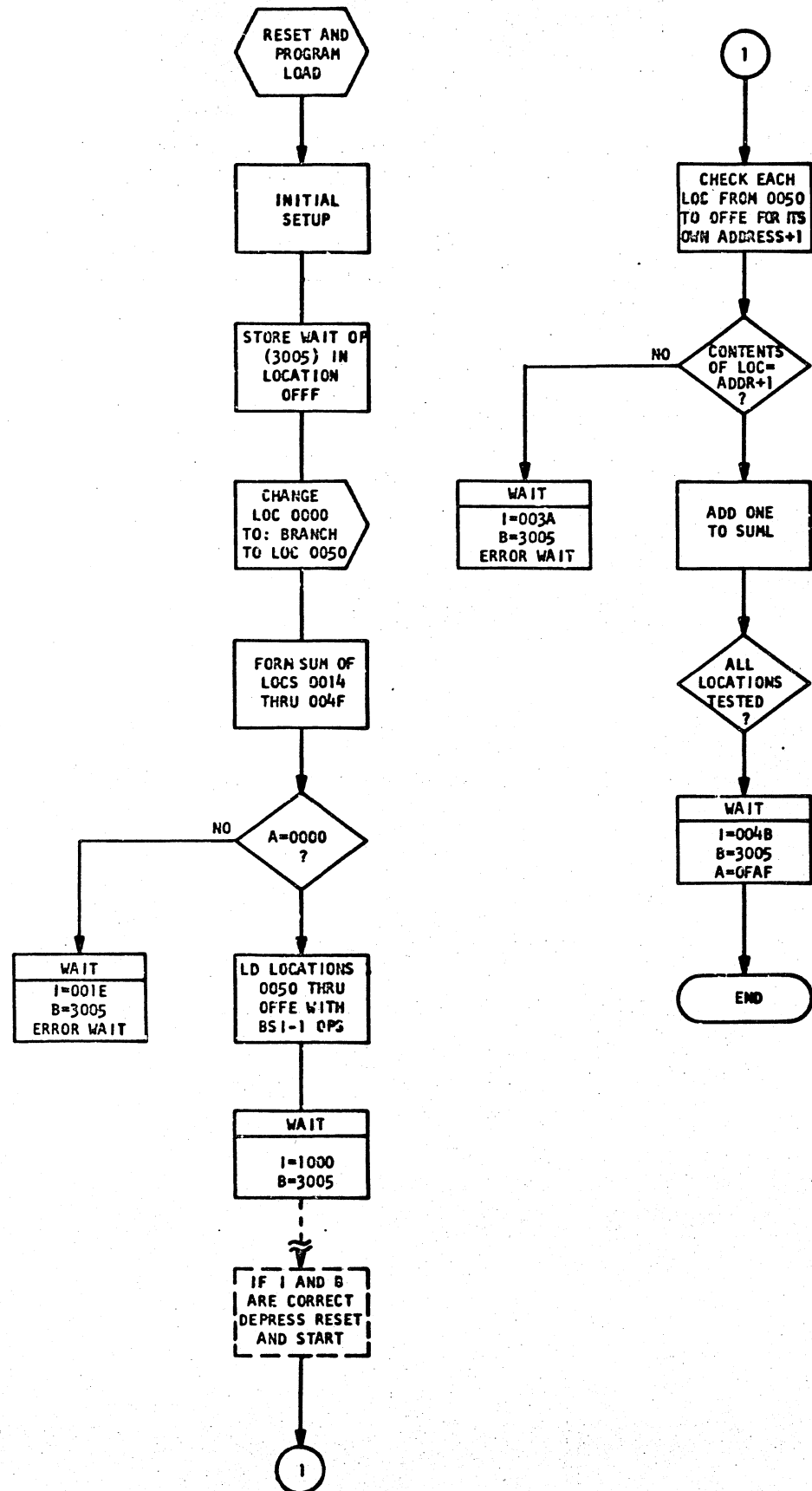


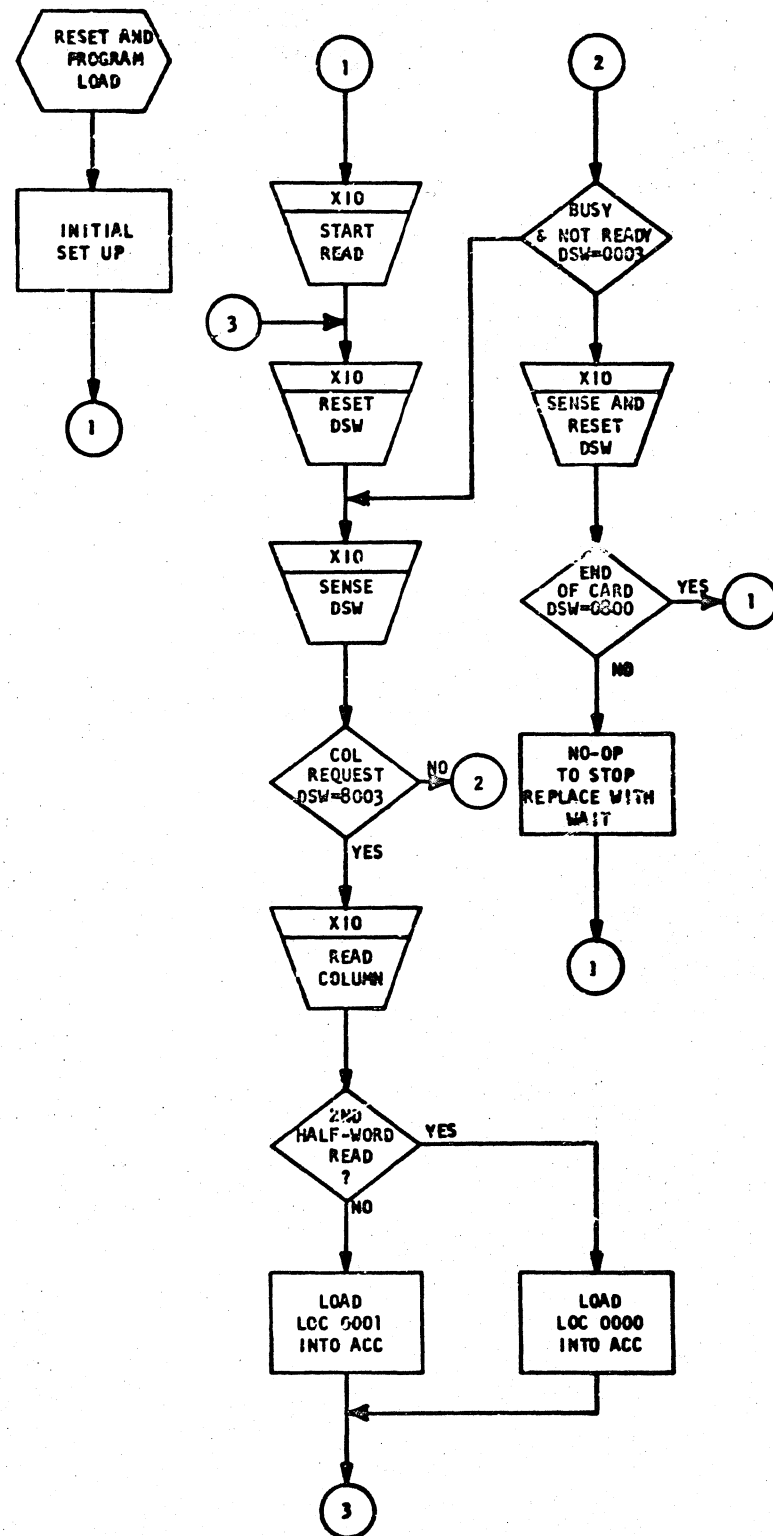




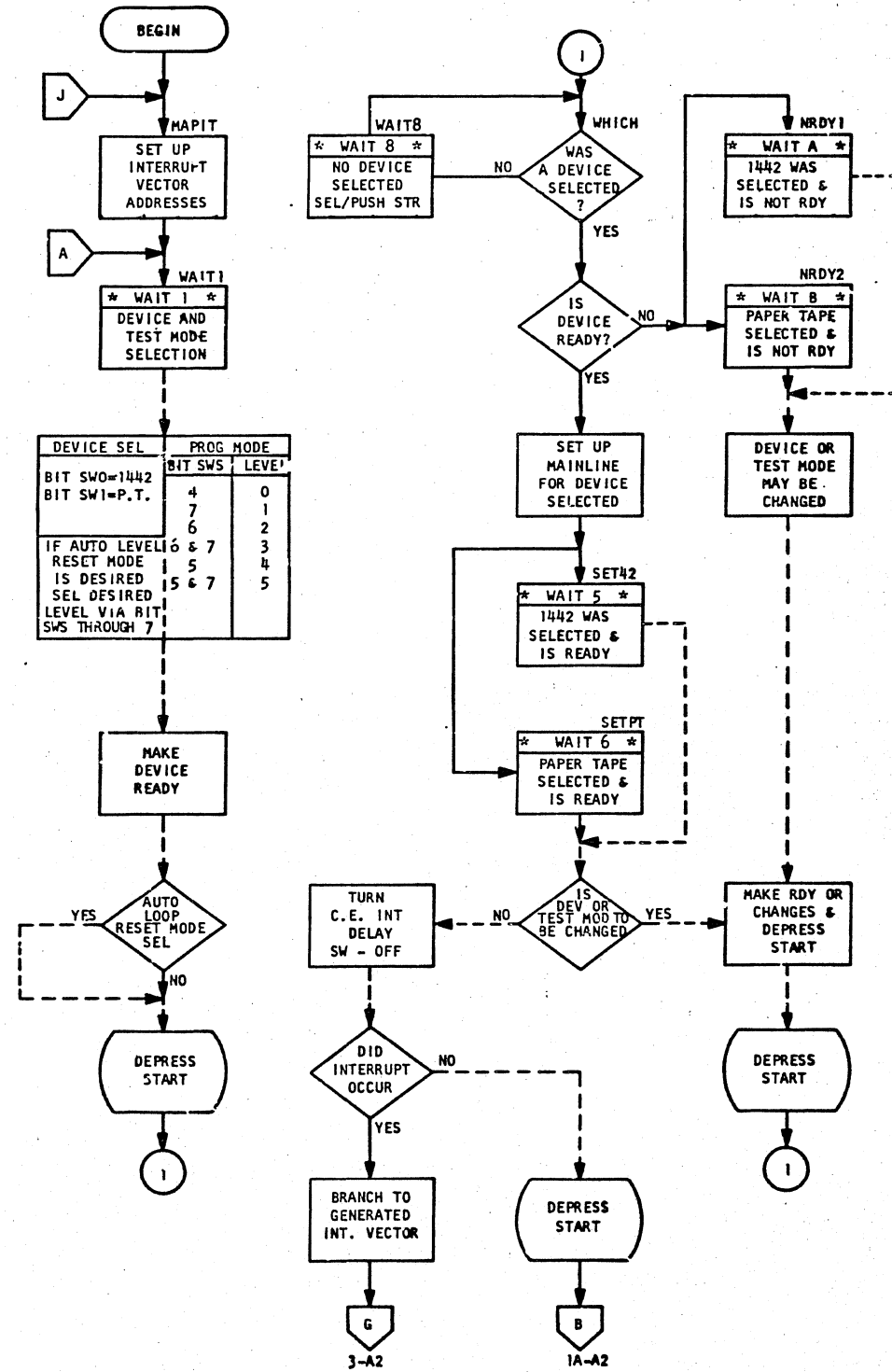




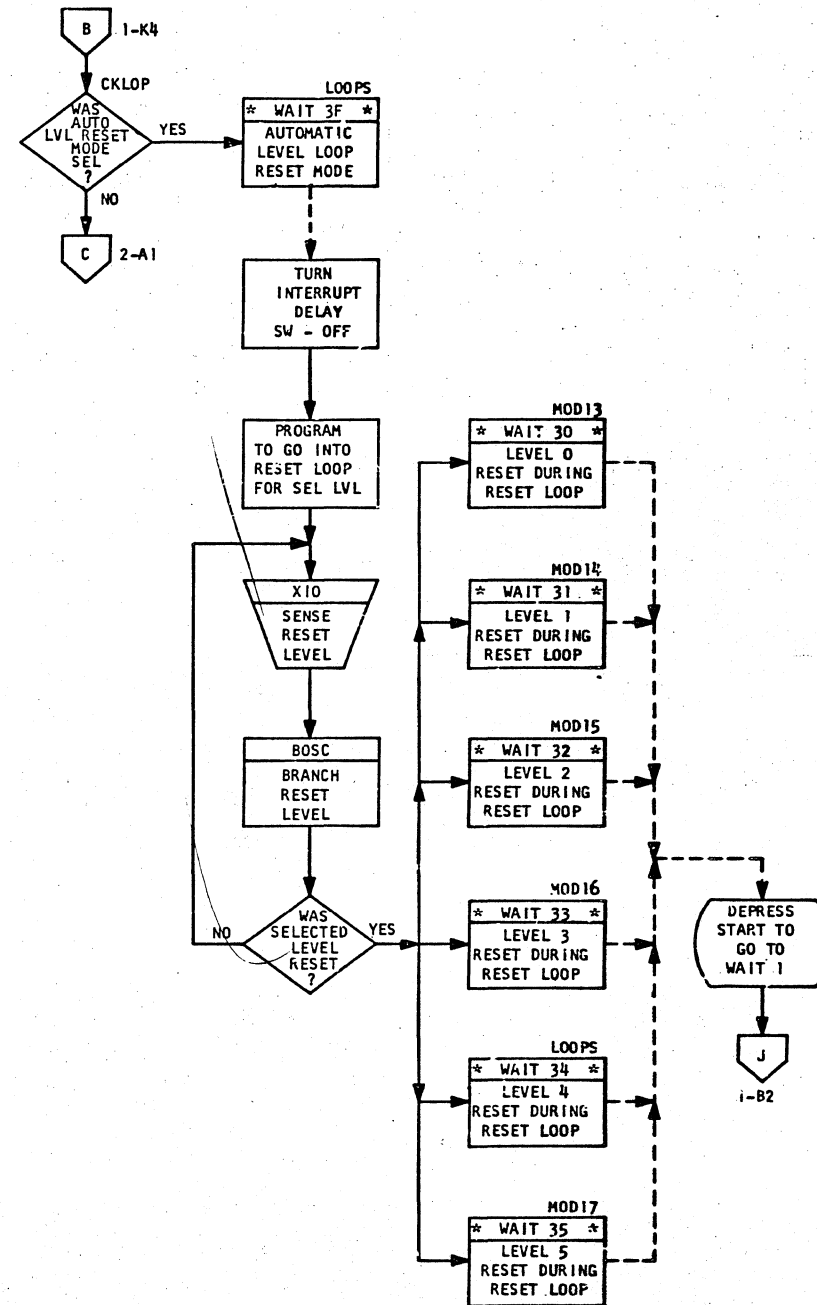




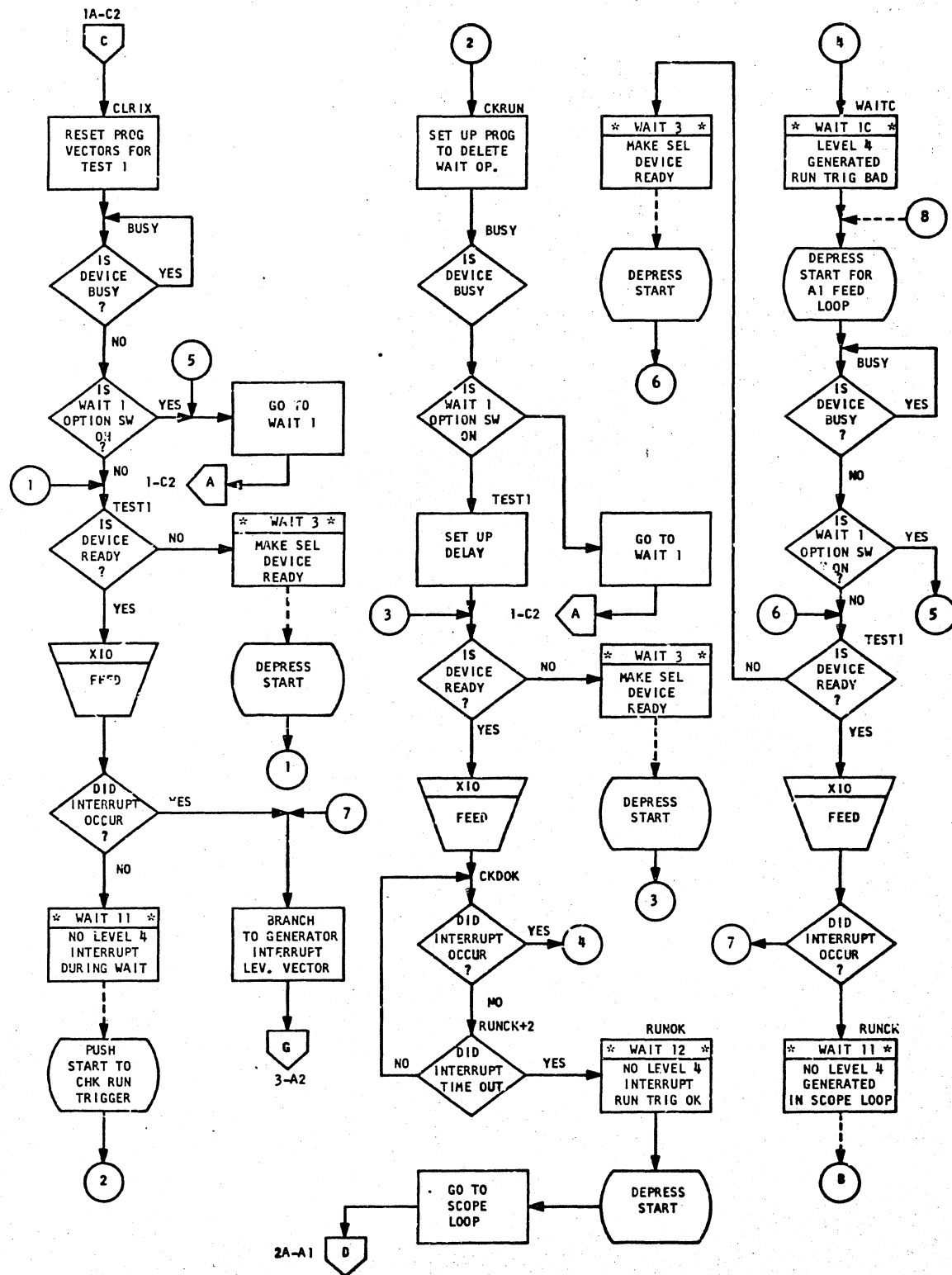
DEVICE & PROGRAM MODE SELECT



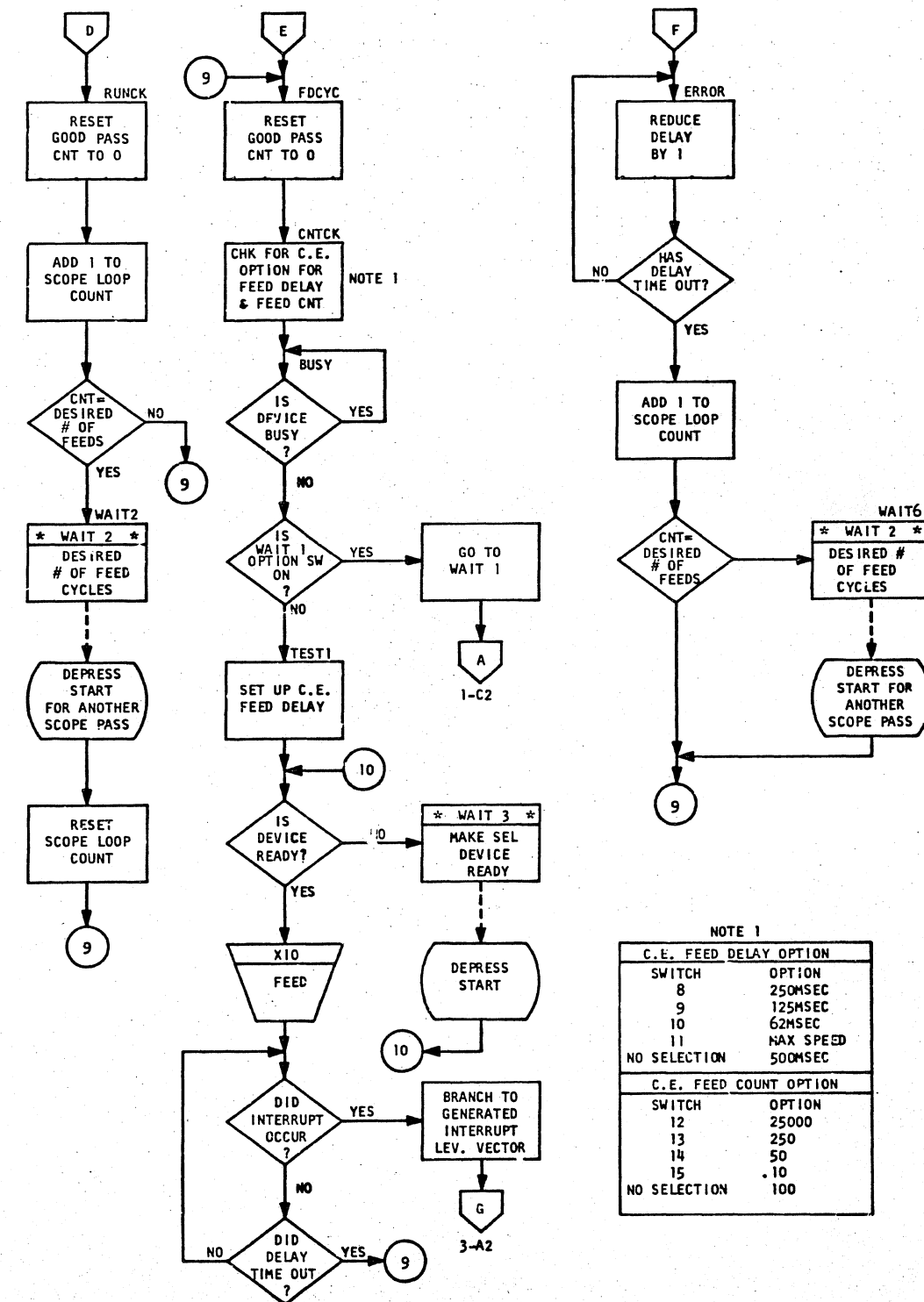
AUTOMATIC LEVEL RESET MODE



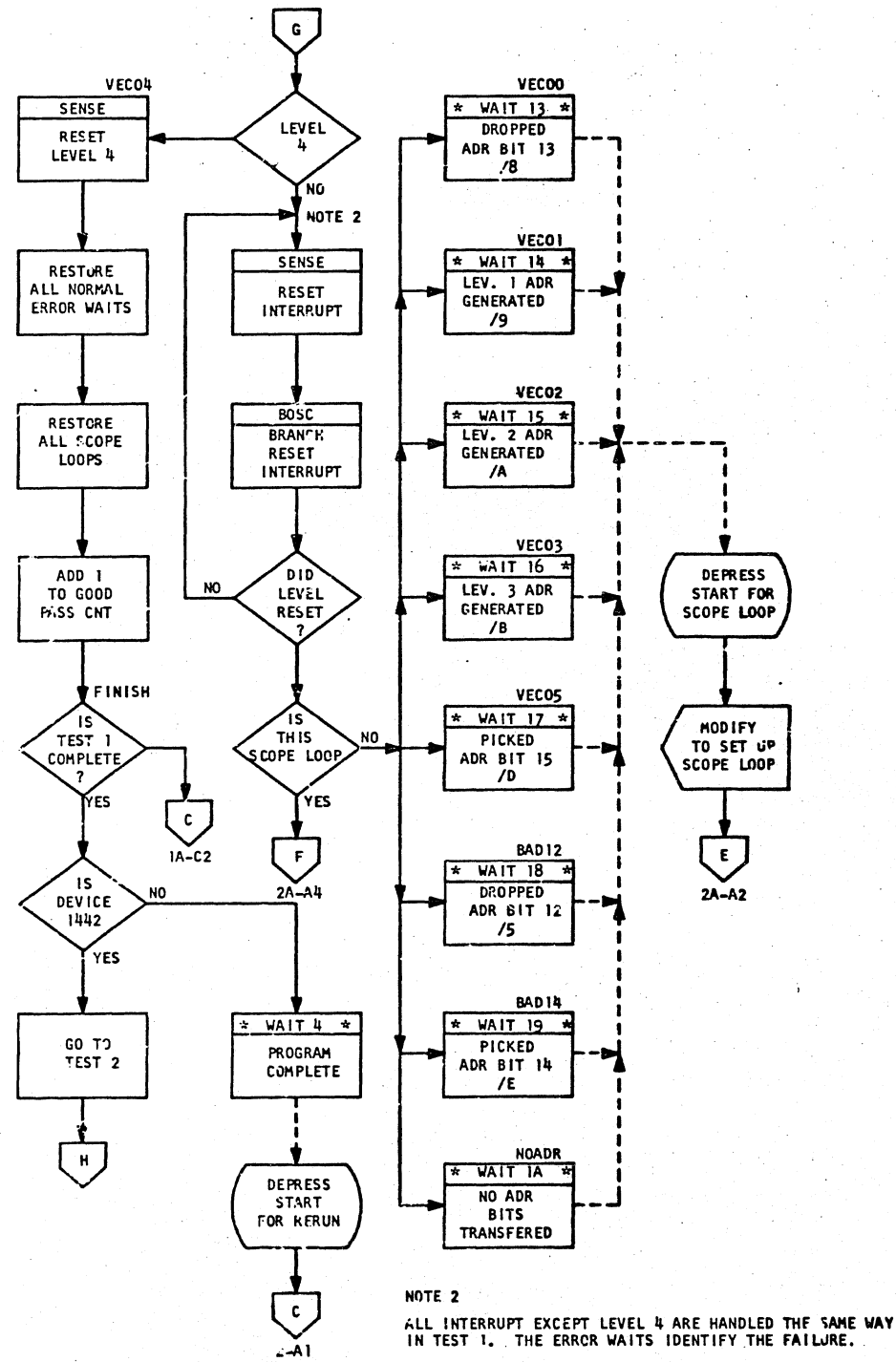
TEST 1 MAINLINE & NO INTERRUPT ROUTINE



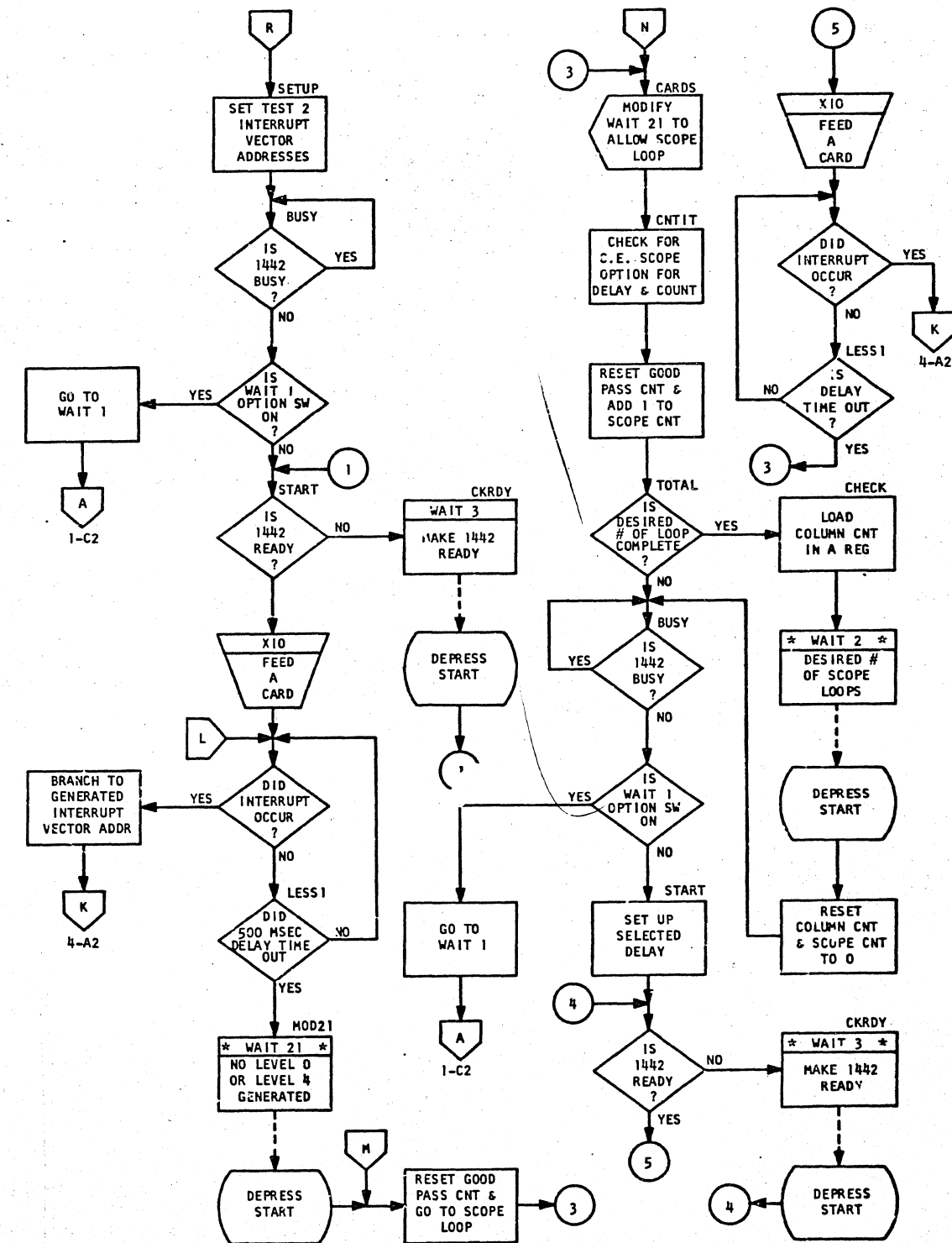
TEST 1 SCOPE LOOP ROUTINE



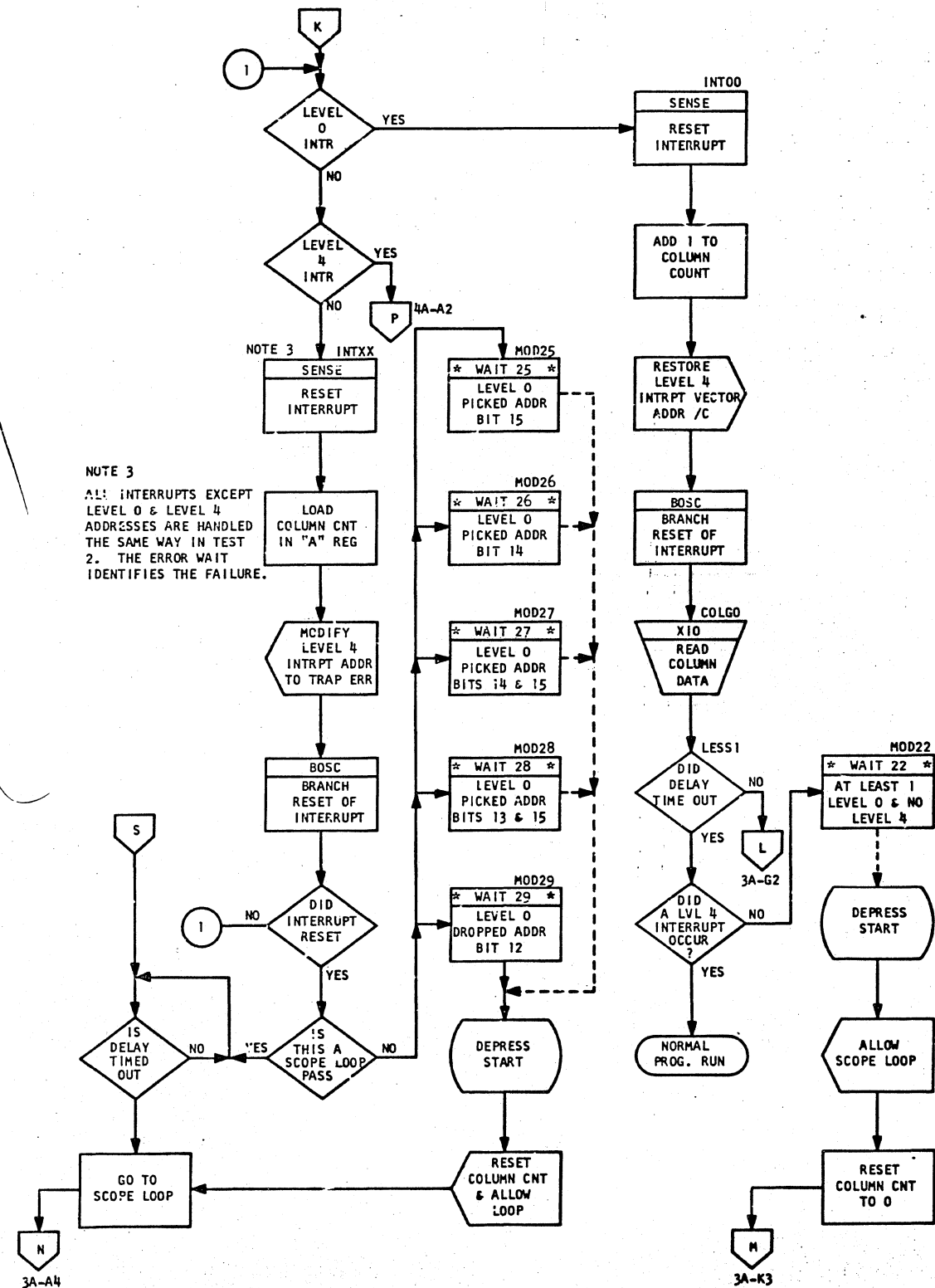
TEST 1 INTERRUPT ROUTINES



TEST 2 MAINLINE SCOPE LOOP, & NO INTERRUPT ROUTINE



INTERRUPT ROUTINES



NOTE 3
ALL INTERRUPTS EXCEPT LEVEL 0 & LEVEL 4 ADDRESSES ARE HANDLED THE SAME WAY IN TEST 2. THE ERROR WAIT IDENTIFIES THE FAILURE.

INTERRUPT 4 & 80 COLUMN CHECK ROUTINES

