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000001      TITLE DCMC2, *REV A*      CLA TEST (SAF)
000002      *
000003      *
000004      * DESCRIPTION:
000005      *
000006      * THIS T&V PROGRAM VERIFIES PROPER OPERATION OF THE LEVEL 6 COMMUNICATION LINE
000007      * ADAPTERS, WHICH ARE ATTACHMENTS TO THE DUALLINE COMMUNICATION PTOCESSOR.
000008      *
000009      * THE SUBSYSTEM ITEMS SUPPORTED BY THIS PROGRAM ARE:
000010      *
000011      *     DLC9101 DUALLINE COMMUNICATION PRCESSOR
000012      *     DLC9102 DUALLINE COMMUNICATION PRCESSOR
000013      *     DLC9103 DUALLINE COMMUNICATION PRCESSOR
000014      *
000015      * REVISION HISTORY:
000016      *
000017      *     REV     DATE
000018      *     A       FEB 78     ORIGINAL RELEASE
000019      *
000020      *
000021      * *****
000022      * THIS DOCUMENT AND THE INFORMATION CONTAINED
000023      * THEREIN IS CONFIDENTIAL AND PROPRIETARY TO AND THE
000024      * EXCLUSIVE PROPERTY OF HONEYWELL INFORMATION
000025      * SYSTEMS INC. IT IS MADE AVAILABLE ONLY TO HONEY-
000026      * WELL AUTHORIZED RECIPIENTS FOR THEIR USE SOLELY IN
000027      * THE MAINTENANCE AND OPERATION OF HONEYWELL
000028      * PRODUCTS. THIS DOCUMENT AND INFORMATION MUST BE
000029      * MAINTAINED IN STRICTEST CONFIDENCE; IT MUST NOT
000030      * BE REPRODUCED IN WHOLE OR IN PART; AND IT SHALL
000031      * NOT BE DISCLOSED TO ANY OTHER PARTY WITHOUT THE
000032      * PRIOR WRITTEN CONSENT OF HONEYWELL.
000033      *
000034      * PROGRAM PREPARATION:
000035      *
000036      * THE ROOT SOURCE OF THIS PROGRAM, AFTER THE ADDITION OF APPROPRIATE
000037      * TITLE AND END STATEMENTS, WAS PROCESSED BY THE HOST RESIDENT ASSEMBLER
000038      * TO CREATE EITHER SHORT OR LONG ADDRESS FORM (SAF OR LAF) OBJECT TEXT
000039      * AND LISTING. THE OBJECT TEXT WAS FURTHER PROCESSED BY THE HOST RESIDENT
000040      * LINKER USING THE APPROPRIATE CONSOLE ZVSLIB LIBRARY TO CREATE A PUNCH
000041      * SEGMENT CONTAINING AN EXECUTABLE MODULE. THE ASSEMBLY LISTING WAS
000042      * AUGMENTED WITH CROSS REFERENCE DATA PLUS THE LOAD MAP FROM THE LINKER
000043      * TO CREATE A LIST SEGMENT.
000044      *
000045      *
000046      *          ROOT          SAF          LAF
000047      *          ----          ---          ---
000048      *     NAME     DCMX1         DCM51         DCML1
000049      *     DOCUMENT XXXXXXXX-001 XXXXXXXX-001 XXXXXXXX-001
000050      *
000051      * PROGRAM DISTRIBUTION:
000052      *
000053      *
000054      * THE ELEMENTARY ITEMS SUBMITTED TO THE T&V PROGRAM DISTRIBUTION CENTER
000055      * WERE THE EXECUTABLE PUNCHED CARD DECKS OF DCM51 AND DCML1, AND MAGNETIC
000056      * TAPE IMAGES OF THE AUGMENTED LISTINGS.
000057      *
000058      * REPRODUCTIONS OF THE EXECUTABLE CARD DECKS MAY BE AS DUPLICATE CARD DECKS
000059      * OR AS MEMBER *VL* OF A MULTIPLE MEMBER FILE. IN THE MOST FREQUENT CASE
000060      * IT WILL BE FOUND AS MEMBER VL WITHIN FILE PROGFILE OF A DISKETTE VOLUME
000061      * ENTITLED DIAGS.
000062      *
000063      * DISTRIBUTION OF THE LISTINGS, WHICH SHOULD BE AVAILABLE IF ANY COMPLEX
000064      * MAINTENANCE OR REPAIR IS TO BE PERFORMED, IS NORMALLY MADE AS A
000065      * PRINTED COPY.
000066      *
000067      *
000068      * ROUTINE DEMONSTRATION:
000069      *
000070      *
000071      * A MINIMUM SATISFACTORY TEST FOR NORMAL OPERATION MAY BE OBTAINED BY
000072      * ANSWERING TH QUESTION *NEXT* WITH AN *A* AND COMPLETING ONE PASS.
000073      *
000074      *
000075      * MAIN MEMORY REQUIREMENT:
000076      *
000077      *
000078      * THIS PROGRAM REQUIRES 8K WORDS OF MAIN MEMORY AND WILL USE
000079      * ALL OF AVAILABLE MEMORY THROUGH 8K WORDS.
000080      *
000081      * *****
000082      *
000083      *
000084      * TEST PROCEEDURE
000085      *
000086      *
000087      * BEFORE RUNNING THE CLA TEST, THE MOTHER BOARD TEST (DCML1)
000088      * SHOULD BE RUN TO MAKE SURE THE DLCP IS OPERATING CORRECTLY.
000089      *
000090      * THE PROGRAM AUTOMATICALLY TEST ALL CLA'S WHICH ARE LISTED ABOVE
000091      * UNDER DESCRIPTION. IT PROCEEDS TO EXERCISE ALL THE LINE ADAPTER
000092      * FUNCTIONAL TESTS IN LOOP BACK MODE.
000093      *
000094      * THE TESTS ARE SET UP SO THAT ALL THE CLA'S ARE TESTED ACCORDING TO A
000095      * PARTICULAR FUNCTION. SOME OF THE FUNCTIONAL TESTS INCLUDE CHARACTER
000096      * SIZE TEST, LINE SPEED TEST, PARITY AND CRC TEST, AND UNDERRUN AND
000097      * OVERRRN TEST.
000098      *
000099      *
000100      *
000101      * OPERATING INSTRUCTIONS
000102      *
000103      *
000104      * LOAD AND START (OR RESTART) THE PROGRAM. THE PROGRAM IDENTIFICATION WILL
000105      * BE DISPLAYED ON THE CONSOLE. THE INITIAL START WILL ALSO DISPLAY:
000106      *
000107      *     THE ZVSLIB REVISION NUMBER
000108      *     THE ADDRESS FORM (SAF OR LAF)
000109      *     I/O EQUIPMENT DETECTED IN THE SYSTEM
000110      *     MEMORY SIZE
000111      *
000112      * THIS DISPLAY MUST BE VERIFIED BY THE OPERATOR. THIS DISPLAY IS OMITTED
000113      * ON RESTARTS.

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000114 *
000115 *
000116 * THE CONSOLE SEARCH RULES ARE: FIND THE CONSOLE WITH THE LOWEST CHANNEL
000117 * NUMBER CONNECTED THRU A DMC CONTROLLER. IF THERE IS NO CONSOLE ON A
000118 * DMC, THEN SEARCH FOR A TERMINAL WITH THE HIGHEST CHANNEL NUMBER ASSIGNED
000119 * TO AN ACIA ADAPTER ON A DLC CONTROLLER. IF NO ASYNC ADAPTER IS FOUND,
000120 * THEN GO TO THE FULL CONTROL PANEL.
000121 *
000122 * THERE ARE THREE CONSOLE CHANNEL OPTIONS DETERMINED BY THE VALUE OF LO-
000123 * CATION "ZV$ITY".
000124 *
000125 * IF ZV$TTY EQUALS (0000), SEARCH FOR A CONSOLE.
000126 * IF ZV$ITY EQUALS (FFFF), ASSUME THERE IS NO CONSOLE.
000127 * IF ZV$TTY EQUALS NEITHER (0000), NOR (FFFF), THEN IT IS THE CONSOLE CHAN-
000128 * NEL NUMBER. NOTE: DEFAULT IS TO SEARCH FOR A CONSOLE.
000129 *
000130 * ALL CONSOLE I/O IS EVEN PARITY. IF CONSOLE IS ON DLC, IT MUST BE ASYNC
000131 * AND THE BAUD RATE SET AT 1200 TO MATCH THE PROGRAM SUPPLIED RATE. IF IT
000132 * IS NECESSARY TO CHANGE THE PROGRAM BAUD RATE, THEN THE NEW BAUD RATE
000133 * CODE SHOULD BE PUT INTO LOCATION "ZV$BUD" IN HEX. THE TERMINAL BAUD RATE
000134 * MUST BE SET TO MATCH THIS NEW BAUD RATE. THE CORRECT HEX VALUE MAY BE
000135 * OBTAINED FROM THE FOLLOWING TABLE.
000136 *
000137 * -----*
000138 * BAUD RATE TABLE *
000139 * -----*
000140 *
000141 * ACIA I.D.E.(2118) (2110) E(2108)
000142 * BAUD-RATE
000143 * 50 E 0 EE 1
000144 * 75 E 1 EE 2
000145 * 110 E 2 EE 3
000146 * 134 E 3 EE 4
000147 * 150 E 4 EE 5
000148 * 200 E 5 EE ---
000149 * 300 E 6 EE 6
000150 * 600 E 7 EE 7
000151 * 900 E --- EE 8
000152 * 1050 E 8 EE ---
000153 * 1200 E 9 EE 9
000154 * 1800 E 10 (A) EE 10 (A)
000155 * 2000 E 11 (b) EE ---
000156 * 2400 E 12 (C) E 11 (B)
000157 * 3600 E --- EE 12 (C)
000158 * 4800 E 13 (D) EE 13 (D)
000159 * 7200 E --- EE 14 (E)
000160 * 9600 E 14 (E) EE 15 (F)
000161 * 19200 E 15 (F) EE ---
000162 *
000163 * TO MAKE ANY OF THE ABOVE CHANGES, LOAD AND HALT THE PROGRAM BEFORE EX-
000164 * ECUTION. INSERT CHANGE THEN EXECUTE. MEMORY LOCATIONS OF "ZV$TTY" AND
000165 * "ZV$BUD" MAY BE FOUND IN MAP AT END OF LISTING.
000166 * CONSULT LEVEL-6 T&V MANUAL "AW94" FOR DETAILS ON HOW TO LOAD THE TESTS.
000167 *
000168 * THE FOLLOWING IS A TYPICAL RESULT OF LOADING AND STARTING TO RUN THE
000169 * PROGRAM.
000170 *
000171 * CLA TEST <PGM NAME> <PGM DATE> <PGM REV>
000172 * ZV$LIB REV. 6.0
000173 * ZV$AF= 1 <2>
000174 * WDT
000175 * CHAN DEVL ID
000176 * 0400 DSKT 2010
000177 * 0480 DSKT 2010
000178 * 0580 CDR 2008
000179 * 1200 DISC 2330
000180 * 1280 DISC 2330
000181 * 1300 LPT 2000
000182 * 1380 CONS 2019
000183 * MEMORY LOW 00002B2D
000184 * MEMORY HIGH 00003FFF 16K
000185 *
000186 * THE PROGRAM WILL THEN ASK:
000187 *
000188 * POWER LINE FREQ. ?;
000189 *
000190 * THE OPERATOR NOW ENTERS THE POWER LINE FREQUENCY IN DECIMAL
000191 * FOLLOWED BY A CARRIAGE RETURN. THE DEFAULT IS 60 HERTZ.
000192 * IF THIS IS DESIRED A CARRIAGE RETURN IS ENTERED.
000193 *
000194 * NEXT THE PROGRAM WILL DISPLAY:
000195 *
000196 * DLC CHANNEL(S) ?;
000197 *
000198 * AT THIS TIME THE CHANNEL(S) OF THE LINE(S) YOU WANT
000199 * TO TEST SHOULD BE ENTERED, FOLLOWED BY A CARRIAGE RETURN.
000200 * THE CLA CHANNEL(S) WILL NORMALLY BE DISPLAYED AS AN ITEM(S) IN THE
000201 * CONFIGURATION PRINTOUT.
000202 *
000203 * THE PROGRAM WILL THEN RESPOND WITH:
000204 *
000205 * NEXT ?;
000206 *
000207 * THE OPERATOR HAS TO ENTER ONE OF THE FOLLOWING LETTERS:
000208 *
000209 * "A" - RUN LINE(S) WITH INTERNAL LOOP TESTS
000210 *
000211 * "C" - RUN LINE(S) WITH CONNECTOR LOOP TESTS
000212 *
000213 * IN ORDER TO RUN WITH "C" SELECTED, AN EXTERNAL LOOP CONNECTOR
000214 * MUST BE ATTACHED TO THE LINE ADAPTER(S) EITHER AT THE
000215 * LINE ADAPTER CONNECTOR OF THE END OF THE CABLE, THE REQUIRED
000216 * EXTERNAL LOOP CONNECTOR AND INSTRUCTIONS FOR ITS USE ARE
000217 * DESCRIBED IN EACH LINE ADAPTER'S PRODUCT MANUAL.
000218 *
000219 * IF THERE IS NO CONSOLE PRESENT REFER TO "SERIES 60
000220 * LEVEL 6 MODEL 34/36 T & V OPERATING INSTRUCTION
000221 * MANUAL" ORDER NO. FL39 FOR INSTRUCTIONS ON ENTERING DATA
000222 * AND INTERPRETING PROGRAM MESSAGES.
000223 *
000224 * THE PROGRAM WILL RUN APPROXIMATELY 1 MINUTE PER ASYNCHRONOUS LINE AND
000225 * 30 SECONDS PER SYNCHRONOUS LINE.
000226 *

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000227 * DURING THE LINE SPEED TEST THE PROGRAM WILL PRINT OUT THE SPEED AT WHICH
000228 * THE SYNCHRONOUS LINE(S) ARE TESTED AT WITHIN 3% OF THE ACTUAL SPEED.
000229 * ASYNCHRONOUS LINE(S) ARE TESTED AT ALL SELECTABLE BAUD RATES.
000230 *
000231 * IF THERE ARE NO FAULTS. IT WILL THEN DISPLAY;
000232 *
000233 * PASS
000234 *
000235 * THE PROGRAM WILL CONTINUE AND TYPE "PASS" WITH NO HALTS BETWEEN PASSES.
000236 * TO RESTART; START THE PROGRAM AT HEX 100
000237 *
000238 *
000239 * ERROR REPORTS
000240 *
000241 * ERRORS WILL CAUSE THE PROGRAM TO HALT. AN ERROR MESSAGE WILL
000242 * BE DISPLAYED IF A CONSOLE IS PRESENT.
000243 *
000244 * ERROR DISPLAYS ARE AS FOLLOWS:
000245 *
000246 * ERR YP @ ZZZZ
000247 *
000248 * B7 B6 B5 B4 B3 B2 B1 I
000249 * R7 R6 R5 R4 R3 R2 R1 M
000250 *
000251 * Y = TEST TYPE
000252 * P = LINE NUMBER
000253 * ZZZZ = ERROR LOCATION IN LISTING. HAS COMMENT
000254 * GIVING FAILING FUNCTION.
000255 *
000256 * B1-b7, I, R1-R7, M ARE CONTENTS OF REGISTERS.
000257 * INTERPRETATION OF THESE IS FOR SPECIALIST USAGE.
000258 *
000259 * IN ALL CASES;
000260 *
000261 * R3 = CHANNEL NUMBER
000262 *
000263 * IN GENERAL
000264 *
000265 * R6 = SHOULD BE DATA
000266 * R5 = ACTUAL DATA
000267 * R7 = WORD NUMBER IN BLOCK TRANSFER
000268 *
000269 *
000270 *
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000271 /
000272 0000 ZERO EQU $
000273 XLOC ZHN15A,ZH15,ZVSHR
000274 XLOC ZH15A2,ZH1AFB
000275 XLOC ZHWDTCC,ZHRTCC,ZVSTTY
000276 XLOC ZHRTCC,ZHRTCC
000277 XLOC ZHPR,ZHCOMM
000278 00FF 0000 ORG ZERU*X'FF'
000279 00FF 0000 STOP HLT NO ERROR FOUND IN DLCC
000280 *
000281 * OPERATOR MUST ENTER CLA ADDRESS
000282 *
000283 0100 0100 STRT EQU $
000284 0100 0F8C B >STP BRANCH TO NORMAL RUN
000285 0105 0105 ORG ZERU*X'105'
000286 CALL ZV$KD,ZV$IZ INITIALIZE FOR NON-CONSOLE USE

0105 FBF0 0001 X
0107 D380 0000 X
000287 0109 8700 0000 CL <ZV$TTY CLEAR CONSOLE CHANNEL
000288 010B 0F87 B >NTYR
000289 STP CALL ZV$RD,MESG

010C FBC0 0003 X
010E D380 0000 X
0110 0F80
0111 1300
000290 0112 8980 1116 NTYR CMZ <FWFG
000291 0114 09AC BNE >EDLINT
000292 *
000293 * ASK FOR POWER LINE FREQ.
000294 *
000295 0115 8C51 STS =SR1 ASK ONLY ON 6/36 AND 3/34
000296 0116 82D1 LB =SR1,Z'2000'
0117 2000
000297 0118 050D BBT >CP40
000298 CALL ZV$WC,MESG9

0119 FBC0 0003 X
011B D380 0000 X
011D 0F80
011E 1339
000299 CALL ZV$ID,HRTZ

011F FBC0 0003 X
0121 D380 0000 X
0123 0F80
0124 1119

000300 *
000301 * FIND NUMBER OF MILSEC. IN RTC TICK
000302 *
000303 CP40 CL =SR6
000304 0125 8756 LDR SR7,Z'C350'
000305 0126 F870 C350 DIV SR7,<HRTZ
000306 0128 F300 1119 STR SR6,<TLOC
000307 012C 8756 CL =SR6
000308 012D F370 0064 DIV SR7,=100
000309 012F FF00 1110 STR SR7,<DIV1
000310 0131 70D0 DOR SR7,16
000311 0132 F370 000A DIV SR7,=10
000312 0134 FF00 1111 STR SR7,<DIV2
000313 0136 EF00 1112 STR SR6,<DIV3
000314 0138 8756 CL =SR6
000315 0139 F800 1125 LDR SR7,<TLOC
000316 013B 7FOA MLV SR7,=10
000317 013C F300 1119 DIV SR7,<HRTZ
000318 013E FF00 1113 STR SR7,<DIV4
000319 *
000320 *
000321 *
000322 *
000323 * INITIALIZE THE ACTIVE LINE TABLE
000324 * INITIALIZE THE LINE ADDRESS TABLE
000325 *
000326 EDLINT CALL ZV$F,ATLT,CPFLG+3,C16

0140 FBC0 0003 X
0142 D380 0000 X
0144 0F80
0145 1186
0146 119A
0147 125C
000327 0148 FE00 1108 SWR SR7,<CHANL
000328 CALL ZV$WC,MESG7

014A FBC0 0003 X
014C D380 0000 X
014E 0F80
014F 132B
000329 CALL ZV$IM,ADDRS,CB

0150 FBC0 0003 X
0152 D380 0000 X
0154 0F80
0155 118E
0156 125B
000330 0157 8752 CL =SR2 CLEAR INDEX
000331 0158 AB80 118E LAB $B2,<ADDRS LOAD $B2 WITH ADDRESS OF ACTIVE LINE TABLE
000332 015A CB80 125F LAB $B4,<TSA1 SET UP FOR TRAP ON
000333 015C CF80 0000 STB $B4,<ZHN15A UNAVAILABLE RESOURCE
000334 015E CB80 0192 LAB $B4,<NODEVF
000335 0160 CF80 0000 STB $B4,<ZH15
000336 0162 986E SALT LDR SR1,$B2,+SR2 LOAD ADDRESS FROM TABLE
000337 0163 1931 BEZ SR1,>CON3
000338 0164 9570 FFC0 AND SR1,=Z'FFC0' STRIP CHANNEL WITH HEX FFC0
000339 0166 9970 03C0 CMR SR1,=X'3C0' COMPARE CHANNEL WITH HEX 3C0
000340 0168 0302 BG >KCK1 BRANCH IF GREATER
000341 0169 0F96 B >DEVNF CHANNEL NUMBER TO SMALL
000342 016A 1E26 RCK1 ADV SR1,=X'26' PUT FUNCTION IN
000343 016B 8055 IO =SR5,=SR1 INPUT DEVICE NUMBER
016C 0051
000344 016D C855 LDR SR4,=SR5
000345 016E D570 FF00 AND SR5,=Z'FF00'
000346 0170 D970 3100 CMR SR5,=Z'3100' COMPARE DEVICE NUMBER RESPONSE
000347 0172 098D BNE >DEVNF
000348 0173 8851 LDR SR3,=SR1 SET SR3 FOR INDEX OF LINE NUMBER
000349 0174 B570 03C0 AND SR3,=X'3C0'
000350 0176 3047 SOR SR3,7
000351 0177 CF30 1186 STR SR4,<ATLT,SR3 STORE ID INTO ACTIVE LINE TABLE
000352 0179 9570 FC00 AND SR1,=Z'FC00'

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000353	017B	9F00	1108		STR	\$R1,<CHANL		
000354	017D	0F80	0162		B	<SALT		
000355								
000356					* DEVNF	CALL	ZV\$TC,ERMGI	DLLC NOT FOUND ON THIS ADDRESS
	017F	FBC0	0003					
	0181	D380	0000	X				
	0183	0F80						
	0184	1366						
000357	0185	88D2			DEC	= \$R2		
000358	0186	9820	118E		LDR	\$R1,<ADDRS,\$R2		STORE ADDRESS THAT FAILED
000359	0188	9F00	1125		STR	\$R1,<TLUC		
000360					PDKS	CALL	ZV\$TH,TLOC	PRINT ADDRESS THAT FAIL
	018A	FBC0	0003					
	018C	D380	0000	X				
	018E	0F80						
	018F	1125						
000361	0190	0F80	0140		B	<EDLINT		
000362								
000363	0192	8755			* NODEVF	CL	= \$R5	TRAP HANDLER ROUTINE
000364	0193	0003				RTT		
000365					*			
000366					*			
000367					*			
000368					*			
000369					*			
000370					*			CHECK TO SEE IF THIS IS A CLA I CAN TEST
000371	0194	8751			CON3	CL	= \$R1	CLEAR INDEX
000372	0195	AB80	118E			LAB	\$B2,<ATLT	
000373	0197	9B80	117F			LAB	\$B1,<BIT0	
000374	0199	2CF6			CAK	LDV	\$R2,=-10	
000375	019A	C85E			RON	LDR	\$R4,\$B2,*\$R1	
000376	019B	1D09				CMV	\$R1,=9	
000377	019C	0910				BE	>CON3A	
000378	019D	497D				BEZ	\$R4,>RON	
000379	019E	C921			CYN	CMR	\$R4,\$B1,\$R2	
000380	019F	097A				BE	>CAK	
000381	01A0	27FE				BINC	\$R2,>CYN	
000382						CALL	ZV\$TC,ERMGI	
	01A1	FBC0	0003					
	01A3	D380	0000	X				
	01A5	0F80						
	01A6	1376						
000383	01A7	88D1				DEC	= \$R1	
000384	01A8	1007				SOL	\$R1,7	
000385	01A9	9400	1108			OR	\$R1,<CHANL	
000386	01AB	0FDD				B	>PDKS	
000387					*			
000388	01AC	2CF4			CON3A	LDV	\$R2,=-12	NUMBER OF I/O TO BE CHANGED
000389	01AD	9800	118E			LDR	\$R1,<ADDRS	LOAD \$R1 WITH ADDRESS OF CLA ACTIVE
000390	01AF	DB80	110E			LAB	\$B5,<FID,1	PUT ADDRESS OF CONTROL TABLE IN \$B5
000391	01B1	C870	003F		ALL	LDR	\$R4,=X'3F'	LOAD MASK TO CLEAR CHANNEL
000392	01B3	9570	FC00			AND	\$R1,=Z'FC00'	CLEAR SUBCH. & FUNCTION
000393	01B5	C525				AND	\$R4,\$B5,\$R2	CLEAR CHANNEL
000394	01B6	9454				OR	\$R1,=\$R4	PUT CHANNEL NUMBER IN
000395	01B7	9F25				STR	\$R1,\$B5,\$R2	STORE IT BACK IN CONTROL TABLE
000396	01B8	27F9				BINC	\$R2,>ALL	
000397	01B9	8700	111F			CL	<P\$FG	CLEAR PRINT SYNC. SPEED FLAG
000398	01BB	8700	1117			CL	<HGSPD	CLEAR HIGH SPEED FLAG
000399	01BD	8740	0F5A			CL	HISPD	
000400	01BF	F900	1108			CMR	\$R7,<CHANL	CHECK TO ONLY PRINT ONCE
000401	01C1	091D				BE	>INVD	
000402					*			
000403					*			PRINT FIRMWARE REV.
000404					*			
000405	01C2	3001				SOL	\$R3,1	SHIFT \$R3 TO ALIGN WITH CHANNEL NUMBER
000406	01C3	C380	0AF3			LNJ	\$B4,<GENITZ	INITIALIZE CONTROLLER
000407	01C5	E380	0B5E			LNJ	\$B6,<NDLCT	READ LOCATION 1 IN LCT TO PRINT REV.
000408	01C7	0000				DC	0	ADDRESS
000409	01C8	0002				DC	2	RANGE
000410					*			
000411	01C9	8780	112E			CLH	<LCTV	
000412	01CB	C800	112E			LDR	\$R4,<LCTV	
000413	01CV	CF00	11E1			STR	\$R4,<FRIMRV	STORE FIRMWARE REV NUMBER
000414	01CF	490F				BEZ	\$R4,>INVD	
000415						CALL	ZV\$TC,FREV	
	01D0	FBC0	0003					
	01D2	D380	0000	X				
	01D4	0F80						
	01D5	135C						
000416						CALL	ZV\$TD,LCTV	
	01D6	FBC0	0003					
	01D8	D380	0000	X				
	01DA	0F80						
	01DB	112E						
000417	01DC	8A80	1116			INC	<FWFG	SET FLAG TO INHIBIT PRINT OUT
000418					*			
000419					*			ASK WHAT MODE OF OPERATION
000420					*			
000421					INVD	CALL	ZV\$UC,MESG6	
	01DE	FBC0	0003					
	01E0	D380	0000	X				
	01E2	0F80						
	01E3	1328						
000422						CALL	ZV\$IA,TP2,TLOC	
	01E4	FBC0	0003					
	01E6	D380	0000	X				
	01E8	0F80						
	01E9	1127						
	01EA	1125						
000423	01EB	7C08				LDV	\$R7,=8	
000424	01EC	D380	0B4C			LNJ	\$B5,<IDLAY	DELAY FOR DLC CONSOLE
000425	01EE	C800	1125			LDR	\$R4,<TLOC	
000426	01F0	4048				SOR	\$R4,8	SHIFT MODE TYPE ANSWER OVER
000427	01F1	4D53				CMV	\$R4,=X'53'	IF 'S' DO SHORT TEST ONLY
000428	01F2	0900	021E			BE	<SHIST	
000429	01F4	4043				CMV	\$R4,=X'43'	IF 'C' DO CABLE LOOP TEST ONLY
000430	01F5	0900	022F			BE	<CABLP	
000431	01F7	404D				CMV	\$R4,=X'4D'	IF 'M' GO TO PATCH ROUTINE
000432	01F8	0900	0218			BE	<PCH	
000433	01FA	4D41				CMV	\$R4,=X'41'	IF 'A' DO INTERNAL LOOP TEST ONLY
000434	01FB	09E3				BNE	>INVD	
000435					*			
000436	01FC	8700	110B			CL	<COUNT	

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000437 01FE 8700 112A CL <SHORT
000438 0200 C870 C714 RTST LDR SR4,=Z'C714' STORE LINE CONTROL FOR INTERNAL LOOP
000439 0202 CF00 128B STR SR4,<LC1LCR
000440 0204 A380 0246 LNJ SB2,<LPTA TEST A: GO TO LOOP TEST
000441 0206 A380 02AF LNJ SB2,<SPSYTC TEST B: GO TO LINE SPEED TEST
000442 0208 A380 03F1 LNJ SB2,<CRPTB TEST D: GO TO CRC AND PARITY TEST
000443 020A A380 04D5 LNJ SB2,<CHS2TD TEST E: GO TO CHARACTER SIZE TEST
000444 020C A380 052D LNJ SB2,<STBITE TEST F: GO TO STOP BIT TEST
000445 020E A380 05B6 LNJ SB2,<PARERF TEST G: GO TO PARITY ERROR TEST
000446 0210 A380 0617 LNJ SB2,<STIOG TEST H: GO TO STOP I/O TEST
000447 0212 A380 06EA LNJ SB2,<UORNTN TEST J: GO TO OVERRUN AND UNDERRUN TEST
000448 0214 A380 0BE1 LNJ SB2,<EDPCK
000449 0216 0F80 0200 B <RTST
000450
000451 * PCH CALL ZV3PCH
0218 FBFO 0001
021A D380 0000 X B <INVD
021C 0F80 01DE
000452
000453 * SHTST LDR SR4,=Z'C714' STORE LINE CONTROL FOR INTERNAL LOOP
000454 021E C870 C714 STR SR4,<LC1LCR SET FLAG
000455 0220 CF00 128B STR SR4,<SHORT
000456 0222 CF00 112A LNJ SB2,<LPTA TEST A: GO TO LOOP TEST
000457 0224 A380 0246 LNJ SB2,<SPSYTC TEST B: LINE SPEED TEST TO SET HIGH SPEED FLA
000458 0226 A380 02AF LNJ SB2,<CRPTB TEST D: CRC AND PARITY TEST
000459 0228 A380 03F1 LNJ SB2,<PARERF TEST G: PARITY ERROR TEST
000460 022A A380 05B6 LNJ SB2,<EDPCK PRINT PASS MESSAGE
000461 022C A380 0BE1 B
000462 022E 0FF0 >SHTST
000463
000464 * CABLP LDV SR4,=7
000465 022F 4C07 STR SR4,<COUNT
000466 0230 CF00 110B CL <SHORT
000467 0232 8700 112A RTST1 LDR SR4,=Z'C314' STORE LINE CONTROL FOR CONNECTOR LOOP
000468 0234 CF00 128B STR SR4,<LC1LCR
000469
000470 * LNJ SB2,<LPTA TEST A: GO TO LOOP DATA TEST
000471 0238 A380 0246 LNJ SB2,<LPCBLK TEST K: GO TO LOOP DSS IN DSC
000472 023A A380 0777 LNJ SB2,<XMMSL TEST L: GO TO XMIT MARK & SPACE TEST
000473 023C A380 080D DEC <COUNT
000474 023E 8880 110B CMZ <COUNT
000475 0240 8980 110B BNE >RTST1
000476 0242 09F2 LNJ SB2,<EDPCK
000477 0244 A380 0BE1 B >CABLP
000478
000479 * -----
000480 *
000481 * DATA LOOP TEST
000482 *
000483 LPTA LDR SR4,=A'A '
000484 0246 C870 4120 STR SR4,<ERMG+1
000485 0248 CF00 1364 LDR SR4,=X'E18' STORE FAST LINE SPEED
000486 024A C870 0E18 STR SR4,<LCT15
000487 024C CF00 128A LNJ SB3,<CLFG CLEAR ALL TEST FLAGS
000488 024E B380 098B CL <MASK SET MASK FOR ZV3C
000489 0250 8700 111A CL <LC1SYF CLEAR SYNC. FLAG FOR ASYNC. LINES
000490 0252 8700 128C LDV SR7,=6 NUMBER OF 30 MS DELAY
000491
000492 *
000493 * TEST ASYNCHRONOUS LINES WITH BAUD RATE
000494 * AT 9600 BAUD, WITH 2 STOP BITS
000495 *
000496 I1 LAB SB1,<ASYN1 LINE TYPE
000497 0255 9880 1176 T1A LNJ SB3,<DTALOP
000498 0257 B380 0881 B
000499 0259 0F85 DC >T2AA
000500 025A 12D4 DC <WCP1
000501 025B 1286 DC <LCT1
000502 025C 003F DC (DPLT-DOUT) RANGE IN WORDS
000503 025D 119E DC <DOUT TRANSMIT BUFFER ADDRESS
000504
000505 *
000506 * TEST CURRENT LOOP ASYNCHRONOUS
000507 *
000508 T2AA CMZ <COUNT IF CONNECTOR LOOP
000509 025E 8980 110B BE <T2 CHANGE SPEED TO TEST AT 9600 BAUD
000510 0260 0900 0266 LDR SR4,=X'E18'
000511 0262 C870 0E18 STR SR4,<LCT15
000512 0264 CF00 128A T2 LDR SR4,=SB1 LOAD ID TYPE INTO SR4
000513 0266 C871 CMR SR4,<ASYNC CHECK TO SEE IF ALREADY DONE
000514 0268 0902 BE >T2A
000515 026A 0FED B >T1A
000516 026C 026B T2A EQU $
000517 026E 8980 112A CMZ <SHORT
000518 0268 09A1 BNE >NCBL
000519 026A C870 0118 LDR SR4,=X'118' CHANGE BAUD RATE TO 50
000520 026C CF00 128A STR SR4,<LCT15
000521 026E 7C78 LDV SR7,=120 NUMBER OF 30 MS DELAY FOR DATA TRANSFER
000522
000523 *
000524 * TEST ASYNCHRONOUS LINES WITH NEW BAUD RATE
000525 * AT 50 BAUD, WITH 2 STOP BITS
000526 *
000527 T3 LDR SR4,=X'18'
000528 0273 C870 0018 STR SR4,<LCT15
000529 0275 CF00 128A LAB SB1,<ASYN1 LOAD LINE TYPE
000530 0277 9880 1176 T3A LNJ SB3,<DTALOP LOOP SUB-ROUTINE
000531 0279 B380 0881 B >T4
000532 027B 0F85 DC <WCP1
000533 027C 12D4 DC <LCT1
000534 027D 1286 DC 7
000535 027E 0007 DC <DOUT1
000536
000537 *
000538 * TEST CURRENT LOOP ASYNCHRONOUS
000539 *
000540 T4 LDR SR4,=SB1 LOAD ID TYPE INTO SR4
000541 0280 C871 CMR SR4,<ASYNC CHECK TO SEE IF ALREADY DONE
000542 0281 C900 1177 BE >T4A
000543 0283 0902 B >T3A
000544 0285 0FF5 T4A EQU $
000545 0287 C800 128B LDR SR4,<LC1LCR CHECK LINE CONTROL FOR CONNECTOR LOOP
000546 0288 82D4 LB =SR4,=Z'0400' IF CONNECTOR LOOP SET DIRECT CONNECT BIT
000547 028A 0400
000548 028C 0505 BBT >NCBL
000549 028E C870 CB14 LDR SR4,=Z'CB14'
000550 0288 CF00 128B STR SR4,<LC1LCR
000551 028A C870 3A18 LDR SR4,=X'3A18' PUT IN SYNC. CHARACTER TO TEST
000552 028C CF00 128A STR SR4,<LCT15 SYNC. LINE TYPES
000553 028E C870 021C LDR SR4,=X'21C' SET LCI SYNC. FLAG
000554 0290 CF00 128A
000555 0292 C870 021C

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000547 0294 CF00 128C STR $R4,<LC1SYF
000548 0296 7C2D LDV $R7:=45 NUMBER OF 30 MS DELAY FOR 800 BAUD
000549 *
000550 * TEST SYNCHRONOUS (3A AS SYNC. CHAR.)
000551 *
000552 0297 9880 1178 LAB $B1,<SYNC LOAD LINE TYPE
000553 0299 B380 0881 QA1 LNJ $B3,<DTALOP LOOP SUB-ROUTINE
000554 029B 0F85 B >T5
000555 029C 12D4 DC <WCP1
000556 029D 1286 DC <LC11
000557 029E 002F DC (DFLT=DOUT1)
000558 029F 11AE DC <DOUT1
000559 *
000560 * TEST BI-SYNCHRONOUS LINES (3A AS SYNC. CHAR.)
000561 *
000562 02A0 C871 T5 LDR $R4,+$B1 LOAD ID TYPE INTO $R4
000563 02A1 C900 CMR $R4,<BISYNC CHECK TO SEE IF ALREADY DONE
000564 02A3 0902 BE >T6
000565 02A4 0FF5 B >QA1
000566 *
000567 * TEST MIL 188 SYNCHRONOUS (3A AS SYNC. CHAR.)
000568 *
000569 02A5 9880 117A T6 LAB $B1,<SYN188 LOAD LINE TYPE
000570 02A7 B380 0881 LNJ $B3,<DTALOP LOOP SUB-ROUTINE
000571 02A9 0F85 B >T30
000572 02AA 12D4 DC <WCP1
000573 02AB 1286 DC <LC11
000574 02AC 002F DC (DFLT=DOUT1)
000575 02AD 11AE DC <DOUT1
000576 *
000577 02AE 8382 T30 JMP $B2 JUMP OUT OF LOOP TEST
000578 *
000579 *
000580 *
000581 * LINE SPEED TEST
000582 *
000583 02AF C870 4220 SPSYTC LDR $R4,=A*B
000584 02B1 CF00 1364 STR $R4,<ERMG+1
000585 02B3 B380 098B LNJ $B3,<CLFG CLEAR ALL TEST FLAGS
000586 02B5 C800 1119 LDR $R4,<HRTZ CALCULATE THE LOW AND HIGH RANGE OF RTC TICKS
000587 02B7 4002 SOL $R4,2
000588 02B8 4EF8 ADV $R4,=-8
000589 02B9 CF00 11DF STR $R4,<SECL
000590 02BB 4E10 ADV $R4,=16
000591 02BC CF00 11E0 STR $R4,<SECH
000592 02BE C800 1119 LDR $R4,<HRTZ CALCULATE NUMBER OF RTC TICKS IN 5 SEC.
000593 02C0 4FOA MLV $R4,=10
000594 02C1 CF00 11DE STR $R4,<FIVSEC
000595 02C3 C880 02CD LAB $B4,<SPSYT1 SET UP INTERRUPT VECTORS AND
000596 02C5 CF80 1281 STB $B4,<ISA4P POINTERS TO CHANGE RUNNING LEVEL TO 4
000597 02C7 C880 127E LAB $B4,<ISA4+$AF
000598 02C9 CF80 0004 X STB $B4,<ZHISAZ+4*$AF
000599 02CB E870 8004 X LEV =Z'8004' CHANGE RUNNING LEVEL TO 4
000600 02CD 4C01 SPSYTI LDV $R4,=1 SET RTC LEVEL TO 1
000601 02CE CF00 0000 X STR $R4,<ZHRTCL
000602 *
000603 02D0 C880 03D6 * LAB $B4,<NOSPD SET RUPT POINTER FOR RTC INTERRUPT
000604 02D2 CF80 127B * STB $B4,<ISA1P
000605 *
000606 02D4 C880 1278 * LAB $B4,<ISA1+$AF SET RUPT VECTOR FOR LEVEL 1 RUPT
000607 02D6 CF80 0001 X STB $B4,<ZHISAZ+$AF
000608 *
000609 02D8 8980 112A * CMZ <SHORT
000610 02DA 0996 BNE >STSP1
000611 02DB 4C0A LDV $R4,=10 STORE CHAR. SIZE
000612 02DC CF00 110D STR $R4,<CSB
000613 02DE 8700 129C CL <LC3SYF CLEAR SYNC. FLAG FOR ASYNC. LINES
000614 02E0 8700 1107 CL <BYTP SET BYTE POSITION TO ZERO
000615 *
000616 * TEST LINE SPEEDS FOR ASYNCHRONOUS NEW BAUD RATE
000617 *
000618 02E2 4C18 LDV $R4,=X'18' SET $R4 WITH FIRST SPEED TO TEST (ASYNCR)
000619 02E3 9880 1176 LAB $B1,<ASYN1 LOAD LINE TYPE
000620 02E5 E880 11F2 LAB $B6,<NSPTBL LOAD ADDRESS OF CHAR. NUMBER TABLE
000621 02E7 8380 0318 LNJ $B3,<SPDTLP
000622 *
000623 * TEST LINE SPEEDS FOR ASYNCHRONOUS CURRENT LOOP
000624 *
000625 02E9 4C18 LDV $R4,=X'18'
000626 02EA 9880 1177 LAB $B1,<ASYNCR
000627 02EC E880 11F2 LAB $B6,<NSPTBL
000628 02EE B380 0318 LNJ $B3,<SPDTLP
000629 *
000630 02F0 C870 0F00 STSP1 LDR $R4,=3840 SET RANGE FOR SYNC. LINES
000631 02F2 CF00 1120 STR $R4,<RANGE
000632 02F4 E880 1120 LAB $B6,<RANGE LOAD ADDRESS OF RANGE FOR SPEED TEST SUB-ROUT
000633 *
000634 02F6 C880 03E0 * LAB $B4,<IRKG SET RUPT POINTER FOR RTC INTERRUPT (SYNCR)
000635 02F8 CF80 127B STB $B4,<ISA1P
000636 02FA 8A80 1107 INC <BYTP SET BYTE POSITION TO ONE
000637 *
000638 02FC C870 021C * LDR $R4,=X'21C' SET LCT SYNC. LINE FLAG
000639 02FE CF00 129C STR $R4,<LC3SYF
000640 0300 4C08 LDV $R4,=8 SET CHAR. SIZE FOR SPEED CALCULATION
000641 0301 CF00 110D STR $R4,<CSB
000642 0303 C870 C118 LDR $R4,=Z'C118' LOAD $R4 WITH SYNC CHARACTER TO BE USED
000643 *
000644 * MEASURE AND PRINT LINE SPEED FOR BI-SYNCHRONOUS
000645 *
000646 0305 9880 1179 LAB $B1,<BISYNCR LOAD ADDRESS OF LINE TYPE
000647 0307 B380 0318 LNJ $B3,<SPDTLP GO TO LINE SPEED TEST
000648 *
000649 * MEASURE AND PRINT LINE SPEED FOR SYNCHRONOUS
000650 *
000651 0309 9880 1178 LAB $B1,<SYNCR LOAD ADDRESS OF LINE TYPE
000652 030B B380 0318 LNJ $B3,<SPDTLP GO TO LINE SPEED TEST
000653 *
000654 * MEASURE AND PRINT LINE SPEED FOR MIL 188
000655 *
000656 030D 9880 117A LAB $B1,<SYN188
000657 030F B380 0318 LNJ $B3,<SPDTLP GO TO LINE SPEED TEST
000658 *
000659 0311 8A80 111F INC <PSFG SET FLAG TO INHIBIT PRINTOUT

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000660          CALL      ZV$RSD          RE-INITIALIZE INTERRUPT VECTOR AND POINTERS 6
000661          0313  FBF0 0001
000662          0315  D380 0000          X
*
*          SET LEVEL TO ZERO
*          JUMP OUT OF TEST
000662          0317  8382          *          JMP      $B2
000663          *
000664          *          *****
000665          *
000666          *          LINE SPEED LOOP
000667          *
000668          0318  8F00 1155          SPDTLP SAVE <SAV3,=Z'0810'          SAVE REGS.
000669          031A  0810
000670          031B  8753
000671          031C  9F80 0320          CL      =R3          " R3
000672          031E  D380 0B38          STB    $B1,<ST1          STORE LINE TYPE FOR FIND LINE ON
000673          0320  0000          *
000674          0321  8383          JPT3  LNJ    $B5,<FLN          FIND LINE ON UF TYPE BELOW
000675          *          ST1  RESV  $AF,0          LINE TYPE FOUND AND STORED HERE
000676          0322  8751          *          JMP     $B3          NO MORE LINE THIS TYPE END TEST
000677          0323  8752          *          CL     =R1          CLEAR $R1
000678          0324  CF00 129A          CL     =R2          CLEAR INDEX ON NEW LINE
000679          *          NXSPD STR  $R4,<LCT35          STORE LINE SPEED IN LCT TABLE
000680          0326  C808 0320          *
000681          0328  C900 1177          NXSPD1 LDR  $R4,=*ST1
000682          032A  0983          CMR   $R4,<ASYN
000683          032B  C380 09FD          BNE  >NXSPD2
000684          032D  C380 0AF3          LNJ  $B4,<SCLS          B IF NOT CURRENT LOOP ADAPT.
000685          032F  C570 FF80          LNJ  $B4,<GENITZ          SET CURRENT LOOP ADAPTER SPEED
000686          0331  C470 0040          AND  $R4,=Z'FF80'          GENERAL INITIALIZE
000687          0333  CF00 110A          OR   $R4,=X'40'          PREPARE TO WRITE CHANNEL PROG
000688          *          STR  $R4,<CHNZ
000689          *          CALL   ZV$MLW,CHLP1,RAG1,HX200,CHNZ
000689          0335  FBC0 0003
000690          0337  D380 0000          X
000691          0339  0F80
000692          033A  0BF8
000693          033B  12D6
000694          033C  125D
000695          033D  110A
000689          CALL      ZV$MLW,CHLP1A,RAG2,HX400,CHNZ
000690          033E  FBC0 0003
000691          0340  D380 0000          X
000692          0342  0F80
000693          0343  0C34
000694          0344  12D7
000695          0345  125E
000696          0346  110A
000690          *
000691          0347  D380 0B24          *          LNJ    $B5,<SETLCT          SEND OUT LCT TABLE
000692          0349  1296          DC    <LCT3
000693          *
000694          034A  C826          LDR   $R4,$B6,$R2          LOAD $R4 WITH RANGE IN BYTES
000695          034B  8AD3          INC  =R3
000696          034C  D380 0B82          LNJ  $B5,<MCCB          FORM CCB FOR XMIT
000697          034E  119E          DC    <DOU1
000698          034F  0060          DC    X'60'
000699          0350  88D3          DEC  =R3          CHANGE TO RECV. SIDE
000700          0351  4900 03C4          BEZ  $R4,<TSNL
000701          0353  CF55          STR  $R4,=$R5
000702          0354  C970 03C0          DLCCB CMR   $R4,=X'3C0'          MAKE MULTIPLE CCB IF LARGE RANGE
000703          0356  0A80 0362          BALE <SGCCB
000704          0358  C870 03C0          LDR  $R4,=X'3C0'
000705          035A  D380 0B82          LNJ  $B5,<MCCB
000706          035C  1C00          DC    <DIN
000707          035D  0040          DC    X'40'
000708          035E  88D1          DEC  =R1
000709          035F  D254          SUB  $R5,=$R4
000710          0360  DF54          STR  $R5,=$R4
000711          0361  0FF3          B    >DLCCB
000712          *
000713          0362  D380 0B82          *          SGCCB LNJ  $B5,<MCCB          FORM CCB FO RECV.
000714          0364  1C00          DC    <DIN
000715          0365  0060          DC    X'60'
000716          *
000717          *
000718          *          START I/O XMIT FIRST THEN RECV.
000719          *
000720          0366  7C01          LDV  $R7,=1
000721          0367  8AD3          INC  =R3          CHANGE TO XMIT SIDE
000722          0368  C800 10FC          LDR  $R4,<FOCC
000723          036A  C380 0B7E          LNJ  $B4,<CGSCH
000724          036C  8000 1180          IO   <BIT1,=$R4
000725          036E  0054
000726          036F  0703          BIOT >MELC01
000727          0370  F380 0BA3          LNJ  $B7,<ERROR
000728          0372  D380 0B4C          MELC01 LNJ  $B5,<TDLAY
000729          0374  88D3          DEC  =R3
000730          *
000731          0375  88D1          DEC  =R1
000732          0376  C800 10FC          LDR  $R4,<FOCC
000733          0378  C380 0B7E          LNJ  $B4,<CGSCH
000734          037A  F800 11DE          LDR  $R7,<FIVSEC          SYNC. UP CLOCK
000735          037C  FF00 0000          STR  $R7,<ZHRTCC          STORE RTCC WITH 5 SEC OF TICKS
000736          037E  0004          RTCN
000737          037F  F900 0000          X          SYCT  CMR   $R7,<ZHRTCC          TURN ON REAL TIME CLOCK
000738          0381  027E          *          BL    >SYCT
000739          0382  8040          *          IO   BIT1,=$R4          START I/O
000740          0384  0054
000741          0385  0703          BIOT >JPT1
000742          0386  F380 0BA3          LNJ  $B7,<ERROR          ERROR: I/O WAS NAK'ED
000743          0388  C380 0B0E          JPT1  LNJ  $B4,<INXT          INPUT NEXT STATUS
000744          038A  C800 1101          LDR  $R4,<FIS          LOOP ON INPUT STATUS UNTIL STATUS COMPLETE
000745          038C  C380 0B7E          LNJ  $B4,<CGSCH
000746          038E  8055          ST3  IO   =R5,=$R4          INPUT STATUS
000747          038F  0054
000748          0390  0703          BIOT >JPT2
000749          0391  F380 0BA3          LNJ  $B7,<ERROR          ERROR: I/O WAS NAK'ED
000750          0393  82D5          JPT2  LB   =R5,=Z'1000'          SEE IF STATUS COMPLETE
000751          0394  1000
000752          0395  0506          BBT  >JPT5
000753          0396  D870 0100          LDR  $R5,=Z'0100'
000754          0398  5701 FFFF          BDEC $R5,$          TIME DELAY FOR STATUS CHECK
000755          039A  0FF4          B    >ST3

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000752 039B 17ED JPT5 BINC $R1->JPT1 LOOP TO FINAL CCB
000753 *
000754 039C 0005 RTCF TURN OFF REAL TIME CLOCK
000755 039D C800 11DE LDR $R4,<FIVSEC CALCULATE ELAPSED TIME
000756 039F C200 0000 X SUB $R4,<ZHRTCC
000757 03A1 8755 CL =SR5
000758 03A2 8980 1129 CMZ <SBIFG CHECK FLAG FOR STOP BIT TIME TEST
000759 03A4 0980 0577 BNE <SBTE
000760 *
000761 03A6 8980 129C JPT4 CMZ <LC3SYF SEE IF SYNC. OR ASYNC. LINE
000762 03A8 09A2 BNE >SYNSP
000763 03A9 C900 11DF CMR $R4,<SSECL SEE IF NUMBER OF TICKS WITHIN
000764 03AB 0AB4 0AB4 BALE >TRYM ACCEPTABLE RANGE
000765 03AC C900 11E0 CMR $R4,<SECH
000766 03AE 080E BAL >TYXL
000767 03AF F800 1114 TRYM LDR $R7,<ERF CLEAR ERROR FLAG TO
000768 03B1 8700 1114 CL <ERF SUPPRESS REGISTER DUMP
000769 03B3 F380 0BA3 LNJ $B7,<ERROR
000770 03B5 C380 0ABD LNJ $B4,<PRTLSP ERROR: INCORRECT LINE SPEED
000771 03B7 FF00 1114 STR $R7,<ERF
000772 03B9 4C0A LDV $R4,=10
000773 03BA CF00 110D STR $R4,<CSB
000774 03BC 8AD2 TYXL INC =SR2
000775 03BD C800 129A LDR $R4,<LCT35 SET LCI TO NEXT SPEED
000776 03BF CA70 0100 ADD $R4,=X'100' CHANGE TO NEXT SPEED
000777 03C1 2D0F CMV $R2,=15 SEE IF ALL SPEED TESTED
000778 03C2 0800 0324 BAL <NXSPD BRANCH TO TEST NEXT SPEED
000779 *
000780 03C4 3E02 TSNL ADV $R3,=2 TEST NEXT LINE
000781 03C5 8F80 1155 RSTR <SAV3,=Z'0810' RESTORE REGS.
000782 03C6 0F80 031E B <JPT3
000783 *
000784 03CA 8980 111F SYNSP CMZ <PSFG
000785 03CB 0986 BNE >SYNSP1
000786 03CD 8980 112A CMZ <SHUR1
000787 03CF 0983 BNE >SYNSP1
000788 03D0 C380 0ABD LNJ $B4,<PRTLSP PRINT SYNCHRONOUS LINE SPEED
000789 03D2 4C08 SYNSP1 LDV $R4,=8
000790 03D3 CF00 110D STR $R4,<CSB
000791 03D5 0FEF B >TSNL
000792 *
000793 *
000794 *
000795 *
000796 03D6 0005 NUSPD RTCF TURN OFF RTC
000797 03D7 8700 110D CL <CSB
000798 03D9 DB80 03A6 LAB $B5,<JPT4 SET RUPT POINTER TO RETURN TO LEVEL 4
000799 03DB DF80 1281 STB $B5,<ISA4P
000800 03DD 8E70 8004 LEV =Z'8004'
000801 03DF 0FF7 B >NUSPD
000802 *
000803 *
000804 *
000805 *
000806 *
000807 03E0 0005 IRRG RTCF TURN OFF RTC
000808 03E1 C380 0B14 LNJ $B4,<INRG INPUT RANGE INTO $R5
000809 03E3 8AD1 INC =SR1 FORM TOTAL RESIDUAL RANGE
000810 03E4 1204 =EZ $R1->IRRG2 FROM OTHER CCB'S
000811 03E5 DA70 03C0 IRRG1 ADD $R5,=X'3C0'
000812 03E7 17FE BINC $R1->IRRG1
000813 03E8 C800 11DE IRRG2 LDR $R4,<FIVSEC PUT ELAPSED TIME IN $R4
000814 03EA DB80 03CA LAB $B5,<SYNSP SET RUPT POINTER FOR RETURN TO LEVEL 4
000815 03EC DF80 1281 STB $B5,<ISA4P
000816 03EE 8E70 8004 LEV =Z'8004'
000817 03F0 0FF0 B >IRRG
000818 *
000819 *
000820 *
000821 *
000822 *
000823 03F1 C870 4420 CRPTB LDR $R4,=A'D '
000824 03F3 CF00 1364 STR $R4,<ERMG+1
000825 03F5 B380 098B LNJ $B3,<CLFG CLEAR ALL TEST FLAGS
000826 03F7 8A80 110F INC <CRCFG SET FLAG FOR DTALOP SUB-ROUTINE
000827 03F9 8700 111A CL <MASK SET MASK FOR LVSC
000828 03FB C870 C802 LDR $R4,=Z'C802' SET LCI CONFIG. FOR
000829 03FD CF00 1290 STR $R4,<LC2CFR RECV., ODD PARITY, CRC16
000830 03FF 4E20 ADV $R4,=X'20'
000831 0400 CF00 1291 STR $R4,<LC2CFX XMIT LCT CONFIG. ODD PARITY
000832 0402 EB80 1197 LAB $B6,<CPFLG LOAD ADDRESS OF LCT FLAG TABLE
000833 0404 C876 LDR $R4,=$B6 LOAD $R4 WITH LCT FLAG
000834 0405 CF00 129A CRPTB1 STR $R4,<LCT2FG STORE INTO LCT TABLE
000835 0407 F870 0E18 LDR $R7,=X'E18' STORE INITIAL SPEED FOR ASYC. LINES
000836 0409 FF00 1292 STR $R7,<LCT25
000837 040B 7C06 LDV $R7,=6 NUMBER OF 30 MS TIME DELAY FOR TRANSFER
000838 *
000839 *
000840 *
000841 040C 9B80 1176 T8 LAB $B1,<ASYN1 TEST ASYNCHRONOUS LINES WITH BAUD RATE
000842 040E B380 0881 T8A LNJ $B3,<DTALOP 9600 BAUD, WITH 2 STOP BITS
000843 0410 0F85 B GO TO LOUP SUB-ROUTINE
000844 0411 12D9 DC <WCP2 ADDRESS TABLE FOR BLOCK WRITE
000845 0412 128E DC <LCT2 LCT TABLE
000846 0413 003F DC (DFLT=DOUT) RANGE IN WORDS
000847 0414 119E DC <DOUT
000848 *
000849 *
000850 *
000851 0415 C871 T9 LDR $R4,=$B1 LOAD ID TYPE INTO $R4
000852 0416 C900 1177 CMR $R4,<ASYNC CHECK ID SEE IF ALREADY DONE
000853 0418 0902 BE >T9A
000854 0419 0FF5 B >T8A
000855 041A T9A EGU $ STORE SYNC. CHAR. INTO LCT TABLE
000856 041A F870 3218 LDR $R7,=X'3218'
000857 041C FF00 1292 STR $R7,<LCT25
000858 041E 7C28 LDV $R7,=40 NUMBER OF 30 MS TIME DELAYS
000859 *
000860 *
000861 *
000862 041F 9B80 1178 LAB $B1,<SYNC TEST SYNCHRONOUS LINES (32 AS SYNC. CHAR.)
000863 0421 B380 0881 QA2 LNJ $B3,<DTALOP LOAD LINE TYPE
GO TO LOUP SUB-ROUTINE

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000965 04A8 CF00 1121          STR    $R4,<RCRC
000966                      *
000967 04AA E380 0B5E          LNJ    $B6,<RDLCI          READ XMIT CRC RESIDUE
000968 04AC 0000          RCRXL  DC    0          RAM ADDRESS
000969 04AD 0002          DC    2          RANGE
000970                      *
000971 04AE C800 112E          LDR    $R4,<LCTV
000972 04B0 4018          SCL    $R4,8
000973 04B1 CF00 112D          STR    $R4,<XCRC
000974                      *
000975 04B3 E880 1185          LAB    $B6,<CRCI6          LOAD CRC TYPE
000976 04B5 E800 110C          LDR    $R6,<CS          LOAD CHAR. SIZE
000977 04B7 BC80 08EF          LDB    $B3,<TG3          LOAD ADDRESS OF OUTPUT BUFFER
000978 04B9 C842 0006          LDR    $R4,$B2.6          CHECK FLAG TO SEE IF OUTPUT BUFFER CORRECT
000979 04BB 4D17          CMV    $R4,=X'17'
000980 04BC 0983          BNE    >CKPCR
000981 04BD B880 1D00          LAB    $B3,<DINI
000982 04BF F800 1120          CKPCR  LDR    $R7,<RANGE          LOAD RANGE OF TRANSFER IN WORDS
000983                      *
000984 04C1 C380 09A0          LNJ    $B4,<CRC          CALCULATE CRC FOR THE TRANSFERS
000985                      *
000986 04C3 E800 110E          LDR    $R6,<CRCAC          LOAD $R6 WITH SHOULD BE
000987 04C5 D800 1121          LDR    $R5,<RCRC          LOAD CRC RECV. RESIDUE
000988 04C7 E955          CMR    $R6,=$R5          COMPARE RECEIVE CRC
000989 04C8 0903          BE    >RCRCG
000990 04C9 F380 0BA3          LNJ    $B7,<ERROR          ERROR: RECEIVE CRC RESIDUE WRONG
000991 04CB D800 112D          RCRCG  LDR    $R5,<XCRC          LOAD XMIT CRC RESIDUE
000992 04CD E955          CMR    $R6,=$R5          COMPARE XMIT CRC
000993 04CE 0903          BE    >EDCK
000994 04CF F380 0BA3          LNJ    $B7,<ERROR          ERROR: XMIT RESIDUE WRONG
000995 04D1 8F80 1145          EDCK  RSTR    <SAV2,=Z'91E0'          RESTORE REGS.
000996 04D3 91E0          DC    3
000997 04D4 8385          JMP    $B5          GO BACK TO DATLOP SUB-ROUTINE
000998                      *
000999                      *
001000                      *
001001                      *
001002                      *
001003 04D5 C870 4520          CHSZTD LDR    $R4,=A'E '
001004 04D7 CF00 1364          STR    $R4,<ERMG+1
001005 04D9 B380 098B          LNJ    $B3,<CLFG          CLEAR ALL TEST FLAGS
001006 04DB 8A80 1109          INC    <CHSZFG          SET CHARACTER SIZE TEST FLAG
001007 04DD 4C02          LDU    $R4,=2          LOAD CONFIGURATION FOR 1 STOP BIT ODD
001008 04DE CF00 12A0          STR    $R4,<LC4CFR          PARITY: 5 BIT CHAR. RECV.
001009 04E0 4E20          ADV    $R4,=X'20'
001010 04E1 CF00 12A1          STR    $R4,<LC4CFX
001011 04E3 C870 1F1F          LDR    $R4,=Z'1F1F'          STORE XMIT CONF.
001012 04E5 CF00 111A          STR    $R4,<MASK          SET MASK FOR FIVE BIT CHAR.
001013 04E7 89C0 0C30          CMZ    H15PD
001014 04E9 0980 051A          BNE    <T36          IF SYNC 9600 BAUD, NO 5 BIT MODE
001015 04EB 7C0A          CHSZT1 LDU    $R7,=10          NUMBER OF 30 MS TIME DELAY FOR TRANSFER
001016 04EC C870 0E18          LDR    $R4,=X'E18'          SET LINE SPEED FOR ASYNC. LINE
001017 04EE CF00 12A2          STR    $R4,<LCT45
001018                      *
001019                      *
001020                      *
001021                      *
001022 04F0 9880 1176          T14   LAB    $B1,<ASYN1          TEST ASYNCHRONOUS LINES WITH BAUD RATE
001023 04F2 B380 0881          T14A  LNJ    $B3,<DTALOP          9600 BAUD, WITH 1 STOP BIT
001024 04F4 0F85          B    >T15          GO TO LOOP SUB-ROUTINE
001025 04F5 12DE          DC    <WCP3          ADDRESS TABLE FOR BLOCK WRITE
001026 04F6 129E          DC    <LCT4          LCT TABLE
001027 04F7 003F          DC    (DFLT-DOUT)          RANGE IN WORDS
001028 04F8 119E          DC    <DOUT
001029                      *
001030                      *
001031                      *
001032 04F9 C871          T15   LDR    $R4,=$B1          TEST CURRENT LOOP ASYNCHRONOUS
001033 04FA C900 1177          CMR    $R4,<ASYN          LOAD ID TYPE INTO $R4
001034 04FC 0902          BE    >T15A          CHECK TO SEE IF ALREADY DONE
001035 04FD 0FF5          B    >T14A
001036 04FE 04FE          EQU    $          T15A
001037 04FE F870 E618          LDR    $R7,=Z'E618'          STORE SYNC. CHAR. INTO LCT TABLE
001038 0500 FF00 12A2          STR    $R7,<LCT45
001039 0502 7C28          LDU    $R7,=40          NUMBER OF 30 MS TIME DELAYS
001040                      *
001041                      *
001042                      *
001043 0503 9880 1178          GA3   LAB    $B1,<SYNC          TEST SYNCHRONOUS LINES (E6 AS SYNC. CHAR.)
001044 0505 B380 0881          LNJ    $B3,<DTALOP          LOAD LINE TYPE
001045 0507 0F85          B    >T16          GO TO LOOP SUB-ROUTINE
001046 0508 12DE          DC    <WCP3          ADDRESS TABLE FOR BLOCK WRITE
001047 0509 129E          DC    <LCT4          LCT TABLE
001048 050A 002F          DC    (DFLT-DOUT1)          RANGE IN WORDS
001049 050B 11AE          DC    <DOUT1
001050                      *
001051                      *
001052                      *
001053 050C C871          T16   LDR    $R4,=$B1          TEST BI-SYNCHRONOUS LINES (E6 AS SYNC. CHAR.)
001054 050D C900 1179          CMR    $R4,<BISYN          LOAD ID TYPE INTO $R4
001055 050F 0902          BE    >T17          CHECK TO SEE IF ALREADY DONE
001056 0510 0FF5          B    >GA3
001057                      *
001058                      *
001059                      *
001060 0511 9880 117A          T17   LAB    $B1,<SYN188          TEST MIL 188 SYNCHRONOUS (E6 AS SYNC. CHAR.)
001061 0513 B380 0881          LNJ    $B3,<DTALOP          LOAD LINE TYPE
001062 0515 0F85          B    >T36          GO TO LOOP SUB-ROUTINE
001063 0516 12DE          DC    <WCP3          ADDRESS TABLE FOR BLOCK WRITE
001064 0517 129E          DC    <LCT4          LCT TABLE
001065 0518 002F          DC    (DFLT-DOUT1)          RANGE IN WORDS
001066 0519 11AE          DC    <DOUT1
001067                      *
001068                      *
001069                      *
001070 051A 8A80 110F          T36   INC    <CRCFG          CHANGE CONFIGURATION TO NEXT CHAR. SIZE
001071 051C C800 12A0          LDR    $R4,<LC4CFR
001072 051E CAT0 4000          ADD    $R4,=X'4000'
001073 0520 0608          BCT    >CHSZDE          IF CARRY ALL CHAR. SIZES HAVE BEEN TESTED
001074 0521 CF00 12A0          STR    $R4,<LC4CFR          STORE RECV CONFIG.
001075 0523 4E20          ADV    $R4,=X'20'
001076 0524 CF00 12A1          STR    $R4,<LC4CFX          STORE XMIT CONFIG.
001077 0526 0F80 04EB          B    <CHSZT1          DO NEXT CHAR.

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001077 *
001078 0528 8700 110F CHSZDE CL <CRCFG CLEAR CRC AND PARITY FLAG
001079 052A 8700 1109 CL <CHSZFG CLEAR CHARACTER SIZE TEST FLAG
001080 052C 8382 JMP $B2 JUMP OUT OF TEST
*
* -----
*
* STOP BIT TEST
*
001081 *
001082 *
001083 *
001084 *
001085 *
001086 052D C870 4620 STBITE LDR $R4,=A'F '
001087 052F CF00 1364 STR $R4,=ERMG+1
001088 0531 B380 0988 LNJ $B3,<CLFG CLEAR ALL TEST FLAGS
001089 0533 CB80 0546 LAB $B4,<SBIT1 SET UP INTERRUPT VECTORS AND
001090 0535 CF80 1281 STB $B4,<ISA4P POINTERS TO CHANGE RUNNING LEVEL TO 4
001091 0537 CB80 127E LAB $B4,<ISA4+$AF
001092 0539 CF80 0004 X STB $B4,<ZHISAZ+4
001093 *
001094 053B C800 1119 LDR $R4,<HRTZ CALCULATE THE LOW AND THE
001095 053D 4F06 MLV $R4,=6 HIGH RANGE OF ACCEPTABLE CLOCK TICKS
001096 053E 4EF7 ADV $R4,=9
001097 053F CF00 11DF STR $R4,<SECL
001098 0541 4E12 ADV $R4,=18
001099 0542 CF00 11E0 STR $R4,<SECH
001100 0544 8E70 8004 LEV =Z'8004' CHANGE RUNNING LEVEL TO 4
001101 0546 4C01 SBIT1 LDV $R4,=1 SET RTC LEVEL TO 1
001102 0547 CF00 0000 X STR $R4,<ZHRTCL
001103 *
001104 0549 CB80 05AC LAB $B4,<NBITT SET RUPT POINTER FOR RTC INTERRUPT
001105 054B CF80 127B STB $B4,<ISA1P
001106 *
001107 054D CB80 1278 LAB $B4,<ISA1+$AF SET RUPT VECTOR FOR LEVEL 1 RUPT
001108 054F CF80 0001 X STB $B4,<ZHISAZ+1
001109 *
001110 0551 8700 129C CL <LC3SYF CLEAR SYNC. FLAG FOR ASYNC. LINE
001111 0553 8A80 1129 INC <SBTIFG SET STOP BIT TIME TEST FLAG
001112 0555 C870 C002 LDR $R4,=Z'C002' LOAD AND STORE INITIAL LINE CONF.
001113 0557 CF00 1298 STR $R4,<LC3CFR FOR RECV.
001114 0559 4E20 ADV $R4,=X'20'
001115 055A CF00 1299 STR $R4,<LC3CFX FOR XMIT
001116 055C C870 0318 LDR $R4,=X'318' LOAD $R4 WITH SPEED FOR ASYNC. LINE
001117 055E 9B80 1175 LAB $B1,<ASYN LOAD LINE TYPE
001118 0560 EB80 1231 LAB $B6,<SBITBL LOAD ADDRESS OF CHAR. NUMBER TABLE
001119 0562 B380 0318 LNJ $B3,<SPDTLP GO TO SPEED TIME LOOP
001120 *
001121 0564 C870 0218 LDR $R4,=X'218' LOAD $R4 WITH SPEED SELECT FOR NEW ASYC.
001122 0566 9B80 1176 LAB $B1,<ASYN1 LOAD ADDRESS OF LINE TYPE
001123 0568 B380 0318 LNJ $B3,<SPDTLP GO TO SPEED TIME LOOP
001124 *
001125 056A C870 0218 LDR $R4,=X'218'
001126 056C 9B80 1177 LAB $B1,<ASYN
001127 056E B380 0318 LNJ $B3,<SPDTLP
001128 *
001129 CALL ZV$RD RE-INITIALIZE INTERRUPT POINTERS AND VECTORS
*
001130 0570 FBF0 0001 X
001131 0572 D380 0000 CL <SBIFG CLEAR STOP BIT TEST FLAG
001132 0574 8700 1129 JMP $B2 JUMP OUT OF TEST
*
* *****
*
* STOP BIT TIME CHECKED HERE
*
001133 *
001134 *
001135 *
001136 *
001137 0577 C940 0C68 SBTE CMR $R4,SECH SEE IF CLOCK TICK WITHIN
001138 0579 0803 BAL >SBTE1 ACCEPTED RANGE
001139 057A F380 0BA3 LNJ $B7,<ERRORR ERROR: STOP BITS ARE TOO LONG
001140 057C C900 11DF SBTE1 CMR $R4,<SECL
001141 057E 0A03 BAG >SBTE2
001142 057F F380 0BA3 LNJ $B7,<ERRORR ERROR: STOP BITS ARE TOO SHORT
001143 0581 D800 1298 SBTE2 LDR $R5,<LC3CFR LOAD LCT CONFIG.
001144 0583 8955 LBT =SR5,=Z'0800' SEE IF BIT SET AND SET IT
001145 0584 0800
001146 0585 0509 BBT >SBTE3 IF TRUE TRY SLOW SPEED FOR 1.5 STOP BIT
001147 0586 DF00 1298 STR $R5,<LC3CFR STORE LCT CONFIG. WITH 2 STOP BITS
001148 0588 5E20 ADV $R5,=X'20'
001149 0589 DF00 1299 STR $R5,<LC3CFX STORE CONFIG. XMIT
001150 058B 8AD2 INC =SR2 INCREMENT INDEX
001151 058C 0F80 0326 B <NXSPD1
*
001152 058E 2D03 SBTE3 CMV $R2,=3 SEE IF ALL STOP BIT S CHECKED
001153 058F 0900 05A3 BE <SBTE4
001154 0591 4C02 LDV $R4,=2 CHANGE CONFIG. FOR 5 BIT CHAR.
001155 0592 CF00 1298 STR $R4,<LC3CFR STORE RECV CONFIG.
001156 0594 4E20 ADV $R4,=X'20'
001157 0595 CF00 1299 STR $R4,<LC3CFX STORE XMIT CONFIG.
001158 0597 8AD2 INC =SR2 INCREMENT INDEX
001159 0598 C870 0218 LDR $R4,=X'218' LDR $R4 TO SET LINE SPEED TO 75 BAUD
001160 059A D801 LDR $R5,$B1 IF NEW ASYC. RESET $R4 FOR 75 BAUD
001161 059B D900 1175 CMR $R5,<ASYN
001162 059D 0900 0324 BE <NXSPD
001163 059F C870 0118 LDR $R4,=X'118'
001164 05A1 0F80 0324 B <NXSPD
*
001165 *
001166 05A3 C870 C002 SBTE4 LDR $R4,=Z'C002' LOAD AND STORE BEGINNING CONF.
001167 05A5 CF00 1298 STR $R4,<LC3CFR FOR RECV.
001168 05A7 4E20 ADV $R4,=X'20'
001169 05A8 CF00 1299 STR $R4,<LC3CFX FOR XMIT
001170 05AA 0F80 03C4 B <T$NL
*
*
*
* *****
*
* CLOOOCK INTERRUPT ROUTINE
*
001171 *
001172 *
001173 *
001174 *
001175 *
001176 *
001177 05AC 0005 NBITT RTCF TURN OFF CLOCK
001178 05AD F380 0BA3 LNJ $B7,<ERRORR ERROR: STATUS NOT COMPLETE AFTER 5 SEC.
001179 05AF DB80 0326 LAB $B5,<NXSPD1 RETRY THE LINE
001180 05B1 DF80 1281 STB $B5,<ISA4P WITH SAME CONFIG.
001181 05B3 8E70 8004 LEV =Z'8004'
001182 05B5 0FF7 B >NBITT
*
*
*
* -----

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001187 *
001188 *
001189 *
001190 05B6 C870 4720 PARERF LDR $R4,=A*G #
001191 05B8 CF00 1364 STR $R4,<ERMG+1
001192 05BA B380 098B LNJ $B3,<CLFG CLEAR TEST FLAGS
001193 05BC 8A80 111D INC <PAKFG
001194 05BE C870 C802 LDR $R4,=Z'C802' LOAD CONFIG. ON REC. FOR ODD PARITY
001195 05C0 CF00 1290 STR $R4,<LC2CFR
001196 05C2 CA70 1020 ADD $R4,=X'1020' SET CONFIG. ON XMIT TO EVEN PARITY
001197 05C4 CF00 1291 STR $R4,<LC2CFX
001198 05C6 C870 F017 LDR $R4,=Z'F017' SET CCP FLAG FOR PARITY ONLY
001199 05C8 CF00 1294 STR $R4,<LCT2FG
001200 05CA F870 0E18 LDR $R7,=X'E18' STORE INITIAL SPEED FOR ASYC. LINES
001201 05CC FF00 1292 STR $R7,<LCT2S
001202 05CE 7C06 LDV $R7,=6 NUMBER OF 30 MS TIME DELAY FOR TRANSFER
001203 *
001204 *
001205 *
001206 05CF 9880 1176 T39 LAB $B1,<ASYN1 LOAD LINE TYPE
001207 05D1 B380 0881 T39A LNJ $B3,<DTALOP GO TO LOOP SUB-ROUTINE
001208 05D3 0F85 B >T40
001209 05D4 12D9 DC <WCP2 ADDRESS TABLE FOR BLOCK WRITE
001210 05D5 128E DC <LCT2 LCT TABLE
001211 05D6 003F DC (DFLT-DOUT) RANGE IN WORDS
001212 05D7 119E DC <DOUT
001213 *
001214 *
001215 *
001216 05D8 C871 T40 LDR $R4,+$B1 LOAD IO TYPE INTO $R4
001217 05D9 C900 1177 CMR $R4,<ASYN CHECK IO SEE IF ALREADY DONE
001218 05DB 0902 BE >T40A
001219 05DC 0FF5 B >T39A
001220 05DD 05DD EQU $ STORE SYNC. CHAR. INTO LCT TABLE
001221 05DD F870 3218 LDR $R7,=X'3218'
001222 05DF FF00 1292 STR $R7,<LCT2S
001223 05E1 7C28 LDV $R7,=40 NUMBER OF 30 MS TIME DELAYS
001224 *
001225 *
001226 *
001227 05E2 9880 1178 LAB $B1,<SYNC LOAD LINE TYPE
001228 05E4 B380 0881 QA4 LNJ $B3,<DTALOP GO TO LOOP SUB-ROUTINE
001229 05E6 0F85 B >T41
001230 05E7 12D9 DC <WCP2 ADDRESS TABLE FOR BLOCK WRITE
001231 05E8 128E DC <LCT2 LCT TABLE
001232 05E9 002F DC (DFLT-DOUT1) RANGE IN WORDS
001233 05EA 11AE DC <DOUT1
001234 *
001235 *
001236 *
001237 05EB C871 T41 LDR $R4,+$B1 LOAD IO TYPE INTO $R4
001238 05EC C900 1179 CMR $R4,<B1SYNC CHECK IO SEE IF ALREADY DONE
001239 05EE 0902 BE >T42
001240 05EF 0FF5 B >QA4
001241 *
001242 *
001243 *
001244 05F0 9880 117A T42 LAB $B1,<SYN188 LOAD LINE TYPE
001245 05F2 B380 0881 LNJ $B3,<DTALOP GO TO LOOP SUB-ROUTINE
001246 05F4 0F85 B >T45
001247 05F5 12D9 DC <WCP2 IORB ADDRESS FOR BLOCK WRITE
001248 05F6 128E DC <LCT2 LCT TABLE
001249 05F7 002F DC (DFLT-DOUT1) RANGE IN WORDS
001250 05F8 11AE DC <DOUT1
001251 *
001252 05F9 8700 111D T45 CL <PAKFG
001253 05FB 8382 JMP $B2
001254 *
001255 *
001256 *
001257 *
001258 *
001259 05FC C380 0B0E PAR1 LNJ $B4,<INXT INPUT NEXT STATUS
001260 05FE 82D5 LB $R5,=X'1000'
001261 0600 0503 BBT >PAR2
001262 0601 F380 0BA3 LNJ $B7,<ERROR ERROR: STATUS COMPLETE BIT NOT SET
001263 0603 82D5 LB $R5,=X'40' SEE IF DATA CHECK ERROR BIT SET
001264 0605 0503 BBT >PAR3
001265 0606 F380 0BA3 LNJ $B7,<ERROR ERROR: DATA CHECK ERROR BIT NOT SET
001266 0608 8AD3 INC $R3
001267 0609 C380 0B0E LNJ $B4,<INXT INPUT NEXT STATUS
001268 060B 82D5 LB $R5,=X'1000'
001269 060C 1000
001269 060D 0503 BBT >PAR4
001270 060E F380 0BA3 LNJ $B7,<ERROR ERROR: STATUS NOT COMPLETE ON XMIT SIDE
001271 0610 82D5 LB $R5,=X'40'
001272 0611 0040
001272 0612 0583 BBT >PAR5
001273 0613 F380 0BA3 LNJ $B7,<ERROR ERROR: DATA CHECK ERROR BIT SET ON XMIT
001274 0615 0F80 0985 PAR5 B <JP5A
001275 *
001276 *
001277 *
001278 *
001279 *
001280 *
001281 0617 C870 4820 STIOG LDR $R4,=A*H #
001282 0619 CF00 1364 STR $R4,<ERMG+1
001283 061B B380 098B LNJ $B3,<CLFG CLEAR ALL TEST FLAGS
001284 061D A800 1181 LDR $R2,<BIT2 LOAD $R2 WITH FUNCTION COMMAND
001285 061F 8A80 112B INC <STPFG
001286 0621 8700 12AD CL <LCTSYF CLEAR SYNC. FLAG FOR ASYNC. LINES
001287 0623 C870 0E18 LDR $R4,=X'E18'
001288 0625 CF00 12A9 STR $R4,<LCT5S
001289 0627 7C02 LDV $R7,=2 NUMBER OF 30 MS DELAY
001290 *
001291 *
001292 *
001293 *
001294 0628 9880 1176 T19 LAB $B1,<ASYN1 LINE TYPE
001295 062A B380 0881 T19A LNJ $B3,<DTALOP

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001405 06A4 DF00 06B1          *   STR   $R5,<TSTF8B
001406
001407 06A6 E380 0B5E          *   LNJ   $B6,<RDLC   READ LCT
001408 06A8 0000                TSTF2B DC   0         ADDRESS
001409 06A9 0002                DC   2
001410          *
001411 06AA 8980 112E          *   CMZ   <LCIV
001412 06AC 0903                BE   >T51F7K
001413 06AD F380 0BA3          *   LNJ   $B7,<ERROR   ERROR: RECV. LCT STATUS NOT CLEAR
001414          *
001415 06AF E380 0B5E          *   TSTF7K LNJ   $B6,<RDLC   READ LCT
001416 06B1 0000                TSTF8B DC   0
001417 06B2 0002                DC   2
001418          *
001419 06B3 8980 112E          *   CMZ   <LCIV           CHECK LCT STATUS IS CLEARED
001420 06B5 0903                BE   >T51F2
001421 06B6 F380 0BA3          *   LNJ   $B7,<ERROR   ERROR: LCT STATUS IS NOT CLEAR
001422 06B8 9800 1120          *   LDR   $R1,<RANGE   CALCULATE RANGE OF GOOD DATA TO BE CHECK
001423 06BA 9F00 1115          *   STR   $R1,<FBRG
001424 06BC 1001                SOL   $R1,1           CHANGE TO BYTE RANGE
001425 06BD 9200 1126          *   SUB   $R1,<TP1
001426 06BF 1041                SOR   $R1,1           CHANGE TO WORDS
001427 06C0 88D1                DEC   =R1
001428 06C1 9F00 1120          *   STR   $R1,<RANGE
001429          *
001430 06C3 8980 12AD          *   CMZ   <LC5SYF       CHECK FOR SYNC LINE
001431 06C5 0917                BE   >T51F4C
001432 06C6 1E04                ADV   $R1,=4
001433 06C7 C800 1126          *   LDR   $R4,<TP1     SYNC LINE CHECK FOR LINE MARKING
001434 06C9 C200 1127          *   SUB   $R4,<TP2
001435 06CB 4041                SOR   $R4,1
001436 06CC CA51                ADD   $R4,=$R1
001437 06CD 4EF9                ADV   $R4,-7
001438 06CE CF00 1127          *   STR   $R4,<TP2
001439 06D0 C870 FFFF          *   LDR   $R4,=Z'FFFF'  LINE MARKING DATA
001440 06D2 C910 1C00          *   CMR   $R4,<DIN,$R1
001441 06D4 0903                BE   >T51F5
001442 06D5 F380 0BA3          *   LNJ   $B7,<ERROR   ERROR: SYNC. LINE NOT MARKING WITH XMIT OFF
001443 06D7 8AD1                TSTF5  INC   =R1
001444 06D8 9900 1127          *   CMR   $R1,<TP2     SEE IF ALL MARKING DATA CHECK
001445 06DA 0882                BAGE  >T51F4C
001446 06DB 0FF7                B     >T51F5A
001447          *
001448 06DC C800 11DD          *   TSTF4C LDR   $R4,<DFLT   CHECK REMAINING DATA IS DEFAULT (5555)
001449 06DE 1E07                ADV   $R1,=7
001450 06DF C910 1C00          *   TSTF4D CMR   $R4,<DIN,$R1
001451 06E1 0903                BE   >T51F4E
001452 06E2 F380 0BA3          *   LNJ   $B7,<ERROR   ERROR:RECEIVE DATA IN NO DATA AREA
001453 06E4 8AD1                TSTF4E INC   =R1
001454 06E5 9900 1115          *   CMR   $R1,<FBRG
001455 06E7 0878                BAL   >T51F4D
001456 06E8 0F80 0940          *   B     <JP4         CHECK FINAL GOOD DATA IN DTALOP
001457          *
001458          *
001459          *
001460          *
001461          *
001462          *
001463          *
001464 06EA C870 4A20          *   UORNTL LDR   $R4,=A'J '
001465 06EC CF00 1364          *   STR   $R4,<ERMG+1   CLEAR ALL TEST FLAGS
001466 06EE B380 098B          *   LNJ   $B3,<CLFG
001467 06F0 8700 12CA          *   CL   <LCT8FG
001468 06F2 8A80 112C          *   INC   <WDBFG
001469 06F4 8A80 1124          *   INC   <RUNFG       SET UNDERRUN AND OVERRUN TEST FLAG
001470 06F6 C870 021D          *   LDR   $R4,=X'21D'
001471 06F8 CF00 12B6          *   STR   $R4,<LCT6TF
001472 06FA 7C06                LDV   $R7,=6           NUMBER OF 30 MS TIME DELAY FOR TRANSFER
001473 06FB C870 0E18          *   LDR   $R4,=X'E18'
001474 06FD CF00 12B4          *   STR   $R4,<LCT6S   SET LINE SPEED FOR ASYC. LINES
001475          *
001476          *
001477          *
001478          *
001479 06FF 9880 1176          *   T24  LAB   $B1,<ASYN1   LOAD LINE TYPE
001480 0701 B380 0881          *   LNJ   $B3,<DTALOP   GO TO LOOP SUB-ROUTINE
001481 0703 0F85                B     >T25
001482 0704 12E3                DC   <WCP4           ADDRESS TABLE FOR BLOCK WRITE
001483 0705 12AF                DC   <LCT6           LCT TABLE
001484 0706 003F                DC   (DFLT=DOUT)    RANGE IN WORDS
001485 0707 119E                DC   <DOUT
001486          *
001487          *
001488          *
001489 0708 C871                *   T25  LDR   $R4,+$B1   LOAD ID TYPE INTO $R4
001490 0709 C900 1177          *   CMR   $R4,<ASYN    CHECK TO SEE IF ALREADY DONE
001491 070B 0902                BE   >T25Z
001492 070C 0FF5                B     >T24Z
001493          *
001494 070D 070D                *   T25Z EQU   $
001495 070F F870 401D          *   CL   <WDBFG
001496 0711 FF00 12B6          *   LDR   $R7,=Z'401D'
001497 0713 F870 1618          *   STR   $R7,<LCT6TF
001498 0715 FF00 12B4          *   LDR   $R7,=Z'1618'  STORE SYNC. CHAR. INTO LCT TABLE
001499 0717 7C40                STR   $R7,<LCT6S
001500          *
001501          *
001502          *
001503 0718 9880 1178          *   LDV   $R7,=X'40'   NUMBER OF 30 MS TIME DELAYS
001504 071A B380 0881          *   LAB   $B1,<SYNC    TEST SYNCHRONOUS LINES (16 AS SYNC. CHAR.)
001505 071C 0F85                *   QA6  LNJ   $B3,<DTALOP  LOAD LINE TYPE
001506 071D 12E3                *   B     >T26         GO TO LOOP SUB-ROUTINE
001507 071E 12AF                *   DC   <WCP4           ADDRESS TABLE FOR BLOCK WRITE
001508 071F 002F                *   DC   <LCT6           LCT TABLE
001509 0720 11AE                *   DC   (DFLT=DOUT1)   RANGE IN WORDS
001510          *
001511          *
001512          *
001513 0721 C871                *   T26  LDR   $R4,+$B1   LOAD ID TYPE INTO $R4
001514 0722 C900 1179          *   CMR   $R4,<BISYN    CHECK TO SEE IF ALREADY DONE
001515 0724 0902                *   BE   >T27
001516 0725 0FF5                *   B     >QA6
001517          *

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001518 * TEST MIL 188 SYNCHRONOUS (16 AS SYNC. CHAR.)
001519 *
001520 0726 9880 117A T27 LAB $B1,<SYN188 LOAD LINE TYPE
001521 0728 B380 0881 LNJ $B3,<DTALOP GO TO LOOP SUB=ROUTINE
001522 072A 0F85 B >T32
001523 072B 12E3 DC <WCP4 ADDRESS TABLE FOR BLOCK WRITE
001524 072C 12AF DC <LCT6 LCT TABLE
001525 072D 002F DC (DFLT=DOUT1) RANGE IN WORDS
001526 072E 11AE DC <DOUT1
001527 *
001528 *
001529 072F 8700 112C T32 CL <WDBFG
001530 0731 8700 1124 CL <RUNFG
001531 0733 8700 12CA CL <LCT8FG
001532 0735 8382 JMP $B2
001533 *
001534 *
001535 * * * * *
001536 *
001537 * UNDERRUN AND OVERRUN CHECK ROUTINE
001538 *
001539 0736 88D3 RUNCK DEC =SR3
001540 0737 E380 099C LNJ $B6,<BASE LOAD $R5 WITH LCT BASE ADDRESS
001541 0739 5E3C ADV $R5,<X'3C'
001542 073A DF00 0761 STR $R5,<RNCK1
001543 *
001544 073C 8980 112C CMZ <WDBFG
001545 073E 080F BAL >RNCK4
001546 073F C800 1101 LDR $R4,<FIS
001547 0741 C380 0B7E LNJ $B4,<CGSCH INPUT STATUS TO CHECK DATA
001548 0743 8055 IO =SR5,=$R4 SERVICE ERROR (BIT2) SET
001549 0744 0054
001549 0745 0703 BIOT >RNCK4A
001550 0746 F380 0BA3 LNJ $B7,<ERROR ERROR; I/O NAK'ED
001551 0748 82D5 RNCK4A LB =SR5,=Z'2000'
001552 0749 2000
001552 074A 0503 BBT >RNCK4
001553 074B F380 0BA3 LNJ $B7,<ERROR ERROR; DATA SERVICE ERROR BIT NOT SET ON OVR
001554 074D 8AD3 RNCK4 INC =SR3 CHECK XMIT DATA SERVICE ERROR
001555 074E 8980 112C CMZ <WDBFG
001556 0750 0A0F BAG >RNCK5
001557 0751 C800 1101 LDR $R4,<FIS
001558 0753 C380 0B7E LNJ $B4,<CGSCH
001559 0755 8055 IO =SR5,=$R4
001560 0756 0054
001560 0757 0703 BIOT >RNCK4B
001561 0758 F380 0BA3 LNJ $B7,<ERROR ERROR; I/O NAK'ED
001562 *
001563 075A 82D5 RNCK4B LB =SR5,=Z'2000'
001564 075B 2000
001564 075C 0503 BBT >RNCK5
001565 075D F380 0BA3 LNJ $B7,<ERROR ERROR; DATA SERVICE ERROR BIT NOT SET ON UDR
001566 075F E380 0B5E RNCK5 LNJ $B6,<RDLC READ LCT
001567 0761 0000 RNCK1 DC 0
001568 0762 0002 DC 2
001569 *
001570 0763 8980 112C CMZ <WDBFG CHECK FLAG
001571 0765 080A BAL >RNCK9
001572 0766 8280 112E LB <LCTV,=Z'0200'
001573 0768 0200
001573 0769 050C BBT >RNCK6
001574 076A F380 0BA3 LNJ $B7,<ERROR ERROR; OVERRUN FAIL TO SET BIT IN LRS
001575 076C 8980 112C CMZ <WDBFG CHECK FLAG TO TEST UNDERRUN BIT
001576 076E 0987 BNE >RNCK6
001577 *
001578 076F 8280 112E RNCK9 LB <LCTV,=Z'0100'
001579 0771 0100
001579 0772 0503 BBT >RNCK6
001580 0773 F380 0BA3 LNJ $B7,<ERROR ERROR; UNDERRUN FAIL TO SET BIT IN LRS
001581 0775 0F80 0985 RNCK6 B <JP5A
001582 *
001583 *
001584 * -----
001585 *
001586 * LOOP DSC INTO DSS WITH CABLE LOUP
001587 *
001588 *
001589 * THE SIGNALS ARE TIED IN ACCORDING TO THE FOLLOWING CHART:
001590 *
001591 *
001592 * -----
001593 * $ $ $ $ $ $
001594 * $ DSS $ ASYNCHRONOUS $ SYNCHRONOUS $ BROADBAND $ BROADBAND $
001595 * $ $ $ 6 MIL 188 $ 301/303 $ V35 $
001596 * -----
001597 * $ $ $ $ $ $
001598 * $ OUT CKT 1 $ IN CKT 1 $ IN CKT 1 $ IN CKT 4 $ IN CKT 1 $
001599 * $ $ $ $ $ $
001600 * -----
001601 * $ $ $ $ $ $
001602 * $ OUT CKT 2 $ IN CKT 2 $ IN CKT 2 $ IN CKT 1 $ IN CKT 2 $
001603 * $ $ $ $ $ $
001604 * -----
001605 * $ $ $ $ $ $
001606 * $ OUT CKT 3 $ IN CKT 4 $ --- $ --- $ --- $
001607 * $ $ $ $ $ $
001608 * -----
001609 * $ $ $ $ $ $
001610 * $ OUT CKT 4 $ --- $ IN CKT 4 $ --- $ --- $
001611 * -----
001612 *
001613 *
001614 0777 C870 4B20 LPCBLK LDR $R4,=A'K '
001615 0779 CF00 1364 STR $R4,<ERMG+1
001616 077B B380 098B LNJ $B3,<CLFG CLEAR ALL TEST FLAGS
001617 077D 8700 111A CL <MASK
001618 077F 9B80 1235 LAB $B1,<TLTBLA PUT ADDRESS OF SHOULD BE TABLE IN ZV5C
001619 0781 9F80 07F6 STB $B1,<LOPD7+4+SAF
001620 0783 9B80 125A LAB $B1,<C4 PUT ADDRESS OF RANGE IN ZV5C
001621 0785 9F80 07F9 STB $B1,<LOPD7+4+SAF
001622 0787 C870 FC1D LDR $R4,=Z'FC1D'
001623 0789 CF00 12BD STR $R4,<LCT75M SET MASK FO ASYC. LINE
001624 078B 4C08 LDV $R4,=8 SET RANGE FOR CHECK ROUTINE
    
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001625 078C CF00 1127          STR  $R4,<1P2
001626 078E 7C02          LDV  $R7=#2          LOAD $R7 WITH NUMBER OF 30MS DELAY
001627          *
001628 078F 9B80 1175          LAB  $B1,<ASYN        LOAD $B1 WITH ADDRESS OF LINE TYPE
001629 0791 8380 07B4          LNJ  $B3,<LOPDS        BRANCH TO LOOP DSS INTO USC AND CHECK
001630          *
001631 0793 9B80 1176          LAB  $B1,<ASYN1       LOAD ADDRESS OF LINE TYPE
001632 0795 8380 07B4          LNJ  $B3,<LOPDS
001633          *
001634 0797 9B80 1239          LAB  $B1,<TLTBLC       STORE SHOULD BE ADDRESS FOR ZV$C
001635 0799 9F80 07F6          STB  $B1,<LOPD7+4+$AF
001636 079B 9B80 1177          LAB  $B1,<ASYN        LOAD ADDRESS OF LINE TYPE
001637 079D 8380 07B4          LNJ  $B3,<LOPDS
001638          *
001639 079F 9B80 123D          LAB  $B1,<ILTBL5
001640 07A1 9F80 07F6          STB  $B1,<LOPD7+4+$AF
001641 07A3 C870 F81D          LDR  $R4,=Z'F81D'    CHANGE MASK FOR SYNC. LINES
001642 07A5 CF00 12BD          STR  $R4,<LC75M
001643 07A7 9B80 1179          LAB  $B1,<B15YNC
001644 07A9 8380 07B4          LNJ  $B3,<LOPDS
001645          *
001646 07AB 9B80 1178          LAB  $B1,<SYNC
001647 07AD 8380 07B4          LNJ  $B3,<LOPDS
001648          *
001649 07AF 9B80 117A          LAB  $B1,<SYN188
001650 07B1 8380 07B4          LNJ  $B3,<LOPDS
001651 07B3 8382          JMP  $B2
001652          *
001653          * * * * *
001654          *
001655          *          LOOP AND TEST DSS INTO DSS LOOP
001656          *
001657          *
001658 07B4 9F80 07B9          LOPDS STB  $B1,<LOPD1    STORE ADDRESS OF LINE TYPE
001659 07B6 8753          CL    =SR3
001660 07B7 0380 0B38          LOPD  LNJ  $B5,<FLN      FIND LINE ON
001661 07B9 0000          LOPD1 RESV $AF,0        LINE TYPE ADDRESS
001662 07BA 8383          JMP  $B3
001663 07BB C380 0AF3          LNJ  $B4,<GENITZ       GENERAL INITIALIZE
001664          *
001665          *          WRITE CHANNEL PROGRAM AND TLU TABLE INTO RAM
001666          *
001667 07BD C570 FF80          AND  $R4,=Z'FF80'
001668 07BF C470 0040          OR   $R4,=X'40'
001669 07C1 CF00 110A          STR  $R4,<CHNZ
001670          CALL ZV$MLW,CHLP5,WCP5,HX200,CHNZ
001671          *
001671 07C3 FBC0 0003          X
001671 07C5 D380 0000          X
001671 07C7 0F80
001671 07C8 0E49
001671 07C9 12E8
001671 07CA 125D
001671 07CB 110A          CALL ZV$MLW,TLUTB2,WCP5B,HX400,CHNZ
001672          *
001673 07D5 D380 0B24          *
001674 07D7 12B9          DC   <LC17          SEND OUT LCT LOCATIONS
001675          *
001676 07D8 C800 1127          LDR  $R4,<1P2        LOAD $R4 WITH RANGE
001677 07DA D380 0B82          LNJ  $B5,<MCCB        FORM CLB TO TRANSFER DSS INTO MEMORY
001678 07DC 1C00          DC   <DIN
001679 07DD 0060          DC   X'60'
001680          *
001681 07DE C800 10FC          LDR  $R4,<FOCC        FORM I/O CONTROL WORD
001682 07E0 C380 0B7E          LNJ  $B4,<CGSCH       FOR START I/O
001683 07E2 8000 1180          IO   <BIT1,=$R4
001684 07E4 0054
001685 07E5 0703          BIOT >LOPD2
001686 07E6 F380 0BA3          LNJ  $B7,<ERROR       ERROR: I/O WAS NAK'ED
001687 07E8 D380 0B4C          LOPD2 LNJ  $B5,<TDLAY   TIME DELAY
001688          *
001688 07EA C380 0B0E          LNJ  $B4,<INXT        INPUT NEXT STATUS
001689 07EC 82D5          LB   =SR5,=Z'1000'   CHECK STATUS COMPLETE
001690 07ED 1000
001690 07EE 0503          BBT  >LOPD7
001691 07EF F380 0BA3          LNJ  $B7,<ERROR       ERROR: STATUS NOT COMPLETE AFTER GNB
001692          LOPD7 CALL ZV$C,S,DIN,MASK,S,ERAR
001692 07F1 FBC0 0003          X
001692 07F3 D380 0000          X
001692 07F5 0F80
001692 07F6 07F1
001692 07F7 1C00
001692 07F8 111A
001692 07F9 07F1
001692 07FA 1131
001693 07FB 8980 1131          CMZ  <ERAR          CHECK FOR ERROR
001694 07FD 09D0          BE   >LOPD3
001695 07FE D800 1133          LDR  $R5,<ERAR+2     PUT 'IS' INTO $R5
001696 0800 E800 1132          LDR  $R6,<ERAR+1     PUT 'SHOULD BE' INTO $R6
001697 0802 F800 1131          LDR  $R7,<ERAR       PUT WORD COUNT INTO $R7
001698 0804 F380 0BA3          LNJ  $B7,<ERROR       ERROR: USC DID NOT LOOP INTO DSS CORRECTLY
001699          CALL ZV$C0          CONTINUE CHECKING FOR ERROR
001700 0806 FBF0 0001          X
001700 0808 D380 0000          X
001700 080A 3E02          LOPD3 ADV $R3,=2     CHANGE TO NEXT LINE
001701 080B 0F80 07B7          B    <LOPD
001702          *
001703          *
001704          * -----
001705          *
001706          *          TRANSMIT MARK AND SPACE TEST
001707          *
001708 080D C870 4C20          XMMSL LDR  $R4,=A'L '
001709 080F CF00 1364          STR  $R4,<ERMG+1
001710 0811 8380 098B          LNJ  $B3,<CLFG
001711 0813 C870 FF07          LDR  $R4,=Z'FF07'    ALTER DATA

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001712 0815 CF00 11A1 STR SR4,<DOUT*3
001713 0817 8700 111A CL <MASK
001714 0819 C870 081A LDR SR4,*X'81A' LOAD LCT WITH MARK BIT
001715 081B 8A80 111D INC <PARFG
001716 081D 8A80 1128 INC <TRMRK
001717 081F EB80 1177 U2 LAB SB6,<ASYN LOAD SB6 WITH ADDRESS OF TABLE END
001718 0821 CF00 12D0 STR SR4,<LCT9MS
001719 0823 7C06 LDV SR7,*6 NUMBER OF 60 MS DELAY
001720
*
* TEST ALL ASYNCHRONOUS LINES
001721
*
*
001722
*
* LDV SR2,*1 LOAD INDEX
001723 0824 2C01 LAB SB1,<ASYN
001724 0825 9B80 1175 U3 LNJ SB3,<DTALOP LOOP SUB-ROUTINE
001725 0827 B380 0881 B >U1
001726 0829 0F85 DC <WCP9
001727 082A 12FB DC <LCT9
001728 082B 12CC DC 5
001729 082C 0005 DC <DOUT
001730 082D 119E
*
*
001731
*
* U1 EQU $
001732 082E 082E CMB SB1,*SB6 SEE IF ALL ASYNCHRONOUS LINES TESTED
001733 082E 9DD6 BE >U5
001734 082F 0903 LAB SB1,*SB1,*SR2
001735 0830 9BA1 B >U3
001736 0831 0FF6
*
*
001737
*
* U5 LDR SR4,*X'101A' LOAD LCT FOR SPACE CONDITION
001738 0832 C870 101A CMR SR4,<LCT9MS CHECK IF ALREADY DONE
001739 0834 C900 12D0 BNE >U2
001740 0836 09E9 CL <PARFG
001741 0837 8700 111D CL <TRMRK
001742 0839 8700 1128 LDR SR4,*X'607' PUT DATA BACK TO NORMAL
001743 083B C870 0607 STR SR4,<DOUT*3
001744 083D CF00 11A1 JMP SB2
001745 083F 8382
*
*
001746
*
* *****
001747
*
*
* DATA CHECK FOR XMIT MARK AND SPACE
001748
*
*
001749
*
*
001750
*
* XMSCK EQU $
001751 0840 EQU $
001752 0840 8F00 1145 SAVE <SAV2,*Z'0002' SAVE SB6
0842 0002
001753 0843 DB80 1251 LAB SB5,<XMSB LOAD ADDRESS FOR ZVSC
001754 0845 C800 12D0 LDR SR4,<LCT9MS CHECK LCT TO SEE IF MARK
001755 0847 C970 081A CMR SR4,*X'81A' B IF MARK TEST
0849 0903 BE >XMSCK1
001756 084A DB80 1255 LAB SB5,<XSSB LOAD ADDRESS OF SPACE SHOULD BE
001757 084C DF80 0853 XMSCK1 STB SB5,<XMSCK2*4*SAF STORE SHOULD BE ADDRESS FOR ZVSC
001758 084E FBC0 0003 XMSCK2 CALL ZVSC,*S,DIN,MASK,C4,ERAR
0850 D380 0000
0852 0F80
0853 084E
0854 1C00
0855 111A
0856 125A
0857 1131
001760 0858 8980 1131 CMZ <ERAR CHECK FOR ERROR
001761 085A 090D BE >XMSCK3
001762 085B D800 1133 LDR SR5,<ERAR*2 PUT 'IS' INTO SR5
001763 085D E800 1132 LDR SR6,<ERAR*1 PUT 'SHOULD BE' INTO SR6
001764 085F F800 1131 LNJ SR7,<ERAR PUT WORD COUNT INTO SR7
001765 0861 F380 08A3 LNJ SB7,<ERROR ERROR; TRANSMIT MARK OR SPACE ERROR
001766 0863 FB00 0001 CALL ZVSC0 CONTINUE CHECKING FOR ERROR
0865 D380 0000
001767 0867 C870 101A XMSCK3 LDR SR4,*X'101A'
001768 0869 C900 12D0 CMR SR4,<LCT9MS
001769 086B 0991 BNE >XMSCK5
001770 086C E380 099C LNJ SB6,<BASE LOAD SB5 WITH LCT BASE
001771 086E 5E3C ADV SR5,*60
001772 086F DF00 0873 STR SR5,<XMSCK4 STORE ADDRESS TO READ LCT
001773
*
*
001774 0871 E380 085E LNJ SB6,<RDLC T
001775 0873 0000 XMSCK4 DC 0 ADDRESS
001776 0874 0002 DC 2
*
*
001777
*
* LDR SR5,*Z'FE00' LOAD SB5 WITH SHOULD BE
001778 0875 D870 FE00 CMR SR5,<LCTV
001779 0877 D900 112E BE >XMSCK5
001780 0879 0903 LNJ SB7,<ERROR ERROR; FRAMING ERROR FAILURE
001781 087A F380 08A3 XMSCK5 MSTR <SAV2,*Z'0002' RESTORE SB6
001782 087C 8F80 1145 B <JP5A
001783 087F 0F80 0985
*
*
001784
*
*
001785
*
*
001786
*
*
001787 0881 8F00 1155 DTALOP SAVE <SAV3,*Z'0112' SAVE SB3,*SB6
0883 0112
001788 0884 9F80 0889 STB SB1,<TG1 FOR FIND LINE ON
001789 0886 8753 CL *SR3 CLEAR SR3
001790 0887 D380 0B38 LNJ SB5,<FLN FIND LINE ON
001791 0889 0000 TGI RESV *SAF,0
001792 088A 8383 JMP SB3 NO LINE ON RETURN
001793
*
*
001794 CALL ZV*F,DIN,DFLT,HX200 FILL INPUT BUFFER
0888 FBC0 0003
088D D380 0000
088F 0F80
0890 1C00
0891 11DD
0892 125D
001795 0893 C808 0889 LDR SR4,*<TG1
001796 0895 C900 1177 CMR SR4,<ASYN
001797 0897 0983 BNE >DTAL B IF NOT CURRENT LOOP ADAPTER
001798 0898 C380 09FD LNJ SB4,<SCLS SET CURRENT LOOP ADAPTER SPEED
001799 089A BBC3 0001 LAB SB3,*SB3.1 DUMMY INCREMENT OF SB3
001800 089C C380 0AF3 LNJ SB4,<GENITZ GENERAL INITIALIZE
001801 089E DCF3 LDB SB5,*SB3 GET ADDRESS TO STORE FOR ZV$MLW CHANNEL
001802 089F C570 FF80 AND SR4,*Z'FF80' SET CHANNEL FOR ZV$MLW
001803 08A1 C470 0040 OR SR4,*X'40' OR IN DIRECTION BIT
001804 08A3 CF00 110A STR SR4,<CHNZ STORE CHANNEL FOR ZV$LIB CALL
001805 08A5 CCF5 LDB SB4,*SB5 LOAD AND STORE ADDRESS FOR

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001806	08A6	CF80	08BA		STB	\$B4,<LPWRT+4+\$AF	ZVSLIB CALL
001807	08A8	CCF5			LDB	\$B4,+SB5	
001808	08A9	CF80	08C3		STB	\$B4,<LPWRT1+4+\$AF	
001809	08AB	DF80	08BB		STB	\$B5,<LPWRT+4+2*\$AF	
001810	08AD	DBC5	0001		LAB	\$B5,\$B5.1	INCREMENT \$B5
001811	08AF	DF80	08C4		STB	\$B5,<LPWRT1+4+2*\$AF	
001812	08B1	DBC5	0001		LAB	\$B5,\$B5.1	
001813	08B3	DF80	08C5		STB	\$B5,<LPWRT1+4+3*\$AF	
001814							
001815					* LPWRT CALL	ZV\$MLW,\$,\$,HX200,CHNZ	
	08B5	FBC0	0003				
	08B7	D380	0000	X			
	08B9	0F80					
	08BA	08B5					
	08BB	08B5					
	08BC	125D					
	08BD	110A					
001816					LPWRT1 CALL	ZV\$MLW,\$,\$,\$,CHNZ	
	08BE	FBC0	0003				
	08C0	D380	0000	X			
	08C2	0F80					
	08C3	08BE					
	08C4	08BE					
	08C5	08BE					
	08C6	110A					
001817					*		
001818	08C7	DCF3			LDB	\$B5,+SB3	STORE ADDRESS FOR LCT TABLE
001819	08C8	DF80	08CC		STB	\$B5,<TG2	
001820					*		
001821	08CA	D380	0824		LNJ	\$B5,<SETLCT	SEND OUT LCT
001822	08CC	0000			RESV	\$AF,0	
001823					*		
001824	08CD	C800	10FC		LDR	\$R4,<FOCC	CCB RESEI FOR REC
001825	08CF	C380	087E		LNJ	\$B4,<CGSCH	
001826	08D1	8000	1184		IO	<BIT7,=\$R4	
	08D3	0054					
	08D4	0703			BIOT	>DT2	
001827					*		
001828							
001829	08D5	F380	08A3		LNJ	\$B7,<ERROR	ERROR: I/O WAS NAK'ED
001830					*		
001831	08D7	8AD3			DT2 INC	=\$R3	
001832	08D8	C800	10FC		LDR	\$R4,<FOCC	CCB RESEI FOR TRANSMIT
001833	08DA	C380	087E		LNJ	\$B4,<CGSCH	
001834	08DC	8000	1184		IO	<BIT7,=\$R4	
	08DE	0054					
001835	08DF	0703			BIOT	>DT3	
001836					*		
001837	08E0	F380	08A3		LNJ	\$B7,<ERROR	ERROR: I/O WAS NAK'ED
001838					*		
001839	08E2	C803			DT3 LDR	\$R4,\$B3	LOAD RANGE
001840	08E3	CF00	1120		STR	\$R4,<RANGE	STORE RANGE FOR ZV\$C
001841	08E5	4001			SOL	\$R4,+1	CONVERT TO BYTES FOR CCB RANGE
001842					*		
001843	08E6	BBC3	0001		LAB	\$B3,\$B3.1	STORE ADDRESS FOR TRANSMIT BUFFER
001844	08E8	DCF3			LDB	\$B5,+SB3	FOR CCB FORMATION
001845	08E9	DF80	08EF		STB	\$B5,<TG3	FOR DATA CHECK CALL (ZV\$C)
001846	08EB	DF80	0945		STB	\$B5,<JP4+4+\$AF	
001847					*		
001848	08ED	D380	0882		LNJ	\$B5,<MCCB	FORM CCB FOR TRANSMIT
001849	08EF	0000			RESV	\$AF,0	
001850	08F0	0060			DC	X'60'	
001851					*		
001852	08F1	88D3			DEC	=\$R3	GET RECV. CHANNEL
001853	08F2	D380	0882		LNJ	\$B5,<MCCB	FORM CCB FOR RECIEVE SIDE
001854	08F4	1C00			DC	<DIN	
001855	08F5	0060			DC	X'60'	
001856					*		
001857	08F6	5C01			LDR	\$R5,=1	START I/O ON XMIT SIDE TO SET
001858	08F7	8AD3			INC	=\$R3	CONFIG. AND XMIT FILL CHAR. (SYNC. ONLY)
001859	08F8	FE55			SWR	\$R7,=\$R5	
001860	08F9	C800	10FC		LDR	\$R4,<FOCC	
001861	08FB	C380	087E		LNJ	\$B4,<CGSCH	
001862	08FD	8980	1128		CMZ	<TRMK	
001863	08FF	0980	0907		BNE	<MELCO	
001864	0901	8000	1180		IO	<BIT1,=\$R4	
	0903	0054					
001865	0904	0703			BIOT	>MELCO	
001866	0905	F380	08A3		LNJ	\$B7,<ERROR	
001867	0907	D380	084C		MELCO LNJ	\$B5,<TDLAY	
001868	0909	FE55			SWR	\$R7,=\$R5	
001869	090A	88D3			DEC	=\$R3	
001870					*		
001871	090B	C800	10FC		LDR	\$R4,<FOCC	
001872	090D	C380	087E		LNJ	\$B4,<CGSCH	FORM I/O FOR START I/O
001873	090F	8000	1180		IO	<BIT1,=\$R4	
	0911	0054					
001874	0912	0703			BIOT	>JP2	
001875	0913	F380	08A3		LNJ	\$B7,<ERROR	
001876	0915	D380	084C		JP2 LNJ	\$B5,<TDLAY	TIME DELAY
001877	0917	8980	1128		CMZ	<STPFG	CHECK STOP I/O TEST FLAG
001878	0919	0903			BE	>JP2A	
001879	091A	0F80	0659		B	<STIO	BRANCH TO STOP I/O AND CHANNEL RESET TEST
001880	091C	8980	111D		JP2A CMZ	<PAKFG	
001881	091E	0908			BE	>JP2B	
001882	091F	8980	110B		CMZ	<COUNT	
001883	0921	0900	05FC		BE	<PAK1	BRANCH TO CHECK THAT PARITY ERROR SET
001884	0923	8AD3			INC	=\$R3	
001885	0924	0F80	0840		B	<XMSCK	B TO MARK AND SPACE CHECK ROUTINE
001886	0926	C380	080E		JP2B LNJ	\$B4,<INXT	INPUT NEXT STATUS ON REC. SIDE
001887	0928	E870	1000		LDR	\$R6,=Z'1000'	
001888	092A	82D5			LB	=\$R5,=Z'1000'	SEE IF STATUS COMPLETE
	092B	1000					
001889	092C	0503			BBT	>JP3	
001890	092D	F380	08A3		LNJ	\$B7,<ERROR	STATUS NOT COMPLETE ON REC.
001891					*		
001892	092F	8AD3			JP3 INC	=\$R3	
001893	0930	C380	060E		LNJ	\$B4,<INXT	INPUT NEXT STATUS ON XMIT SIDE
001894	0932	82D5			LB	=\$R5,=Z'1000'	SEE IF STATUS COMPLETE
	0933	1000					
001895	0934	0503			BBT	>JP4A	
001896	0935	F380	08A3		LNJ	\$B7,<ERROR	STATUS INCOMPLETE ON XMIT SIDE
001897	0937	8980	1124		JP4A CMZ	<RUNFG	
001898	0939	0980	0736		BNE	<RUNCK	BRANCH TO OVERRUN AND UNDERRUN TEST

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001899 *
001900 CMZ <CRCFG TEST CRC AND PARITY FLAG
001901 BE >JP4 FLAG NOT SET JUST CHECK DATA
001902 LNJ $B5,<CRPRT GO CHECK CRC RESIDUE AND PARITY
001903 *
001904 *
001905 * CHECK IF DATA LOOP CORRECTLY
001906 *
001907 JP4 CALL ZVSC,$DIN,MASK,RANGE,ERAR

0940 FBC0 0003
0942 D380 0000 X
0944 OF80
0945 0940
0946 1C00
0947 111A
0948 1120
0949 1131

001908 094A 8980 1131 CMZ <ERAR CHECK FOR ERROR
001909 094C 090D BE >JP5
001910 094D D800 1133 LDR $R5,<ERAR+2 PUT IS IN $R5
001911 094F E800 1132 LDR $R6,<ERAR+1 PUT SHOULD BE INTO $R6
001912 0951 F800 1131 LDR $R7,<ERAR PUT WORD COUNT INTO $R7
001913 0953 F380 0BA3 LNJ $B7,<ERROR
001914 CALL ZVSC0 CONTINUE LOOKING FOR ERRORS

0955 FBF0 0001
0957 D380 0000 X
0959 8980 1109 JP5 CMZ <CHS2FG CHECK CHARACTER SIZE TEST FLAG
001915 095B 092A BE >JP5A IF SET CHECK HIGH ORDER DIGITS FOR ZERO'S
001916 095C 5C08 LDV $R5,#8
001917 095D D900 110C CMK $R5,<CS
001918 095F 0926 BE >JP5A
001919 0960 8600 111A CPL <MASK COMPLEMENT MASK TO CHECK OTHER BITS
001921 CALL ZVSP,DIN1,CPFLG+3,RANGE CLEAR SHOULD BE TO ZERO'S

0962 FBC0 0003
0964 D380 0000 X
0966 OF80
0967 1D00
0968 119A
0969 1120

001922 CALL ZVSC,DIN1,DIN,MASK,RANGE,ERAR

096A FBC0 0003
096C D380 0000 X
096E OF80
096F 1D00
0970 1C00
0971 111A
0972 1120
0973 1131

001923 0974 8980 1131 CMZ <ERAR CHECK FOR ERRORS
001924 0976 090D BE >JP5B
001925 0977 D800 1133 LDR $R5,<ERAR+2 PUT IS INTO $R5
001926 0979 E800 1132 LDR $R6,<ERAR+1 PUT SHOULD BE INTO $R6
001927 097B F800 1131 LDR $R7,<ERAR PUT WORD COUNT INTO $R7
001928 097D F380 0BA3 LNJ $B7,<ERROR ERROR; SOME HIGH ORDER BITS SET IN CHARACTER
001929 CALL ZVSC0 CONTINUE LOOKING FOR ERRORS

097F FBF0 0001
0981 D380 0000 X
0983 8600 111A JP5B CPL <MASK RESET MASK

001930 *
001931 * JP5A JNC $R3 FIND NEXT LINE ON OF THIS TYPE
001932 0985 8AD3 RSTR <SAV3,=Z'0112' RESTORE $B3,$B6
001933 0986 8F80 1155 B <NXLT
001934 0989 0F80 0887
001935 *
001936 *
001937 *
001938 *
001939 * CLEAR TEST FLAGS
001940 *
001941 098B 8700 110F CLFG CL <CRCFG
001942 098D 8700 111D CL <PARFG
001943 098F 8700 1129 CL <SBIFG
001944 0991 8700 112B CL <STPFG
001945 0993 8700 112C CL <WDBFG
001946 0995 8700 1106 CL <ARFG
001947 0997 8700 1109 CL <CHS2FG
001948 0999 8700 1124 CL <RUNFG
001949 099B 8383 JMP $B3

001950 *
001951 *
001952 *
001953 * LOAD $R5 WITH LCT BASE ADDRESS
001954 *
001955 099C D853 BASE LDR $R5,=$R3
001956 099D 5041 SOR $R5,1
001957 099E 5F40 MLV $R5,=X'40'
001958 099F 8386 JMP $B6
001959 *
001960 *
001961 *
001962 *
001963 * CRC GENERATOR
001964 *
001965 * OUTPUT - TAKES TABLE OF DATA AND FORMS ACCUMULATED CRC.
001966 * ONLY FOR CRC16 AND CCITT.
001967 *
001968 * TO USE, MUST SET:
001969 * R6 - NUMBER OF BITS IN CHAR.
001970 * R7 - RANGE IN BYTES
001971 * B3 - TABLE ADDRESS
001972 * B6 - ADDRESS OF CRC TYPE.
001973 *
001974 *
001975 *
001976 *
001977 *
001978 * $B3 - ADDRESS WHERE CRC ACCUMULATOR IS STORED
001979 * $B6 - ADDRESS OF CRC TYPE
001980 *
001981 * $R6 - CONTAINS NUMBER OF BITS IN CHARACTER
001982 * $R7 - THE NUMBER OF CRC TO BE
001983 * GENERATED FROM THE TABLE AT $B3
001984 *
001985 * THE ACCUMULATED CRC IS STORED IN LOCATION CRCAC.

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001985
001986 09A0 8F00 1135      *
CRC      SAVE  <SAV1,=Z'F994'
001987 09A3 C876          LDR  $R4,+$B6      PUT CRC TYPE INTO $R4
001988 09A4 8751          CL   = $R1
001989 09A5 8753          CL   = $R3      CLEAR $R3 FOR LONG SHIFT
001990 09A6 8752          CL   = $R2
001991 09A7 8256          NEG  = $R6
001992 09A8 EF00 1125      STR  $R6,<TLOC      STORE COUNTER
001993 09AA D2FF          LLH  $R5,$B3,+$R3
001994 09AB 7001          SOL  $R7,1
001995 09AC 8257          NEG  = $R7
001996 09AD DF51          CRCA STR  $R5,=$R1
001997 09AE 5041          SOR  $R5,1
001998 09AF 9652          XOR  $R1,=$R2
001999 09B0 2041          SOR  $R2,1
002000 09B1 1B02          BEVN $R1,>CRCB
002001 09B2 A654          XUR  $R2,=$R4
002002 09B3 67FA          CRCB BINC $R6,>CRCA
002003 09B4 D0FF          LDH  $R5,$B3,+$R3
002004 09B5 E800 1125      LDR  $R6,<TLOC      RESET CHAR. COUNT
002005 09B7 77F6          BINC $R7,>CRCA      DO NEW CHARACTER
002006 09B8 AF00 110E      STR  $R2,<CHCAC      STORE LAST CRC ACCUMULATED
002007 09BA 8F80 1135      RSTR <SAV1,=Z'F994'  RESTORE REGS.
002008 09BC F994
002009 09BD 8384          *      JMP  $B4
002010
002011
002012
002013
002014
002015
002016
002017
002018
002019
002020
002021 09BE 8F00 1135      *
CPAR      SAVE  <SAV1,=Z'EFD0'      SAVE REGS.
002022 09C0 EFD0
002023 09C1 9800 110C      LDR  $R1,<C5      LDR $R1 WITH NUMBER TO SHIFT
002024 09C3 8B01          DEC  = $R1
002025 09C4 8755          CL   = $R5
002026 09C5 8752          CL   = $R2
002027 09C6 8257          NEG  = $R7      CLEAR INDEX
002028 09C7 C2A3          CMZ  <CHZFG      NEGATE RANGE FOR BRANCH AND INCREMENT
002029 09C8 8980 1109      LLH  $R4,$B3,$R2  LOAD $R4 WITH HALF WORD OF DATA BUFFER
002030 09CA 090F          BE   >CPAR3      SEE IF CHAR. SIZE TEST
002031 09CB 6C08          LDV  $R6,=8      IF 8 BIT CHAR. DO NOT STRIP BITS
002032 09CC E900 110C      CMR  $R6,<C5
002033 09CE 090B          BE   >CPAR3
002034 09CF 8B06          DEC  = $R6      IF 7 OR 6 BIT CHAR. STRIP FOR PARITY TO BE SE
002035 09D0 E900 110C      CMR  $R6,<C5
002036 09D2 0904          BE   >CPAR4
002037 09D3 E870 001F      LDR  $R6,=X'1F'  MASK TO STRIP WITH 6 BIT CHAR.
002038 09D5 0F83          B    >CPAR5
002039 09D6 E870 003F      LDR  $R6,=X'3F'  MASK TO STRIP WITH 7 BIT CHAR.
002040 09D8 C556          CPAR5 AND $R4,=$R6  STRIP BIT TO CHAR SIZE MINUS PARITY BIT
002041 09D9 CF56          CPAR3 STR  $R4,=$R6  SAVE CHAR IN $R6
002042
002043
002044 09DA 50C4          *
DOR  $R5,4      CALCULATE PARITY FOR THE CHAR.
002045 09DB CF00 1125      STR  $R4,<TLOC
002046 09DD 8754          CL   = $R4
002047 09DE 5084          DOL  $R5,4
002048 09DF C600 1125      XUR  $R4,<TLOC
002049 09E1 50C2          DOR  $R5,2
002050 09E2 CF00 1125      STR  $R4,<TLOC
002051 09E4 8754          CL   = $R4
002052 09E5 5082          DOL  $R5,2
002053 09E6 C600 1125      XOR  $R4,<TLOC
002054 09E8 4041          SOR  $R4,1
002055 09E9 0682          BCF  >CPAR1
002056 09EA 8AD4          INC  = $R4
002057 09EB C600 111E      CPAR1 XOR  $R4,<PTTY
002058
002059 09ED 4000          *
SOL  $R4,0      SHIFT PARITY BIT TO POSITION
002060 09EE E454          OR   $R6,=$R4      PUT PARITY BIT INTO DATA
002061 09EF E7ED          STH  $R6,$B1,+$R2  STORE INTO DATA SHOULD BE
002062 09F0 7780 09C7      BINC $R7,<CPAR2      DO NEXT CHARACTER
002063 09F2 8F80 1135      RSTR <SAV1,=Z'EFD0'  RESTORE REG.
002064 09F4 EFD0
002065 09F5 8384          *      JMP  $B4      JUMP OUT OF PARITY FORMATION SUB-ROUTINE
002066
002067
002068
002069
002070
002071
002072 09F6 8F00 1145      *
STMD      SAVE  <SAV2,=Z'DF8C'      SAVE REGS.
002073 09F8 DF8C
002074 09F9 3EFE          ADV  $R3,=-2      SET $R3 TO RECV. CHAN. ON FIRST LINE
002075 09FA CB80 0BEE      LAB  $B4,<CHLP      LOAD $B4 WITH ADDRESS OF CHANNEL PROG.
002076 09FC 0F8E          B    >STMD2
002077
002078
002079
002080 09FD 8F00 1145      *
SCLS      SAVE  <SAV2,=Z'DF8C'      SAVE REGS.
002081 09FF DF8C
002082 0A00 C800 10FC          LDR  $R4,<FOCC
002083 0A02 C380 0B7E          LNJ  $B4,<CGSCH
002084 0A04 8000 117F          SCLS1 IO  <B110,=$R4      CHANNEL INITIALIZE
002085 0A06 0054
002086 0A07 07FD          BIOF >SCLS1
002087 0A08 CB80 0BF3          LAB  $B4,<CHLPS
002088 0A0A CF80 0A19          STB  $B4,<STMD3+4+$AF  LOAD $B4 WITH ADDRESS OF CHANNEL PROG.
002089 0A0C C800 1102          LDR  $R4,<FINS      STORE ADDRESS FOR ZV$MLW
002090 0A0E C380 0B7E          LNJ  $B4,<CGSCH      LOAD AN I/O TO GET CHANNEL FOR ZV$MLW
002091 0A10 C570 FFC0          AND  $R4,=Z'FFC0'  STRIP OFF DIRECTION BIT AND FUN CODE
002092 0A12 CF00 110A          STR  $R4,<CHNZ      STORE CHANNEL FOR CALL

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002091          *
002092          * STMD3 CALL ZV$MLW,$,WCP,HX200,CHNZ
          0A14 FBC0 0003
          0A16 D380 0000      X
          0A18 0F80
          0A19 0A14
          0A1A 12D3
          0A1B 125D
          0A1C 110A
002093          0A1D D380 0B24      LNJ      $B5,<SETLCT      SEND OUT LCT
002094          0A1F 1283          DC          <LCT
002095          *
002096          0A20 8757          CL          =$R7          CLEAR FOR SHORT DELAY OF 30 MS.
002097          0A21 C800 10FC          LDR          $R4,<FOCC          FORM CONTROL WORD
002098          0A23 C380 0B7E          LNJ          $B4,<CGSCH          FOR
002099          0A25 8000 1180          IO          <BIT1,=$R4          START I/O
          0A27 0054
002100          0A28 0700 0A2C          BIOT        <STMD1
002101          0A2A F380 0BA3          LNJ          $B7,<ERROR          ERROR; I/O WAS NAK'ED
002102          *
002103          0A2C D380 0B4C          * STMD1 LNJ      $B5,<TDLAY          TIME DELAY
002104          0A2E 8F80 1145          RSTR        <SAV2,=Z'DF8C'      RESTORE REGS.
          0A30 DF8C
          0A31 8384          JMP          $B4
          *
          *=====
          *
          *          CONVERT RTC TICKS TO MILSEC.
          *
002111          0A32 8F00 1135          TKSEC SAVE  <SAV1,=Z'D380'      SAVE REGS.
          0A34 D380          *
002112          *
002113          0A35 CF51          *          STR      $R4,=$R1          DUPLICATE VALUE IN $R1
002114          0A36 C800 1110          MUL          $R4,<DIV1
002115          0A38 CF00 1125          STR          $R4,<TLOC
002116          0A3A C851          LDR          $R4,=$R1
002117          0A3B C800 1111          MUL          $R4,<DIV2
002118          0A3D C370 000A          DIV          $R4,=10
002119          0A3F CA00 1125          ADD          $R4,<TLOC
002120          0A41 CF00 1125          STR          $R4,<TLOC
002121          0A43 C851          LDR          $R4,=$R1
002122          0A44 C800 1112          MUL          $R4,<DIV3
002123          0A46 C370 0064          DIV          $R4,=100
002124          0A48 CA00 1125          ADD          $R4,<TLOC
002125          0A4A CF00 1125          STR          $R4,<TLOC
002126          0A4C C851          LDR          $R4,=$R1
002127          0A4D C800 1113          MUL          $R4,<DIV4
002128          0A4F C370 03E8          DIV          $R4,=1000
002129          0A51 CA00 1125          ADD          $R4,<TLOC
          *
002130          *
002131          0A53 8F80 1135          *          RSTR    <SAV1,=Z'D380'
          0A55 D380          JMP          $B5
          0A56 8385
          *
          *=====
          *
          *          CONVERT MS TO BITS/SEC (GIVEN CHAR. SIZE & NUMBER)
          *
002138          0A57 8F00 1135          BPS      SAVE  <SAV1,=Z'DF80'      SAVE REGS.
          0A59 DF80          *
002139          0A5A 8756          *          CL          =$R6          CLEAR $R6 FOR DIVIDE
002140          0A5B 9826          LDR          $R1,$B6,$R2          LOAD $R1 WITH NUMBER OF CHAR.
002141          0A5C 9970 0F00          CMR          $R1,=3840
002142          0A5E 0983          BNE          >BPS1
002143          0A5F 1E07          ADV          $R1,=7
002144          0A60 9255          SUB          $R1,=$R5
002145          0A61 F870 03E8          BPS1     LDR          $R7,=1000          CONVERSION FACTOR FOR MS TO C.
002146          0A63 FB00 110D          MUL          $R7,<CSB          MULTIPLY BY CHARACTER SIZE
002147          0A65 F354          DIV          $R7,=$R4          DIVIDE BY ACTUAL TIME IN MS
002148          0A66 8D00 1126          SDI          <TP1          STORE DOUBLE INTEGER
002149          0A68 8756          CL          =$R6
002150          0A69 F800 1127          LDR          $R7,<TP2
002151          0A6B FB51          MUL          $R7,=$R1
002152          0A6C 82D7          LB          =$R7,=Z'8000'
          0A6D 8000
002153          0A6E 0502          BBT          >BPS2A
002154          0A6F 6903          BEZ          $R6,>BPS2
002155          0A70 8A80 1117          BPS2A   INC          <HGSPD
002156          0A72 8D00 111B          BPS2     SDI          <MSB5
002157          0A74 8D30 11E2          SDI          <SYLSP0,$K3
002158          0A76 F800 1126          LDR          $R7,<TP1
002159          0A78 7F0A          MLV          $R7,=10
002160          0A79 F354          DIV          $R7,=$R4
002161          0A7A FF55          STR          $R7,=$R5
002162          0A7B DB51          MUL          $R5,=$R1
002163          0A7C D370 000A          DIV          $R5,=10
002164          0A7E DA00 111C          ADD          $R5,<MSB5+1
002165          0A80 70D0          DOR          $R7,16
002166          0A81 7F0A          MLV          $R7,=10
002167          0A82 F354          DIV          $R7,=$R4
002168          0A83 FB51          MUL          $R7,=$R1
002169          0A84 F370 0064          DIV          $R7,=100
002170          0A86 FA55          ADD          $R7,=$R5
002171          0A87 FF00 111C          STR          $R7,<MSB5+1
002172          0A89 8F80 1135          RSTR        <SAV1,=Z'DF80'
          0A8B DF80
          0A8C 8385          JMP          $B5
          *
          *=====
          *
          *          PRINT LINE SPEED
          *
002179          0A8D 8F00 1145          PRTLSP SAVE  <SAV2,=Z'B88F'      SAVE REGS.
          0A8F B88F          *
002180          0A90 3041          *          SOR          $R3,1          SHIFT $R3 TO REPRESENT LINE NUMBER
002181          0A91 BF00 1125          *          STR          $R3,<TLOC          STORE LINE NUMBER FOR ZV$TD
002182          *          CALL         ZV$TC,MESG2,BYTP      PRINT LINE TYPE
          0A93 FBC0 0003
          0A95 D380 0000      X
          0A97 0F80
          0A98 1313
          0A99 1107
002183          CALL         ZV$TD,TLOC          PRINT LINE NUMBER

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0A9A FBC0 0003
0A9C D380 0000 X
0A9E OF80
0A9F 1125
002184 CALL ZV$T,MESG4 PRINT 'SPEED'
0AA0 FBC0 0003
0AA2 D380 0000 X
0AA4 OF80
0AA5 131C
002185 UAA6 D380 0A32 * LNJ $B5,<TKSEC CONVERT NUMBER OF TICK IN $R4 TO MS
002186 *
002187 UAA8 D380 0A57 LNJ $B5,<BPS USE $R4 TO CALCULATE BITS/SEC
002188 OAAA 8980 1117 CMZ <HGSPD CHECK FOR HIGH SPEED LINE
002189 UAAC 0919 BE >PRLT3
002190 OAAD 8C80 111B LDI <MSBS LOAD DOUBLE INTERGER INTO $R6,$R7
002191 OAAF F370 03E8 DIV $R7,=1000 FIND NUMBER OF KB
002192 OAB1 FF00 111B STR $R7,<MSBS
002193 OAB3 6D64 CMV $R6,=100
002194 OAB4 0A02 BAG >PRLT5
002195 OAB5 6C64 LDV $R6,=100
002196 OAB6 EF00 111C PRLT5 STR $R6,<MSBS+1
002197 CALL ZV$T0,MSBS,BKLS PRINT NUMBER OF KB
0AB8 FBC0 0003
0ABA D380 0000 X
0ABC OF80
0ABD 111B
0ABE 119D
002198 CALL ZV$T,COMMA
0ABF FBC0 0003
0AC1 D380 0000 X
0AC3 OF80
0AC4 119C
002199 PRLT3 CALL ZV$T0,MSBS+1,BKLS PRINT LINE SPEED
0AC5 FBC0 0003
0AC7 D380 0000 X
0AC9 OF80
0ACA 111C
0ACB 119D
002200 OACC 8980 129C CMZ <LC3SYF IF SYNC. LINE ALL DONE
002201 OACE 0999 BNE >PRLT1 IF ASYNC. PRINT SHOULD BE
002202 OACF EB80 1212 LAB $B6,<ALSPBL LOAD ADDRESS OF 'SHOULD BE' SPEED TABLE
002203 OAD1 C870 2108 LDR $R4,=X'2108' IF NEW LINE SPEED CHANGE $B6 TO POINT TO TABL
002204 OAD3 C908 0320 CMR $R4,*<ST1
002205 OAD5 0903 BE >PRLT7
002206 OAD6 EB80 1221 LAB $B6,<CMSPBL
002207 PRLT7 CALL ZV$T,MESG5 PRINT 'SHOULD BE'
0AD8 FBC0 0003
0ADA D380 0000 X
0ADC OF80
0ADD 1322
002208 OADE B826 LDR $R3,$B6,$R2 LOAD $R3 WITH LINE SPEED
002209 OADF BF00 1125 STR $R3,<TLOC
002210 CALL ZV$T0,TLOC
0AE1 FBC0 0003
0AE3 D380 0000 X
0AE5 OF80
0AE6 1125
002211 OAE7 9800 111C PRLT1 LDR $R1,<MSBS+1 STORE AWAY LINE SPEED (SYNC)
002212 OAE9 9F00 1118 STR $R1,<HISPD
002213 OAEB 8700 111B CL <MSBS
002214 OAE0 8700 111C CL <MSBS+1
002215 OAEF 8F80 1145 RSTR <SAV2,=Z'1118' RESTORE R
002216 OAF1 B88F JMP $B4
002217 OAF2 8384
*
*=====
*
* GENERAL INITIALIZE FOR DLCC
*
002220 GENITZ SAVE <SAV2,=Z'1118' SAVE B7,$R7
002221
002222 OAF3 8F00 1145 LDV $R7,=26 ALLOW 800 MS DELAY
002223 OAF5 1118 LDR $R4,<FOMC GET FUNCTION CODE FOR INITIALIZE
002224 OAF6 7C1A LNJ $B4,<CGSCH MODIFY FOR CHANNEL
002225 OAF7 C800 10FA IO <B110,=$R4 INITIALIZE
002226 OAF9 C380 0B7E
002227 OAFB 8000 117F
002228 OAFD 0054
002229 OAFE 0703 BIOT >N2
002230 OAFF F380 0BA3 LNJ $B7,<ERROR INITIALIZE WAS NAK'ED
002231 OB01 D380 0B4C N2 LNJ $B5,<TDLAY WAIT 30 MS
002232 OB03 8F80 1145 RSTR <SAV2,=Z'1118'
002233 OB05 1118
002234 OB06 8384 JMP $B4 RETURN
*
*=====
*
* OUTPUT CHANNEL CONTROL
*
* $R2 - CONTAINS CHANNEL CONTROL TYPE
*
002237 STCR SAVE <SAV1,=Z'0008' SAVE REG.
002238
002239 OB07 8F00 1135
002240 OB09 0008
002241 OB0A D852 LDR $R5,=$R2
002242 OB0B C800 10FC LDR $R4,<FOCC LOAD FUNCTION CODE FOR CHANNEL CONTROL
002243 OB0D 0F8C B >N1A
*
*=====
*
* INPUT NEXT STATUS TO R5
*
002247 INXT SAVE <SAV1,=Z'0008' B4
002248
002249 OB10 0008
002250 OB11 C800 1102 LDR $R4,<FINS GET CONTROL WORD FOR INPUT NEXT STATUS
002251 OB13 0F86 B >N1A
*
*=====
*
* INPUT RANGE
*
* $R5 - RESULT OF RANGE INPUTED
*
002256 INRG SAVE <SAV1,=Z'0008' SAVE REG.
002257
002258 OB14 8F00 1135
002259 OB16 0008
002260 OB17 C800 1100 LDR $R4,<FIR LOAD $R4 WITH FUNCTION CODE FOR INPUT RANGE
002261 OB19 C380 0B7E LNJ $B4,<CGSCH MODIFY FOR CHANNEL
002262 OB1B 8055 IO =R5,=$R4

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002260 OB1C 0054
002260 OB1D 0703
002261 OB1E F380 0BA3
002262 OB20 8F80 1135
002262 OB22 0008
002263 OB23 8384
002264
002265
002266
002267
002268
002269
002270
002271
002272 OB24 8F00 1135
002272 OB26 E8E0
002273 OB27 9CF5
002274 OB28 8751
002275
002276 OB29 A811
002277 OB2A 8AD1
002278 OB2B 2985
002279 OB2C 8F80 1135
002279 OB2E E8E0
002280 OB2F 8385
002281
002282 OB30 C800 10FE
002283 OB32 C380 0B7E
002284 OB34 8052
002284 OB35 0054
002285 OB36 07FE
002286 OB37 0FF2
002287
002288
002289
002290
002291
002292
002293
002294
002295
002296
002297
002298
002299
002300
002301 OB38 8F00 1135
002301 OB3A A0E0
002302 OB3B 3041
002303 OB3C 9CF5
002304 OB3D A801
002305 OB3E AB80 1186
002306 OB40 3D08
002307 OB41 0287
002308 OB42 A97E
002309 OB43 09FD
002310 OB44 88D3
002311 OB45 3001
002312 OB46 DBC5 0001
002313 OB48 8F80 1135
002313 OB4A A0E0
002314 OB4B 8385
002315
002316
002317
002318
002319
002320
002321
002322 OB4C 8F00 1135
002322 OB4E C180
002323 OB4F 1C09
002324 OB50 9F00 0000
002325 OB52 1C05
002326 OB53 0004
002327 OB54 9900 0000
002328 OB56 0882
002329 OB57 0FFD
002330 OB58 0005
002331 OB59 7776
002332 OB5A 8F80 1135
002332 OB5C C180
002333 OB5D 8385
002334
002335
002336
002337
002338
002339
002340
002341
002342
002343 OB5E 8F00 1135
002343 OB60 C88C
002344 OB61 C800 1100
002345 OB63 C380 0B7E
002346 OB65 C570 FF80
002347 OB67 CF00 110A
002348 OB69 C806
002349 OB6A CF00 1122
002350 OB6C C846 0001
002351 OB6E CF00 1123
002352
002352 OB70 FB00 0003
002352 OB72 D380 0000
002352 OB74 0F80
002352 OB75 112E
002352 OB76 1123
002352 OB77 1122
002352 OB78 110A
002353 OB79 8F80 1135
002353 OB7B C88C
002354 OB7C 83C6 0002

```

```

BIOT >N1
LNJ $B7,<ERROR
RSTR <SAV1,Z'0008'
ERROR: I/O WAS NAK'ED

N1
JMP $B4

*
*=====
*
* SET UP LCT FOR CHANNEL SPECIFIED IN R3
*
* LNJ $B5,<SETLCT
* DC TABLE
*
SETLCT SAVE <SAV1,Z'E8E0' R1,R2,R4,B2,B1
LDB $B1,+SB5 GET ADDRESS OF TABLE
CL =SR1
*
LCT4A LDR $R2,$B1,$R1 GET BYTE TO OUTPUT
INC =SR1
BNEZ $R2,>LCT5A BRANCH IF NOT AT END OF TABLE
RSTR <SAV1,Z'E8E0'
JMP $B5 RETURN
*
LCT5A LDR $R4,<FOBL FUNCTION CODE FOR OUT LCT BYTE
LNJ $B4,<CGSCH FORM IO CONTROL WORD
LCT3A IO =SR2,+SR4 OUTPUT BYTE
*
BIOF >LCT3A CHECK IF TAKEN
B >LCT4A GET NEXT BYTE
*
*=====
*
* FIND FIRST ACTIVE LINE
*
* $R3-START CHECK ON THIS LINE AND
* CYCLE THROUGH ALL LINES IF
* ONE FOUND ON LINE NUMBER IS HERE
*
* LNJ $B5,<FLN
* DC OF LINE TYPE TO SEARCH FOR
* <RETURN> IF NO LINES ON
* <RETURN>
*
FLN SAVE <SAV1,Z'A0E0' SAVE REG. $R2,$B2,$B1
SOR $R3,1
LDB $B1,+SB5
LDR $R2,$B1 PUT LINE TYPE INTO $R2
LAB $B2,<ATLT ADDRESS OF ACTIVE LINE TABLE
FLN2 CMV $R3,=B SEE IF AT BOTTOM OF TABLE
BGE >FLN3
CMR $R2,$B2,+SR3 SEE IF INDEX LINE ON
BNE >FLN2
DEC =SR3 SET $R3 BACK TO VALUE FOUND
SOL $R3,1
LAB $B5,$B5.1 INCREMENT TO VALID RETURN
FLN3 RSTR <SAV1,Z'A0E0' RESTORE REG.
JMP $B5 JUMP OUT OF ROUTINE
*
*=====
*
* TIME DELAY
*
* LNJ $B5,<TDLAY
*
TDLAY SAVE <SAV1,Z'C180' SAVE REG. $R1,$R7
TDLA LDV $R1,=9 LOAD CLOCK INITIAL COUNT
STR $R1,<ZHRTCC
LDV $R1,=5
RTCN TURN ON REAL TIME CLOCK
TDLA1 CMR $R1,<ZHRTCC
BAGE >TDLA3
B >TDLA1
TDLA3 RTCF TURN OFF REAL TIME CLOCK
BDEC $R7,>TDLA
RSTR <SAV1,Z'C180' RESTORE $R1
JMP $B5
*
*=====
*
* READ LCT
*
* LNJ $B6,<RDLC
* DC <ADDRESS>
* DC <RANGE IN BYTES>
*
RDLC SAVE <SAV1,Z'C88C' SAVE REGS.
LDR $R4,<FIR LOAD DUMMY I/O
LNJ $B4,<CGSCH PUT PROPER CHANNEL IN
AND $R4,=Z'FF80'
STR $R4,<CHNZ STORE CHANNEL NUMBER FOR CALL
LDR $R4,$B6 STORE ADDRESS IN ZV$MLR CALL
STR $R4,<RLCTAD
LDR $R4,$B6.1 STORE RANGE FOR ZV$MLR
STR $R4,<RLCTRG
CALL ZV$MLR,<LCTV,<RLCTRG,<RLCTAD,<CHNZ

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002355 *
002356 *-----*
002357 *
002358 *
002359 *
002360 *
002361 *
002362 *
002363 *
002364 *
002365 *
002366 0B7E 3006 CGSCH SOL $R3,6 SHIFT TO SUBCHANNEL POSITION
002367 0B7F C453 OR $R4,=$R3 OR SUBCHANNEL NUMBER INTO CONTROL WORD
002368 0B80 3046 SOR $R2,6 SHIFT TO NORMAL POSITION
002369 0B81 8384 JMP $B4 GO BACK
002370 *
002371 *-----*
002372 *
002373 *
002374 *
002375 *
002376 *
002377 *
002378 *
002379 *
002380 *
002381 *
002382 0B82 8F00 1135 MCCB SAVE <SAV1,=Z'FDF8' SAVES $B1,$B3,$B2,$B4,$R7,$R5,$R4,R2,6 SRI
002383 0B84 FDF8 LDV $R7,=126
002384 0B85 7C7E LDB $B2,=$B5 LOAD $B2 WITH CPU ADDRESS
002385 0B86 ACF5 LDR $R2,$B5 PUT RAM ADDRESS IN $R2
002386 0B87 A805 LDR $R5,=$R4
002387 0B88 D854 LDR $R4,<FOLD LOAD $R4 WITH I/O CONTROL WORD
002388 0B89 C800 10FD LNJ $B4,<CGSCH
002389 0B8B C380 0B7E MCB5 BEZ $R7,<MCER
002390 0B8D 7900 0B9C IOLD $B2,=$R4,=$R5 OUTPUT ADDRESS AND RANGE
002391 0B8F 8182
002392 0B90 0054
002393 0B91 0055
002394 0B92 88D7 DEC =$R7
002395 0B93 07FA BIOF >MCB5
002396 0B94 C800 10FF LDR $R4,<FOCB LOAD $R4 WITH I/O CONTROL WORD
002397 0B96 C380 0B7E LNJ $B4,<CGSCH PUT I/O CONTROL WORD IN $R4
002398 0B98 8052 MCB2 IO =$R2,=$R4 OUTPUT DLCC RAM ADDRESS
002399 0B99 0054
002400 0B9A 07FE BIOF >MCB2
002401 0B9B 0F83 B >MCB6
002402 0B9C F380 0BA3 MCER LNJ $B7,<ERROR IOLD HAS BEEN NAK'ED
002403 0B9E 8F80 1135 MCB6 RSTR <SAV1,=Z'FDF8' RESTORE REGS.
002404 0BA0 FDF8
002405 0BA1 83C5 0001 JMP $B5.1
002406 *
002407 *-----*
002408 *
002409 *
002410 *
002411 *
002412 *
002413 *
002414 *
002415 *
002416 *
002417 *
002418 0BA3 8F00 1165 ERROR SAVE <SAV4,=Z'FFFF' SAVE REGISTERS
002419 0BA5 FFFF
002420 0BA6 8800 1364 LBF <ERMG+1,=X'FF'
002421 0BA8 00FF
002422 0BA9 3041 SOR $R3,1 SHIFT TO GET LINE NUMBER
002423 0BAA 3E30 ADV $R3,=X'30' CHANGE NUMBER INTO ASCII
002424 0BAB B400 1364 OR $R3,<ERMG+1
002425 0BAD BF00 1364 STR $R3,<ERMG+1 STORE LINE NUMBER
002426 0BAF 1CF0 LDV $R1,=-16 INITIALIZE LOOP COUNTER
002427 0BB0 8753 CL =$R3 CLEAR INDEX
002428 0BB1 EBC7 FFEE LAB $B6,$B7,-1-$AF DECREMENT $B7
002429 0BB3 EF80 0BBA STB $B6,<ERR1+4+$AF STORE ERROR ADDRESS FOR ERROR CALL
002430 0BB5 FBC0 0003
002431 0BB7 D380 0000 X
002432 0BB9 0F80
002433 0BBA 0B85
002434 0BBB 1363
002435 0BBC EB80 1198 LAB $B6,<CRLF PUT CR-L INTO CALL
002436 0BBE CB80 1245 LAB $B4,<SPACE
002437 0BC0 AB80 0BD2 LAB $B2,<ERR3+4+2*$AF
002438 0BC2 EF82 ER11 STB $B6,$B2
002439 0BC3 8980 1114 CMZ <ERF CHECK FLAG FOR SHORT REPORT
002440 0BC5 0900 0BDD BE <ERK5
002441 0BC7 B8B0 1165 ERR2 LAB $B3,<SAV4.$R3 FETCH AND STORE REGISTER
002442 0BC9 9880 0BD1 LAB $B1,<ERR3+4+$AF
002443 0BCB BF81 ERR21 STB $B3,$B1
002444 ERR3 CALL ZV$THZ,$,$
002445 0BCC FBC0 0003
002446 0BCE D380 0000 X
002447 0BD0 0F80
002448 0BD1 0BCC
002449 0BD2 0BCC
002450 0BD3 8AD3 ER34 INC =$R3 BUMP INDEX
002451 0BD4 CF82 STB $B4,$B2
002452 0BD5 3D08 CMV $R3,=7*$AF+1 SEE IF CR-LF NEEDED
002453 0BD6 0984 BNE >ERR4
002454 0BD7 EB80 1198 LAB $B6,<CRLF
002455 0BD9 EF82 STB $B6,$B2 PUT CR-LF INTO CALL
002456 0BDA 1780 0BC7 ERR4 BINC $R1,<ERR2 DO NEXT REGISTER
002457 0BDC 0000 HLT HALT AFTER ERROR
002458 0BDD 8F80 1165 ERR5 RSTR <SAV4,=Z'FFFF' RESTORE REGISTERS
002459 0BDF FFFF
002460 0BE0 8387 JMP $B7 RETURN
002461 SAFF NULL
002462 *
002463 *-----*
002464 *
002465 *
002466 *
002467 *
002468 *
002469 *
002470 *
002471 *
002472 *
002473 *
002474 *
002475 0BE1 8700 00FF EDPCK CL <STOP
002476 0BE3 8980 0000 CMZ <ZV$TTY
002477 0BE5 0900 00FF BE <STOP
002478 0BE7 FBC0 0003 CALL ZV$TC,MESG8
002479 0BE9 D380 0000 X

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002640 OC1C 270F
002641
002642 OC1D BD31 * WAIT
002643 * B CKST
002644 OC1E 0020
002647 * LOC GETC
002648 024F GETC EQU X'024F'
002649 * RECV 0 INPUT CHARACTER
002650 OC1F EFBD
002653 OC20 3060
002654 * ST, STORE INTO DATA BUFFER
002655 OC21 BD30
002656 * BLCT HLP1A
002657 OC22 CODE
002658 OC23 6327
002661 * LOC GETC1
002662 0259 GETC1 EQU X'0259'
002663 * WAIT
002664 OC24 05BD
002665 OC25 3100
002666 * B GETC
002669 OC26 20F1
002670 * LOC HLP1A
002671 025E HLP1A EQU X'025E'
002672 * BLBT GETX
002673 OC27 9665
002674 OC28 8520
002677 OC29 2605
002678 * GNB
002679 OC2A BD31
002680 * B GETC
002681 OC2B 2020
002684 * LOC GETX
002685 0269 GETX EQU X'0269'
002686 * LD =0 TURN OFF REC.
002687 OC2C E6C6
002690 * OUT 2
002691 OC2D 00BD
002694 OC2E 3028
002695 * GNB
002696 OC2F BD31
002697 * WAIT
002698 OC30 20BD
002699 OC31 3100
002700 * B HLP1
002703 OC32 208A
002704 * NOP
002705 *
002706 *
002707 * CHANNEL PROGRAM FOR LOOP AT LINE ADAPT (XMIT)
002708 *
002709 *
002710 * ORG X'400'
002711 OC33 0100
002712 OC34 CHLP1A EQU $
002713 * LOC LP1A
002714 0400 LP1A EQU X'0400'
002715 * LD 2 GET LINE CONFIGURATION
002718 OC34 D602
002719 * OUT 6 OUTPUT II
002720 OC35 BD30
002723 * LD =X'AA' LOAD FILL CHAR.
002724 OC36 38C6
002727 * OUT 4 OUTPUT SYNC. FILL CHAR.
002728 OC37 AABD
002731 OC38 3030
002732 * WAIT
002733 OC39 BD31
002734 * LD 28 LOAD SYNC. FLAG
002735 OC3A 00D6
002738 * BZT LP1ABD IF ZERO BRANCH TO OUTPUT CHAR.
002739 OC3B 1C5D
002742 OC3C 271F
002743 * LD =6
002746 OC3D C606
002747 * LOC XMSY
002748 0414 XMSY EQU X'0414'
002749 * ST 63
002752 OC3E D73F
002753 * LD 24 LOAD SYNC. CHAR
002756 OC3F D618
002757 * SEND 0 SEND IT
002758 OC40 BD30
002761 * LD 63 DO SIX TIMES
002762 OC41 40D6
002765 * DEC
002766 OC42 3F5A
002767 * BZT SDSY
002768 OC43 5D27
002771 * WAIT
002772 OC44 05BD
002773 OC45 3100
002774 * B XMSY
002777 OC46 20EE
002778 * LOC SDSY
002779 0426 SDSY EQU X'0426'
002780 * WAIT
002781 OC47 BD31
002782 * LD =X'FF'
002783 OC48 00C6
002786 * SEND 0
002787 OC49 FFBD
002790 OC4A 3040
002791 * WAIT
002792 OC4B BD31
002793 * LOC LP1ABD
002794 0431 LP1ABD EQU X'0431'
002795 * LD, GET CHARACTER FROM DATA BUFFER
002796 OC4C 00BD
002797 OC4D 30A0
002798 * SEND 0 OUTPUT IT
002799 OC4E BD30
002802 * BLCT LP1AB

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002803	0C4F	40DE			
002804	0C50	6327			
002807			*	LOC	LPIAA
002808		043B	LPIAA	EQU	X'043B'
002809			*	WAIT	
002810	0C51	05BD			
002811	0C52	3100			
002812			*	B	LPIABD
002815	0C53	20F1			
002816			*	LOC	LPIAB
002817		0440	LPIAB	EQU	X'0440'
002818			*	WAIT	
002819	0C54	BD31			
002820			*	LD	=X'FF'
002821	0C55	00C6			
002824			*	SEND	0
002825	0C56	FFBD			
002828	0C57	3040			
002829			*	BLBT	LPIAC1
002830	0C58	9665			
002831	0C59	8520			
002834	0C5A	2605			
002835			*	GNB	
002836	0C5B	BD31			
002837			*	B	LPIAA
002838	0C5C	2020			
002841			*	LOC	LPIAC1
002842		0453	LPIAC1	EQU	X'0453'
002843			*	GNB	
002844	0C5D	E8BD			
002845	0C5E	3120			
002846			*	WAIT	
002847	0C5F	BD31			
002848			*	B	LPIA
002849	0C60	0020			
002852			*	NOP	
002853	0C61	A501			
002854			*		
002855			*		
002856			*		RECEIVE PROGRAM FOR CRC AND PARITY
002857			*		
002858			*		
002859			*	ORG	X'200'
002860			CHLP2	EQU	\$
002861			*	LD	24
002864	0C62	D618			GET LINE SPEED (ASYNC) OR SYNC CHAR (SYNC)
002865			*	OUT	4
002866	0C63	BD30			OUTPUT IT
002869			*	LD	20
002870	0C64	30D6			LOAD LINE CONTROL
002873			*	OUT	2
002874	0C65	14BD			OUTPUT IT
002877	0C66	3028			
002878			*	RECV	0
002879	0C67	BD30			
002882			*	WAIT	
002883	0C68	60BD			
002884	0C69	3100			
002885			*		
002886			*	LD	24
002887	0C6A	D618			SEE IF SYNC. LINE
002890			*	C	=X'E'
002893	0C6B	860E			IF SYNC. SET UP TO RECV.
002894	0C6C	1097			
002895			*	BET	RCVA
002896	0C6D	4196			4 OR 5 SYNC. CHAR.
002897	0C6E	4127			
002900			*	LOC	JHQD
002901		021B	JHQD	EQU	X'021B'
002902			*	RECV	0
002903	0C6F	38BD			RECEIVE FIRST SYNC CHAR.
002906	0C70	3060			
002907			*	WAIT	
002908	0C71	BD31			
002909			*	RECV	0
002910	0C72	00BD			RECEIVE SECOND SYNC.
002913	0C73	3060			
002914			*	C	24
002917	0C74	9618			CHECK TO SEE IF TRUE SYNC.
002918	0C75	1097			
002919			*	BET	JFQQ
002920	0C76	4196			
002921	0C77	4127			
002924			*	LD	20
002925	0C78	0ED6			SHUT OFF LINE
002928			*	AND	=X'FD'
002929	0C79	14C4			
002932			*	OUT	2
002933	0C7A	FDBD			
002936	0C7B	3028			
002937			*	LD	20
002940	0C7C	D614			SHUT LINE BACK ON
002941			*	OUT	2
002942	0C7D	BD30			
002945			*	B	JHQD
002946	0C7E	2820			
002949			*	LOC	JFQQ
002950		023B	JFQQ	EQU	X'023B'
002951			*	WAIT	
002952	0C7F	E0BD			
002953	0C80	3100			
002954			*	RECV	0
002955	0C81	BD30			RECEIVE THIRD SYNC.
002958			*	LOC	NSTR
002959		0241	NSTR	EQU	X'0241'
002960			*	WAIT	
002961	0C82	60BD			
002962	0C83	3100			
002963			*	RECV	0
002964	0C84	BD30			RECEIVE SYNC. OR START OF DATA CHAR. (FF)
002967			*	C	=X'FF'
002968	0C85	6086			
002971	0C86	FF10			

002972	UC87	9741			
002973			*	BEF	.NSTR
002974	UC88	9641			
002977	UC89	26F1			
002978			*	WAIT	
002979	UC8A	BD31			
002980			*		
002981			*	LOC	RCVA
002982		0253	RCVA EQU X'0253'		
002983			*	LD	23
002984	UC8B	00D6			LOAD FLAG FOR TYPE OF RECV.
002987			*	C	=1
002988	UC8C	17B6			IF TRUE TEST JUST CRC
002991	UC8D	0110			
002992	UC8E	9741			
002993			*	BET	RECV1
002994	UC8F	9641			
002997	UC90	2718			
002998			*	C	=X'F0'
003001	UC91	86F0			IF TRUE TEST JUST
003002	UC92	1097			
003003			*	BET	RECV2
003004	UC93	4196			
003005	UC94	4127			
003008			*		
003009			*	LOC	RECV3
003010		0267	RECV3 EQU X'0267'		
003011			*	RECV	3
003012	UC95	1EBD			TEST CRC AND PARITY
003015	UC96	3078			
003016			*	ST,	STURE
003017	UC97	BD30			INTO BUFFER
003018			*	BLCT	RCV
003019	UC98	CODE			BRANCH IF LAST CHARACTER TRUE
003020	UC99	6327			
003023			*	WAIT	
003024	UC9A	23BD			
003025	UC9B	3100			
003026			*	B	RECV3
003029	UC9C	20F1			
003030			*		
003031			*		
003032			*	LOC	RECV1
003033		0276	RECV1 EQU X'0276'		
003034			*	RECV	1
003035	UC9D	BD30			TEST CRC
003038			*	ST,	STURE
003039	UC9E	68BD			IN BUFFER
003040	UC9F	30C0			
003041			*	BLCT	RCV
003042	OCA0	DE63			
003045	UCA1	2714			
003046			*	WAIT	
003047	UCA2	BD31			
003048			*	B	RECV1
003049	UCA3	0020			
003052			*		
003053			*		
003054			*	LOC	RECV2
003055		0285	RECV2 EQU X'0285'		
003056			*	RECV	2
003057	OCA4	F1BD			TEST PARITY
003060	OCA5	3070			
003061			*	ST,	
003062	OCA6	BD30			
003063			*	BLCT	RCV
003064	OCA7	CODE			
003065	OCA8	6327			
003068			*	WAIT	
003069	OCA9	05BD			
003070	OCAA	3100			
003071			*	B	RECV2
003074	OCAB	20F1			
003075			*		
003076			*		
003077			*	LOC	RCV
003078		0294	RCV EQU X'0294'		
003079			*	LD	=0
003082	OCAC	C600			LOAD ZERO TO TURN OFF
003083			*	OUT	2
003084	OCAD	BD30			RECEIVE
003087			*	GNB	
003088	OCAE	28BD			
003089	OCAF	3120			
003090			*	WAIT	
003091	OCB0	BD31			
003092			*	B	RCVA
003093	OCB1	0020			
003096			*	NOP	
003097	OCB2	B201			
003098			*		
003099			*		
003100			*		XMIT PROGRAM FOR CRC AND PARITY TEST
003101			*		
003102			*		
003103			*	ORG	X'400'
003104	OCB3		CHLP2A EQU	\$	
003105			*	LD	2
003108	OCB3	D602			GET LINE CONFIGURATION
003109			*	OUT	6
003110	OCB4	BD30			OUTPUT IT
003113			*	LD	=X'FA'
003114	OCB5	38C6			LOAD FILL CHAR.
003117			*	OUT	4
003118	OCB6	FABD			OUTPUT SYNC. FILL CHAR.
003121	OCB7	3030			
003122			*	WAIT	
003123	OCB8	BD31			
003124			*		
003125			*	LD	24
003126	OCB9	00D6			SEE IF SYNC. LINE
003129			*	C	=X'E'
003130	OCBA	1886			IF SYNC. LINE SEND OUT FILL CHAR (LR 4)

003133	OCBB	OE10			
003134	OCBC	9741			
003135			*	BET	SNC1
003136	OCBD	9641			AND 3 SYNC. (OR FILL)
003139	OCBE	2724			
003140			*	LD	=X'FA'
003143	OCBF	C6FA			LOAD FILL CHAR.
003144			*	OUT	4
003145	OCCO	BD30			
003148			*	LD	=6
003149	OCC1	30C6			
003152			*	LOC	XMV
003153		041F		XMV EQU X'041F'	
003154			*	ST	63
003155	OCC2	06D7			
003158			*	LD	24
003159	OCC3	3FD6			LOAD SYNC. CHAR
003162			*	SEND	0
003163	OCC4	18BD			SEND IT
003166	OCC5	3040			
003167			*	LD	63
003170	OCC6	D63F			DD SIX TIMES
003171			*	DEC	
003172			*	BZT	SDVV
003173	OCC7	5A5D			
003176	OCC8	2705			
003177			*	WAIT	
003178	OCC9	BD31			
003179			*	B	XMV
003180	OCCA	0020			
003183			*	LOC	SDVV
003184		0431		SDVV EQU X'0431'	
003185			*	WAIT	
003186	OCCB	EEDD			
003187	OCCC	3100			
003188			*	LD	=X'FF'
003191	OCCD	C6FF			
003192			*	SEND	0
003193	OCCF	BD30			XMIT START OF DATA CHAR. (FF)
003196			*	WAIT	
003197	OCCF	40BD			
003198	UCDO	3100			
003199			*		
003200			*	LOC	SNC1
003201		043C		SNC1 EQU X'043C'	
003202			*	LD	23
003205	UCD1	D617			LOAD FLAG FOR TYPE OF SEND INST.
003206			*	C	=1
003209	UCD2	8601			IF TRUE TEST JUST CRC
003210	UCD3	1097			
003211			*	BET	CPCRC
003212	UCD4	4196			
003213	UCD5	4127			
003216			*	C	=X'F0'
003217	UCD6	1886			IF TRUE TEST PARITY
003220	UCD7	F010			
003221	UCD8	9741			
003222			*	BET	CPPAR
003223	UCD9	9641			
003226	UCDA	271E			
003227			*	LOC	SNC3
003228		0450		SNC3 EQU X'0450'	
003229			*	LD	
003230	UCDB	BD30			
003231			*	SEND	3
003232	UCDC	A0BD			
003235	UCDD	3058			
003236			*	BLCT	LCB1
003237	UCDE	DE63			BRANCH ON LAST CHAR.
003240	UCDF	2723			
003241			*	WAIT	
003242	UCE0	BD31			
003243			*	B	SNC3
003244	UCE1	0020			DUE NEXT CHAR.
003247			*		
003248			*	LOC	CPCRC
003249		045F		CPCRC EQU X'045F'	
003250			*	LD	LOAD
003251	UCE2	F1BD			CHAR. FROM DATA BUFFER
003252	UCE3	30A0			
003253			*	SEND	1
003254	UCE4	BD30			XMIT CHAR. AND CALCULATE CRC
003257			*	BLCT	LCB1
003258	UCE5	48DE			BRANCH ON LAST CHAR.
003259	UCE6	6327			
003262			*	WAIT	
003263	UCE7	14BD			
003264	UCE8	3100			
003265			*	B	CPCRC
003268	UCE9	20F1			DUE NEXT CHAR.
003269			*		
003270			*	LOC	CPPAR
003271		046E		CPPAR EQU X'046E'	
003272			*	LD	LOAD
003273	UCEA	BD30			CHAR. FROM DATA BUFFER
003274			*	SEND	2
003275	UCEB	A0BD			MIT CHAR. WITH PARITY
003278	UCEC	3050			
003279			*	BLCT	LCB1
003280	UCED	DE63			
003283	UCEE	2705			
003284			*	WAIT	
003285	UCEF	BD31			
003286			*	B	CPPAR
003287	UCF0	0020			
003290			*		
003291			*	LOC	LCB1
003292		047D		LCB1 EQU X'047D'	
003293			*	WAIT	
003294	UCF1	F1BD			
003295	UCF2	3100			
003296			*	LD	=X'FF'
003299	UCF3	C6FF			OUTPUT DUMMY CHAR. FOR SHIFT REG.

```

003300          *      SEND      0
003301 0CF4 BD30          *      BLBT      NWT
003304          *
003305 0CF5 4096          *
003306 0CF6 6585          *
003307 0CF7 2026          *
003310          *      B          SNCL
003311 0CF8 0220          *
003314          *
003315          *      LOC      NWT
003316          048D      NWT EQU X'048D'
003317          *      LD      20
003318 0CF9 AFD6          *
003321          *      AND      =X'FE'
003322 0CFA 14C4          *
003325          *      OUT      2
003326 0CFB FEBD          *
003329 0CFC 3028          *
003330          *      GNB
003331 0CFD BD31          *
003332          *      WAIT
003333 0CFE 20BD          *
003334 0CFF 3100          *
003335          *      B          SNCL
003338 0D00 20A0          *
003339          *      NOP
003340          *
003341          *
003342          *      RECEIVE PROGRAM FOR CHARACTER SIZE TEST
003343          *
003344          *
003345          *      ORG      X'200'
003346 0D01 0100          *
003347          0D02          CHLP3 EQU $
003348          *      LOC      RCP3
003349          0200          RCP3 EQU X'0200'
003350          *      LD      24
003353 0D02 D618          *
003354          *      OUT      4
003355 0D03 BD30          *
003358          *      LD      20
003359 0D04 30D6          *
003362          *      OUT      2
003363 0D05 14BD          *
003366 0D06 3028          *
003367          *      RECV      0
003368 0D07 BD30          *
003371          *      WAIT
003372 0D08 60BD          *
003373 0D09 3100          *
003374          *
003375          *      LD      24
003378 0D0A D618          *
003379          *      C          =X'E'
003382 0D0B 860E          *
003383 0D0C 1097          *
003384          *      BET      RCLH
003385 0D0D 4196          *
003386 0D0E 4127          *
003389          *      LOC      JHYY
003390          021B          JHYY EQU X'021B'
003391          *      RECV      0
003392 0D0F 3EBD          *
003395 0D10 3060          *
003396          *      WAIT
003397 0D11 BD31          *
003398          *      LD      24
003399 0D12 00D6          *
003402          *      AND      28
003403 0D13 18D4          *
003406          *      ST      59
003407 0D14 1CD7          *
003410          *      RECV      0
003411 0D15 38BD          *
003414 0D16 3060          *
003415          *      C          59
003418 0D17 963B          *
003419 0D18 1097          *
003420          *      BET      JFYY
003421 0D19 4196          *
003422 0D1A 4127          *
003425          *      LD      20
003426 0D1B 0ED6          *
003429          *      AND      =X'FD'
003430 0D1C 14C4          *
003433          *      OUT      2
003434 0D1D FDBD          *
003437 0D1E 3028          *
003438          *      LD      20
003441 0D1F D614          *
003442          *      OUT      2
003443 0D20 BD30          *
003446          *      B          JHYY
003447 0D21 2820          *
003450          *      LOC      JFYY
003451          0241          JFYY EQU X'0241'
003452          *      WAIT
003453 0D22 DABD          *
003454 0D23 3100          *
003455          *      RECV      0
003456 0D24 BD30          *
003459          *      LOC      RCKSY
003460          0247          RCKSY EQU X'0247'
003461          *      WAIT
003462 0D25 60BD          *
003463 0D26 3100          *
003464          *      RECV      0
003465 0D27 BD30          *
003468          *      C          =X'IF'
003469 0D28 6086          *
003472 0D29 1F10          *
003473 0D2A 9741          *
003474          *      BEF      RCKSY

```

BRANCH ON LAST BLOCK

GET LINE SPEED (ASYNCR) OR SYNC CHAR (SYNCR)

OUTPUT IT

OUTPUT LINE CONTROL

SEE IF SYNC TO RECV.

INPUT FIRST SYNC.

INPUT SECOND SYNC.

CHECK TO SEE IF TRUE SYNC.

SHUT OFF LINE

SHUT LINE BACK ON

INPUT THIRD SYNC.

SEE IF START OF DATA CHARACTER

```

003475 0D2B 9641
003478 0D2C 26F1
003479
003480 0D2D BD31
003481
003482 0259
003483
003484 0D2E 00D6
003487
003488 0D2F 025D
003491 0D30 270F
003492
003493
003494
003495
003496 025E
003497
003498 0D31 BD30
003501
003502 0D32 70BD
003503 0D33 30C0
003504
003505 0D34 DE63
003508 0D35 2714
003509
003510 0D36 BD31
003511
003512 0D37 0020
003515
003516
003517
003518
003519 026D
003520
003521 0D38 F1BD
003524 0D39 3060
003525
003526 0D3A BD30
003527
003528 0D3B CODE
003529 0D3C 6327
003532
003533 0D3D 05BD
003534 0D3E 3100
003535
003538 0D3F 20F1
003539
003540
003541
003542
003543 027C
003544
003547 0D40 C600
003548
003549 0D41 BD30
003552
003553 0D42 28BD
003554 0D43 3120
003555
003556 0D44 BD31
003557
003558 0D45 003F
003561 0D46 FF76
003562
003563
003564
003565
003566
003567
003568 0D47 0100
003569 0D48
003570
003573 0D48 D602
003574
003575 0D49 BD30
003578
003579 0D4A 38C6
003582
003583 0D4B FABD
003586 0D4C 3030
003587
003588 0D4D BD31
003589
003590 0D4E 00C6
003593
003594 0D4F 1FD7
003597
003598 0D50 1BD7
003601
003602 0D51 1CC6
003605
003606 0D52 FABD
003609 0D53 3030
003610
003613 0D54 D602
003614
003615 0D55 5D27
003618
003619
003620
003621
003622 0D56 2886
003625 0D57 4010
003626 0D58 9741
003627
003628 0D59 9641
003631 0D5A 2609
003632
003635 0D5B C63F
003636
003639 0D5C D71C
003640
003641

*      WAIT
*      LOC      RCCH
RCCH EQU X'0259'
*      LD      2
CHECK FOR FIVE BIT CHAR.
*      BZT     NPRY
*
*      RECV. CHAR WITH PARITY 6-7 BIT CHAR.
*
*      LOC      PRYT
PRYT EQU X'025E'
*      RECV   2
*
*      ST
*
*      BLCT   SHFL
*
*      WAIT
*
*      B      PRYT
*
*      RECV. FIVE BIT CHAR. NO PARITY
*
*      LOC      NPRY
NPRY EQU X'026D'
*      RECV   0
*
*      ST
*
*      BLCT   SHFL
*
*      WAIT
*
*      B      NPRY
*
*      TURN OFF THE LINE
*
*      LOC      SHFL
SHFL EQU X'027C'
*      LD      #0
*
*      OUT    2
*
*      GNB
*
*      WAIT
*
*      JUMP   RCP3
*
*      DC RCP3=X'028A'
*      NOP
*
*      TRANSMIT PROGRAM FOR CHARACTER SIZE TEST
*
*      ORG    X'400'
*
*      CHLP3A EQU 5
LD      2
GET LINE CONFIGURATION
*      OUT    6
OUTPUT IT
*      LD     =X'FA'
LOAD FILL CHAR.
*      OUT    4
OUTPUT SYNC. FILL CHAR.
*
*      WAIT
*
*      LD     =X'1F'
LOAD DEFAULT MASK TO STRIP 5 BITS
*
*      ST     27
*
*      ST     28
*
*      LD     =X'FA'
LOAD FILL CHAR.
*      OUT    4
OUTPUT SYNC. CHAR.
*
*      LD     2
*
*      BZT   XPYQ
*
*      SET MASK TO STRIP BITS FOR SYNC. CHAR. ON RECV. SIDE
*
*      C     =X'140'
*
*      BEF   N61
*
*      LD     =X'3F'
*
*      ST     28
*
*      SR
*      ST     27

```



003642	0D5D	54D7			
003645			*	B	XPYQ
003646	0D5E	1B20			
003649			*	LOC	N61
003650		042F	N61 EQU X'042F'		
003651			*	C	=X'80'
003652	0D5F	1986			
003655	0D60	8010			
003656	0D61	9741			
003657			*	BEF	N71
003658	0D62	9641			
003661	0D63	2609			
003662			*	LD	=X'7F'
003665	0D64	C67F			
003666			*	ST	28
003669	0D65	D71C			
003670			*	SR	
003671			*	ST	27
003672	0D66	54D7			
003675			*	B	XPYQ
003676	0D67	1B20			
003679			*	LOC	N71
003680		0441	N71 EQU X'0441'		
003681			*	LD	=X'FF'
003682	0D68	07C6			
003685			*	ST	28
003686	0D69	FFD7			
003689			*	SR	
003690	0D6A	1C54			
003691			*	ST	27
003694	0D6B	D71B			
003695			*	LOC	XPYQ
003696		0448	XPYQ EQU X'0448'		
003697			*	LD	24
003700	0D6C	D618			
003701			*	C	=X'E'
003704	0D6D	860E			
003705	0D6E	1097			
003706			*	BET	XMNS
003707	0D6F	4196			BRANCH ON ASYNC. LINE
003708	0D70	4127			
003711			*	LD	=6
003712	0D71	1FC6			
003715			*	LOC	XMZZ
003716		0455	XMZZ EQU X'0455'		
003717			*	ST	63
003718	0D72	06D7			
003721			*	LD	24
003722	0D73	3FD6			LOAD SYNC. CHAR
003725			*	SEND	0
003726	0D74	18BD			SEND IT
003729	0D75	3040			
003730			*	LD	63
003733	0D76	D63F			DO SIX TIMES
003734			*	DEC	
003735			*	BZT	SDZZ
003736	0D77	5A5D			
003739	0D78	2705			
003740			*	WAIT	
003741	0D79	B031			
003742			*	B	XMZZ
003743	0D7A	0020			
003746			*	LOC	SDZZ
003747		0467	SDZZ EQU X'0467'		
003748			*	WAIT	
003749	0D7B	EEBD			
003750	0D7C	3100			
003751			*	LD	=X'1F'
003754	0D7D	C61F			
003755			*	SEND	0
003756	0D7E	BD30			XMIT START OF DATA CHAR.
003759			*	WAIT	
003760	0D7F	40BD			
003761	0D80	3100			
003762			*	LOC	XMNS
003763		0472	XMNS EQU X'0472'		
003764			*	LD	2
003767	0D81	D602			
003768			*	BZT	XNPY
003769	0D82	5D27			
003772			*		
003773			*	SEND DATA WITH PARITY	
003774			*		
003775			*	LOC	XPY
003776		0477	XPY EQU X'0477'		
003777			*	LD	
003778	0D83	11BD			
003779	0D84	30A0			
003780			*	AND	27
003783	0D85	D41B			
003784			*	SEND	2
003785	0D86	BD30			
003788			*	BLCT	EDXT
003789	0D87	50DE			
003790	0D88	6327			
003793			*	WAIT	
003794	0D89	16BD			
003795	0D8A	3100			
003796			*	B	XPY
003799	0D8B	20EF			
003800			*		
003801			*	SEND DATA FIVE BIT NO PARITY	
003802			*		
003803			*	LOC	XNPY
003804		0488	XNPY EQU X'0488'		
003805			*	LD	
003806	0D8C	BD30			
003807			*	AND	27
003808	0D8D	A0D4			
003811			*	SEND	0
003812	0D8E	1BBD			
003815	0D8F	3040			
003816			*	BLCT	EDXT

```

003817 0D90 DE63
003820 0D91 2705
003821          *      WAIT
003822 0D92 BD31
003823          *      B      XNPY
003824 0D93 0020
003827          *
003828          *      XMIT DUMMY CHAR. TO SHIFT LAST DATA
003829          *
003830          *      LOC      EDXT
003831          *      EDXT EQU X'0499'
003832          *      WAIT
003833 0D94 EFBD
003834 0D95 3100
003835          *      LD      27
003838 0D96 D61B
003839          *      SEND   0
003840 0D97 BD30
003843          *      BLBT   EOP
003844 0D98 4096
003845 0D99 6585
003846 0D9A 2026
003849          *      WAIT
003850 0D9B 05BD
003851 0D9C 3100
003852          *      B      XMNS
003855 0D9D 20C6
003856          *
003857          *      LOC      EOP
003858          *      EOP EQU X'04AC'
003859          *      GNB
003860 0D9E BD31
003861          *      WAIT
003862 0D9F 20BD
003863 0DA0 3100
003864          *      B      EDXT
003867 0DA1 20E5
003868          *      NOP
003869          *
003870          *
003871          *
003872          *      RECEIVE PROGRAM FOR OVERRUN TEST
003873          *
003874          *
003875          *      ORG      X'200'
003876 0DA2 0100
003877 0DA3          CHLP4 EQU $
003878          *
003879          *      LD      =10
003882 0DA3 C60A
003883          *      ST      28
003886 0DA4 D71C
003887          *      LD      2
003890 0DA5 D602
003891          *      OUT    6
003892 0DA6 BD30
003895          *      LD      24
003896 0DA7 38D6
003899          *      OUT    4
003900 0DA8 18BD
003903 0DA9 3030
003904          *      LD      20
003907 0DAA D614
003908          *      OUT    2
003909 0DAB BD30
003912          *      RECV   0
003913 0DAC 28BD
003916 0DAD 3060
003917          *      WAIT
003918 0DAE BD31
003919          *
003920          *
003921          *      LOC      BEG
003922          *      BEG EQU X'0219'
003923          *      LD      24
003924 0DAF 00D6
003927          *      C      =X'E'
003928 0DB0 1886
003931 0DB1 0E10
003932 0DB2 9741
003933          *
003934 0DB3 9641
003937 0DB4 2738
003938          *
003939          *      LOC      JHRR
003940          *      JHRR EQU X'0224'
003941          *      RECV   0
003944 0DB5 BD30
003944          *      WAIT
003945 0DB6 60BD
003946 0DB7 3100
003947          *
003948 0DB8 BD30
003951          *      C      24
003952 0DB9 6096
003955 0DBA 1810
003956 0DBB 9741
003957          *
003958 0DBC 9641
003961 0DBD 270E
003962          *
003963 0DBE D614
003965 0DBF C4FD
003966          *      AND    =X'FD'
003970          *      OUT    2
003971 0DC0 BD30
003974          *
003975 0DC1 28D6
003978          *      OUT    2
003979 0DC2 14BD
003982 0DC3 3028
003983          *
003986 0DC4 20E0
003987          *      LOC      JFRR

```

GET LINE SPEED (ASYNC) OR SYNC CHAR (SYNC)  
OUTPUT IT

1ST SYNC.

2ND SYNC.

CHECK TO SEE IF TRUE SYNC.

SHUT OFF LINE

SHUT LINE BACK ON

003988		0244	JFRR EQU X'0244'	
003989			* WAIT	
003990	ODC5	BD31		
003991			* RECV 0	3RD SYNC.
003992	ODC6	00BD		
003995	ODC7	3060		
003996			* LOC NSCH	
003997		024A	NSCH EQU X'024A'	
003998			* WAIT	
003999	ODC8	BD31		
004000			* RECV 0	
004001	ODC9	00BD		
004004	ODCA	3060		
004005			* C =X'FF'	SEE IT START DATA CHAR.
004008	ODCB	86FF		
004009	ODCC	1097		
004010			* BEF NSCH	
004011	ODCD	4196		
004012	ODCE	4126		
004015			* WAIT	
004016	ODCF	F1BD		
004017	ODD0	3100		
004018			* LOC RMCH	
004019		025C	RMCH EQU X'025C'	
004020			* RECV 0	
004021	ODD1	BD30		
004024			* ST	
004025	ODD2	60BD		
004026	ODD3	30C0		
004027			* BLCT REND	
004028	ODD4	DE63		
004031	ODD5	2705		
004032			* WAIT	
004033	ODD6	BD31		
004034			* B RMCH	
004035	ODD7	0020		
004038			*	
004039			*	
004040			* LOC REND	
004041		026B	REND EQU X'026B'	
004042			* LD =X'C5'	
004043	ODD8	F1C6		
004046			* OUT 2	
004047	ODD9	C5BD		
004050	ODDA	3028		
004051			* GNB	
004052	ODDB	BD31		
004053			* WAIT	
004054	ODDC	20BD		
004055	ODDD	3100		
004056			* B BEG	
004059	ODDE	20A1		
004060			*	
004061			*	
004062			*	
004063			* TRANSMIT PROGRAM FOR UNDERRUN TEST	
004064			*	
004065			*	
004066			* ORG X'2C0'	
004067	QDDE		CHLPA EQU \$	
004068			* LOC TOP	
004069		02C0	TOP EQU X'02C0'	
004070			* WAIT	
004071	ODDF	BD31		
004072			* LD 24	
004073	ODE0	00D6		
004076			* C =X'E'	
004077	ODE1	1886		
004080	ODE2	0E10		
004081	ODE3	9741		
004082			* BET NSY	
004083	ODE4	9641		
004086	ODE5	274A		
004087			* LD =X'AA'	LOAD FILL CHAR.
004090	ODE6	C6AA		
004091			* OUT 4	
004092	ODE7	BD30		
004095			* LD =6	
004096	ODE8	30C6		
004099			* LOC XMRR	
004100		02D5	XMRR EQU X'02D5'	
004101			* ST 63	
004102	ODE9	06D7		
004105			* LD 24	LOAD SYNC. CHAR
004106	ODEA	3FD6		
004109			* SEND 0	SEND IT
004110	ODEB	18BD		
004113	ODEC	3040		
004114			* LD 63	DD SIX TIMES
004117	ODED	D63F		
004118			* DEC	
004119			* BZT SDRR	
004120	ODEE	5A5D		
004123	ODEF	2705		
004124			* WAIT	
004125	ODF0	BD31		
004126			* B XMRR	
004127	ODF1	0020		
004130			* LOC SDRR	
004131		02E7	SDRR EQU X'02E7'	
004132			* WAIT	
004133	ODF2	EEBD		
004134	ODF3	3100		
004135			* LD =X'FF'	
004138	ODF4	C6FF		
004139			* SEND 0	
004140	ODF5	BD30		
004143			* WAIT	
004144	ODF6	40BD		
004145	ODF7	3100		
004146			* LD =2	
004149	ODF8	C602		
004150			* ST 26	

004153	ODF9	D71A			
004154			*	LOC	NZ7
004155		02F6	NZ7	EQU X'02F6'	
004156			*	LD	
004157	ODFA	BD30			
004158			*	SEND	0
004159	ODFB	A0BD			
004162	ODFC	3040			
004163			*	LD	26
004166	ODFD	D61A			
004167			*	DEC	
004168			*	ST	26
004169	ODFE	5AD7			
004172			*	BZT	CDLY
004173	ODFF	1A5D			
004176	OE00	2705			
004177			*	WAIT	
004178	OE01	BD31			
004179			*	B	NZ7
004180	OE02	0020			
004183			*		
004184			*		
004185			*	LOC	CDLY
004186		0309	CDLY	EQU X'0309'	
004187			*	BS	DLAY
004188	OE03	EDBD			
004189	OE04	3080			
004192	OE05	2067			
004193			*	IN	5
004194	OE06	BD30			
004197			*	ST	60
004198	OE07	14D7			
004201			*	WAIT	
004202	OE08	3CBD			
004203	OE09	3100			
004204			*	B	NT3
004207	OE0A	2028			
004208			*		
004209			*		
004210			*	LOC	NSY
004211		0318	NSY	EQU X'0318'	
004212			*	LD	
004213	OE0B	BD30			
004214			*	SEND	0
004215	OE0C	A0BD			
004218	OE0D	3040			
004219			*	LD	28
004222	OE0E	D61C			
004223			*	DEC	
004224			*	ST	28
004225	OE0F	5AD7			
004228			*	BZT	OVLY
004229	OE10	1C5D			
004232	OE11	2705			
004233			*	WAIT	
004234	OE12	BD31			
004235			*	B	NSY
004236	OE13	0020			
004239			*	LOC	OVLY
004240		032B	OVLY	EQU X'032B'	
004241			*	BS	DLAY
004242	OE14	EDBD			
004243	OE15	3080			
004246	OE16	2045			
004247			*	SEND	0
004248	OE17	BD30			
004251			*	BS	DLAY
004252	OE18	40BD			
004253	OE19	3080			
004256	OE1A	203D			
004257			*	IN	5
004258	OE1B	BD30			
004261			*	ST	60
004262	OE1C	14D7			
004265			*	WAIT	
004266	OE1D	3CBD			
004267	OE1E	3100			
004268			*	LOC	NT3
004269		0340	NT3	EQU X'0340'	
004270			*	LD	
004271	OE1F	BD30			
004272			*	SEND	0
004273	OE20	A0BD			
004276	OE21	3040			
004277			*	BLCT	EXM1
004278	OE22	DE63			
004281	OE23	2705			
004282			*	WAIT	
004283	OE24	BD31			
004284			*	B	NT3
004285	OE25	0020			
004288			*		
004289			*		
004290			*	LOC	EXM1
004291		034F	EXM1	EQU X'034F'	
004292			*	WAIT	
004293	OE26	F1BD			
004294	OE27	3100			
004295			*	LD	=X'FF'
004298	OE28	C6FF			
004299			*	SEND	0
004300	OE29	BD30			
004303			*	BLBT	NSXM
004304	OE2A	4096			
004305	OE2B	6585			
004306	OE2C	2026			
004309			*	WAIT	
004310	OE2D	06BD			
004311	OE2E	3100			
004312			*	JUMP	TOP
004315	OE2F	3FFF			
004316			*		
004317			*	LOC	NSXM

GO TO CHAR. DELAY

SEE IF UNDERRUN BIT SET

```

004318      0363      NSXM EQU X'0363'
004319      *          GNB
004320      0E30 50BD
004321      0E31 3120
004322      *          LD      20
004325      0E32 D614
004326      *          AND     =X'FE'
004329      0E33 C4FE
004330      *          ST      20
004333      0E34 D714
004334      *          OUT     2
004335      0E35 BD30
004336      *          WAIT
004339      0E36 28BD
004340      0E37 3100
004341      *          B       NSXM
004344      0E38 20EF
004345      *          NOP
004346      *          *
004347      *          LOC     DLAY
004348      0375      DLAY EQU X'0375'
004349      *          LD      29
004350      0E39 01D6
004353      *          BZF     EDLY
004354      0E3A 1D5D
004357      0E3B 2604
004358      *          RET
004359      0E3C DE52
004360      0E3D 6E00
004361      *          LOC     EDLY
004362      037E      EDLY EQU X'037E'
004363      *          ST      30
004366      0E3E D71E
004367      *          LD      =94
004370      0E3F C65E
004371      *          *
004372      0382      *          LOC     LESS
004373      *          LESS EQU X'0382'
004374      *          DEC
004375      0E40 5A5D
004378      0E41 2702
004379      *          B       LESS
004382      0E42 20FA
004383      *          *
004384      0388      *          LOC     NXC
004385      *          NXC EQU X'0388'
004388      0E43 D61E
004389      *          LD      30
004390      *          DEC
004391      0E44 5A5D
004394      0E45 26F0
004395      *          BZF     EDLY
004396      *          RET
004399      0E46 DE52
004397      0E47 6E00
004398      *          NOP
004399      *
004400      *
004401      *          CHANNEL PROGRAM TO LOOP AND CHECK DSC INTO DSS
004402      *
004403      *
004404      *          ORG     X'200'
004405      0E48 0100
004406      0E49
004407      *          CHLP5 EQU $
004408      0200      *          LOC     TOP1
004409      *          TOP1 EQU X'0200'
004412      0E49 C607
004413      *          LD      =7
004416      0E4A D71E
004417      *          ST      30
004420      0E4B D61D
004421      *          LD      29
004424      0E4C 86F0
004425      0E4D 1097
004426      *          C       =X'F0'
004427      0E4E 4196
004428      0E4F 4126
004431      *          BEF     TOP3
004432      0E50 08C6
004433      *          LD      =16
004436      0E51 10D7
004439      *          ST      58
004440      0E52 3AC6
004443      *          LD      =2
004444      0E53 0220
004447      *          B       TCMB1
004448      *          *
004449      *          LOC     TOP3
004450      0E54 0FB6
004453      0E55 FC10
004454      0E56 9741
004455      *          TOP3 EQU X'0217'
004456      *          C       =X'FC'
004459      0E57 9641
004458      0E58 2704
004460      *          BET     TOP2
004463      *          LD      =8
004464      0E59 C608
004467      *          ST      58
004468      0E5A D73A
004469      *          *
004470      *          LOC     TOP2
004473      0E5B C600
004474      *          TOP2 EQU X'0224'
004475      *          LD      =0
004476      *          *
004477      *          LOC     TCMB1
004479      0E5C D719
004480      *          TCMB1 EQU X'0226'
004483      0E5D 8604
004484      0E5E 1097
004485      *          ST      25
004486      *          C       =4
004487      0E5F 4196
004488      0E60 4126
004489      *          BEF     DSET
004490      *          LD      =X'80'

```

004491	0E61	08C6			
004494			*	OR	28
004495	0E62	80DA			
004498			*	ST	28
004499	0E63	1CD7			
004502			*	LD	=4
004503	0E64	1CC6			
004506			*	LOC	DSET
004507		0239	*	DSET EQU X'0239'	
004508			*	TLU	57
004509	0E65	04DE			
004512	0E66	39BD			
004513	0E67	30E0			
004514			*	OR	28
004517	0E68	DA1C			
004518			*	OUT	2
004519	0E69	BD30			
004522			*	CCH	TIME
004523	0E6A	28BD			DELAY FOR CABLE TURN AROUND
004524	0E6B	3160			
004525			*	NOP	
004526			*	NOP	
004527	0E6C	0101			
004528			*	NOP	
004529			*	CCH	
004530	0E6D	018D			
004531	0E6E	3160			
004532			*	CCH	
004533	0E6F	BD31			
004534			*	CCH	
004535	0E70	60BD			
004536	0E71	3160			
004537			*	CCH	
004538	0E72	BD31			
004539			*	IN	5
004540	0E73	60BD			
004543	0E74	3014			
004544			*	ST	
004545	0E75	BD30			
004546			*	LD	25
004547	0E76	C0D6			
004550			*	C	30
004551	0E77	1996			
004554	0E78	1E10			
004555	0E79	9741			
004556			*	BET	ENDTC
004557	0E7A	9641			
004560	0E7B	2707			
004561			*	XOR	=X'FF'
004564	0E7C	C8FF			
004565			*	DEC	
004566			*	XOR	=X'FF'
004567	0E7D	5AC8			
004570			*	B	TCMB1
004571	0E7E	FF20			
004574			*	LOC	ENDTC
004575		026D	*	ENDTC EQU X'026D'	
004576			*	GNB	
004577	0E7F	B9BD			
004578	0E80	312Q			
004579			*	WAIT	
004580	0E81	BD31			
004581			*	B	TOPI
004582	0E82	0020			
004585			*	NOP	
004586	0E83	8B01			
004587			*		
004588			*		CHANNEL PROGRAM FOR BROADBAND(RECV)
004589			*		ADAPTER READY TEST & UNDERRUN OVERRUN TEST
004590			*		
004591			*	ORG	X'200'
004592			*	CHLP6 EQU	\$
004593			*	LD	24
004596	0E84	D618			GET LINE SPEED (ASYNC) OR SYNC CHAR (SYNC)
004597			*	OUT	4
004598	0E85	BD30			OUTPUT IT
004601			*	LD	20
004602	0E86	30D6			
004605			*	OUT	2
004606	0E87	14BD			
004609	0E88	3028			
004610			*	WAIT	
004611	0E89	BD31			
004612			*	LOC	SINK
004613		020D	*	SINK EQU X'020D'	
004614			*	RECV	0
004615	0E8A	00BD			INPUT 1ST SYNC.
004618	0E8B	3060			
004619			*	WAIT	
004620	0E8C	BD31			
004621			*	RECV	0
004622	0E8D	00BD			INPUT 2ND SYNC.
004625	0E8E	3060			
004626			*	C	24
004629	0E8F	9618			
004630	0E90	1097			
004631			*	BET	NSYK
004632	0E91	4196			
004633	0E92	4127			
004636			*	LD	20
004637	0E93	0ED6			
004640			*	AND	=X'FD'
004641	0E94	14C4			
004644			*	OUT	2
004645	0E95	FDBD			
004648	0E96	3028			
004649			*	LD	20
004652	0E97	D614			
004653			*	OUT	2
004654	0E98	BD30			
004657			*	B	SINK
004658	0E99	2820			
004661			*	LOC	NSYK

004662		022D	NSYK EQU X'022D'
004663			* WAIT
004664	0E9A	E0BD	
004665	0E9B	3100	
004666			* RECV 0
004667	0E9C	BD30	
004670			* WAIT
004671	0E9D	60BD	
004672	0E9E	3100	
004673			* LD 30
004676	0E9F	D61E	
004677			* BZF UDOR
004678	0EA0	5D26	
004681			* BARF REKR
004682	0EA1	3B20	
004685			* LD 20
004686	0EA2	0ED6	
004689			* OUT 2
004690	0EA3	14BD	
004693	0EA4	3028	
004694			* LOC BUFE
004695		0242	BUFE EQU X'0242'
004696			* RECV 0
004697	0EA5	BD30	
004700			* ST
004701	0EA6	60BD	
004702	0EA7	30C0	
004703			* BARF BUFE
004706	0EA8	20F8	
004707			* BART CHRL
004708			* LOC REKR
004709		024B	RERR EQU X'024B'
004710			* LD =X'FF'
004711	0EA9	01C6	
004714			* ST 61
004715	0EAA	FFD7	
004718			* LOC CHRL
004719		024F	CHRL EQU X'024F'
004720			* LD =X'33'
004721	0EAB	3DC6	
004724			* ST 62
004725	0EAC	33D7	
004728			* LD =127
004729	0EAD	3EC6	
004732			* ST 59
004733	0EAE	7FD7	
004736			* LOC BEPY3
004737		0257	BEPY3 EQU X'0257'
004738			* WAIT
004739	0EAF	38BD	
004740	0EB0	3100	
004741			* RECV 0
004742	0EB1	BD30	
004745			* ST
004746	0EB2	60BD	
004747	0EB3	30C0	
004748			* LD 59
004751	0EB4	D63B	
004752			* DEC
004753			* BZT STP3
004754	0EB5	5A5D	
004757	0EB6	2758	
004758			* ST 59
004761	0EB7	D73B	
004762			* IN 5
004763	0EB8	BD30	
004766			* AND =X'04'
004767	0EB9	14C4	
004770			* BZF BEPY3
004771	0EBA	045D	
004774	0EBB	26E7	
004775			* LD =0
004778	0EBC	C600	
004779			* ST 62
004782	0EBD	D73E	
004783			* B STP3
004786	0EBE	2048	
004787			* LOC UDOR
004788		0276	UDOR EQU X'0276'
004789			* C =X'F'
004792	0EBF	860F	
004793	0EC0	1097	
004794			* BET UND
004795	0EC1	4196	
004796	0EC2	4127	
004799			* RECV 0
004800	0EC3	0BBD	
004803	0EC4	3060	
004804			* ST
004805	0EC5	BD30	
004806			* WAIT
004807	0EC6	C0BD	
004808	0EC7	3100	
004809			* B STP3
004812	0EC8	2034	
004813			*
004814			*
004815			* LOC UND
004816		028A	UND EQU X'028A'
004817			* LD =64
004820	0EC9	C640	
004821			* LOC FIFE
004822		028C	FIFE EQU X'028C'
004823			* ST 59
004826	0ECA	D73B	
004827			* BARF FIF3
004830	0ECB	200C	
004831			* RECV 0
004832	0ECC	BD30	
004835			* ST
004836	0ECD	60BD	
004837	0ECE	30C0	
004838			* LD 59

CHECK BUFFER EMPTY BIT

```

004841 OECF D63B
004842 * DEC
004843 * BZT B1
004844 OEDO 5A5D
004847 OED1 2702
004848 * LOC FIF3
004849 * FIF3 EQU X'029C'
004850 * B FIFE
004853 OED2 20EE
004854 * LOC B1
004855 * B1 EQU X'029E'
004856 * RECV 0
004857 OED3 BD30
004860 * ST
004861 OED4 60BD
004862 OED5 30C0
004863 * IN 5
004864 OED6 BD30
004867 * ST 60
004868 OED7 14D7
004871 * LD 20
004872 OED8 3CD6
004875 * OR =1
004876 OED9 14CA
004879 * ST 20
004880 OEDA 01D7
004883 * OUT 2
004884 OEDB 14BD
004887 OEDC 3028
004888 * WAIT
004889 OEDD BD31
004890 * RECV 0
004891 OEDE 00BD
004894 OEDF 3060
004895 * ST
004896 OEE0 BD30
004897 * WAIT
004898 OEE1 COBD
004899 OEE2 3100
004900 * LOC STP3
004901 * STP3 EQU X'02BE'
004902 * LD =0
004905 OEE3 C600
004906 * OUT 2
004907 OEE4 BD30
004910 * GNB
004911 OEE5 28BD
004912 OEE6 3120
004913 * WAIT
004914 OEE7 BD31
004915 * NOP
004916 OEE8 0001
004917 *
004918 * CHANNEL PROGRAM FOR BROADBAND LOOP(XMIT)
004919 * ADAPTER READY & UNDERRUN OVERRUN TEST
004920 *
004921 *
004922 *
004923 * ORG X'400'
004924 * CHLP6A EQU $
004925 * LD 2
004927 OEE9 D602
004928 * OUT 6
004929 OEEA BD30
004932 * LD 24
004933 OEEB 38D6
004936 * OUT 4
004937 OEEC 18BD
004940 OEEED 3030
004941 * WAIT
004942 OEEE BD31
004943 * IN 5
004944 OEEF 00BD
004947 OEF0 3014
004948 * AND =X'04'
004951 OEF1 C404
004952 * BZT BEPY
004953 OEF2 5D27
004956 * LD =X'11'
004957 OEF3 04C6
004960 * ST 62
004961 OEF4 11D7
004964 * LOC BEPY
004965 * BEPY EQU X'0419'
004966 * LD =3
004967 OEF5 3EC6
004970 * LOC XMBB
004971 * XMBB EQU X'041B'
004972 * ST 59
004973 OEF6 03D7
004976 * LD 24
004977 OEF7 38D6
004980 * SEND 0
004981 OEF8 18BD
004984 OEF9 3040
004985 * LD 59
004988 OEFA D63B
004989 * DEC
004990 * BZT SDBB
004991 OEFB 5A5D
004994 OEFc 2705
004995 * WAIT
004996 OEFD BD31
004997 * B XMBB
004998 OEFE 0020
005001 * LOC SDBB
005002 * SDBB EQU X'042D'
005003 * WAIT
005004 OEFF EEBD
005005 OF00 3100
005006 * LD 30
005009 OF01 D61E
005010 * BZF ORUD
005011 OF02 5D26

```

TURN ON XMIT

GET LINE CONFIGURATION

OUTPUT IT

CHECK BUFFER EMPTY BIT

LOAD SYNC. CHAR

SEND IT

DO SIX TIMES



005014			*	LD	20	
005015	0F03	48D6	*	AND	=X'FD'	
005018			*	OUT	2	
005019	0F04	14C4				
005022			*	OUT	2	
005023	0F05	F8BD				
005026	0F06	3028				
005027			*	LD	=58	
005030	0F07	C63A				
005031			*	LOC	PGCX	
005032		043E	PGCX EQU X'043E'			
005033			*	ST	59	
005036	0F08	D73B				
005037			*	LD		
005038	0F09	BD30				
005039			*	SEND	0	
005040	0F0A	A0BD				
005043	0F0B	3040				
005044			*	LD	59	
005047	0F0C	D63B				
005048			*	DEC		
005049			*	BZT	ALD	
005050	0F0D	5A5D				
005053	0F0E	2702				
005054			*	B	PGCX	
005057	0F0F	20F0				
005058			*	LOC	ALD	
005059		044E	ALD EQU X'044E'			
005060			*	IN	5	CHECK BUFFER EMPTY BIT
005061	0F10	BD30				
005064			*	AND	=X'04'	
005065	0F11	14C4				
005068			*	BZF	BEPY1	
005069	0F12	045D				
005072	0F13	2604				
005073			*	LD	=X'22'	
005076	0F14	C622				
005077			*	ST	62	
005080	0F15	D73E				
005081			*	LOC	BEPY1	
005082		045A	BEPY1 EQU X'045A'			
005083			*	LD	=70	
005086	0F16	C646				
005087			*	LOC	AL76	
005088		045C	AL76 EQU X'045C'			
005089			*	ST	59	
005092	0F17	D73B				
005093			*	LD		
005094	0F18	BD30				
005095			*	SEND	0	
005096	0F19	A0BD				
005099	0F1A	3040				
005100			*	LD	59	
005103	0F1B	D63B				
005104			*	DEC		
005105			*	BZT	AL77	
005106	0F1C	5A5D				
005109	0F1D	2706				
005110			*	BART	AL76	
005111			*	WAIT		
005112	0F1E	01BD				
005113	0F1F	3100				
005114			*	B	TRCV	
005117	0F20	2004				
005118			*	LOC	AL77	
005119		0470	AL77 EQU X'0470'			
005120			*	LD	=X'FF'	
005123	0F21	C6FF				
005124			*	ST	60	
005127	0F22	D73C				
005128			*	LOC	TRCV	
005129		0474	TRCV EQU X'0474'			
005130			*	LD	20	
005133	0F23	D614				
005134			*	AND	=X'FC'	TURN OFF XMIT AND REC.V.
005137	0F24	C4FC				
005138			*	OUT	2	
005139	0F25	BD30				
005142			*	B	ALD1	
005143	0F26	2820				
005146			*	LOC	ORUD	
005147		047D	ORUD EQU X'047D'			
005148			*	C	=X'F'	
005149	0F27	4386				
005152	0F28	0F10				
005153	0F29	9741				
005154			*	BEF	OVR	
005155	0F2A	9641				
005158	0F2B	261A				
005159			*	LD	20	
005162	0F2C	D614				
005163			*	AND	=X'FE'	
005166	0F2D	C4FE				
005167			*	ST	20	
005170	0F2E	D714				
005171			*	OUT	2	TURN OFF XMIT
005172	0F2F	BD30				
005175			*	WAIT		
005176	0F30	28BD				
005177	0F31	3100				
005178			*	LD		
005179	0F32	BD30				
005180			*	SEND	0	
005181	0F33	A0BD				
005184	0F34	3040				
005185			*	GNB		
005186	0F35	BD31				
005187			*	WAIT		
005188	0F36	20BD				
005189	0F37	3100				
005190			*	B	ALD1	
005193	0F38	2020				
005194			*			

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005195 *
005196 * LOC OVR
005197 04A0 OVR EQU X'04A0'
005198 * LD
005199 OF39 BD30 *
005200 * SEND 0
005201 OF3A A0BD *
005204 OF3B 3040 * BART OVR
005206 * LD =74
005207 OF3C 01C6 *
005210 * LOC CNT
005211 04A9 CNT EQU X'04A9'
005212 * ST 30
005213 OF3D 4AD7 *
005216 * BARF ROTR
005217 OF3E 1E20 *
005220 * LD
005221 OF3F 0CBD *
005222 OF40 30A0 *
005223 * SEND 0
005224 OF41 BD30 *
005227 * LD 30
005228 OF42 40D6 *
005231 * BZT OVCK
005232 OF43 1E5D *
005235 OF44 2703 *
005236 * DEC
005237 * LOC ROTR
005238 04B9 ROTR EQU X'04B9'
005239 * B CNT
005240 OF45 5A20 *
005243 * LOC OVCK
005244 04BB OVCK EQU X'04BB'
005245 * IN 5
005246 OF46 EEBD *
005249 OF47 3014 *
005250 * ST 60
005253 OF48 D73C *
005254 *
005255 *
005256 * LOC ALD1
005257 04C0 ALD1 EQU X'04C0'
005258 * LD 20
005261 OF49 D614 *
005262 * AND =X'FE'
005265 OF4A C4FE *
005266 * ST 20
005269 OF4B D714 *
005270 * OUT 2
005271 OF4C BD30 *
005274 * GNB
005275 OF4D 28BD *
005276 OF4E 3120 *
005277 * LOC TAMPA
005278 04CC TAMPA EQU X'04CC'
005279 * WAIT
005280 OF4F BD31 *
005281 * B TAMPA
005282 OF50 0020 *
005285 * NOP
005286 OF51 FB01 *
005287 *
005288 *
005289 * CHANNEL PROGRAM FOR STOP 1/0(BROADBAND REC.V)
005290 *
005291 *
005292 *
005293 * ORG X'200'
005294 OF52 CHLP7 EQU $
005297 OF52 D618 * LD 24 GET LINE SPEED (ASync) OR SYNC CHAR (SYNC)
005298 * OUT 4 OUTPUT IT
005299 OF53 BD30 *
005302 * LD 20
005303 OF54 30D6 *
005306 * OUT 2
005307 OF55 14BD *
005310 OF56 3028 *
005311 * WAIT
005312 OF57 BD31 *
005313 * LOC SINK1
005314 020D SINK1 EQU X'020D'
005315 * RECV 0 INPUT 1ST SYNC.
005316 OF58 00BD *
005319 OF59 3060 *
005320 * WAIT
005321 OF5A BD31 *
005322 * RECV 0 INPUT 2ND SYNC.
005323 OF5B 00BD *
005326 OF5C 3060 *
005327 * C 24
005330 OF5D 9618 *
005331 OF5E 1097 *
005332 * BET NSYK1
005333 OF5F 4196 *
005334 OF60 4127 *
005337 * LD 20
005338 OF61 0ED6 *
005341 * AND =X'FD'
005342 OF62 14C4 *
005345 * OUT 2
005346 OF63 FDBD *
005349 OF64 3028 *
005350 * LD 20
005353 OF65 D614 *
005354 * OUT 2
005355 OF66 BD30 *
005358 * B SINK1
005359 OF67 2820 *
005362 * LOC NSYK1
005363 022D NSYK1 EQU X'022D'
005364 * WAIT
005365 OF68 E0BD *

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005366 OF69 3100
005367
005368 OF6A BD30
005371
005372 0233
005373
005374 OF6B 60BD
005375 OF6C 3100
005376
005377 OF6D BD30
005380
005381 OF6E 6086
005384 OF6F DB10
005385 OF70 9741
005386
005387 OF71 9641
005390 OF72 26F1
005391
005392 OF73 BD31
005393
005394 0245
005395
005396 OF74 00BD
005399 OF75 3060
005400
005401 OF76 BD30
005402
005403 OF77 CODE
005404 OF78 6327
005407
005408 OF79 0601
005409
005410 OF7A BD31
005411
005412 OF7B 0020
005415
005416 0255
005417
005418 OF7C F0C6
005421
005422 OF7D 00BD
005425 OF7E 3028
005426
005427 OF7F BD31
005428
005429 OF80 20BD
005430 OF81 3100
005431
005434 OF82 20AB
005435
005436
005437
005438
005439
005440
005441
005442
005443
005444 OF83 0100
005445 OF84
005446
005449 OF84 D602
005450
005451 OF85 BD30
005454
005455 OF86 38C6
005458
005459 OF87 AABD
005462 OF88 3030
005463
005464 OF89 BD31
005465
005466 OF8A 00C6
005469
005470 040F
005471
005472 OF8B 06D7
005475
005476 OF8C 3FD6
005479
005480 OF8D 18BD
005483 OF8E 3040
005484
005487 OF8F D63F
005488
005489
005490 OF90 5A5D
005493 OF91 2705
005494
005495 OF92 BD31
005496
005497 OF93 0020
005500
005501 0421
005502
005503 OF94 EEBD
005504 OF95 3100
005505
005508 OF96 C6DB
005509
005510 OF97 BD30
005513
005514 OF98 40BD
005515 OF99 3100
005516
005517 042C
005518
005519 OF9A BD30
005520
005521 OF9B A0BD
005524 OF9C 3040
005525
005526

* RECV 0
* LOC LKB1
LKB1 EQU X'0233'
* WAIT
* RECV 0
* C =X'DB'
* BEF LKB1
* WAIT
* LOC T25A
T25A EQU X'0245'
* RECV 0
* ST
* BLCT KER
* BART T25A
* WAIT
* B T25A
* LOC KER
KER EQU X'0255'
* LD =0
* OUT 2
* GNB
* WAIT
* B SINK1
* NOP
*
* CHANNEL PROGRAM FOR STOP I/O (BROADBAND XMIT)
*
* ORG X'400'
CHLPTA EQU $
* LD 2 GET LINE CONFIGURATION
* OUT 6 OUTPUT IT
* LD =X'AA'
* OUT 4
* WAIT
* LD =6
* LOC XMWW
XMWW EQU X'040F'
* ST 63
* LD 24 LOAD SYNC. CHAR
* SEND 0 SEND IT
* LD 63 DO SIX TIMES
* DEC
BZT SDWW
* WAIT
* B XMWW
* LOC SDWW
SDWW EQU X'0421'
* WAIT
* LD =X'DB'
* SEND 0
* WAIT
* LOC T25B
T25B EQU X'042C'
* LD
* SEND 0
* BART T25B
* WAIT

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005527	OF9D	01BD			
005528	OF9E	3100			
005529			*	LOC	HANG
005530		0436	HANG EQU X'0436'		
005531			*	LD	
005532	OF9F	BD30			
005533			*	SEND	0
005534	0FA0	A0BD			
005537	0FA1	3040			
005538			*	BLCT	ERR
005539	0FA2	DE63			
005542	0FA3	2705			
005543			*	WAIT	
005544	0FA4	BD31			
005545			*	B	HANG
005546	0FA5	0020			
005549			*	LOC	ERR
005550		0445	ERR EQU X'0445'		
005551			*	WAIT	
005552	0FA6	F1BD			
005553	0FA7	3100			
005554			*	LD	20
005557	0FAB	D614			
005558			*	AND	=X'FE'
005561	0FA9	C4FE			
005562			*	OUT	2
005563	0FAA	BD30			
005566			*	GNB	
005567	0FAB	28BD			
005568	0FAC	3120			
005569			*	WAIT	
005570	0FAD	BD31			
005571			*	B	T25B
005572	0FAE	0020			
005575			*	NOP	
005576	0FAF	D501			
005577			*		
005578			*		
005579			*		
005580			*		
005581			*		
005582			*		
005583			*	ORG	X'200'
005584		0FB0	CHLPB EQU		
005585			LD	24	
005588	0FB0	D618			
005589			*	OUT	4
005590	0FB1	BD30			OUTPUT SYNC. CHARACTER
005593			*	LD	20
005594	0FB2	30D6			
005597			*	OUT	2
005598	0FB3	14BD			OUTPUT LINE CONTROL
005601	0FB4	3028			
005602			*	WAIT	
005603	0FB5	BD31			
005604			*		
005605			*	LOC	SYCH
005606		020D	SYCH EQU X'020D'		GET IN RECV. IN SYNC.
005607			*	RECV	0
005608	0FB6	00BD			
005611	0FB7	3060			
005612			*	BART	SCY2
005613			*	WAIT	
005614	0FB8	01BD			
005615	0FB9	3100			
005616			*	LOC	SCY2
005617		0214	SCY2 EQU X'0214'		
005618			*	RECV	0
005619	0FBA	BD30			
005622			*	C	28
005623	0FBB	6096			
005626	0FBC	1C10			
005627	0FBD	9741			
005628			*	BET	SYC
005629	0FBE	9641			
005632	0FBF	2705			
005633			*	SFS	
005634	0FC0	BD31			
005635			*	B	SYCH
005636	0FC1	4020			
005639			*	LOC	SYC
005640		0225	SYC EQU X'0225'		
005641			*	BART	SCY1
005642	0FC2	E801			
005643			*	WAIT	
005644	0FC3	BD31			
005645			*	LOC	SCY1
005646		0229	SCY1 EQU X'0229'		
005647			*	RECV	0
005648	0FC4	00BD			
005651	0FC5	3060			
005652			*	C	=X'19'
005655	0FC6	8619			CHECK FOR START OF MESSAGE SHAR.
005656	0FC7	1097			
005657			*	BEF	SYC
005658	0FC8	4196			
005659	0FC9	4126			
005662			*		
005663			*	LD	24
005664	0FCA	F0D6			
005667			*	C	=X'32'
005668	0FCB	1886			
005671	0FCC	3210			
005672	0FCD	9741			
005673			*	BET	CPRT
005674	0FCE	9641			IF EQUAL GO TO CRC AND PARITY ROUTINES
005677	0FCF	2760			
005678			*		
005679			*	LD	2
005682	0FD0	D602			START OF CHAR. SIZE TEST
005683			*	BZT	FVBC
005684	0FD1	5D27			BRANCH TO FIVE BIT CHAR. OUTPUT LOOP
005687			*	C	=X'40'

005688	OFD2	3686			
005691	OFD3	4010			
005692	OFD4	9741			
005693			*	BEF	NFBA
005694	OFD5	9641			
005697	OFD6	2619			
005698			*	LOC	SXBC
005699	024E		SXBC	EQU X'024E'	
005700			*	BART	SXB1
005701			*	WAIT	
005702	OFD7	01BD			
005703	OFD8	3100			
005704			*	LOC	SXB1
005705	0252		SXB1	EQU X'0252'	
005706			*	RECV	2
005707	OFD9	BD30			
005710			*	C	30
005711	OFDA	7096			
005714	OFDB	1E10			
005715	OFDC	9741			
005716			*	BET	SXBC
005717	OFDD	9641			
005720	OFDE	27F0			
005721			*	ST	
005722	OFDF	BD30			
005723			*	BLCT	EXT
005724	OFE0	CODE			
005725	OFE1	6327			
005728			*	B	SXBC
005729	OFE2	7F20			
005732			*		
005733			*	LOC	NFBA
005734	0267		NFBA	EQU X'0267'	
005735			*	BART	NFVB
005736	OFE3	E701			
005737			*	WAIT	
005738	OFE4	BD31			
005739			*	LOC	NFVB
005740	026B		NFVB	EQU X'026B'	
005741			*	RECV	2
005742	OFE5	00BD			
005745	OFE6	3070			
005746			*	ST	
005747	OFE7	BD30			
005748			*	BLCT	EXT
005749	OFE8	CODE			
005750	OFE9	6327			
005753			*	B	NFBA
005754	OFEA	6F20			
005757			*	LOC	FVBY
005758	0277		FVBY	EQU X'0277'	
005759			*	BART	FVBC
005760	OFEB	F001			
005761			*	WAIT	
005762	OFEC	BD31			
005763			*	LOC	FVBC
005764	027B		FVBC	EQU X'027B'	
005765			*	RECV	0
005766	OFED	00BD			
005769	OFEE	3060			
005770			*	C	30
005773	OFEF	961E			
005774	OFF0	1097			
005775			*	BET	FVBY
005776	OFF1	4196			
005777	OFF2	4127			
005780			*	ST	
005781	OFF3	F0BD			
005782	OFF4	30C0			
005783			*	C	'X'1E'
005786	OFF5	861E			
005787	OFF6	1097			
005788			*	BET	TKF
005789	OFF7	4196			
005790	OFF8	4127			
005793			*	LOC	TKFN
005794	0293		TKFN	EQU X'0293'	
005795			*	BLCT	EXT
005796	OFF9	06DE			
005797	OFFA	6327			
005800			*	B	FVBY
005801	OFFB	4D20			
005804			*	LOC	TKF
005805	0299		TKF	EQU X'0299'	
005806			*	LD	30
005807	OFFC	DED6			
005810			*	ST	
005811	OFFD	1EBD			
005812	OFFE	30C0			
005813			*	B	TKFN
005816	OFFF	20F3			
005817			*	LOC	CPRT
005818	02A0		CPRT	EQU X'02A0'	
005819			*	LD	23
005822	1000	D617			
005823			*	C	=1
005826	1001	8601			
005827	1002	1097			
005828			*	BET	RE1A
005829	1003	4196			
005830	1004	4127			
005833			*	C	'X'F0'
005834	1005	1986			
005837	1006	F010			
005838	1007	9741			
005839			*	BET	RE2A
005840	1008	9641			
005843	1009	2720			
005844			*	LOC	RE3A
005845	02B4		RE3A	EQU X'02B4'	
005846			*	BART	RE3
005847			*	WAIT	
005848	100A	01BD			

005849	100B	3100			
005850			*	LOC	RE3
005851		02B8	RE3 EQU X'02B8'		
005852			*	RECV	3
005853	100C	BD30			
005856			*	ST	
005857	100D	78BD			
005858	100E	30C0			
005859			*	BLCT	EXT
005860	100F	DE63			
005863	1010	2722			
005864			*	B	RE3A
005867	1011	20F0			
005868			*	LOC	RE1A
005869		02C4	RE1A EQU X'02C4'		
005870			*	BART	RE1
005871			*	WAIT	
005872	1012	01BD			
005873	1013	3100			
005874			*		
005875			*	LOC	RE1
005876		02C8	RE1 EQU X'02C8'		
005877			*	RECV	1
005878	1014	BD30			
005881			*	ST	
005882	1015	68BD			
005883	1016	30C0			
005884			*	BLCT	EXT
005885	1017	DE63			
005888	1018	2712			
005889			*	B	RE1A
005892	1019	20F0			
005893			*	LOC	RE2A
005894		02D4	RE2A EQU X'02D4'		
005895			*	BART	RE2
005896			*	WAIT	
005897	101A	01BD			
005898	101B	3100			
005899			*		
005900			*	LOC	RE2
005901		02D8	RE2 EQU X'02D8'		
005902			*	RECV	2
005903	101C	BD30			
005906			*	ST	
005907	101D	70BD			
005908	101E	30C0			
005909			*	BLCT	EXT
005910	101F	DE63			
005913	1020	2702			
005914			*	B	RE2A
005917	1021	20F0			
005918			*		
005919			*	LOC	EXT
005920		02E4	EXT EQU X'02E4'		
005921			*	LD	=0
005924	1022	C600			
005925			*	OUT	2
005926	1023	BD30			
005929			*	GNB	
005930	1024	28BD			
005931	1025	3120			
005932			*	WAIT	
005933	1026	BD31			
005934			*	NOP	
005935	1027	0001			
005936			*		
005937			*		
005938			*		
005939			*		
005940			*		
005941			*		
005942			*	ORG	X'400'
005943		1028	CHLP8A EQU		
005944			*	LD	2
005947	1028	D602			GET LINE CONFIGURATION
005948			*	OUT	6
005949	1029	BD30			OUTPUT IT
005952			*	LD	=X'FF'
005953	102A	38C6			
005956			*	OUT	4
005957	102B	FFBD			
005960	102C	3030			
005961			*	ST	28
005964	102D	D71C			
005965			*	WAIT	
005966	102E	BD31			
005967			*	LD	24
005968	102F	00D6			
005971			*	C	=X'32'
005972	1030	1886			
005975	1031	3210			
005976	1032	9741			
005977			*	BET	Z48
005978	1033	9641			
005981	1034	273C			
005982			*	LD	=X'1F'
005985	1035	C61F			
005986			*	ST	27
005989	1036	D71B			
005990			*	ST	28
005993	1037	D71C			
005994			*	LD	2
005997	1038	D602			
005998			*	BZT	Z47
005999	1039	5D27			
006002			*	C	=X'40'
006003	103A	2886			
006006	103B	4010			
006007	103C	9741			
006008			*	BEF	N611
006009	103D	9641			
006012	103E	2609			
006013			*	LD	=X'3F'

006016	103F	C63F			
006017			*	ST	28
006020	1040	D71C			
006021			*	SR	
006022			*	ST	27
006023	1041	54D7			
006026			*	B	Z47
006027	1042	1B20			
006030			*	LOC	N611
006031		0437	N611 EQU X'0437'		
006032			*	C	=X'80'
006033	1043	1986			
006036	1044	8010			
006037	1045	9741			
006038			*	BEF	N711
006039	1046	9641			
006042	1047	2609			
006043			*	LD	=X'7F'
006046	1048	C67F			
006047			*	ST	28
006050	1049	D71C			
006051			*	SR	
006052			*	ST	27
006053	104A	54D7			
006056			*	B	Z47
006057	104B	1B20			
006060			*	LOC	N711
006061		0449	N711 EQU X'0449'		
006062			*	LD	=X'FF'
006063	104C	07C6			
006066			*	ST	28
006067	104D	FFD7			
006070			*	SR	
006071	104E	1C54			
006072			*	ST	27
006075	104F	D71B			
006076			*		
006077			*	LOC	Z47
006078		0450	Z47 EQU X'0450'		
006079			*	LD	28
006082	1050	D61C			
006083			*	AND	=X'FF'
006086	1051	C4FF			
006087			*	ST	30
006090	1052	D71E			
006091			*	LOC	Z48
006092		0456	Z48 EQU X'0456'		
006093			*	LD	28
006096	1053	D61C			
006097			*	AND	24
006100	1054	D418			
006101			*	ST	28
006104	1055	D71C			
006105			*		
006106			*	LD	=6
006109	1056	C606			
006110			*	LOC	Z22
006111		045E	Z22 EQU X'045E'		
006112			*	ST	63
006115	1057	D73E			
006116			*	LD	24
006119	1058	D618			
006120			*	SEND	0
006121	1059	BD30			
006124			*	LD	63
006125	105A	40D6			
006128			*	DEC	
006129	105B	3F5A			
006130			*	BZT	Z32
006131	105C	5D27			
006134			*	BART	Z22
006135	105D	0101			
006136			*	LOC	Z32
006137		046C	Z32 EQU X'046C'		
006138			*	LD	=X'19'
006141	105E	C619			
006142			*	SEND	0
006143	105F	BD30			
006146			*		
006147			*	LD	24
006148	1060	40D6			
006151			*	C	=X'32'
006152	1061	1886			
006155	1062	3210			
006156	1063	9741			
006157			*	BET	CPRTX
006158	1064	9641			
006161	1065	2729			
006162			*	LD	2
006165	1066	D602			
006166			*	BZT	ZXZ1
006167	1067	5D27			
006170			*	LOC	XPYZ
006171		0481	XPYZ EQU X'0481'		
006172			*	LD	
006173	1068	12BD			
006174	1069	30A0			
006175			*	AND	27
006178	106A	D41B			
006179			*	SEND	2
006180	106B	BD30			
006183			*	BLCT	ZEXT
006184	106C	50DE			
006185	106D	6327			
006188			*	BART	XPYZ
006189	106E	5C01			
006190			*	WAIT	
006191	106F	BD31			
006192			*	B	XPYZ
006193	1070	0020			
006196			*		
006197			*	LOC	ZXZ1
006198		0493	ZXZ1 EQU X'0493'		

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006199          *      LD
006200 1071 EEBD
006201 1072 30A0
006202          *      AND   27
006205 1073 D41B
006206          *      SEND  0
006207 1074 BD30
006210          *      BLCT  ZEXT
006211 1075 40DE
006212 1076 6327
006215          *      BART  ZXZ1
006216 1077 4A01
006217          *      WAIT
006218 1078 BD31
006219          *      B      ZXZ1
006220 1079 0020
006223          *
006224          *      LOC   CPRTX
006225          *      CPRTX EQU X'04A5'
006226          *      LD    23
006227 107A EED6
006230          *      C      =1
006231 107B 1786
006234 107C 0110
006235 107D 9741
006236          *
006237 107E 9641
006240 107F 2719
006241          *      C      =X'F0'
006244 1080 86F0
006245 1081 1097
006246          *
006247 1082 4196
006248 1083 4127
006251          *
006252          *      LOC   BBPCR
006253          *      BBPCR EQU X'04B9'
006254          *      LD
006255 1084 20BD
006256 1085 30A0
006257          *      SEND  3
006258 1086 BD30
006261          *      BLCT  ZEXT
006262 1087 58DE
006263 1088 6327
006266          *      BART  BBPCR
006267 1089 2601
006268          *      WAIT
006269 108A BD31
006270          *      B      BBPCR
006271 108B 0020
006274          *
006275          *      LOC   BBPCR
006276          *      BBPCR EQU X'04C9'
006277          *      LD
006278 108C F0BD
006279 108D 30A0
006280          *      SEND  1
006281 108E BD30
006284          *      BLCT  ZEXT
006285 108F 48DE
006286 1090 6327
006289          *      BART  BBPCR
006290 1091 1601
006291          *      WAIT
006292 1092 BD31
006293          *      B      BBPCR
006294 1093 0020
006297          *
006298          *      LOC   BBPCR
006299          *      BBPCR EQU X'04D9'
006300          *      LD
006301 1094 F0BD
006302 1095 30A0
006303          *      SEND  2
006304 1096 BD30
006307          *      BLCT  ZEXT
006308 1097 50DE
006309 1098 6327
006312          *      BART  BBPCR
006313 1099 0601
006314          *      WAIT
006315 109A BD31
006316          *      B      BBPCR
006317 109B 0020
006320          *
006321          *      LOC   ZEXT
006322          *      ZEXT EQU X'04E9'
006323          *      LD    =0
006324 109C F0C6
006327          *      SEND  0
006328 109D 00BD
006331 109E 3040
006332          *      SEND  0
006333 109F BD30
006336          *      LD    20
006337 10A0 40D6
006340          *      AND   =X'FE'
006341 10A1 14C4
006344          *      OUT   2
006345 10A2 FEBD
006348 10A3 3028
006349          *      GNB
006350 10A4 BD31
006351          *      WAIT
006352 10A5 20BD
006353 10A6 3100
006354          *      NOP
006355          *
006356          *      CHANNEL PROGRAM FOR MARK & SPACE (REC)
006357          *
006358          *
006359          *      ORG   X'200'

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006360	10A7	0100					
006361		10A8	CHLP9	EQU	5		
006362			*	LD	2		GET LINE CONFIG.
006365	10A8	D602					
006366			*	OUT	6		OUTPUT IT
006367	10A9	BD30					
006370			*	LD	=X'D'		LOAD LINE SPEED
006371	10AA	38C6					
006374			*	OUT	4		OUTPUT IT
006375	10AB	0DBD					
006378	10AC	3030					
006379			*	LD	20		GET LINE CONTROL
006382	10AD	D614					
006383			*	OUT	2		
006384	10AE	BD30					
006387			*	WAIT			
006388	10AF	28BD					
006389	10B0	3100					
006390			*	RECV	0		
006391	10B1	BD30					
006394			*	ST			
006395	10B2	60BD					
006396	10B3	30C0					
006397			*	WAIT			
006398	10B4	BD31					
006399			*	LOC	RAN		
006400		021B	RAN EQU X'021B'				
006401			*	RECV	0		
006402	10B5	00BD					
006405	10B6	3060					
006406			*	ST			
006407	10B7	BD30					
006408			*	BZF	BINTA		
006409	10B8	C05D					
006412	10B9	260C					
006413			*	IN	5		
006414	10BA	BD30					
006417			*	AND	=1		
006418	10BB	14C4					
006421			*	BZT	BINTA		
006422	10BC	015D					
006425	10BD	2704					
006426			*	LD	=X'FE'		
006429	10BE	C6FE					
006430			*	ST	60		
006433	10BF	D73C					
006434			*	LOC	BINTA		
006435		0230	BINTA EQU X'0230'				
006436			*	BLCT	TONI		
006437	10C0	DE63					
006440	10C1	2705					
006441			*	WAIT			
006442	10C2	BD31					
006443			*	B	RAN		
006444	10C3	0020					
006447			*	LOC	TONI		
006448		0239	TONI EQU X'0239'				
006449			*	LD	=0		
006450	10C4	E2C6					
006453			*	OUT	2		
006454	10C5	00BD					
006457	10C6	3028					
006458			*	GNB			
006459	10C7	BD31					
006460			*	WAIT			
006461	10C8	20BD					
006462	10C9	3100					
006463			*	NOP			
006464			*				
006465			*				
006466			*				
006467			*				
006468			*				
006469			*	ORG	X'400'		
006470	10CA	0100					
006471		10CB	CHLP9A EQU	5			
006472			*	LOC	CLKK		
006473		0400	CLRK EQU X'0400'				
006474			*	LD	GET		CHARACTER TO SEND OUT
006475	10CB	BD30					
006476			*	C	=5		CHECK TO SET MARK AND SPACE CONDITION
006477	10CC	A086					
006480	10CD	0510					
006481	10CE	9741					
006482			*	BEF	DENW		
006483	10CF	9641					
006486	10D0	260B					
006487			*	LD	20		SET MARK OR SPACE CONDITION
006490	10D1	D614					
006491			*	OR	26		
006494	10D2	DA1A					
006495			*	ST	20		
006498	10D3	D714					
006499			*	OUT	2		
006500	10D4	BD30					
006503			*	LD	=5		
006504	10D5	28C6					
006507			*	LOC	DENW		
006508		0417	DENW EQU X'0417'				
006509			*	C	=7		CHECK TO RESET MARK AND SPACE CONDITION
006510	10D6	0586					
006513	10D7	0710					
006514	10D8	9741					
006515			*	BEF	BAMB		
006516	10D9	9641					
006519	10DA	2622					
006520			*	LD	20		RESET MARK OR SPACE CONDITION
006523	10DB	D614					
006524			*	XOR	26		
006527	10DC	D81A					
006528			*	ST	20		
006531	10DD	D714					
006532			*	OUT	2		

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006533 10DE BD30
006536 * LD 26
006537 10DF 28D6
006540 * C =X'10' IF SPACE CONDITION CHECK FOR FRAMING ERROR
006541 10E0 1A86
006544 10E1 1010
006545 10E2 9741
006546 * BEF KUNTA
006547 10E3 9641
006550 10E4 260C
006551 * IN 5
006552 10E5 BD30
006555 * AND =1 MASK OTHER BITS OUT
006556 10E6 14C4
006559 * BZT KUNTA BRANCH ON FRAMING ERROR SET
006560 10E7 015D
006563 10E8 2704
006564 * LD =X'EE'
006567 10E9 C6EE
006568 * ST 61
006571 10EA D73D
006572 * LOC KUNTA
006573 0440 KUNTA EQU X'0440'
006574 * LD =7
006577 10EB C607
006578 * LOC BAMB
006579 0442 BAMB EQU X'0442'
006580 * SEND 0
006581 10EC BD30
006584 * BLCT RAY
006585 10ED 40DE
006586 10EE 6327
006589 * WAIT
006590 10EF 05BD
006591 10F0 3100
006592 * B CLRK
006595 10F1 20B2
006596 * LOC RAY
006597 044E RAY EQU X'044E'
006598 * WAIT
006599 10F2 BD31
006600 * GNB
006601 10F3 00BD
006602 10F4 3120
006603 * LOC MARK
006604 0454 MARK EQU X'0454'
006605 * LD =X'FF'
006608 10F5 C6FF
006609 * SEND 0
006610 10F6 BD30
006613 * WAIT
006614 10F7 40BD
006615 10F8 3100
006616 * B MARK
006619 10F9 20F6
006620 * NOP
006621 10FA CHLP10 EQU $
006622 *
006623 *
006624 *
006625 *
=====
* I/O FUNCTION CODES
*
006626 10FA 0001 FOMC DC X'1' OUTPUT DLCP CONTROL
006627 10FB 0003 FOIC DC X'3' OUTPUT INTERRUPT CONTROL
006628 10FC 0005 FOCC DC X'5' OUTPUT CHANNEL CONTROL
006629 10FD 0009 FOLD DC X'9' OUTPUT ADDRESS AND RANGE
006630 10FE 000B FOBL DC X'B' OUTPUT BYTE INTO LCT
006631 10FF 000F FOCB DC X'F' OUTPUT CCB CONTROL
006632 1100 000C FIR DC X'C' INPUT RANGE
006633 1101 0018 FIS DC X'18' INPUT STATUS
006634 1102 001A FINS DC X'1A' INPUT NEXT STATUS
006635 1103 001C FIDSS DC X'1C' INPUT DATA SET STATUS
006636 1104 001E FILS DC X'1E' INPUT LCT ATATUS
006637 1105 0026 FID DC X'26' INPUT ID
006638 *
006639 *
006640 *
006641 *
=====
* STORAGE CONSTANTS
*
006642 1106 0000 ARFG DC 0 ADAPTER READY TEST FLAG
006643 1107 0000 BYTP DC 0 BYTE OFFSET FOR V$CALLS
006644 1108 0000 CHANL DC 0
006645 1109 0000 CHSZFG DC 0 CHARACTER SIZE TEST FLAG
006646 110A 0000 CHNZ DC 0
006647 110B 0000 COUNT DC 0
006648 110C 0000 CS DC 0
006649 110D 0000 CSB DC 0 CHARACTER SIZE
006650 110E 0000 CRCAC DC 0 CHARACTER SIZE INCLUDING START AND STOP BITS
006651 110F 0000 CRCFCG DC 0 CRC ACCUMULATOR
006652 1110 0000 DIV1 DC 0
006653 1111 0000 DIV2 DC 0 DIVISIONS FOR TICKS TO MILLSEC SUB-ROUTINE
006654 1112 0000 DIV3 DC 0
006655 1113 0000 DIV4 DC 0
006656 1114 0034 ERF DC 52
006657 1115 0000 FBKG DC 0
006658 1116 0000 FWFG DC 0 FIRMWARE PRINT FLAG
006659 1117 0000 HGSPD DC 0 HIGH SPEED FLAG
006660 1118 0000 HISPD DC 0
006661 1119 003C HRTZ DC 60 LINE FREQUENCY
006662 111A 0000 MASK DC 0
006663 111B 0000 MSBS RESV 2,0 BITS/SEC
006664 111D 0000 PARFG DC 0 PARITY ERROR TEST FLAG
006665 111E 0000 PTTY DC 0 PARITY TYPE FLAG
006666 111F 0000 PSFG DC 0 SYNC. PRINT SPEED FLAG
006667 1120 0000 RANGE DC 0
006668 1121 0000 RCRC DC 0
006669 1122 0000 RLCTAD DC 0 RECEIVE CRC RESIDUE
006670 1123 0000 RLCTRG DC 0 RAM ADDRESS TO READ
006671 1124 0000 RUNFG DC 0 RANGE FOR REAU FROM RAM
006672 1125 0000 TL0C DC 0 OVERRUN AND UNDERRUN TEST FLAG
006673 1126 0000 TP1 DC 0 TEMPORARY LOCATION
006674 1127 0000 TP2 DC 0
006675 1128 0000 TRMRK DC 0
006676 1129 0000 SBTFG DC 0 STOP BIT TIME TEST FLAG
006677 112A 0000 SHORT DC 0 TEST FLAG

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006678 112B 0000 STPFG DC 0 STOP I/O AND CHANNEL RESET FLAG
006679 112C 0000 WDBFG DC 0 BROADBAND TEST FLAG
006680 112D 0000 XCRC DC 0 TRANSMIT CRC RESIDUE
006681 112E FFFF LCTV RESV 3*-1
006682 1131 0000 ERAR RESV 4*0 ERROR ARRAY FOR ZV5C
006683 *
006684 1135 SAV1 RESV 9+7*SAF
006685 1145 SAV2 RESV 9+7*SAF
006686 1155 SAV3 RESV 9+7*SAF
006687 1165 SAV4 RESV 9+7*SAF
006688 *
006689 *
006690 *
006691 *
006692 *
006693 1175 3108 ASYN DC X*3108' ASYNC. ID
006694 1176 3118 ASYN1 DC X*3118' ASYNC. ID NEW BAUD RATE
006695 1177 3110 ASYNC DC X*3110' CURRENT LOOP ASYNC.
006696 1178 3158 SYNC DC X*3158' SYNC. ID
006697 1179 3150 BISYNC DC X*3150' BISYNC. ID
006698 117A 3160 SYN188 DC X*3160' SYNC. MIL 188 ID
006699 117B 3138 WDB3 DC X*3138' WIDEBAND WITH 301/303
006700 117C 3130 WDB3B DC X*3130' BI-SYNC. WIDEBAND WITH 301/303
006701 117D 3168 WDBV35 DC X*3168' WIDEBAND WITH V35
006702 117E 3128 WDBV5B DC X*3128' BI-SYNC. WIDEBAND WITH V35
006703 *
006704 117F 8000 BIT0 DC Z*8000'
006705 1180 4000 BIT1 DC Z*4000'
006706 1181 2000 BIT2 DC X*2000'
006707 1182 0800 BIT4 DC X*800'
006708 1183 0200 BIT6 DC X*200'
006709 1184 0100 BIT7 DC X*100'
006710 *
006711 1185 A001 CRC16 DC Z*A001'
006712 *
006713 1186 0000 ATLT RESV 8*0 ACTIVE LINE TABLE
006714 *
006715 118E 0000 ADDR5 RESV 9*0 LINE ADDRESS TABLE
006716 *
006717 1197 0117 CPFLG DC X*117' LCT FLAG FOR CRC ONLY
006718 1198 F017 DC Z*F017' PARITY ONLY
006719 1199 0017 DC X*017' CRC AND PARITY
006720 119A 0000 DC 0
006721 *
006722 119B 805C CRLF DC Z*805C'
006723 119C 2C00 COMMA DC X*2C00'
006724 119D 005C BKLS DC X*5C'
006725 *
006726 119E 0001 0203 0405 DOUT TEXT Z*000102030405060708090A0B0C0D0E0F'
11A1 0607 0809 0A0B
0C0D 0E0F
006727 11A6 1011 1213 1415 TEXT Z*101112131415161718191A1B1C1D1E1F'
11A9 1617 1819 1A1B
1C1D 1E1F
006728 11A1 2021 2223 2425 DOUT1 TEXT Z*202122232425262728292A2B2C2D2E2F'
11B1 2627 2829 2A2B
2C2D 2E2F
006729 11B6 3031 3233 3435 TEXT Z*303132333435363738393A3B3C3D3E3F'
11B9 3637 3839 3A3B
3C3D 3E3F
006730 11BE 4041 4243 4445 TEXT Z*404142434445464748494A4B4C4D4E4F'
11C1 4647 4849 4A4B
4C4D 4E4F
006731 11C6 5051 5253 5455 TEXT Z*505152535455565758595A5B5C5D5E5F'
11C9 5657 5859 5A5B
5C5D 5E5F
006732 11CE 6061 6263 6465 TEXT Z*606162636465666768696A6B6C6D6E6F'
11D1 6667 6869 6A6B
6C6D 6E6F
006733 11D6 7071 7273 7475 TEXT Z*707172737475767778797A7B7C7D'
11D9 7677 7879 7A7B
7C7D
006734 11DD 5555 DFLT DC X*5555' DEFAULT CHARACTER
006735 *
006736 11DE 0000 FIVSEC DC 0 5 SECONDS OF TICK ON RTC
006737 11DF 0000 SECL DC 0
006738 11E0 0000 SECH DC 0
006739 *
006740 11E1 0000 FRIMRV DC 0 FIRMWARE REV NUMBER
006741 *
006742 * LINE SPEED IS IN SECOND WORD FOR LOW AND MEDIUM
006743 * SPEED LINES. FOR HIGH SPEED LINES THE TWO
006744 * WORDS ARE PUT TOGETHER FORMING A SINGLE HEX WORD
006745 * CONSISTING OF 32 BITS.
006746 *
006747 11E2 0000 SYLSP0 RESV 2*0 SYNC. LINE 0 SPEED
006748 11E4 0000 SYLSP1 RESV 2*0 SYNC. LINE 1 SPEED
006749 11E6 0000 SYLSP2 RESV 2*0 SYNC. LINE 2 SPEED
006750 11E8 0000 SYLSP3 RESV 2*0 SYNC. LINE 3 SPEED
006751 11EA 0000 SYLSP4 RESV 2*0 SYNC. LINE 4 SPEED
006752 11EC 0000 SYLSP5 RESV 2*0 SYNC. LINE 5 SPEED
006753 11EE 0000 SYLSP6 RESV 2*0 SYNC. LINE 6 SPEED
006754 11F0 0000 SYLSP7 RESV 2*0 SYNC. LINE 7 SPEED
006755 *
006756 11F2 000A NSPTBL DC 10
006757 11F3 000F DC 15
006758 11F4 0016 DC 22
006759 11F5 001B DC 27
006760 11F6 001E DC 30
006761 11F7 0028 DC 40
006762 11F8 003C DC 60
006763 11F9 0078 DC 120
006764 11FA 00D2 DC 210
006765 11FB 00F0 DC 240
006766 11FC 0168 DC 360
006767 11FD 0190 DC 400
006768 11FE 01E0 DC 480
006769 11FF 03C0 DC 960
006770 1200 0780 DC 1920
006771 1201 0F00 DC 3840
006772 *
006773 1202 000A OSPTBL DC 10
006774 1203 000F DC 15

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006775	1204	0016	DC	22	
006776	1205	001B	DC	27	
006777	1206	001E	DC	30	
006778	1207	003C	DC	60	
006779	1208	0078	DC	120	
006780	1209	00B4	DC	180	
006781	120A	00F0	DC	240	
006782	120B	0168	DC	360	
006783	120C	01E0	DC	480	
006784	120D	02D0	DC	720	
006785	120E	03C0	DC	960	
006786	120F	05A0	DC	1440	
006787	1210	0780	DC	1920	
006788	1211	0000	DC	0	
006789					
006790	1212	0032	* ALSPLB DC	50	OLD BAUD RATE LINE SPEED TABLE
006791	1213	004B	DC	75	
006792	1214	006E	DC	110	
006793	1215	0087	DC	135	
006794	1216	0096	DC	150	
006795	1217	012C	DC	300	
006796	1218	0258	DC	600	
006797	1219	0384	DC	900	
006798	121A	04B0	DC	1200	
006799	121B	0708	DC	1800	
006800	121C	0960	DC	2400	
006801	121D	0E10	DC	3600	
006802	121E	12C0	DC	4800	
006803	121F	1C20	DC	7200	
006804	1220	2580	DC	9600	
006805					
006806	1221	0032	* CMSPBL DC	50	NEW BAUD RATE LINE SPEED TABLE
006807	1222	004B	DC	75	
006808	1223	006E	DC	110	
006809	1224	0087	DC	135	
006810	1225	0096	DC	150	
006811	1226	00C8	DC	200	
006812	1227	012C	DC	300	
006813	1228	0258	DC	600	
006814	1229	041A	DC	1050	
006815	122A	04B0	DC	1200	
006816	122B	0708	DC	1800	
006817	122C	07D0	DC	2000	
006818	122D	0960	DC	2400	
006819	122E	12C0	DC	4800	
006820	122F	2580	DC	9600	
006821	1230	4800	DC	19200	
006822					
006823	1231	0021	* SBITBL DC	33	
006824	1232	001E	DC	30	
006825	1233	0020	DC	32	
006826	1234	001E	DC	30	
006827					
006828	1235	0018	* TLTBLA DC	X'18'	SHOULD BE TABLE FOR D55 TO USC LOOP ASYC.
006829	1236	6078	DC	X'6078'	
006830	1237	8098	DC	Z'8098'	
006831	1238	E0F8	DC	Z'E0F8'	
006832					
006833	1239	80B0	* TLTBLC DC	Z'80B0'	SHOULD BE TABLE FOR CURRENT LOOP ADAPT.
006834	123A	F0F0	DC	Z'F0F0'	
006835	123B	80B0	DC	Z'80B0'	
006836	123C	F0F0	DC	Z'F0F0'	
006837					
006838	123D	0111	* TLTBL5 DC	X'111'	SHOULD BE TABLE FOR D55 TO D5C LOOP SYNC.
006839	123E	6171	DC	X'6171'	
006840	123F	8191	DC	Z'8191'	
006841	1240	E1F1	DC	Z'E1F1'	
006842					
006843	1241	00E0	* TLWB3 DC	X'E0'	SHOULD BE TABLE FOR BROADBAND 301
006844	1242	10F0	DC	X'010F0'	
006845					
006846	1243	0060	* TLUV35 DC	X'60'	SHOULD BE TABLE FOR BROADBAND V35
006847	1244	90F0	DC	Z'90F0'	
006848					
006849	1245	2020	* SPACE DC	Z'2020'	
006850					
006851	1246	0020	* TLUTB2 DC	X'20'	
006852	1247	4060	DC	X'4060'	
006853	1248	0020	DC	X'20'	
006854	1249	4060	DC	X'4060'	
006855	124A	0010	DC	X'10'	
006856	124B	4050	DC	X'4050'	
006857	124C	0010	DC	X'10'	
006858	124D	4050	DC	X'4050'	
006859	124E	0000	DC	0	
006860	124F	0040	DC	X'40'	
006861	1250	0040	DC	X'40'	
006862					
006863	1251	0001	* XMSB DC	1	XMIT MARK SHOULD BE
006864	1252	0203	DC	X'0203'	
006865	1253	FF07	DC	Z'FF07'	
006866	1254	0809	DC	X'0809'	
006867					
006868	1255	0001	* XSSB DC	1	XMIT SPACE SHOULD BE
006869	1256	0203	DC	X'0203'	
006870	1257	0007	DC	X'0007'	
006871	1258	0809	DC	X'0809'	
006872					
006873	1259	0002	* C2 DC	2	
006874	125A	0004	C4 DC	4	
006875	125B	0008	C8 DC	8	
006876	125C	0010	C16 DC	16	
006877	125D	0200	HX200 DC	X'200'	
006878	125E	0400	HX400 DC	X'400'	
006879					
006880					
006881					
006882					
006883					
006884	125F	1267	TSA1 DC	<TSA2	
006885	1260	0000	RESV	4+3*SAF,0	
006886	1267	126F	TSA2 DC	<TSA3	
006887	1268	0000	RESV	4+3*SAF,0	

TRAP SAVE AREAS

```

006888 126F 0000      TSA3  RESV  8*0
006889
006890
006891
006892
006893
006894 1277 0000      ISA1  RESV  3*SAF*0      INTERRUPT SAVE AREA FOR LEVEL 1 (RTC)
006895 127b 03D6      ISA1P DC    <N05PD
006896 127C 6000      DC    X'6000'
006897
006898 127D 0000      ISA4  RESV  3*SAF*0
006899 1281 02CD      ISA4P DC    <SP5YT1
006900 1282 6000      DC    X'6000'
006901
006902
006903
006904
006905
006906 1283 0206      LCT   DC    X'206'      STARTING ADDRESS OF CHANNEL PROG.
006907 1284 0414      DC    X'414'      RECV. LINE CONTROL SET TEST MODE
006908 1285 0000      DC    0          TABLE END
006909
006910 1286 0206      LCT1  DC    X'206'
006911 1287 0426      DC    X'426'
006912 1288 C802      DC    Z'C802'
006913 1289 C822      DC    Z'C822'
006914 128A 0E18      LCT15 DC    X'E18'
006915 128B C714      LC1LCR DC    Z'C714'      LINE CONTROL
006916 128C 0000      LC1SYF DC    0          SYNC. FLAG
006917 128D 0000      DC    0          END OF TABLE
006918
006919 128E 0206      LCT2  DC    X'206'
006920 128F 0426      DC    X'426'
006921 1290 C802      LC2CFR DC    Z'C802'
006922 1291 C822      LC2CFX DC    Z'C822'
006923 1292 0E18      LCT2S  DC    X'E18'
006924 1293 C714      DC    Z'C714'
006925 1294 0117      LCT2FG DC    X'117'      FLAG FOR TYPE OF SEND AND RECV
006926 1295 0000      DC    0
006927
006928 1296 0206      LCT3  DC    X'206'      RAM STARTING ADDRESS (RECV)
006929 1297 0426      DC    X'426'      RAM STARTING ADDRESS (XMIT)
006930 1298 C002      LC3CFR DC    Z'C002'      CONFIGURATION (RECV)
006931 1299 C022      LC3CFX DC    Z'C022'      CONFIGURATION (XMIT)
006932 129A 0000      LCT3S  DC    0          SPEED IN SCRATCH LOCATION
006933 129B C714      DC    Z'C714'      LINE CONTROL (RECV)
006934 129C 0000      LC3SYF DC    0          SYNC FLAG
006935 129D 0000      RESV  1*0
006936
006937 129E 0206      LCT4  DC    X'206'
006938 129F 0426      DC    X'426'
006939 12A0 0002      LC4CFR DC    2
006940 12A1 0022      LC4CFX DC    X'22'
006941 12A2 0000      LCT4S  DC    0
006942 12A3 C714      DC    Z'C714'
006943 12A4 0000      RESV  1*0
006944
006945 12A5 0206      LCT5  DC    X'206'
006946 12A6 0426      DC    X'426'
006947 12A7 C802      DC    Z'C802'
006948 12A8 C822      DC    Z'C822'
006949 12A9 0E18      LCT5S  DC    X'E18'
006950 12AA C714      DC    Z'C714'
006951 12AB 0F10      DC    X'F10'      LCT STATUS BYTES
006952 12AC 0F11      DC    X'F11'      "
006953 12AD 0000      LC5SYF DC    0          SYNC. LINE FLAG
006954 12AE 0000      DC    0
006955
006956 12AF 0206      LCT6  DC    X'206'
006957 12B0 0226      DC    X'226'
006958 12B1 C027      DC    Z'C027'
006959 12B2 C802      DC    Z'C802'
006960 12B3 C822      DC    Z'C822'
006961 12B4 0E18      LCT6S  DC    X'E18'
006962 12B5 C714      DC    Z'C714'
006963 12B6 021D      LCT6TF DC    X'21D'
006964 12B7 0125      DC    X'125'      DISABLE PAUSE
006965 12B8 0000      DC    0
006966
006967 12B9 0206      LCT7  DC    X'206'
006968 12BA 0226      DC    X'226'
006969 12BB C802      DC    Z'C802'
006970 12BC C822      DC    Z'C822'
006971 12BD FC1D      LCT7SM DC    Z'FC1D'      MASK TO CHECK BITS (FC-ASY, F8-SYC.)
006972 12BE 0439      DC    X'439'      TLU POINTER TABLE 1
006973 12BF 0105      DC    X'105'
006974 12C0 0125      DC    X'125'
006975 12C1 0000      RESV  1*0
006976
006977 12C2 0206      LCT8  DC    X'206'
006978 12C3 0426      DC    X'426'
006979 12C4 C802      DC    Z'C802'
006980 12C5 C822      DC    Z'C822'
006981 12C6 C518      DC    Z'C518'
006982 12C7 C714      DC    Z'C714'
006983 12C8 0105      DC    X'105'      DISABLE PAUSE
006984 12C9 0125      DC    X'125'      DISABLE PAUSE
006985 12CA 0000      LCT8FG DC    0
006986 12CB 0000      RESV  1*0
006987
006988 12CC 0206      LCT9  DC    X'206'
006989 12CD 0426      DC    X'426'
006990 12CE C802      DC    Z'C802'
006991 12CF C822      DC    Z'C822'
006992 12D0 0000      LCT9MS DC    0
006993 12D1 C314      DC    Z'C314'
006994 12D2 0000      DC    0
006995
006996
006997
006998
006999
007000

```

007001	12D3	0006	WCP	DC	6	RANGE OF PROGRAM IN BYTES
007002			*			
007003	12D4	0BF8	WCP1	DC	<CHLP1	CHANNEL PROGRAM ADDRESS
007004	12D5	0C34		DC	<CHLP1A	XMIT CHANNEL PROGRAM ADDRESS
007005	12D6	0078	RAG1	DC	(CHLP1A-CHLP1)*2	RANGE OF CHANNEL PROGRAM
007006	12D7	005C	RAG2	DC	(CHLP3-CHLP1A)*2	RANGE OF PROGRAM IN BYTES
007007	12D8	0400		DC	X'400'	RAM ADDRESS
007008			*			
007009	12D9	0C62	WCP2	DC	<CHLP2	CHANNEL PROGRAM ADDRESS
007010	12DA	0CB3		DC	<CHLP2A	XMIT CHANNEL PROGRAM ADDRESS
007011	12DB	00A2		DC	(CHLP2A-CHLP2)*2	RANGE OF CHANNEL PROGRAM
007012	12DC	009E		DC	(CHLP3-CHLP2A)*2	RANGE OF CHANNEL PROGRAM
007013	12DD	0400		DC	X'400'	RAM STARTING ADDRESS
007014			*			
007015	12DE	0D02	WCP3	DC	<CHLP3	
007016	12DF	0D48		DC	<CHLP3A	
007017	12E0	008C		DC	(CHLP3A-CHLP3)*2	
007018	12E1	00B6		DC	(CHLP4-CHLP3A)*2	
007019	12E2	0400		DC	X'400'	
007020			*			
007021	12E3	0DA3	WCP4	DC	<CHLP4	
007022	12E4	0DDF		DC	<CHLP4A	
007023	12E5	0078		DC	(CHLP4A-CHLP4)*2	
007024	12E6	00D4		DC	(CHLP5-CHLP4A)*2	
007025	12E7	02C0		DC	X'2C0'	
007026			*			
007027	12E8	0076	WCP5	DC	(CHLP6-CHLP5)*2	
007028	12E9	0016	WCP5B	DC	22	
007029			*			
007030	12EA	0E84	WCP6	DC	<CHLP6	
007031	12EB	0EE9		DC	<CHLP6A	
007032	12EC	00CA		DC	(CHLP6A-CHLP6)*2	
007033	12ED	00D2		DC	(CHLP7-CHLP6A)*2	
007034	12EE	0400		DC	X'400'	
007035			*			
007036	12EF	0F52	WCP7	DC	<CHLP7	
007037	12F0	0F84		DC	<CHLP7A	
007038	12F1	0064		DC	(CHLP7A-CHLP7)*2	
007039	12F2	0058		DC	(CHLP8-CHLP7A)*2	
007040	12F3	0400		DC	X'400'	
007041	12F4	0000		RESV	2,0	
007042			*			
007043	12F6	0FB0	WCP8	DC	<CHLP8	
007044	12F7	1028		DC	<CHLP8A	
007045	12F8	00F0		DC	(CHLP8A-CHLP8)*2	
007046	12F9	0100		DC	(CHLP9-CHLP8A)*2	
007047	12FA	0400		DC	X'400'	
007048			*			
007049	12FB	10A8	WCP9	DC	<CHLP9	
007050	12FC	10CB		DC	<CHLP9A	
007051	12FD	0046		DC	(CHLP9A-CHLP9)*2	
007052	12FE	005E		DC	(CHLP10-CHLP9A)*2	
007053	12FF	0400		DC	X'400'	
007054			*			
007055			*			
007056			*			
007057			*			
007058			*			
007059	1300	434C 4120 5445	MSG	TEXT	'CLA TEST'	
		1303 5354				
007060				IFZ	(\$AF-2),PLAF	
007061	1304	2020 4443 4D43		TEXT	' DCMC2,REV-A '	
		1307 322C 5245 562D				
		4120				
007065	130B	2020 2041 5052	SLAF	TEXT	' APR 06 19785'	
		130E 2030 3620 3139				
		3738 2400				
007066	1313	4153 594E 4348	MSG2	TEXT	'ASYNCHRONOUS LINES'	
		1316 524F 4E4F 5553				
		204C 494E 4524				
007067	131C	2053 5045 4544	MSG4	TEXT	' SPEED IS \$'	
		131F 2049 5320 2024				
007068	1322	2053 484F 554C	MSG5	TEXT	' SHOULD BE \$'	
		1325 4420 4245 2024				
007069	1328	4E45 5854 2024	MSG6	TEXT	'NEXT \$'	
007070	132B	444C 4320 4348	MSG7	TEXT	'DLC CHANNEL(S) \$'	
		132E 414E 4E45 4C28				
		5329 2024				
007071	1333	2044 434D 4332	MSG8	TEXT	' DCMC2 PASS\$'	
		1336 2050 4153 5324				
007072	1339	504F 5745 5220	MSG9	TEXT	'POWER FREQ(HZ) \$'	
		133C 4652 4551 2848				
		5A29 2024				
007073	1341	434F 5059 5249		TEXT	'COPYRIGHT 1976 BY HONEYWELL INFORMATION SYSTEMS INC.'	
		1344 4748 5420 3139				
		3736 2042 5920				
		484F 4E45 5957				
		454C 4C20 2049				
		4E46 4F52 4D41				
		5449 4F4E 2053				
		5953 5445 4D53				
		2049 4E43 2E00				
007074	135C	4649 524D 5741	FREV	TEXT	'FIRMWARE REV.\$'	
		135F 5245 2052 4556				
		2E24				
007075	1363	2020 2020 2024	ERMG	TEXT	' \$'	
007076	1366	434C 4120 4E4F	ERMG1	TEXT	'CLA NOT FOUND ON THIS CHANNEL \$'	
		1369 5420 464F 554E				
		4420 4F4E 2054				
		4849 5320 4348				
		414E 4E45 4C20				
		2024				
007077	1376	494E 434F 5252	ERMG2	TEXT	'INCORRECT ID ON THIS CHANNEL \$'	
		1379 4543 5420 4944				
		204F 4E20 5448				
		4953 2043 4841				
		4E4E 454C 2020				
		2400				
007078		1386	V\$LB	EQU	\$	
007079	1C00			ORG	ZERU+X'1C00'	
007082		1C00	DIN	EQU	\$	
007083	1D00			ORG	DIN+X'100'	
007084		1D00	DIN1	EQU	\$	

007085 1386				ORG	VSLB					
007086			*							
007087 1386 0100				END	DCMC2,STRT					
0000 ERR COUNT	TITLE DCMC2, 'REV A'	CLA TEST (SAF)								
	SAF	597	606	607C	673	1091	1107	1619C	1621C	1635C
	1640C	1661	1758C	1791	1806C	1808C	1809C	1811C	1813C	1822
	1846C	1849	2086C	2415	2416C	2420	2421	2425	2431	2432
	2436	2438	2443C	2446	2452	2460	2461	6684	6685	6686
	6687	6885	6887	6894	6898	7060	7062	7080		
SB1	373	379	494	508	524	534	552	562	569	619
	626	646	651	656	670C	841	851	862	872	879
	931	1022	1032	1043	1053	1060	1117	1122	1126	1160
	1206	1216	1227	1237	1244	1294	1304	1317	1327	1334
	1479	1489	1503	1513	1520	1618	1619C	1620	1621C	1628
	1631	1634	1635C	1636	1639	1640C	1643	1646	1649	1658C
	1724	1733	1735	1735	1788C	2061C	2273	2276	2303	2304
	2431	2436	2437C	2465C						
SB2	331	336	372	375	440B	441B	442B	443B	444B	445B
	446B	447B	448B	457B	458B	459B	460B	461B	470B	471B
	472B	476B	577B	662B	900B	908	909	925	934	951
	978	1080B	1131B	1253B	1344B	1532B	1651B	1745B	2305	2308
	2384	2390	2420	2424C	2425	2427C	2451C	2455C	2461	2464C
	2479B									
SB3	486B	495B	525B	553B	570B	585B	621B	628B	647B	652B
	657B	674B	825B	842B	863B	880B	930	977	981	1005B
	1023B	1044B	1061B	1088B	1119B	1123B	1127B	1192B	1207B	1228B
	1245B	1283B	1295B	1318B	1335B	1466B	1480B	1504B	1521B	1616B
	1629B	1632B	1637B	1644B	1647B	1650B	1662B	1710B	1725B	1792B
	1799	1799	1801	1818	1839	1843	1843	1844	1949B	1993
	2003	2027	2430	2437C	2442	2443C	2465C			
SB4	332	333C	334	335C	406B	595	596C	597	598C	603
	604C	606	607C	634	635C	683B	684B	723B	732B	741B
	743B	770B	788B	808B	940B	984B	1089	1090C	1091	1092C
	1104	1105C	1107	1108C	1259B	1267B	1352B	1354B	1358B	1366B
	1367B	1370B	1373B	1386B	1394B	1547B	1558B	1663B	1682B	1688B
	1798B	1800B	1805	1806C	1807	1808C	1825B	1833B	1861B	1872B
	1886B	1893B	2008B	2064B	2074	2082B	2085	2086C	2088B	2098B
	2105B	2216B	2225B	2231B	2258B	2263B	2283B	2345B	2369B	2388B
	2394B	2419	2426	2451C	2463	2464C				
SB5	390	393	395C	424B	672B	691B	696B	705B	713B	727B
	798	799C	814	815C	950	996B	1179	1180C	1380B	1640B
	1673B	1677B	1686B	1753	1757	1758C	1790B	1801	1805	1807
	1809C	1810	1810	1811C	1812	1812	1813C	1818	1819C	1821B
	1844	1845C	1846C	1848B	1853B	1867B	1876B	1902B	2093B	2103B
	2132B	2173B	2185B	2187B	2229B	2273	2280B	2303	2312	2312
	2314B	2333B	2384	2385	2400B	2423	2424C			
SB6	407B	620	627	632	694	832	833	887	896	897
	953B	959B	967B	975	1118	1401B	1407B	1415B	1540B	1566B
	1717	1733	1770B	1774B	1958B	1987	2140	2202	2206	2208
	2348	2350	2354B	2415	2416C	2418	2427C	2454	2455C	
SB7	726B	740B	746B	769B	948B	990B	994B	1139B	1142B	1178B
	1262B	1265B	1270B	1273B	1357B	1360B	1376B	1390B	1398B	1413B
	1421B	1442B	1452B	1550B	1553B	1561B	1565B	1574B	1580B	1685B
	1691B	1698B	1765B	1781B	1829B	1837B	1866B	1875B	1890B	1896B
	1913B	1928B	2101B	2228B	2261B	2398B	2415	2459B		
SR1	295	296	336	337B	338	339	342	343	348	352
	353C	358	359C	371C	375	376	383C	384	385	389
	392	394	395C	676C	708C	730C	752B	809C	810B	812B
	1422	1423C	1424	1425	1426	1427C	1428C	1432	1436	1440
	1443C	1444	1449	1450	1453C	1454	1988C	1996C	1998	2000B
	2022	2023C	2113C	2116	2121	2126	2140	2141	2143	2144
	2151	2162	2168	2211	2212C	2274C	2276	2277C	2323	2324C
	2325	2327	2413	2456B						
SR2	330C	336	357C	358	374	379	381B	388	393	395C
	396B	677C	694	774C	777	1149C	1152	1158C	1284	1723
	1735	1990C	1998	1999	2001	2006C	2025C	2027	2061C	2140
	2208	2240	2276	2278B	2284	2304	2308	2385	2395	
SR3	348	349	350	351C	405	669C	695C	699C	721C	728C
	780	1266C	1351C	1361B	1362C	1392C	1539C	1554C	1659C	1700
	1789C	1831C	1852C	1858C	1869C	1884C	1892C	1932C	1955	1989C
	1993	2003	2073	2157	2180	2181C	2208	2209C	2302	2306
	2308	2310C	2311	2366	2367	2368	2409	2410	2411	2412C
	2414C	2430	2433	2441C	2442	2450C	2452			
SR4	344	351C	375	378B	379	391	393	394	412	413C
	414B	425	426	427	429	431	433	438	439C	454
	455C	456C	464	465C	467	468C	482	483C	484	485C
	506	507C	508	509	515	516C	522	523C	534	535
	539	540	542	543C	544	545C	546	547C	562	563
	583	584C	586	587	588	589C	590	591C	592	593
	594C	600	601C	611	612C	618	625	630	631C	638
	639C	640	641C	642	678C	680	681	685	686	687C
	694	700B	701C	702	704	709	710C	722	724	731
	738	742	744	755	756	763	765	772	773C	775
	776	789	790C	813	823	824C	828	829C	830	831C
	833	834C	851	852	872	873	887	888B	889	890
	892	893C	894	895C	897	909	910	911	912C	916
	918	920	922	923C	925	926	934	935	936	937
	938C	963	964	965C	971	972	973C	978	979	1003
	1004C	1007	1008C	1009	1010C	1011	1012C	1016	1017C	1032
	1033	1053	1054	1070	1071	1073C	1074	1075C	1086	1087C
	1094	1095	1096	1097C	1098	1099C	1101	1102C	1112	1113C
	1114	1115C	1116	1121	1125	1137	1140	1154	1155C	1156
	1157C	1159	1163	1166	1167C	1168	1169C	1190	1191C	1194
	1195C	1196	1197C	1198	1199C	1216	1217	1237	1238	1281
	1282C	1287	1288C	1304	1305	1309	1310C	1311	1312C	1327
	1328	1372	1374	1385	1387	1393	1395	1433	1434	1435
	1436	1437	1438C	1439	1440	1448	1450	1464	1465C	1470
	1471C	1473	1474C	1489	1490	1513	1514	1546	1548	1557
	1559	1614	1615C	1622	1623C	1624	1625C	1641	1642C	1667
	1668	1669C	1676	1681	1683	1708	1709C	1711	1712C	1714
	1718C	1738	1739	1743	1744C	1754	1755	1767	1768	1795
	1796	1802	1803	1804C	1824	1826	1832	1834	1839	1840C
	1841	1860	1864	1871	1873	1987	2001	2027	2039	2040C
	2045C	2046C	2048	2050C	2051C	2053	2054	2056C	2057	2059
	2060	2081	2083	2087	2089	2090C	2097	2099	2113C	2114
	2115C	2116	2117	2118	2119	2120C	2121	2122	2123	2124
	2125C	2126	2127	2128	2129	2147	2160	2167	2203	2204
	2224	2226	2241	2248	2257	2259	2282	2284	2344	2346
	2347C	2348	2349C	2350	2351C	2367	2386	2387	2390	2393
	2395									
SR5	343	344	345	346	363C	701C	709	710C	744	747
	749	750B	757C	811	945	954	955C	956	957C	987















272	ZERO	278	285	7079	7081							
6322	ZEXT	6186	6187	6189	6213	6214	6216	6264	6265	6267	6287	
		6288	6290	6310	6311	6313						
	ZHCOMM	277										
	ZHIAFB	274										
	ZHISAZ	274	598C	607C	1092C	1108C						
	ZHNTSA	273	333C									
	ZHPFR	277										
	ZHRTCC	275	734C	736	756	2324C	2327					
	ZHRTCI	276										
	ZHRTCL	276	601C	1102C								
	ZHTH15	273	335C									
	ZHWDTC	275										
	ZV3C	942B	1692B	1759B	1907B	1922B						
	ZV3CO	949B	1699B	1766B	1914B	1929B						
	ZV3ER	2417B										
	ZV3F	326B	1794B	1921B								
	ZV3HR	273										
	ZV3IA	422B										
	ZV3ID	299B										
	ZV3IH	329B										
	ZV3IZ	286B										
	ZV3MLR	2352B										
	ZV3MLW	688B	689B	1670B	1671B	1815B	1816B	2092B				
	ZV3PCH	451B										
	ZV3QC	298B	328B	421B								
	ZV3RD	286B	289B	660B	1129B							
	ZV3T	2184B	2198B	2207B	2449B							
	ZV3TC	356B	382B	415B	2182B	2478B						
	ZV3TD	416B	2183B	2197B	2199B	2210B						
	ZV3TH	360B	2444B									
	ZV3THZ	2445B										
	ZV3TTY	275	287C	2476								
6198	ZX21	6168	6169	6173	6221	6222	6227					

678 LABELS  
3109 REFERENCES  
7087 RECORDS  
1 U FLAGS  
0 M FLAGS  
43 N FLAGS  
6 CROSS REF VERSION L - 24 SEPT, 1976  
RS LINKER VERSION 5.00 04/12/78 2052.6 EST WED  
LINK MAP FOR DCMC2  
START 0100  
LOW 0000  
HIGH 1CFF  
CURRENT 1C9A  
\*LOC DEFS  
ZHCOMM 0000  
\*DCMC2 0000 REV A  
ZHPFR 0000  
ZHTSA 0002  
ZHNTSA 0010  
ZHRTCI 0014  
ZHRTCC 0015  
ZHRTCL 0016  
ZHWDTC 0017  
ZHMERC 001F  
ZHIAFB 0020  
ZHTM29 0063  
ZHTM28 0064  
ZHTM27 0065  
ZHTM26 0066  
ZHTM25 0067  
ZHTM24 0068  
ZHTM23 0069  
ZHTM22 006A  
ZHTM21 006B  
ZHTM20 006C  
ZHTH19 006D  
ZHTH18 006E  
ZHTH17 006F  
ZHMEMP 006F  
ZHTH16 0070  
ZHLERR 0070  
ZHTH15 0071  
ZHNRES 0071  
ZHTH14 0072  
ZHPMEM 0072  
ZHTH13 0073  
ZHP-OP 0073  
ZHTH12 0074  
ZHTH11 0075  
ZHTH10 0076  
ZHTH9 0077  
ZHTH8 0078  
ZHTH7 0079  
ZHTH6 007A  
ZHOVFL 007A  
ZHTH5 007B  
ZHOP-N 007B  
ZHTH4 007C  
ZHTH3 007D  
ZHSC-N 007D  
ZHTH2 007E  
ZHTRC 007E  
ZHTH1 007F  
ZHMCL 007F  
ZHISAZ 0080  
ZHIVBS 0080  
ZHTVBS 0080  
\*ZV3F 1386  
ZV3F 1386  
\*ZV3IH 1394  
ZV3ID 1399  
ZV3IH 1394  
ZV3IAD 139E  
ZV3--2 1366  
ZV3--3 13C8  
\*ZV3TH 142D  
ZV3TH 142D  
ZV3TD 1462  
ZV3THZ 1455

*ZV\$IA	147D	REV. 7
ZV\$IA	1480	
ZV\$ARG	152F	
ZV\$ABF	1531	
ZV\$--1	14EC	
ZV\$IAV	147E	
*ZV\$PCH	153C	
ZV\$PCH	153C	
*ZV\$MLW	163E	REV. 0
ZV\$MLW	163E	
ZV\$MLR	166D	
*ZV\$C	1685	REV. 5
ZV\$C	1685	
ZV\$CO	16A8	
*ZV\$T	16B9	REV. 5.0
ZV\$QC	16D6	
ZV\$TC	16C2	
ZV\$T	16B9	
ZV\$Q	16CB	
*ZV\$ER	16EA	REV. 5.0
ZV\$ER	16EA	
ZV\$TA	1716	
ZV\$--0	16FD	
*ZV\$GP	175A	
ZV\$GP	175A	
ZV\$--4	177A	
*ZV\$HA	1786	
ZV\$HA	1786	
ZV\$HZ	1790	
ZV\$HS	178B	
*ZV\$HD	17BF	
ZV\$HD	17BF	
*ZV\$BRK	17F1	
ZV\$BRK	17F1	
*ZV\$RD	180B	REV. 7
ZV\$IZ	1845	
ZV\$TTY	181E	
ZV\$RD	180B	
ZV\$SV1	19E0	
ZV\$SV3	1A00	
ZV\$AF	181C	
ZV\$SV2	19F0	
ZV\$TID	181D	
ZV\$CF2	1827	
ZV\$TK	1823	
ZV\$RAR	1824	
ZV\$ST1	1828	
ZV\$RCC	1829	
ZV\$BUD	181F	
ZV\$OLB	182B	
ZV\$RCB	182C	
ZV\$NSR	1830	
ZV\$STR	182E	
ZV\$BKF	1833	
ZV\$OTP	1862	
ZV\$BKS	1832	
ZV\$HR	183A	
ZV\$LR	1837	
ZV\$DAT	181A	
ZV\$HM	1881	
ZV\$HRU	1834	
ZV\$HRL	1835	
ZV\$LRL	1836	
ZV\$HBD	1838	
ZV\$CF1	1826	
ZV\$--5	183D	
ZV\$RMD	181B	
ZV\$MCP	1839	
HIBAUD	1838	
ZV\$RAW	1825	
ZV\$RDT	1A3C	
ZV\$CTL	1822	
ZV\$B1	195D	
ZV\$TST	1A92	
ZV\$MDC	1A66	
ZV\$R99	1C64	
ZV\$ISA	1840	
ZV\$UIH	183B	
ZV\$ZRO	18BF	
ZV\$BSH	18C1	
ZV\$CPU	1821	
ZV\$R50	189F	
ZV\$R60	18AA	
ZV\$RT	18A1	
ZV\$ALL	1820	
*MLCHPG	1C69	T+V
MLCHPG	1C69	
ENDCHP	1C9A	

