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TITLE DCMC1 *REV. A*

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* DESCRIPTION:

* THIS T&V PROGRAM VERIFIES PROPER OPERATION OF THE DUALLINE
* COMMUNICATION PROCESSOR (DLCP) FOR SERIES 60, LEVEL 6/20.

* THE SUBSYSTEM ITEMS SUPPORTED BY THIS PROGRAM ARE:

* DLC9101 DUAL LINE COMMUNICATION PROCESSOR

* REVISION HISTORY:

* REV DATE
* A FEB 78 ORIGINAL RELEASE

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/ PROGRAM PREPARATION:
 * THE ROOT SOURCE OF THIS PROGRAM, AFTER THE ADDITION OF APPROPRIATE
 * TITLE AND END STATEMENTS, WAS PROCESSED BY THE HOST RESIDENT ASSEMBLER
 * TO CREATE EITHER SHORT OR LONG ADDRESS FORM (SAF OR LAF) OBJECT TEXT
 * AND LISTING. THE OBJECT TEXT WAS FURTHER PROCESSED BY THE HOST RESIDENT
 * LINKER USING THE APPROPRIATE CONSOLE ZV\$LIB LIBRARY TO CREATE A PUNCH
 * SEGMENT CONTAINING AN EXECUTABLE MODULE. THE ASSEMBLY LISTING WAS
 * AUGMENTED WITH CROSS REFERENCE DATA PLUS THE LOAD MAP FROM THE LINKER
 * TO CREATE A LIST SEGMENT.
 * DCMW1 60135054-001 (SOURCE)
 * DCMCI 60135055-001

* PROGRAM DISTRIBUTION:
 * THE ELEMENTARY ITEMS SUBMITTED TO THE I&V PROGRAM DISTRIBUTION CENTER
 * WERE THE EXECUTABLE PUNCHED CARD DECKS OF MLCI1 AND MLCL1, AND MAGNETIC
 * TAPE IMAGES OF THE AUGMENTED LISTINGS.
 * REPRODUCTIONS OF THE EXECUTABLE CARD DECKS MAY BE AS DUPLICATE CARD DECKS
 * OR AS MEMBER "VK" OF A MULTIPLE MEMBER FILE. IN THE MOST FREQUENT CASE
 * IT WILL BE FOUND AS MEMBER VK WITHIN FILE PROGFILE OF A DISKETTE VOLUME
 * ENTITLED DIAGS.
 * DISTRIBUTION OF THE LISTINGS, WHICH SHOULD BE AVAILABLE IF ANY COMPLEX
 * MAINTENANCE OR REPAIR IS TO BE PERFORMED, IS NORMALLY MADE AS A
 * PRINTED COPY.

* ROUTINE DEMONSTRATION:
 * A MINIMUM SATISFACTORY TEST FOR NORMAL OPERATION MAY BE OBTAINED
 * BY ENTERING THE CHANNEL NUMBER TO BE TESTED AND COMPLETING ONE PASS.

* MAIN MEMORY REQUIREMENT:
 * THIS PROGRAM REQUIRES 16K WORDS OF MAIN MEMORY AND WILL USE
 * ALL OF AVAILABLE MEMORY THROUGH 64K WORDS.

 *
 * TEST PROCEDURE AND DESCRIPTION
 * THE PROGRAM AUTOMATICALLY DETERMINES WHAT CHANNELS ARE ACTIVE
 * ON THE DLCP TO BE TESTED. IT PROCEEDS TO EXERCISE ALL MOTHER
 * BOARD FUNCTIONS THAT DO NOT REQUIRE LINE ADAPTERS. THE TEST INCLUDES
 * DLCP MEMORY, INSTRUCTION SET, AND DATA XFER TESTS. PSEUDO RANDOM
 * DATA IS TRANSFERRED VIA ALL CHANNELS. THE MLCP IS MADE TO TRANSFER
 * DATA TO ALL AVAILABLE MEMORY ABOVE THE PROGRAM. DLCP INTERRUPTS ARE
 * MADE ON ALL ACTIVE CHANNELS AT MULTIPLE LEVELS.
 * AN ACTIVE CHANNEL IS ONE WHICH EITHER HAS A LINE ADAPTER PRESENT
 * OR HAS ITS LINE ADAPTER-HOLD SIGNAL TIED TO GROUND. THE TEST
 * PROGRAM CAN ONLY TEST ACTIVE CHANNELS.

* OPERATING INSTRUCTIONS
 * LOAD AND START (OR RESTART) THE PROGRAM. THE PROGRAM IDENTIFICATION WILL
 * BE DISPLAYED ON THE CONSOLE. THE INITIAL START WILL ALSO DISPLAY:
 * THE ZV\$LIB REVISION NUMBER
 * THE ADDRESS FORM (SAF OR LAF)
 * I/O EQUIPMENT DETECTED IN THE SYSTEM
 * MEMORY SIZE
 * THIS DISPLAY MUST BE VERIFIED BY THE OPERATOR. THIS DISPLAY IS OMITTED
 * ON RESTARTS.
 * THE CONSOLE SEARCH RULES ARE: FIND THE CONSOLE WITH THE LOWEST CHANNEL
 * NUMBER CONNECTED THRU AN MDC CONTROLLER. IF THERE IS NO CONSOLE ON AN
 * MDC, THEN SEARCH FOR A TERMINAL WITH THE HIGHEST CHANNEL NUMBER ASSIGNED
 * TO AN ACLA ADAPTER ON AN DLCP CONTROLLER. IF NO ASYNC ADAPTER IS FOUND,
 * THEN GO TO THE FULL CONTROL PANEL.
 * THERE ARE THREE CONSOLE CHANNEL OPTIONS DETERMINED BY THE VALUE OF LO-
 * CATION "ZV\$ITY".
 * IF ZV\$ITY EQUALS (0000), SEARCH FOR A CONSOLE.
 * IF ZV\$ITY EQUALS (FFFF), ASSUME THERE IS NO CONSOLE.
 * IF ZV\$ITY EQUALS NEITHER (0000), NOR (FFFF), THEN IT IS THE CONSOLE CHAN-
 * NEL NUMBER. NOTE: DEFAULT IS TO SEARCH FOR A CONSOLE.
 * ALL CONSOLE I/O IS EVEN PARITY. IF CONSOLE IS ON MLC, IT MUST BE ASYNC
 * AND THE BAUD RATE SET AT 1200 TO MATCH THE PROGRAM SUPPLIED RATE. IF IT
 * IS NECESSARY TO CHANGE THE PROGRAM BAUD RATE, THEN THE NEW BAUD RATE
 * CODE SHOULD BE PUT INTO LOCATION "ZV\$BDU" IN HEX. THE TERMINAL BAUD RATE
 * MUST BE SET TO MATCH THIS NEW BAUD RATE. THE CORRECT HEX VALUE MAY BE
 * OBTAINED FROM THE FOLLOWING TABLE.

-----*
 *
 * BAUD RATE TABLE

* ACLA I.D.E(2118)(2110) E(2108)

* 50	E	0	EE	1
* 75	E	1	EE	2
* 110	E	2	EE	3
* 134	E	3	EE	4
* 150	E	4	EE	5
* 200	E	5	EE	---
* 300	E	6	EE	6
* 600	E	7	EE	7
* 900	E	---	EE	8
* 1050	E	8	EL	---
* 1200	E	9	EE	9

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* 1800 E 10 (A) EE 10 (A)
 * 2000 E 11 (B) EE --
 * 2400 E 12 (C) E 11 (B)
 * 3600 E -- EE 12 (C)
 * 4800 E 13 (D) EE 13 (D)
 * 7200 E -- EE 14 (E)
 * 9600 E 14 (E) EE 15 (F)
 * 19200 E 15 (F) EE --

* TO MAKE ANY OF THE ABOVE CHANGES, LOAD AND HALT THE PROGRAM BEFORE EXECUTION. INSERT CHANGE THEN EXECUTE. MEMORY LOCATIONS OF "ZV\$TTY" AND "ZV\$BUU" MAY BE FOUND IN MAP AT END OF LISTING. CONSULT LEVEL-6 T&V MANUAL "AW94" FOR DETAILS ON HOW TO LOAD THE TESTS.
 * THE FOLLOWING IS A TYPICAL RESULT OF LOADING AND STARTING TO RUN THE PROGRAM.
 * DLCP TEST DLCS1 <PGM DATE> <PGM REV>
 * ZV\$LIB REV. 6.0
 * ZV\$AF= 1 <>
 * WDT
 * CHAN DEVIC ID
 * 0400 DSK1 2010
 * 0480 DSK1 2010
 * 0580 CDR 2008
 * 1200 DISC 2330
 * 1280 DISC 2330
 * 1300 LPT 2000
 * 1380 CONS 2019
 * MEMORY LOW 00002B2D
 * MEMORY HIGH 00003FFF 16K

* THE PROGRAM WILL THEN ASK:
 * PWR FREQ (HZ)?:
 * THE OPERATOR MUST TYPE IN THE FREQUENCY OF THE CLOCK USED TO DRIVE THE REAL TIME CLOCK ON HIS SYSTEM. THE PROGRAM NEXT DISPLAYS:
 * DLCP CHANNEL?:
 * THE OPERATOR MUST RESPOND WITH THE LOWEST ACTIVE ADDRESS ON THE MLCP. ENTER 4 HEX DIGITS FOLLOWED BY A CARRIAGE RETURN. THE MLCP ADDRESS WILL NORMALLY BE DISPLAYED AS AN ITEM IN THE CONFIGURATION PRINTOUT.
 * THE PROGRAM WILL THEN RUN ABOUT 1 MINUTE AND TYPE:
 * FW REV XX
 * THIS IS A PRINTOUT OF THE CURRENT FIRMWARE REVISION. THE ABOVE PRINTOUT IS GIVEN ON THE FIRST PASS ONLY. VALUE "XX" WILL BE STORED IN LOC "FW-REV" FOR EXAMINATION BY USERS WITH NO CONSOLE.
 * IF THERE IS NO CONSOLE PRESENT REFER TO THE MANUAL "SERIES 60 LEVEL 6 MODEL 34/36 SYSTEM COACTIVE MAINTENANCE OPERATORS GUIDE" DOC 71010202-100 FOR INSTRUCTIONS ON ENTERING DATA AND INTERPRETING PROGRAM MESSAGES.
 * THE PROGRAM WILL RUN APPROXIMATELY 3 - 5 MINUTES IF THERE ARE NO HARDWARE FAULTS. IT WILL THEN DISPLAY:
 * PASS
 * AND HALT AT WITH P COUNTER DISPLAY AT HEX 100. TO CONTINUE HIT EXECUTE. THE PROGRAM WILL CONTINUE AND TYPE "PASS" EVERY 3 - 5 MINUTES WITH NO HALTS BETWEEN PASSES.
 * TO CHANGE TO A SECOND DLCP ADDRESS RESTART THE PROGRAM AT HEX 105. PROGRAM OPERATION WILL THEN PROCEED AS AFTER AN INITIAL START AT HEX 100.
 * NOTE - FOR OPERATION WITH THE BASIC CONTROL PANEL THE PROGRAM MUST BE RELOADED TO TEST A SECUND MLCP.

* ERROR REPORTS
 * ERRORS WILL CAUSE THE PROGRAM TO HALT. AN ERROR MESSAGE WILL BE DISPLAYED IF A CONSOLE IS PRESENT. ALL ERROR MESSAGES ARE AN INDICATION THAT THE COMMUNICATIONS PROCESSOR IS FAULTY.
 * ERROR DISPLAYS ARE AS FOLLOWS:
 * ERR MBXX AT YYYY
 * B7 B6 B5 B4 B3 B2 B1 I
 * R7 R6 R5 R4 R3 R2 R1 M
 * XX = TEST TABLE. SEE JUMP TABLE AT LOCATION "BK1".
 * YYYY = ERROR LOCATION IN LISTING. HAS COMMENT GIVING FAILING FUNCTION.
 * B1-B7, I, R1-R7, M ARE CONTENTS OF REGISTERS.
 * INTERPRETATION OF THESE IS FOR SPECIALIST USAGE.
 * IN ALL CASES:
 * R3 = CHANNEL NUMBER
 * IN GENERAL
 * R6 = SHOULD BE DATA
 * R5 = ACTUAL DATA
 * R7 = WORD NUMBER IN BLOCK TRANSFER

* A PROGRAM HALT WITH P DISPLAYING A VALUE LESS THAN

* HEX 100 INDICATES THAT A TRAP OCCURED. P DISPLAYS ONE
* BEYOND THE ACTUAL TRAP VECTOR WHICH WAS TAKEN. THE
* TRAP SAVE AREA IS FROM LOC. 2 - 9. REFER TO MODEL
* 34/36 T + V OPERATING INSTRUCTION MANUAL, LOC
* 71010460-200, SECTION 1.1 .
*
* IF DURING THE OPERATION OF THE PROGRAM THE DISPLAY PANEL
* BECOMES "HUNG" SUCH THAT THE DISPLAYED REGISTER IS
* UNCHANGING AND NO OTHER REGISTER MAY BE SELECTED, THEN
* THE MLCP BEING TESTED SHOULD BE REPLACED.
*
*
* RESTRICTIONS:
* FOR SYSTEMS WITH FIRMWARE REV NUMBER 4 OR LESS, REV. A
* OF THE PROGRAM MUST BE USED.
* THE FIRMWARE REV. NUMBER AT THE TIME OF RELEASING
* REV. B OF THIS PROGRAM IS "5".
* THE FIRMWARE REV NUMBER AT THE TIME OF RELEASING REV C
* OF THE PROGRAM IS "8".
*
* THE FIRMWARE REV. NUMBER AT THE TIME OF RELEASING REV D
* OF THE PROGRAM IS "9". REV D CONTAINS NO FUNCTIONAL
* DIFFERENCES FOR SAF MODE.
*
* IF THIS PROGRAM IS TO BE RUN ON A SYSTEM WITH A BASIC CONTROL
* PANEL, A SYSTEM CONSOLE MUST BE PRESENT.
*

015E	14A8							
015F	14A9							
000387	0160	8700	14C9	*	CL	<CH-CT	CHANNEL COUNT	
000388	0162	8700	14BD		CL	<IMASK	SET LINE MASK WORD TO 0	
000389					LDR	\$R5,=\$R4		
000390	0164	D854			LDR	\$R3,=\$R1		
000391	0165	B851			AND	\$R3,=X'3C0'	STRIP \$R3 TO SUBCH. NUMBER	
000392	0166	B570	03C0		SUR	\$R3,6		
000393	0168	3046			AND	\$R1,=Z'FC3F'	STRIP TO CONTROL WORD WITHOUT SUBCH.	
000394	0169	9570	FC3F		STR	\$R5,<ATLT,\$R3	STORE ID IF SUBCH. PRESENT	
000395	016B	DF30	14AD		STR	\$R3,<1PK		
000396	016D	B700	14CD		CMR	\$R5,=X'3178'		
000397	016F	D970	3178		BNE	>CON2C		
000398	0171	09B0						
000399								
000400								
000401								
000402	0172	C851			LDR	\$R4,=\$R1		
000403	0173	4E02			ADV	\$R4,=2	FORM FC FOR EXT ID	
000404	0174	C380	1173		LNJ	\$B4,<CGSCH		
000405	0176	8055			IO	=SR5,=\$R4		
000406	0177	0054						
000407	0178	0703			BIOT	>S+2+\$AF		
000408	0179	F380	125C		LNJ	\$B7,<ERROR	INPUT EXTEND ID WAS NAK'ED	
000409	017B	DF30	14AD		STR	\$R5,<ATLT,\$R3		
000410	017D	3B60	01A1		BUDU	\$R3,<CON2C		
					CALL	ZV\$TC, IDMSG	LINE	
				X				
000411	0185	3041						
000412	0186	B700	2BAA		SUR	\$R3,1	GET LINE NUMBER	
000413	0188	FBC0	0003		STR	\$R3,<RTB	NUMBER	
	018A	D380	0000	X	CALL	ZV\$TD,RTB		
	018C	UF80						
	018D	2BAA						
000414	018E	B800	14CD		LDR	\$R3,<TPK	GET BACK CHANNEL	
000415	0190	FBC0	0003		CALL	ZV\$T,EQ	EQUALS	
	0192	D380	0000	X				
	0194	OF80						
	0195	1574						
000416	0196	5048			SUR	\$R5,8		
000417	0197	D470	3100		OR	\$R5,=Z'3100'	ALIGN EXTENDED ID	
000418	0199	DF00	14CD		STR	\$R5,<TPK	PUT IN MLCP ADDRESS	
000419	019B	FBC0	0003		CALL	ZV\$IH,TPK	PRINT ID	
	019D	D380	0000	X				
	019F	OF80						
	01A0	14CD						
000420	01A1	8930	14BD		CON2C	LBT	SET MASK WORD OF ACTIVE LINES	
000421	01A3	8A3D			CON2B	INC	SEE IF ALL SUBCH. TESTED	
000422	01A4	3D10				CMV		
000423	01A5	U909				BE		
000424	01A6	C851				LDR		
000425	01A7	C380	1173			\$B4,<CGSCH		
000426	01A9	8055				IO	=SR5,=\$R4	
	01AA	0054						
000427	01AB	0701	FFBF		BIOT	CON2A		
000428	01AD	UFF6			B	>CON2B		
000429								
000430								
000431	01AE	2CF5			CON3	LDR		
000432	01AF	DB80	1567		ALL	LAB	NUMBER OF I/O TO BE CHANGED	
000433	01B1	C870	003F			\$B5,<CONT1+11	PUT ADDRESS OF CONTROL TABLE IN \$B5	
000434	01B3	9570	FC00			AND	LOAD MASK TO CLEAR CHANNEL	
000435	01B5	DF00	14C3			\$R1,=Z'FC00'	CLEAR SUBCH. & FUNCTION	
000436	01B7	7525				STR	STORE MLCP ADDRESS	
000437	01B8	9454				AND	CLEAR CHANNEL	
000438	01B9	9F25				OR	PUT CHANNEL NUMBER IN	
000439	01BA	27F7				STR	STORE IT BACK IN CONTROL TABLE	
000440	01BD	OF80	01CA			BINC		
						B	<BK1	
000441								
000442								
000443								
000444								
000445								
000446	01BD	9800	01C9		STLLOOP	LDR		
000447	01BF	9F00	00FF			STR	\$K1,<NOP	
000448	01C1	8700	14AB			CL	\$K1,<STOP	NO HALIS AFTER FIRST PASS
000449	01C3	8700	1549			<LOOP		
000450	01C5	8700	154A			CL	<SEC	SET LOOP COUNT TO 0
000451	01C7	OF80	01CA			CL	<TTUT	CLEAR SECONDS COUNTER
						B	<BK1	CLEAR TIME TOTALIZER
000452								
000453								
000454								
000455								
000456	01C9	0F03						
000457	01CA	A380	021E		NOP	NOP	>BK1	
000458	01CC	A380	02D6		BKK1	LNJ	\$B2,<RGNT	
000459	01CE	A380	0355		BKK3	LNJ	\$B2,<CCCBKT	
000460	01D0	A380	0470		BKK4	LNJ	\$B2,<MEMT	
000461	01D2	A380	0539		BKK4B	LNJ	\$B2,<LCTOB	
000462	01D4	A380	0639			LNJ	\$B2,<SITEST	
000463	01D6	A380	06B5			LNJ	\$B2,<INST	
000464	01D8	A380	0815		BKK7	LNJ	\$B2,<INCOUNT	
000465	01DA	A380	0C39		BKK7B	LNJ	\$B2,<ST10RC	
000466	01DC	A380	0CEF			LNJ	\$B2,<INST1	
000467	01DE	A380	0E3F		BKK9B	LNJ	\$B2,<DSST	
000468	01E0	A380	086E		BKK10	LNJ	\$B2,<SIYST	
000469	01E2	A380	08BC		BKK7C	LNJ	\$B2,<RC0	
000470	01E4	A380	0436		BKK7D	LNJ	\$B2,<TRAN1	
000471	01E6	A380	05B7		BKK4A	LNJ	\$B2,<MNKT	
000472	01E8	A380	0AD6		BKK5	LNJ	\$B2,<CHPGT	
000473	01EA	A380	0BBC		BKK9	LNJ	\$B2,<CRCT	
000474	01EC	A380	06ED		BKK7A	LNJ	\$B2,<DTPT	
000475	01EE	FBC0	0003			CALL	ZV\$T,ZV\$TC, DON	CHANNEL INITIALIZE TEST
	01FO	D380	0000	X				
	01F2	OF80						
	01F3	154F						

LABLE - DESCRIPTION

- A - GO TO RANGE TEST
- B - GO TO CCL READ TEST
- C - GO TO MEMORY TEST
- E - GO TO OUTPUT BYTE AND LCT TEST
- SI - SOFT INITIALIZE TEST
- G - OUTPUT TEST
- K - STOP I/O TEST
- R - FIRST INTERRUPT TEST
- S - DATA SET SCAN TEST
- T - DEFERRED INTERRUPT TEST
- L - RECIEVE TEST
- M - TRANSMIT TEST
- C - GO TO MEMORY NAK TEST
- F - GO TO CHANNEL PROGRAM TEST
- O - GO TO CRC TEST
- P - GO TO OUTPUT TEST
- Z - CHANNEL INITIALIZE TEST

000476 01F4 0F81 FF0A
 000477 01F6 FBC0 0003
 01F8 D380 0000 X
 01FA 0F80
 01FB 1578
 000478 01FC 0F81 FF02
 000479 01FE 0000 b
 01F6 HLT STOP
 000480 * ACCUMULATE TIME
 000481 01FF 9800 1549
 0201 9970 00B4
 000482 0203 0287
 0204 0F80 01CC
 000483 0206 FBF0 0001
 0208 D380 0000 X
 020E 0F80
 020F 021B
 000484 0210 8A80 14AC
 0212 9800 14AC
 000490 0214 1D01
 000491 0215 0980 0100
 0217 8700 00FF
 000492 0219 0F80 00FF
 000493 021B 5041 5353 2024
 000494 PASMSG TEXT INC <PASS
 000495 *
 000496 *
 000497 *
 000498 021E 9870 4120 RGNT LDR RANGE TEST
 000499 0220 9F00 1581 STR \$R1,=A*A
 000500 CALL \$R1,=<ERMG+1
 000501 0222 FBC0 0003 LNJ \$B1,<INTITZ
 0224 D380 0000 X LDV \$R6,=1
 0226 0F80 LNJ \$B4,<GENITZ
 0227 1590 *
 000502 0228 9380 0F18 LDR \$R1,=X'300'
 000503 022A 6C01 STR \$R1,<IMGA+(\$AF-1)
 000504 022B C380 10FC CL =SR1
 000505 *
 000506 022D 9870 0300 NXCH LNJ =SR3
 000507 022F 9F00 14DA IOLD >FLN
 000508 0231 8751 *
 000509 0232 8753 NXRG LDR \$R4,<CONT1
 000510 0233 C380 10EC LNJ \$B4,<CGSCH
 000511 0235 0FA3 IOLD *IMGA,=\$R4,=\$R6
 000512 *
 000513 0236 4CUA LDV \$R4,=10
 000514 0237 CA00 1549 ADD \$R4,<SEC
 000515 0239 CF00 1549 STR \$R4,<SEC
 000516 023B C800 155C LDR \$R4,<CONT1
 000517 023D C380 1173 LNJ \$B4,<CGSCH
 000518 023F 81C8 129A IOLD *IMGA,=\$R4,=\$R6
 0241 0054 *
 0242 0056
 000519 0243 0703 BIOT >IOT1
 000520 0244 F380 125C LNJ \$B7,<ERROR
 000521 *
 000522 0246 4E06 IOT1 ADV >R4,=6
 000523 0247 8070 0000 IO 0,=\$R4
 000524 0249 0054 *
 000525 024A 0703 BIOT >S+2+\$AF
 000526 024B F380 125C LNJ \$B7,<ERROR
 000527 024D C800 155F *
 000528 024F C380 1173 LDR \$R4,<CONT4
 000529 0251 8040 1275 LNJ \$B4,<CGSCH
 0253 0054 DUMMY,=\$R4
 000530 0254 0F85 *
 000531 0255 F380 125C LNJ >IOT3
 000532 0257 0F82 B
 000533 0258 0F96 JMPIEXT >IOT3
 000534 *
 000535 0259 C800 155D IOT3 LDR \$R4,<CONT2
 000536 025B C380 1173 LNJ \$B4,<CGSCH
 000537 025D 8055 IO =SR5,=\$R4
 025E 0054 *
 000538 025F 0703 BIOT >IOT2
 000539 0260 F380 125C IOT2 LNJ \$B7,<ERROR
 000540 0262 E955 CMK >R4,=\$R5
 000541 0263 0988 BNE >RGE
 000542 *
 000543 *
 000544 0264 EA70 000A RGNT1 ADD \$R6,=Z'000A*
 000545 0266 0680 023B BCF <NXRG
 000546 0268 8AD3 INC =SR3
 000547 0269 0F80 0233 B <NXCH
 000548 *
 000549 026B F380 125C RGE LNJ \$B7,<ERROR
 000550 026D OFF7 B >RGNT1
 000551 *
 000552 * TEST CCB'S DON'T OVERWRITE EACH OTHER
 000553 *
 000554 026E C380 10FC SCCBTS LNJ \$B4,<GENITZ
 000555 0270 2C01 LDV \$R2,=1
 000556 0271 F870 0200 LDR \$R7,=X'200'
 000557 0273 8753 CL =SR3
 000558 0274 C380 10EC NLINE LNJ \$B4,<FLN
 000559 0276 0FA2 B >RCLBS
 000560 0277 1FCF NXTCCB LDR \$R1,=Z-4
 000561 0278 C800 155C LNQ \$R4,<CONT1
 000562 027A C380 1173 IOLD \$B4,<CGSCH
 000563 027C 81C8 125D IO *IMGA,=\$R4,=\$R2
 027E 0054 *
 027F 0052
 000564 0280 0703 BIOT >S+2+\$AF
 000565 0281 F380 125C LNJ \$B7,<ERROR
 000566 0283 4E06 ADV =SR4,=6
 000567 0284 8057 IO =SR7,=\$R4
 000568 *
 000569 *
 000570 *
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000568 0285 0054 BIOT >\$+2+\$AF
000569 0286 0703 LNJ \$B7,<ERROR
000570 0287 F380 125C INC =\$R2
000571 0288 8AD2 ADD \$R7,=X'100'
000572 028A FA70 0100 BINC \$R1,>NXTCCB
000573 028C 17EC SUB \$R4,=Z'0006'
000574 028D C270 0006 IOLD *IMGA,=\$R4,=\$R2
0291 0054
0292 0052
000575 0293 0783 BIOF >\$+2+\$AF
000576 0294 F380 125C- LNJ \$B7,<ERROR
000577 0296 8AD3 INC =\$R3
000578 0297 UFDD B >NLNE

 * READ RANGES SET UP
 RCCBS LDV \$R2,=1
000583 0298 ZC01 F870 0200 LDR \$R7,=X'200'
000584 0299 8753 CL =\$R3
000585 029C C380 10EC NL1 LNJ \$B4,<FLN
000586 029E 8382 JMP \$B2
000587 029F 1CF0 LDV \$R1,=-4
000588 02A0 C800 155F RNXR LDR \$R4,<CONT4
000589 02A2 C380 1173 LNJ \$B4,<CGSCH
000590 02A4 8040 1222 IO DUMMY,=\$R4
02A6 0054
000591 02A7 0703 BIOT >\$+2+\$AF
000592 02A8 F380 125C LNJ \$B7,<ERROR
000593 02AA C800 155D LDR \$R4,<CONT2
000594 02AC C380 1173 LNJ \$B4,<CGSCH
000595 02AE 8055 IO =\$R5,=\$R4
000596 02B0 0703 BIOT >\$+2+\$AF
000597 02B1 F380 125C LNJ \$B7,<ERROR
000598 02B3 E852 LDR \$R6,=\$R2
000599 02B4 D956 CMR \$R5,=\$R6
000600 02B5 0903 BE >\$+2+\$AF
000601 02B6 F380 125C LNJ \$B7,<ERROR
* DO AN INPUT STATUS. IF IT ADVANCES THE INPUT POINTER A FAILURE WILL RESULT.
000602 02B8 C800 1561 LDR \$R4,<CONT6
000604 02BA C380 1173 LNJ \$B4,<CGSCH
000605 02BC 8055 IO =\$R5,=\$R4
000606 02BE 0703 BIOT >\$+2+\$AF
000607 02BF F380 125C LNJ \$B7,<ERROR
000608 02C1 E857 LDR \$R6,=\$R7
000609 02C2 D956 CMR \$R5,=\$R6
000610 02C3 0903 BE >\$+2+\$AF
000611 02C4 F380 125C LNJ \$B7,<ERROR
000612 02C6 8AD2 INC =\$R2
000614 02C7 FA70 0100 ADD \$R7,=X'0100'
000615 02C9 17D7 BINC \$R1,>RNXR

 * GIVE A 5TH INPUT NEXT STATUS AND CHECK ITS NAK'ED
 *
 000619 02CA C800 155F LDR \$R4,<CONT4
000620 02CE C380 1173 LNJ \$B4,<CGSCH
000621 02CF 8055 IO =\$R5,=\$R4
000622 02D0 0783 BIOF >\$+2+\$AF
000623 02D1 F380 125C LNJ \$B7,<ERROR
000625 02D3 8AD3 INC =\$R3
000626 02D4 UF81 FFC7 B NL1 FIND NEXT LINE NUMBER

 * CCB READ TEST - TESTS OUTPUT OF CCB & BLOCK READ
 *
 000635 02D6 9870 4220 CCBRT LDR SR1,=A'B'!
000636 02D8 9F00 1581 STR SR1,<ERMG+1
000637 02DA FBC0 0003 CALL ZV\$TC,TSTB
X
 *
 000638 02E0 8753 NXCB CL =\$R3
000639 02E1 C380 10FC LNJ \$B4,<GENITZ GENERAL INITIALIZE
000640 02E3 9800 14DC *
000641 02E5 9F00 14DD LDR \$R1,<SBCK+\$AF-1
000642 02E7 8751 STR \$R1,<SBCCL
000643 02E8 8700 14E1 NXCB1 CL =\$R1
000644 02EA 4C08 CL <MASK
000645 02EB CF00 14D5 LDV \$R4,=8
000646 02ED 5C04 STR \$R4,<RANGE
000647 02EF DF00 14D7 LDV \$R5,=4
000648 02F0 C380 10EC STR \$R5,<RANGEW
000649 02F2 8382 LNJ \$B4,<FLN
000650 02F3 BF00 2BAA JMP \$B2
000651 02F5 BF00 2BAB STR \$R3,<KTB
000652 02F7 BF00 2BAC STR \$R3,<KTB+1
000653 02F9 BF00 2BAD STR \$R3,<KTB+2
000654 02FB D853 STR \$R3,<KTB+3

 *
 000656 02FC D853 LDR \$R5,=\$R3 FIND ADDRESS FOR BLOCK READ
000657 02FD 5F20 MLV \$R5,=X'20'
000658 02FE DA70 1210 ADD \$R5,=X'1210'
000659 02FF BF00 14CD STR \$R3,<TPR LOCATION OF CCB TO READ
000660 0301 3B03 BEVN \$R3,>CBRT3 STORE CHAN NUMB
000661 0302 88D3 DEC -\$R3 BRANCH IF READ
000662 0303 5EF8 ADV \$R5,=-8 FORM READ CHANNEL
000663 0304 BF00 0354 STR \$R3,<READ FORM CCB ADDRESS FOR WRITE CHANNEL
000664 0306 DF00 030D STR \$R5,<CBRT1 STORE READ CHANNEL
000665 0308 C800 14D5 LDR \$R4,<RANGE

 *
 000667 030A C380 1189 LNJ \$B4,<MCBL FORM CCB FOR BLOCK READ
000668 030C 2BAA DC <RTD RAM ADDRESS TO READ
000669 030D 0000 CBRT1 DC X'0'

000670
 000671
 000672 030E B800 14CD * *
 000673 0310 C800 14D5 LDR \$R3,<TPR
 000674 0312 C380 1189 NXC82 LDR \$R4,<X RANGE
 000675 0314 1547 LNJ \$B4,<MCB
 000676 0315 55AA DC <RMINST
 000677 * * DC X*55AA*
 000678 0316 B800 0354 NXCB3 LDR \$R3,<READ
 000679 0318 C800 1562 LDR \$R4,<CONT7
 000680 031A C380 1173 LNJ \$B4,<CGSCH
 000681 031C 8070 0800 IO =X*800*,=\$R4
 000682 031F 7073 BIOT >\$+2+\$AF
 000683 0320 F380 125C LNJ \$B7,<ERROR
 000684 * * READ BACK STATUS AFTER BLOCK READ TO MAKE SURE IT'S CORRECT.
 000685 * *
 000686 0322 C380 1205 LNJ \$B4,<DLAY TIME DELAY
 000688 * *
 000689 0324 C380 1161 LNJ \$B4,<INXT INPUT NEXT STATUS
 000690 0326 E870 1000 LDR \$R6=X1000* SHOULD BE STATUS. CCB COMPLETE ON.
 000691 0328 D956 CMR \$R5,\$R6
 000692 0329 0903 BE >\$+2+\$AF
 000693 032A F380 125C LNJ \$B7,<ERROR STATUS WRONG AFTER BLOCK READ
 000694 032C B800 14CD LDR \$R3,<TPR GET BACK CHANNEL UNDER TEST
 000695 * * CALL ZVSC,SBCCB,RTB,MASK,RANGEW,ERAK
 000696 032E FBC0 0003 D380 0000 X
 0330 OF80
 0332 14DD
 0333 2BAA
 0334 14E1
 0335 14D7
 0336 14E3
 0337 14E3
 000697 0338 89C0 11AA CMZ ERAK IF ERROR HALT
 000698 033A 090F BEE >NOER1
 000699 033B C800 030D LDR \$R4,<CBRT1 STARTING LOCATION OF CCB IN RAM
 000700 033D D800 14E5 LDR \$R5,<ERAK+2
 000701 033F E800 14E4 LDR \$R6,<ERAK+1
 000702 0341 F800 14E3 LDR \$R7,<ERAK
 000703 0343 F380 125C LNJ \$B7,<ERAK
 000704 * * CALL ZVSC
 0345 FBF0 0001 D380 0000 X
 0347 8980 14AA NOER1 CMZ <FRST
 000705 0349 8980 14AA BNE BLUG CHECK IF FIRST TIME THRU
 000706 034B 0981 0003 LNJ \$B4,<PREV
 000707 034D C380 26C0 INC
 000708 034F 8A80 14AA LINC SET FIRST TIME FLAG
 000709 0351 8AD3 INC INCREMENT CHANNEL NUMBER
 000710 * *
 000711 * *
 000712 0352 0F80 02E1 READ B <NXCB
 000713 0354 0000 02E1 DC 0 READ CHANNEL STORED HERE
 000714 * *
 000715 * *
 000716 * *
 000717 * *
 000718 * *
 000719 * *
 000720 * *
 000721 * *
 000722 0355 9870 4320 MEMT LDR \$R1.=A*C *
 000723 0357 9F00 1581 STK \$R1,<ERMG+1
 000724 * * CALL ZVSI,ZV\$TC,TSTC
 0359 FBC0 0003 D380 0000 X
 035D OF80
 035E 15A2
 000725 035F 8700 14E1 LNJ <MASK
 000726 0361 C380 10FC LNJ \$B4,<GENITZ
 000727 * * CL
 000728 0363 C870 5555 LDK \$R4=X15555*
 000729 0365 CF00 14E2 STR \$R4,<MASK1
 000730 0367 8753 CL =\$R3
 000731 0368 C380 10EC MEMTA LNJ <FLN
 000732 036A 8382 JMP \$B2
 000733 * *
 000734 036B 3B00 036F BEVN \$R3,<MEMTB
 000735 036D 8AD3 INC =\$R3
 000736 036E OFFA B >MEMTA
 000737 036F 8751 MEMTB CL =\$R1
 000738 0370 8754 CL =\$R4
 000739 0371 E380 0380 LNJ <TESM
 000740 0373 4CFE LDV \$R4,=-
 000741 0374 E380 0380 LNJ \$B6,<TESM
 000742 0376 C870 0110 LDR \$R4=X110*
 000743 0378 E380 0380 LNJ \$B6,<TESM
 000744 037A C870 FEEF LDR \$R4=Z'FEEF'
 000745 037C E380 0380 LNJ \$B6,<TESM
 000746 037E 8AD3 INC =\$R3
 000747 037F 0FE9 B >MEMTA
 000748 * *
 000749 * *
 000750 0380 2C01 TESM LDV \$R2,=1
 000751 0381 E870 0300 LDR \$R6=X*300*
 000752 0383 EF00 14D7 STR \$R6,<RANGEW
 000753 0385 8700 14D8 CL <RAMAD
 000754 * *
 000755 0387 9380 0411 LNJ \$B1,<SET
 000756 * *
 000757 0389 BB80 1EBB LAB \$B3,<SDB
 000758 038B E800 14D7 LDR \$R6,<RANGEW
 000759 038D 8256 NEG =\$R6
 000760 038E CF73 FILL STR \$R4,+\$B3
 000761 038F 4011 SCL \$R4,1
 000762 0390 67FE BINC \$R6,>FILL
 000763 * *
 000764 0391 BF00 14CF STR \$R3,<TEMP1
 000765 0393 BF00 14CD STR \$R3,<TPK
 000766 0395 3B85 BUDD \$R3,>MTM6
 000767 0396 8A80 14CD INC <TPK
 FIND WRITE SUBCH.

000768	0396	BEOO	14CD		SWR	\$R3,<TPR		
000769	039A	9380	1069		LNJ	\$B1,<SDATA		BLOCK WRITE DATA PATTERN TO RAM
000770	039C	1EBB			DC	<SDDB		
000771	039U	0600			DC	X'600'		
000772	039E	0200			DC	X'200'		
000773	039F	8000			DC	Z'8000'		EVEN BYTE ADDRESS, 250 MS DELAY
000774				*				
000775				*				
000776	03AU	BEOO	14CD		SWR	\$R3,<TPR		GET READ SUBCH.
000777	03A2	9380	1098		LNJ	\$B1,<RDATA		READ DATA PATTERN FROM RAM
000778	03A4	2BA			DC	<RTB		
000779	03A5	0600			DC	X'600'		
000780	03A6	0200			DC	X'200'		
000781	03A7	8000			DC	Z'8000'		RAM ADDRESS EVEN BYTE, 250 MS DELAY
000782				*				
000783				*				
000784	03AB	9380	041A		LNJ	\$B1,<CRWB		COMPARE READ AND WRITE BLOCKS
000785	03AA	C870	0300		LDR	\$R4,=768		NUMBER OF BYTES TO CCB AREA
000786	03AC	CF00	14D7		STR	\$R4,<RANGEW		
000787				*				
000788	03AE	BEOO	14CD		SWR	\$R3,<TPR		GET WRITE SUBCH.
000789	03B0	9380	1069		LNJ	\$B1,<SDATA		WRITE DATA PATTERN
000790	03B2	1EBB			DC	<SDDB		
000791	03B3	0600			DC	1536		
000792	03B4	0800			DC	2048		
000793	03B5	8000			DC	Z'8000'		RAM ADDRESS EVEN BYTIE, 250 MS DELAY
000794				*				
000795	03B6	9380	0411		LNJ	\$B1,<SET		FILL READ AREA WITH SET PATTERN
000797				*				
000798	03B8	BEOO	14CD		SWR	\$R3,<TPR		GET READ SUBCH.
000799	03B9	9380	1098		LNJ	\$B1,<RDATA		READ DATA PATTERN FROM RAM
000800	03BC	2BA			DC	<RTD		
000801	03BD	0600			DC	1536		
000802	03BE	0800			DC	2048		
000803	03BF	8000			DC	Z'8000'		RAM ADDRESS EVEN BYTIE, 250 MS DELAY
000804				*				
000805	03C0	9380	041A		LNJ	\$B1,<CRWB		COMPARE READ AND WRITE BLOCKS
000807				*				
000808				*				
000809				*				
000810				*				
000811				*				FIND RAM LUCATION AND NUMBER OF BYTES FOR
000812				*				BLOCK READ AND WRITE
000813				*				
000814	03C2	C853			LDR	\$R4,=SR3		COPY SUBCH.
000815	03C3	4F20			MLV	\$R4,=X'20'		
000816	03C4	CF00	03FF		STR	\$R4,<Q5		DISTANCE FROM E00 TO CHANNEL CCP AREA
000817	03C6	CF00	0409		STR	\$R4,<Q6		FOR BLOCK READ AND WRITE
000818	03C8	C70	1240		ADD	\$R4,=X'1240'		
000819	03CA	CF00	03E2		STR	\$R4,<Q2		ADDRESS ABOVE CCBs IN USE
000820	03CC	CF00	03EC		STR	\$R4,<Q4		FOR BLOCK READ AND WRITE
000821	03CE	C270	1280		SUB	\$R4,=X'1280'		
000822	03D0	8254			NEG	=\$R4		
000823	03D1	CF00	03E1		STR	\$R4,<Q1		
000824	03D3	CF00	03EB		STR	\$R4,<Q3		STORE NUMBER OF BYTES FOR BLOCK
000825	03D5	4041			SOR	\$R4,1		READ AND WRITE BELOW CURRENT CHANNEL
000826	03D6	CF00	14D7		STR	\$R4,<RANGEW		FIND AND STORE NUMBER OF WORDS
000827				*				FOR BLOCK COMPARE
000828	03D8	BEOO	14CD		SWR	\$R3,<TPR		GET WRITE SUBCH.
000829	03DA	9800	03E1		LDR	\$R1,<Q1		GET RANGE
000830	03DC	1A80	03F5		BLEZ	\$R1,<MTM10		SKIP NEXT TEST FOR LAST LINE
000831				*				
000832	03DE	9380	1069		LNJ	\$B1,<SDATA		WRITE DATA TO RAM
000833	03E0	1EBB			DC	0		RANGE = TOP - CCB TOP
000834	03E1	0000			Q1	0		RAM ADDRESS, CCB TOP
000835	03E2	0000			Q2	0		EVEN BYTE, 250 MS DELAY
000836	03E3	8000			DC	Z'8000'		
000837				*				
000838				*				
000839				*				
000840	03E4	9380	0411		LNJ	\$B1,<SET		FILL READ BACK AREA WITH SET PATTERN
000841				*				
000842	03E6	BEOO	14CD		SWR	\$R3,<TPR		GET READ SUBCH.
000843	03E8	9380	1098		LNJ	\$B1,<RDATA		READ DATA PATTERN FROM RAM
000844	03EA	2BA			DC	<RTB		
000845	03E9	0000			Q3	0		RANGE = TOP - CCB TOP
000846	03EC	0000			Q4	0		RAM ADDRESS, CCB TOP
000847	03ED	8000			DC	Z'8000'		EVEN BYTE, 250 MS DELAY
000848				*				
000849	03EE	9380	041A		LNJ	\$B1,<CRWB		COMPARE READ AND WRITE BLOCKS
000851				*				
000852	03F0	D870	1240		LDR	\$R5,=X'1240'		SEE IF YOU HAVE TO TEST
000853	03F2	D900	03E2		CMR	\$R5,<Q2		BELLOW CURRENT CCB
000854	03F4	091A			BE	>TESM1		
000855				*				
000856				*				
000857				*				
000858	03F5	C800	03FF		MTM10	LDR	\$R4,<Q5	FIND RANGE IN WORDS FOR
000859	03F7	4041				SOR	\$R4,1	BLOCK COMPARE
000860	03F8	CF00	14D7			STR	\$R4,<RANGEW	
000861				*				
000862	03FA	BEOO	14CD		SWR	\$R3,<TPR		GET WRITE SUBCH.
000863	03FC	9380	1069		LNJ	\$B1,<SDATA		SEND DATA PATTERN TO RAM
000864	03FE	1EBB			DC	<SDDB		
000865	03FF	0000			Q5	0		
000866	0400	1200			DC	X'1200'		EVEN BYTE, 250 MS DELAY
000867	0401	6000			DC	Z'8000'		
000868				*				
000869				*				
000870	0402	9380	0411		LNJ	\$B1,<SET		FILL READ BACK AREA WITH SET PATTERN
000871				*				
000872	0404	BEOO	14CD		SWR	\$R3,<TPR		GET READ SUBCH.
000873	0406	9380	1098		LNJ	\$B1,<RDATA		READ DATA PATTERN FROM RAM
000874	0408	2BA			DC	<RTD		
000875	0409	0000			Q6	0		
000876	040A	1200			DC	X'1200'		
000877	040B	6000			DC	Z'8000'		RAM ADDRESS EVEN BYTE, 250 MS DELAY
000878				*				
000879				*				
000880	040C	9380	041A		LNJ	\$B1,<CRWB		COMPARE READ AND WRITE BLOCKS

000881			*	TESM1	LDR	\$R3,<TEMP1	GET TESTED CHANNEL
000882	040E	B800 14CF		JMP		\$B6	RETURN FROM SUBROUTINE
000883	0410	8386	*				
000884			*				
000885			*				
000886			*	SET	CALL	ZV\$F,RTB,DFLT,RANGEW	
	0411	FBC0 0003	X				
	0413	D380 0000					
	0415	OF80					
	0416	2BAA					
	0417	14BE					
	0418	14D7					
000887	0419	8381		*	JMP	\$B1	
000888			*				
000889			*				
000890			*				
000891			*				
000892			*	CRWB	CALL	ZV\$C,SDB,RTB,MASK,RANGEW,ERAK	
	041A	FBC0 0003	X				
	041C	D380 0000					
	041E	OF80					
	041F	1EBB					
	0420	2BAA					
	0421	14E1					
	0422	14D7					
000893	0423	14E3					
000894	0424	89C0 10BE		CMZ	ERAK		
000895	0426	090F		BE	>MBM		SEE IF ERROR PRESENT
000896	0427	C800 14D8		LDR	\$R4,<RAMAD		IF PRESENT HALT WITH
000897	0429	D800 14E5		LDR	\$R5,<ERAK+2		STARTING LOCATION OF BLOCK IN RAM
000898	042B	E800 14E4		LDR	\$R6,<ERAK+1		'IS' IN \$R5
000899	042D	F800 14E3		LDR	\$R7,<ERAK		'SHOULD BE' IN \$R6
000900	042F	F380 125C		LNJ	\$B7,<ERROR		LOCATION NUMBER OF BLOCK IN \$R7
	0431	FBF0 0001		CALL	ZV\$CU		RAM MEMORY ERROR
000901	0433	D380 0000	X	MBM	JMP	\$B1	CONTINUE TESTING FOR ERRORS
000902	0435	8381		*			
000903			*				
000904			*				
000905			*				
000906			*				
000907			*				
000908			*				
000909			*				
000910	0436	B870 4420		MNKT	LDR	\$R3,=A*D'	
000911	0438	BF00 1581			STR	\$R3,<ERMG+1	
000912					CALL	ZV\$1.ZV\$TC,TSTD	
	043A	FBC0 0003	X				
	043C	D380 0000					
	043E	OF80					
	043F	15A9					
000913	0440	8753			CL	=SR3	
000914	0441	C380 10FC		*	LNJ	\$B4,<GENITZ	INITIALIZE CHANNEL
000915			*				INITIALIZE
000916	0443	C380 10EC			LNJ	\$B4,<FLN	
000917	0445	8382		*	JMP	\$B2	
000918	0446	8751			CL	=SR1	
000919	0447	5C20			LDV	\$R5,=32	
000920	0448	UF00 14D5			STR	\$R5,<RANGE	
000921					CALL	ZV\$KD.ZV\$HM	
	044A	FBF0 0001	X				
	044C	D380 0000					
000922	044E	C800 155C			LDR	\$R4,<CONT1	
000923	0450	C380 1173			LNJ	\$B4,<CGSCH	
000924	0452	8187			IOLD	\$B7,\$R4,\$R5	
	0453	U054					
	0454	U055					
000925	0455	U703			BIOT	>\$+2+\$AF	
000926	0456	F380 125C			LNJ	\$B7,<ERROR	
000927	0458	C800 155E			LDR	\$R4,<CONT3	
000928	045A	C380 1173			LNJ	\$B4,<CGSCH	
000929	045C	8070 0037			I0	=55,\$R4	
	045E	U054					
	045F	U055					
000930	0460	U703			BIOT	>\$+2+\$AF	
000931	0460	F380 125C		*	LNJ	\$B7,<ERROR	
000932			*				
000933	0462	C380 1177			LNJ	\$B4,<CHCT	
000934	0464	UF82		*	B	>\$+2	
000935	0465	0400		*	DC	2*0400*	
000936			*				
000937	0466	C380 1205		*	LNJ	\$B4,<DLAY	
000938			*				
000939	0468	C380 1161		*	LNJ	\$B4,<INXT	
000940			*				
000941	046A	82D5		*	LB	=SR5,=Z*0001*	
	046B	0001		*			
000942	046C	0503			BBT	>IS64K	
000943	046D	F380 125C			LNJ	\$B7,<ERROR	
000944	046F	8382	X		I564K	\$B2	
000945			*		JMP		
000946			*				
000947			*				
000948			*				
000949			*				
000950			*				
000951			*				
000952			*				
000953			*				
000954	0470	B870 4520			LCTOB	\$R3,=A*D'	
000955	0472	BF00 1581				\$R3,<ERMG+1	
000956						ZV\$T.ZV\$TC,TSTE	
	0474	FBC0 0003	X				
	0476	D380 0000					
	0478	OF80					
	0479	15B7					
000957	047A	C380 10FC		*	LNJ	\$B4,<GENITZ	GIVE GENERAL INITIALIZE
000958			*				
000959	047C	8753			LCTB	CL	
000960	047D	C380 10EC			LNJ	=SR3	
000961	047F	8382		*	JMP	\$B2	
000962	0480	BF00 0538		*	STR	\$R3,<TSTCHN	
000963	0482	3B00 0485		*	BEVN	\$K3,<NLXCTA	
			*				

000964 0484 88D3 DEC =\$R3
 000965 0485 C380 0489 * SET UP TABLE TO BE SENT IN OUTPUT BYTE TO LCT
 000966 0487 0F80 04CD NXLCTA LNJ \$B4,<LCTGRB FILL LCT AREA WITH A PATTERN
 000967 0489 5CC0
 000973 0489 8751
 000974 048B A870 0100
 000975 048D C852
 000976 048E C451
 000977 048F CF10 1EBB
 000978 0491 8AD1
 000979 0492 AA70 0100
 000980 0494 5780 048D
 000981 0496 C870 0302
 000982 0498 8751
 000983 0499 A810 04AC
 000984 049B 8AD1
 000985 049C 2800 04A1
 000986 049E CF20 1EBB
 000987 04A0 OFF9
 000988 04A1 9870 8005
 000989 04A3 9F00 1EC0
 000990 04A5 9870 4025
 000991 04A7 9F00 1ED4
 000992 04A9 8700 1EFB
 000993 04AB 8384
 000994 04AC 0008
 001001 04AD 0009
 001002 04AE 0013
 001003 04AF 0021
 001004 04B0 0022
 001005 04B1 0023
 001006 04B2 0028
 001007 04B3 0029
 001008 04B4 000D
 001009 04B5 002D
 001010 04B6 0020
 001011 04B7 0035
 001012 04B8 0000
 001013 04B9 0016
 001014 04BA 0014
 001015 04BB 0015
 001016 04BC 0032
 001017 04BD 0040
 001018 04BE 0041
 001019 04BF 0045
 001020 04C0 0053
 001021 04C1 0055
 001022 04C2 0036
 001023 04C3 0037
 001024 04C4 FFFF
 001025 04C5 0000
 001026 RESV
 001027 04CD 9870 0017
 001028 04CF C870 0017
 001029 04D1 9B80 1EBB
 001030 04D3 CF52
 001031 04D4 2008
 001032 04D5 A454
 001033 04D6 AF5D
 001034 04D7 8AD4
 001035 04D8 9970 0020
 001036 04DA 0981 FFF8
 001037 04DC 8752
 001038 04DD AF11
 001039 04DE B380 0FE0
 001040 04E0 1EBB
 001041 04E1 0016
 001042 04E2 A870 0017
 001043 04E4 BB80 1EBB
 001044 04E5 9823
 001045 04E6 9F56
 001046 04E7 9570 FF00
 001047 04E8 9470 0037
 001048 04E9 C380 0EOC
 001049 04EA C380 11AB
 001050 04EE E570 FF00
 001051 04F0 E570 0003
 001052 04F1 0901 0003
 001053 04F2 D956
 001054 04F3 F380 125C
 001055 04F4 04F7 8AD2
 001056 04F5 F380 125C
 001057 04F6 04F8 A970 0020
 001058 04F7 8AD2
 001059 04F8 0981 FFE8
 001060 04F9 C800 0538
 001061 04FA 0500 0505
 001062 04FB 0500 4F20
 001063 0501 CA70 0017
 001064 0502 CF00 0509
 001065 0503 9380 1098
 001066 0504 0507 2BAA
 001067 0505 0508 0009
 001068 0506 0509 0000
 001069 0507 050A 8000
 001070
 001071
 001072
 001073
 001074 050B 2CF8
 001075 050C BBB0 1EDA
 001076 050E C823

DEC =\$R3
 * SET UP TABLE TO BE SENT IN OUTPUT BYTE TO LCT
 NXLCTA LNJ \$B4,<LCTGRB FILL LCT AREA WITH A PATTERN
 * DATA IS ASCENDING STARTING WITH ONE
 LCTGRB LDV \$R5,=-64 64 ENTRIES IN TABLE
 CL =\$R1 R1 TRACKS ADDRESS
 LDR \$R2,=X'100' R2 TRACKS DATA
 NXLCT LDR \$R4,=\$R2
 OR \$R4,=\$R1 FORM DATA AND ADDRESS
 STR \$R4,<SDB.\$R1
 INC \$R1
 ADD \$R2,=X'100' FORM NEXT DATA WORD
 BINC \$R2,<NXLCT DO NEXT VALUE
 * NOW DUMMY OUT CONTROL CHARACTERS
 LDR \$R4,=Z'0302'
 CL =\$R1
 NXLCT1 LDR \$R2,<NONO.\$R1 GET FORBIDDEN CHARACTER
 INC \$R1
 BLZ \$R2,<GRB1 BRANCH IF END OF LIST
 STR \$R4,<SDB.\$R2
 B >NXLCT1
 * SET DATA BUFFER POINTERS TO NON ZERO
 GRB1 LDR \$R1,=Z'8005'
 STR \$R1,<SDB+5
 LDR \$R1,=Z'4025'
 STR \$R1,<SDB+25
 CL <SDB+64
 JMP \$B4 GET OUT
 * TABLE OF CONTROL LOCATIONS
 NONO DC X'8' CHANNEL COMMAND REC
 DC X'9' CHANNEL CONTROL REC
 DC X'13'
 DC X'21'
 DC X'22'
 DC X'23'
 DC X'28'
 DC X'29'
 DC X'D'
 DC X'2D'
 DC X'2U'
 DC X'35'
 DC X'0'
 DC X'16'
 DC X'14'
 DC X'15'
 DC X'32'
 DC X'40'
 DC X'41'
 DC X'45'
 DC X'53'
 DC X'55'
 DC X'36'
 DC X'37'
 DC X'1'
 RESV 8,0 END OF LIST
 ROOM FOR EXPANSION
 *
 LCTZ LDR \$R1,=23 START SENDING FROM LCT 23
 LDR \$R4,=23
 LAB \$B1,<SDB SEND BUFFER
 LCTCR STR \$R4,=\$R2
 SOL \$R2,8
 OR \$R2,=\$R4
 STR \$R2,\$B1+\$R1 CREAT LCI BUFFER
 INC \$R4
 CMR \$R1,=32
 BNE LCTCR
 CL =\$R2
 STR \$R2,\$B1,\$R1
 LNJ \$B3,<SNLCT MARK END OF THE BUFFER
 DC <SDB SENT LCT BYTES TO RAM
 DC 22 BUFFER ADDRESS
 LDR \$R2,=23
 LAB \$B3,<SDB
 RBYTES LDR \$R1,\$B3-\$R2 ALIGN ADDRESS
 STR \$R1,=\$R6 IN BYTE DIRECTOR
 AND \$R1,=Z'FF00'
 OR \$R1,=X'0037'
 LNJ \$B4,<OUTLCT OUTPUT BYTE
 LNJ \$B4,<INBYTE INPUT LCI BYTE
 AND \$R6,=Z'FF00'
 CMR \$R5,=\$R6 STRIP TO DATA
 BE RBYTE1
 LNJ \$B7,<ERROR BRANCH IF DATA IS RIGHT
 RBYTE1 INC =\$R2 OR ELSE REPOP ERROR
 CMR \$R2,=32
 BNE RBYTES
 LDR \$R4,<TSTCHN
 BODD \$R4,<LCTE
 MLV \$R4,=X'0020'
 ADD \$R4,=23 CALCULATE LCT RAM ADDRESS
 STR \$R4,<LCTA START FROM LCI 23
 LNJ \$B1,<RDATA READ LCT 23 TO 31
 DC <RTD READ BUFFER
 DC 9
 LCTA DC 0
 DC =Z'8000'
 *
 * PACK THE UNPACKED DATA
 *
 LDV \$R2,=-8
 LAB \$B3,<SDB+31
 LCTC LDR \$R4,\$B3,\$R2

```

001077 050F C570 00FF      AND   $R4,=Z'00FF'
001078 0511 CF23           STR   $R4,$B3,$R2
001079 0512 27FC           BINC  $R2,>LCTC
001080 0513 5C08           LDV   $R5,=8
001081 0514 C380 11B6       LNJ   $B4,<PKHIN
001082 0516 1E12           DC    <SDB+23
001083 0517 1F1F           DC    <SDB+100
001084
001085
001086
001087
001088
001089 0518 8751           *
001090 0519 BB80 1F1F       CL    =$R1
001091 051D 9B80 2BAA       LAB   $B3,<SDB+100
001092 051D D813           LAB   $B1,<RTB
001093 051E E811           LCTX  LDR   $R5,$B3,$R1
001094 051F D956           LDR   $R6,$B1,$R1
001095 0520 0903           CMR   $R5,=$R6
001096 0521 F380 125C       BE    >$+2+$AF
001097 0523 8AD1           LNJ   $B7,<ERROR
001098 0524 9970 0003       INC   =$R1
001099 0526 0981 FFF6       CMR   $R1,=3
001100
001101
001102
001103
001104 0528 3B80 0531       BNE   LCTX
001105 052A C380 084E       BUDD $R3,<NOER5
001106
001107
001108 052C 8756           LNJ   $B4,<ILCTST      READ LCT STATUS
001109 052D D956           CMR   =$R6
001110 052E 0903           BE    $R5,=$R6
001111
001112 052F F380 125C       *     LOAD $R6 WITH 'SHOULD BE'
001113 0531 B800 0538       NOER5 LDR   $R3,<TSTCHN
001114 0533 8AD3           INC   =$R3
001115 0534 3D10           CMV   $R3,=16
001116 0535 0981 FF47       BNE   LCTB
001117 0537 8382           JMP   $B2
001118 0538 0000           TSTCHN DC   0
001119
001120
001121
001122
001123
001124
001125
001126 0539 9870 5349       SITEST LDR   $R1,=A'S1'
001127 053D 9F00 1581       STR   $R1,<ERMG+1      REPORT TEST
001128
001129
001130 0543 C380 1UFC       CALL  ZV$T,ZV$TC,TSTS1
001131
001132 0545 8753           X
001133 0546 C380 10EC       053D FBC0 0003
001134 0548 8382           053F D380 0000
001135
001136
001137
001138 0549 FBC0 0003       0541 0F80
001139
001140 0551 BF00 14CD       0542 15C5
001141 0553 3B80 0556       SIT1  LNJ   $B4,<GENITZ      INITIALIZE
001142 0555 8AD3           *     CL    =$R3
001143 0556 9380 1069       LNJ   $B4,<FLN      FIND LINE NUMBER
001144 0558 1E8B           JMP   $B2      NO LINES
001145 0559 0400           *
001146 055A 0300           *     FILL WRITE BUFFER WITH DATA (8408)
001147 055B 8000           *     CALL  ZV$T,SDB,CCITT,BUFRNG
001148
001149
001150
001151
001152
001153 055C 9380 1069       *     WRITE OUT CHANNEL PROGRAM
001154 055E 05AA           SIT9  STR   $R3,<TPR
001155 055F 0008           BUDD $R3,<SIT4
001156 0560 0200           INC   =$R3
001157 0561 0000           *     GET WRITE CHANNEL
001158
001159
001160
001161 0562 B380 0FCC      SIT4  LNJ   $B1,<SDATA
001162 0564 05A5           DC    <SDB
001163
001164
001165
001166 0565 B800 14CD      SIT9  LNJ   $B1,<TEST1
001167 0567 B380 0628      DC    <(TEST1-TEST)*2
001168 0569 C380 1205      SIT10 LNJ   X'200'
001169 056B 9B80 05AE      SIT10 DC    X'400'
001170 056D 8752           SIT10 DC    X'300'
001171 056E 986D           SIT10 DC    Z'8000'
001172 056F C380 0E0C      SIT11 LNJ   $B1,<TSVLU
001173 0571 C380 11A8      SIT11 LNJ   $B2
001174 0573 5903           SIT11 LDR   $R1,$B1+$R2
001175 0574 F380 125C      SIT11 LNJ   $B4,<OUTLCT
001176 0576 2D04           SIT11 LNJ   $B4,<INBYTE
001177 0577 09F7           SIT11 BEZ  $R5,>SIT5
001178
001179
*     INPUT BYIE
*     BRANCH IF CLEARED
*     LCT NOT CLEARED
*     CHECK FOR LAST IN TABLE
*     TEST SOFTWARE SOFT INITIALIZE

```

001180
 001181 0578 C380 0E25 *
 001182 057A E870 0204 LNJ \$B4,<RPVLU READ P VALUE
 001183 057C D956 LDR \$R6,=X'204'
 001184 057D 0900 0581 CMR \$R5,=\$R6
 001185 057F F380 125C BE <S117
 001186 0581 B800 14CD LNJ \$B7,<ERROR CCP SOFT INIT FAILURE
 001187 0582 3800 0586 SIT7 LDR \$R3,<TPK
 001188 0583 88D3 BEVN \$R3,<S111
 001189 0584 DEC \$R3
 001190 0585 88D3 *
 001191 0586 9380 1098 SIT11 LNJ \$B1,<RDATA READ DATA
 001192 0588 2BA0 DC <RTB TO HERE
 001193 0589 0400 DC X'400'
 001194 058A 0300 DC X'300'
 001195 058B 8000 DC Z'8000'
 001196 058C 8700 14E1 *
 001197 058E 9870 0200 LDR <MASK RANGE IN WORDS
 001198 0590 9F00 1407 STR \$R1,<RANGEW COMPARE DATA
 001199 0592 9380 041A LNJ \$B1,<CRWB CHANNEL CONTROL
 001200 0594 C380 1177 SIT10 LNJ \$B4,<CHCT
 001201 0596 0F82 B >\$+2 CCB LIST RESET
 001202 0597 0100 DC X'100'
 001203 0598 8AD3 INC =\$R3 GET WRITE CHANNEL
 001204 0599 C380 1177 LNJ \$B4,<CHCT CHANNEL CONTROL
 001205 059A 0F82 B >\$+2 CCB LIST RESET
 001206 059B 0100 DC X'100'
 001207 059C 8AD3 *
 001208 059D B800 14CD LDR \$R3,<TPK
 001209 059E 8AD3 INC =\$R3
 001210 05A0 C380 10EC LNJ \$B4,<FLN
 001211 05A2 8382 JMP \$B2 FIND NEXT LINE NUMBER
 001212 05A3 OF80 0551 B <S119 NO MORE LINES, DONE
 001213 05A4 8AD3 *
 001214 05A5 4008 IZLCT FOR THIS TEST
 001215 05A6 4028 DC Z'4008' DATA SET SCAN
 001216 05A7 0206 DC Z'4028'
 001217 05A8 0226 DC Z'0206'
 001218 05A9 0000 DC Z'0226'
 001219 05AA 01BD *
 001220 05AB 31AO IZLCT DC Z'01BD' NOP, INZ
 001221 05AC 01BD DC Z'31AO' NOP, WAIT
 001222 05AD 3100 DC Z'01BD'
 001223 05AE 3100 DC Z'3100'
 001224 05AF 0000 *
 001225 05B0 2837 ITST1 EQU \$
 001226 05B1 2937 *
 001227 05B2 01BD *
 001228 05B3 3100 PROG1 DC Z'01BD' NOP, WAIT
 001229 05B4 01BD DC Z'3100'
 001230 05B5 2837 DC Z'01BD'
 001231 05B6 3100 DC Z'3100'
 001232 05B7 20FE DC Z'20FE' B -2
 001233 05B8 0000 *
 001234 05B9 BF00 1581 CHPGT LDR \$R3,=A'F' INITIALIZE CHANNEL NUMBER
 001235 05B0 FBC0 0003 STR \$R3,<ERMG+1 GENERAL INITIALIZE
 001236 05B1 D380 0000 CALL ZV\$1.ZV\$TC,TS1F
 001237 05B2 0F80 X
 001238 05B3 15D0
 001239 05B4 10FC LNJ =\$R3
 001240 05B5 8753 LDR \$B4,<GENITZ
 001241 05B6 8382 *
 001242 05B7 10EC LNJ \$B4,<FLN FIND LINE ON
 001243 05B8 8382 JMP \$B2 NO LINE ON JUMP OUT OF TEST
 001244 05B9 05CA BUDD \$R3,<CHPGT2 GET WRITE SUBCH.
 001245 05C0 15D0 INC =\$R3
 001246 05C1 8753 CHPGT2 CL =\$R1 CLEAR INDEX FOR OUTPUT ADDRESS
 001247 05C2 C380 10FC STR \$R3,<TPK STORE SUBCH.
 001248 05C3 E870 0800 LDR \$R6,=Z'0800'
 001249 05C4 0F80 STR \$R6,RNG NOP BUFFER RANGE
 001250 05C5 14CD CALL ZV\$F,SDB,PNOOP,RNG STORE AWAY
 001251 05C6 0800 *
 001252 05C7 1EB8
 001253 05C8 1EBF
 001254 05C9 14BF
 001255 05D0 14C0
 001256 05D1 FBC0 0003 LNJ \$B1,<SDATA SEND PATTERN TO RAM
 001257 05D2 D380 0000 DC <SD0 BUFFER ADDRESS
 001258 05D3 0F80 DC X'1000'
 001259 05D4 1069 DC X'200'
 001260 05D5 1EB8 DC Z'8000'
 001261 05D6 1EBF *
 001262 05D7 14BF
 001263 05D8 14C0
 001264 05D9 9380 1069 LNJ \$B1,<RDATA WRITE CHANNEL PROGRAM
 001265 05DA 1EB8 DC <PRUG1
 001266 05DB 1000 DC X'000A'
 001267 05DC 0200 DC X'11F6'
 001268 05DD 8000 DC 0 EVEN BYTE BOUNDARY
 001269 05DE 8000 *
 001270 05DF 9380 1069 LNJ \$B1,<SDATA
 001271 05E0 1EB8 DC <SD0
 001272 05E1 000A DC X'000A'
 001273 05E2 11F6 DC X'11F6'
 001274 05E3 0000 DC 0
 001275 05E4 0000 *
 001276 05E5 0000
 001277 05E6 0000
 001278 05E7 0000
 001279 05E8 0000
 001280 05E9 0000
 001281 05EA 0000
 001282 05EB 0000

001283
 001284
 001285 05E5 B380 0FCC * LNJ \$B3,<SETLCT
 001286 05E7 0637 DC <CP14 SEND LCT OUT FOR THIS CHANNEL
 001287
 001288 05E8 C380 1205 * LNJ \$B4,<DLAY
 001289 05EA B380 0628 LNJ \$B3,<STRATIO START IO
 001290
 001291
 001292 05EC E870 0200 LDR \$R6,=Z'0200'
 001293 05EE CF00 14D4 STR \$R4,<IRG
 001294 05F0 9870 FF20 LDR \$R1,=-224
 001295 05F2 0F03 NP4 NOP >\$+2+\$AF
 001296 05F3 1780 05F2 BINC \$R1,<NP4
 001297 05F5 C380 1177 LNJ \$B4,<CHCT
 001298 05F7 0F82 B >\$+2
 001299 05F8 2000 DC Z'2000'
 001300
 001301 05F9 C380 1205 * LNJ \$B4,<DLAY
 001302 05FD C380 0E25 LNJ \$B4,<RPVLU
 001303
 001304 05FD D956 CMR \$R5,=\$R6
 001305 05FF 0303 BG >NP5
 001306 05FF F380 125C LNJ \$B7,<ERROR
 001307
 001308 0601 E870 1200 NP5 LDK \$R6,=X'1200'
 001309 0603 D956 CMR \$R5,=\$R6
 001310 0604 0203 BL >NP6
 001311 0605 F380 125C LNJ \$B7,<ERROR
 * START CHANNEL PROGRAM AGAIN
 * RESET P TO START AT LOC WHICH RESETS START I/O BIT
 *
 001316 0607 B380 0FCC NP6 LNJ \$B3,<SETLCT
 001317 0609 0636 DC <CP15
 001318 060A B380 0628 LNJ \$B3,<STRATIO GIVE START IO
 001319 060C 1CB0 LDV \$R1,=-80
 001320 060D 0F03 NP7 NOP >\$+2+\$AF
 001321 060E 1780 060D BINC \$R1,<NP7
 * DELAY 10 LET CHANNEL PROGRAM START
 * GIVE STOP I/O FOR OTHER CHANNEL. SHOULDN'T STOP PROGRAM.
 *
 001325 0610 88D3 DEC =\$R3
 001326 0611 C380 1177 LNJ \$B4,<CHCT DO CHANNEL CONTROL
 001327 0613 0F82 B >\$+2
 001328 0614 2000 DC Z'2000'
 *
 * GIVE OUTPUT BYTE COMMANDS TO MAKE SURE THEY DON'T STOP CHANNEL PROG.
 *
 001333 0615 8AD3 INC =\$R3
 001334 0616 B380 0FCC LNJ \$B3,<SETLCT
 001335 0618 0633 DC <CP12
 * SAME AS BEFORE
 * NOW GIVE INPUT DATA SET STATUS TO CHECK IT WON'T TERMINATE CHAN
 * PROGRAM
 *
 001340 0619 C380 0ECD LNJ \$B4,<DSSTA
 001341 061B C380 1205 LNJ \$B4,<DLAY
 * READ P TO TEMP
 001342 061D C380 0E25
 *
 001345 061F E870 11FA LDR \$R6,=Z'11FA'
 001347 0621 CF00 14D4 STR \$R4,<IRG
 001348 0623 D956 CMR \$R5,=\$R6
 001349 0624 0903 BE >\$+2+\$AF
 001350 0625 F380 125C LNJ \$B7,<ERROR
 001351 0627 8382 JMP \$B2
 * COUNT WRONG. BAD PROGRAM EXECUTION
 * DONE WITH TEST
 *
 * START IO
 *
 001355 0628 C800 1562 STRTIO LDK \$R4,<CONT7
 001356 062A C380 1173 LNJ \$B4,<CGSCH
 001357 062C B070 4000 IO =Z'4000',=\$R4
 * GET FUNC CODE
 *
 001358 062F 0703 BIOT >\$+2+\$AF
 001359 0630 F380 125C LNJ \$B7,<ERROR
 001360 0632 8383 JMP \$B3
 * COMMAND WAS NAK'D
 *
 001361 0633 0105 CPT2 DC Z'0105'
 001362 0634 0125 DC Z'0125'
 001364 0635 0000 DC
 * RCV PAUSE DISABLE
 * XMIT PAUSE DISABLE
 *
 001365
 001366 0636 2827 CPT5 DC X'2827'
 001367 0637 0226 CPT4 DC X'0226'
 001368 0638 0000 DC
 * RESTART AT 22d
 * P,MSB,XMIT
 * END OF LIST
 *
 *-----
 *
 * INSTRUCTION TEST
 *
 001370
 001371
 001372
 001373
 001374
 001375 0639 B870 4720 INSTT LDR \$R3,=A'G'
 001376 063B BF00 1581 STR \$R3,<ERMG+1
 001377 U63D FBC0 0003 CALL ZV\$T,ZV\$TC,TSTG
 *
 001378 0641 0F80 CL =\$R3
 001379 0642 15D8
 001380 0643 8753
 *
 001381 0644 C380 10EC INSLUP LNJ \$B4,<FLN
 001382 0646 8382 JMP \$B2
 001383 0647 BF00 14CD STR \$R3,<TPK
 001384 0649 BF00 14CF STR \$R3,<TEMP1
 001385 064B C380 10FC LNJ \$B4,<GENITZ
 001386 064D 3882 BUDD \$R3,>INS2
 001387 064E 8AD3 INC =\$R3
 001388 064F 8751 CL =\$R1
 001389 0650 CB70 0364 LDR \$R4,=X'0364'
 001390 0652 CF00 14E2 STR \$R4,<MASK1
 *
 *
 * INITIALIZE CHANNEL NUMBER
 * FIND LINE ON
 * NO LINE ON JUMP OUT OF TEST
 * STORE SUBCH. NUMBER
 * MLCC GENERAL INITIALIZATION
 * GET WRITE SUBCH.
 * CLEAR INDEX FOR OUTPUT ADDRESS
 * LOAD MASK1 WITH EXPECTED
 * P COUNTER

001391
 001392
 001393
 001394
 001395
 001396 0654 9380 1069 *
 001397 0656 1291
 001398 0657 00EE
 001399 0658 0200
 001400 0659 0000
 001401
 001402
 001403 065A 9380 1069
 001404 065C 1308
 001405 065D 0076
 001406 065E 02EE
 001407 065F 0000
 001408
 001409
 001410 0660 9380 1069
 001411 0662 1343
 001412 0663 0060
 001413 0664 036A
 001414 0665 0000
 001415
 001416 0666 9380 1069
 001417 0668 1373
 001418 0669 0008
 001419 066A 03C8
 001420 066B 0000
 001421
 001422 066C 9380 1069
 001423 066E 1377
 001424 066F 000A
 001425 0670 0300
 001426 0671 0000
 001427
 001428 0672 9380 1069
 001429 0674 137C
 001430 0675 000A
 001431 0676 03DA
 001432 0677 0000
 001433
 001434
 001435
 001436
 001437 0678 9380 1069 *
 001438 067A 06B3
 001439 067B 0004
 001440 067C 0402
 001441 067D 0000
 001442
 001443
 001444
 001445 067E b380 UFCC
 001446 0680 06A5
 001447 0681 4C01
 001448 0682 C380 1189
 001449 0684 2BAA
 001450 0685 0040
 001451 0686 b380 0628
 001452
 001453
 001454 0688 C380 1215
 001455
 001456 068A C853
 001457 068B 4F20
 001458 068C E006
 001459 068D CF00 0694
 001460 068F 8803
 001461
 001462 0690 9380 1098
 001463 0692 14D7
 001464 0693 0002
 001465 0694 0000
 001466 0695 8000
 001467
 001468
 001469 0696 D800 14D7
 001470 0698 D570 0FFF
 001471 069A E800 14E2
 001472 069C D956
 001473 069D 0903
 001474 069E F380 125C
 001475 06A0 B800 14CD
 001476 06A2 8A03
 001477 06A3 0F81 FFAU
 001478
 001479
 001480
 001481 06A5 0226
 001482 06A6 EE27
 001483 06A7 FF03
 001484 06A8 AA1C
 001485 06A9 552C
 001486 06AA AA3F
 001487 06AB 0437
 001488 06AC 0238
 001489 06AD 0105
 001490 06AE 0125
 001491 06AF FF17
 001492 06B0 0000
 001493 06B1 0000
 001494 06B2 0000
 001495
 001496
 001497
 001498 06B3 031A
 001499 06B4 8500
 001500
 001501
 001502
 001503

* PREPARE AND LOAD RAM INSTRUCTIONS

LNJ \$B1,<SDATA
 DC <INSTR1
 (INSTR2-INSTR1)*2
 X'200'
 DC 0

RAM ADDRESS
CPU HAS RIGHT BYTE START

LNJ \$B1,<SDATA
 DC <INSTR2
 (INSTR3-INSTR2)*2
 X'02EE'
 DC 0

RAM ADDRESS
EVEN BYTE START

LNJ \$B1,<SDATA
 DC <INSTR3
 (INSTR4-INSTR3)*2
 X'36A'
 DC 0

RAM ADDRESS
EVEN BYTE START

LNJ \$B1,<SDATA
 DC <INSTR4
 (INSTR5-INSTR4)*2
 X'3C8'
 DC 0

SEND DATA
RANGE
RAM ADDRESS
EVEN BYTE IN CPU

LNJ \$B1,<SDATA
 DC <INSTR5
 (INSTR6-INSTR5)*2
 X'3D0'
 DC 0

SEND DATA
RANGE
RAM ADDRESS
EVEN BYTE IN CPU

LNJ \$B1,<SDATA
 DC <INSTR6
 (TRANM1-INSTR6)*2
 X'3DA'
 DC 0

SEND DATA
RANGE
RAM ADDRESS
EVEN BYTE IN CPU

* SEND OUT LOOKUP TABLE FOR INSTRUCTION TEST

LNJ \$B1,<SDATA
 DC <TALU
 4
 DC X'402'
 DC 0

SEND PROGRAM
RANGE OF 4 BYTES
RAM ADDRESS
STARTS ON EVEN BOUNDARY

LNJ \$B3,<SETLCT
 DC <LCITAB
 \$R4,=1

SEND LCT OUT

LNJ \$B4,<MCBC
 DC <RTD
 \$R4,=0

DUMMY LCB TO MAKE LAST CHARA FALSE

LNJ \$B3,<STRTIO
 DC =\$R3

START IO

LNJ \$B4,<DELAYLG
 DC =\$R4,=\$R3

TIME DELAY

LDR \$R4,=\$R3
 MLV \$R4,X'20'
 ADV \$R4,=6

FIND RAM ADDRESS TO READ P COUNTER

STR \$R4,<INS1
 DEC =\$R3

GET READ SUBCH.

LNJ \$B1,<RDATA
 DC <RANGEW
 Z

READ P COUNTER BACK

INS1 DC 0
 DC Z'8000'

500 MS. DELAY

LDR \$R5,<RANGEW
 AND \$R5,X'FFF'
 LDR \$R6,<MASK1
 CMR \$R5,=\$R6
 BE >NO1NF

GET SHOULD BE DATA
SEE IF P COUNTER RIGHT?

NOINF LNJ \$B7,<ERROR
 LDR \$R3,<TPR
 INC =\$R3
 B INSUP

IF NOT EQUAL HALT
INSTRUCTION TEST ERROR \$R5 CONTAINS PCOUNTER
GET BACK TESTED CHANNEL
BUMP CHANNEL NUMBER
DO FOR NEXT CHANNEL

* LCT TABLE FOR THIS TEST

LCTTAB DC Z'0226'
 DC Z'EE27'
 DC Z'FF03'
 DC Z'AA1C'
 DC Z'552C'
 DC Z'AA3F'
 DC Z'0437'
 DC Z'0238'
 DC Z'0105'
 DC Z'0125'
 DC Z'FF17'
 DC 0

LCT CCP POINTER (MSB)
CCP POINTER (LSB)
ADDRESS TESTING VALUE
DITTO
DITTO
DITTO
TLU POINTER, MSB
TLU POINTER, LSB

MARKS END OF TABLE

* TABLE FOR TABLE LOOKUP TEST

TALU DC X'031A'
 DC Z'8500'

* OUTPUT INTERRUPT CONTROL TEST

001504 06B5 B870 4820
 001505 06B7 BF00 1581
 001506 06B9 FBC0 0003
 001506 06B0 0000 X
 001506 06B0 OF80
 001506 06B0 15E4
 001507 06BF 8753
 001508 06C0 C380 10EC
 001509 06C2 8382
 001510 06C3 8C56
 001511 06C4 E570 UFC0
 001512 06C6 E3F
 001513 06C7 C380 10FC
 001514 06C9 C800 1565
 001515 06CB C380 1173
 001516 06CD 8056
 001516 06CE 0054
 001517 06CF 0703
 001518 06D0 F380 125C
 001519 06D1 0000
 001520 06D2 C853
 001521 06D3 4F20
 001522 06D4 4E0C
 001523 06D5 CF00 06E0
 001524 06D7 BF00 14CD
 001525 06D9 3B00 06DC
 001526 06DB 88D3
 001527 06DC 9380 1098
 001528 06DE 14CE
 001529 06DF 0002
 001531 06E0 0000
 001534 06E1 0000
 001533 06E2 B800 14CD
 001534 06E4 B800 14CE
 001535 06E6 0956
 001537 06E7 0903
 001538 06E8 F380 125C
 001539 06EA 8AD3
 001540 06EB OF81 FFD4
 001541
 001542
 001543
 001544
 001545
 001546
 001547
 001548 06ED B870 4A20
 001549 06EF BF00 1581
 001550 06F1 FBC0 0003
 001550 06F3 D380 0000 X
 001550 06F5 OF80
 001550 06F6 15F4
 001551 06F7 8753
 001552 06F8 C380 10EC
 001553 06FA 8382
 001554 06FB C380 0489
 001555 06FD C800 155C
 001556 06FF C380 1173
 001557 0701 CF52
 001558 0702 C800 1563
 001560 0704 C380 1173
 001561 0706 8070 8000
 001562 0708 0054
 001563 0709 0703
 001563 070A F380 125C
 001564 070C 81C0 0DC1
 001565 070E 0052
 001565 070F 0070 0001
 001566 0711 F380 125C
 001567 0712 F380 125C
 001568 0714 8070 8000
 001569 0716 0054
 001570 0717 0783
 001571 0718 F380 125C
 001572 071A C380 1215
 001573 071C 9870 0100
 001574 071E 9F00 14D5
 001575 0720 FBC0 0003
 001576 0722 D380 0000 X
 001577 0724 OF80
 001578 0725 2BAA
 001579 0726 14BE
 001580 0727 14D5
 001581 0728 C380 07C3
 001582 072A BB00 14CD
 001583
 001584
 001585
 001586
 001587 072C C380 0489
 001588 072E C870 0302
 001589 0730 CF00 1ECC
 001590 0732 CF00 1ECC
 001591 0734 CF00 1ECC
 001592 0736 CF00 1ECC
 001593 0738 CF00 1ECC
 001594 073A CF00 1ECC
 001595 073C B380 OFEO
 001596 073E 1EBB
 001597 073F FFFF

INCONT LDR SR3,=A'H'
 STR SR3,<ERMG+1
 CALL ZV\$T.ZV\$TC,TSTH

ICON3 CL =\$R3
 LNJ SB4,<FLN
 JMP SB2
 STS =\$R6
 AND SR6,=X'FC0'
 ADV SR4,=63
 LNJ SR4,<GEN1T4
 LDR SR4,<CONT1
 LNJ SR4,<CGSCH
 IO =\$R6,=\$R4

R3 TRACKS CHANNEL
 FIND ACTIVE LINE NUMBER
 NO MORE LINES
 GET S REG
 STRIP TO RETURN CHANNEL NUMBER
 SET FOR LEVEL 63
 GENERAL INITIALIZE
 OUTPUT INTERRUPT CONTROL FUNCTION
 MODIFY FOR CHANNEL
 OUTPUT INTERRUPT CONTROL

BIOT LNJ >\$+2+\$AF
 SB7,<ERROR

COMMAND WAS NAK'D

LDR SR4,=\$R3
 MLV SR4,=X'20'
 ADV SR4,=X'C'
 STR SR4,<ICON2
 STR SR3,<IPK
 BEVN SR3,<ICON1
 DEC =\$R3

OFFSET FOR CHANNEL
 OFFSET FOR RETURN CHANNEL

* ICON1 LNJ SB1,<RDATA
 DC <TEMP
 DC 2
 ICON2 DC 0

READ TU HERE
 2 BYTES OF DATA
 RAM ADDRESS
 EVEN BYTE ADDRESS

* LDR SR3,<TPR
 LDR SR5,<TEMP
 CMR SR5,=\$R6
 BE >\$+2+\$AF
 LNJ SB7,<ERROR
 INC =\$R3
 B ICON3

RESTORE R3
 CHECK IS VS SD
 ERROR, OUTPUT INTERRUPT CONTROL
 SET FOR NEXT CHANNEL
 DO NEXT CHAN

*-----

* INITIALIZE AND CHANNEL RESET TEST
 * FIRST GIVE GEN INITIALIZE + CHECK IT CLEARS LCT AREA

I2TST LUR SR3,=A'J'
 STR SR3,<ERMG+1
 CALL ZV\$T.ZV\$TC,TSTJ

I2TST6 CL =\$R3
 LNJ SB4,<FLN
 JMP SB2

R3 TRACKS CHANNEL
 FIND ACTIVE LINES
 NO MORE ACTIVE LINES

* LNJ SB4,<LCTGRB
 LDR SR4,<CONT1
 LNJ SB4,<CGSCH
 STR SR4,=\$R2
 LDR SR4,<CONT9
 LNJ SB4,<CGSCH
 IO =Z'8000*,=\$R4

SEND GARBAGE TO LCT AREA
 HOLD FUNCTION CODE
 MODIFY FOR CHANNEL

I2TLD TEMP,=\$R2,=X'1'
 I2LD

INITIALIZE FUNCTION CODE
 INITIALIZE

BIOT LNJ >\$+2+\$AF
 SB7,<ERROR

INITIALIZE WAS NAK'D

* I2LD TEMP,=\$R2,=X'1'
 I2LD

GIVE ANOTHER INSTRUCTION

BIOT LNJ >\$+2+\$AF
 SB7,<ERROR

SHOULD BE NAK'D UNTIL INITIALIZE DONE

I2LD =Z'8000*,=\$R4

GIVE ANOTHER INITIALIZE

BIOT LNJ >\$+2+\$AF
 SB7,<ERROR

INITIALIZE DID NOT NAK'D

* LNJ SB4,<DLAYLG
 SET DEFAULT VALUE IN READ BUFFER
 DELAY 25 MS *****
 LDR SR1,=X'100'
 STR SR1,<RANGE
 CALL ZV\$F,RTB,DLFT,RANGE

*-----

* READ IN LCT AREA FOR THIS CHANNEL AND CHECK
 LNJ SB4,<BRLCLR
 LDR SR3,<TPR

CHECK LCI IS CLEAR
 RESTORE CHAN NUMBER

*-----

* CHANNEL INITIALIZE TEST

*-----

* SEND OUT GARBAGE TO LCT AREA

*-----

LNJ SB4,<LCTGRB
 LDR SR4,=Z'0302'
 STR SR4,<SDB+16
 STR SR4,<SDB+17
 STR SR4,<SDB+5
 STR SR4,<SDB+48
 STR SR4,<SDB+49
 STR SR4,<SDB+37
 LNJ SB3,<SNLCT
 DC <SDB
 DC -1

SEND OUT LCT BYTES

001598
 001599
 001600 0740 9380 080C
 001601 0742 9380 07F1
 001602 0744 C800 1562
 001603 0746 C380 1173
 001604 0748 8070 8000
 001605 074A 0054
 001607 074B 0703
 001608 074C F380 125C
 001609 074E C380 1205
 001610 0750 BF00 14CD
 001611 0752 3120
 001612 0753 BA70 0E00
 001613 0755 BF00 075F
 001614 0757 B800 14CD
 001615 0759 BB53
 001616 075A 0001
 001617 075B 9380 1098
 001618 075D 2BA4
 001619 075E 0020
 001620 075F 0000
 001621 0760 0000
 001622 0763 1CF5
 001623 0764 2C05
 001624 0765 0A00 2BAA
 001625 0766 5903
 001626 0767 F380 125C
 001627 0768 2E08
 001628 0769 17FA
 001629 076A B800 14CD
 001630 076B 0054
 001631 076C C800 155F
 001632 076D C380 1173
 001633 076E 0055
 001634 076F 0054
 001635 0770 0055
 001636 0771 0054
 001637 0772 0783
 001638 0773 F380 125C
 001639 0774 0F07
 001640 0775 0F07
 001641 0776 9380 080C
 001642 0777 9380 07F6
 001643 0778 9380 07F1
 001644 0779 9380 080C
 001645 0780 9380 07F6
 001646 0781 0002
 001647 0782 07C0
 001648 0783 C800 1562
 001649 0784 C380 1173
 001650 0785 8070 0100
 001651 0786 0054
 001652 0787 F380 125C
 001653 0788 0055
 001654 0789 9380 080C
 001655 0790 9380 07F6
 001656 0791 4C45
 001657 0792 CF00 14D7
 001658 0793 9380 0411
 001659 0794 C380 07C3
 001660 0795 0001
 001661 0796 0F81 0002
 001662 0797 07C0
 001663 0798 C380 1177
 001664 0799 0F82
 001665 0800 0100
 001666 0801 0000
 001667 0802 0000
 001668 0803 0000
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 001701 0836 0000
 001702 0837 0000
 001703 0838 0000
 001704 0839 0000
 001705 0840 0000
 001706 0841 0000

* NOW SET UP 4 CCB'S TO INSURE INITIALIZE CLEARS IOLD POINTER
 LNJ \$B1,<SET4
 * ADVANCE INPUT NEXT STAT POINTER A COUPLE OF TIMES
 LNJ \$B1,<ADV2
 * NOW GIVE CHANNEL INITIALIZE
 LDR \$R4,<CONT7
 LNJ \$B4,<CGSCH CHANNEL CONTROL
 IO =Z*6000*,=\$R4 MODIFY FOR CHANNEL
 BIOT >\$+2+\$AF
 LNJ \$B7,<ERROR OUTPUT CHAN CNT NAK'ED
 LNJ \$B4,<DLAY DELAY 25 MS
 * USE OTHER SIDE OF LINE TO BLOCK READ CCB AREA + CHECK
 STR \$R3,<TPR
 MLV \$R3,=X'20'
 ADD \$R3,=Z'0E00'
 STR \$R3,<CADD ADDRESS OF START OF CCB AREA
 LDR \$R3,<TPR
 LBC =\$R3,=Z'0001' GET OTHER SIDE OF LINE
 *
 LNJ \$B1,<RDAT4 BLOCK READ
 DC <RTD RETURN BUFFER
 DC X'20' 20 BYTE RANGE
 CADD DC 0 ADDRESS
 DC 0 EVEN BYTE BOUNDARY
 * CHECK CONTROL BYTES WERE CLEARED
 LDV \$R1,=-4
 LDV \$R2,=5
 LUP LSH \$R5,<RTB,\$R2 GET CONTROL BYTE
 DEZ \$R5,>LUP1 BRANCH IF GOOD
 LUP1 LSH \$B7,<ERROR CHAN INIT. DUN'T RESET DATA POINTERS IN LR 5
 ADV \$R2,=8 SET FOR NEXT CONTROL BYTE
 BINC \$R1,>LUP DO FOR NEXT
 LDR \$R3,<TPR GET BACK TESTED SUBCHANNEL
 * CHECK INPUT NEXT STATUS WILL BE NAK'ED AFTER CHANNEL INITIALIZE
 *
 LDV \$R4,<CONT4
 LNJ \$B4,<CGSCH GIVE INPUT NEXT STATUS COMMAND
 IO =\$R5,=\$R4
 *
 BIOT >\$+2+\$AF SHOULD BE NAK'ED
 LNJ \$B7,<ERROR COMMAND WASN'T NAK'ED
 NOP <EC02
 * SET UP 4 CCB'S AGAIN TO CHECK LOAD POINTER WAS RESET
 EC03 LNJ \$B1,<SET4
 LNJ \$B1,<CHKRNG CHECK STATUS POINTER IS CORRECT
 LNJ \$B1,<ADV2 ADVANCE POINTER 2
 * TEST RESET CCB LIST COMMAND
 *
 EC02 B ECO4 SET DATA BUFF POINTER'S NON ZERO
 <DTP1
 EC04 LDR \$R4,<CONT7 CHANNEL CONTROL
 LNJ \$B4,<CGSCH MODIFY FOR CHANNEL
 IO =Z'0100*,=\$R4 RESET CCB LIST
 BIOT >\$+2+\$AF
 LNJ \$B7,<ERROR COMMAND WAS NAK'ED
 * SET UP 4 CCB'S AGAIN TO CHECK IF CCB RESET WORKS
 *
 LNJ \$B1,<SET4 SET UP 4 CCB'S
 LNJ \$B1,<CHKRNG CHECK INPUT STAT POINTER IS RESET
 * NOW CHECK CHANNEL ECT AREA WAS CLEARED BY INITIALIZE
 *
 LNJ \$B4,<CHCT
 B >\$+2
 DC X'100' RESET CCB LIST
 *
 SET UP DEFAULT
 *
 LDV \$R4,=X'45'
 STR \$R4,<RANGEW
 LNJ \$B1,<SET
 LNJ \$B4,<BRCLKL CHECK LCI AREA FOR CHAN IS CLEAR
 * TEST BVB7 + BVB8 COMMANDS
 *
 LNJ \$B4,<GENITZ GENERAL INITIALIZE
 *
 LNJ \$B1,<SDATA SEND CHANNEL PROGRAM
 <VALTST ADDRESS
 DC (ECP-VALTST)*2 RANGE
 0DC X'200' RAM ADDRESS
 DC 0 EVEN CP ADDRESS
 * SET UP RCV, XMII FOR P START AT 200
 *
 LNJ \$B3,<SETLCT
 DC <IZL
 *
 LNJ \$B1,<SET4 SEND OUT 4 CCB'S WITH VALID BITS
 LNJ \$B3,<STRTIO START CHANNEL PROGRAM
 LNJ \$B4,<DLAY CHANNEL CONTROL
 LNJ \$B4,<CHCT
 B >\$+2
 DC X'100' RESET CCB LIST
 * START SECOND HALF OF CHANNEL PROGRAM
 *
 LNJ \$B3,<STRTIO
 LNJ \$B4,<DLAY
 * READ P TO INSURE CORRECT CHAN PROGRAM OPERATION
 LNJ \$B4,<CHCT CHANNEL CONTROL
 B >\$+2

001707 07B4 0100 DC X'100' RESET CCB LIST
 001708 07B5 C380 0E25 LNJ \$B4,<RPVLU READ P *****
 001709 07B7 E870 024C LDR \$R6,=X'24C'
 001710 07B8 D956 CMR \$R5,=\$R6
 001711 07B9 0903 BE >\$+2+\$AF
 001712 07B0 F380 125C LNJ \$B7,<ERROK BVBT, BVBF, OR CCB LIST RESET ERROR
 001713 07B1 8AD3 INC =\$R3 BUMP TO NEXT CHANNEL
 001714 07B2 0F81 FF39 B 1ZTST6
 001715 * DTPT DC Z'CU05' RCV DATA PTR
 001716 07C0 C005 DC Z'CU25' XMIT DATA POINTER
 001717 07C1 C025 DC 0 END OF LIST
 001718 07C2 0000
 001719 *
 001720 *
 001721 *
 001722 *
 001723 *
 001724 * BLOCK READ A CHANNEL'S LCT AREA TO CHECK IT'S CLEAR
 001725 *
 001726 07C3 C853 BRLCLK LDR \$R4,=\$R3 FORM RAM ADDRESS OF LCT AREA
 001727 07C4 4F20 MLV \$R4,X'20'
 001728 07C5 CF00 07D0 STR \$R4,<1ZTST4
 001729 07C6 BF00 14CD STR \$R3,<TPR
 001730 07C7 3B00 07CC BEVN \$R3,<1Z1512
 001731 07C8 88D3 DEC =\$R3
 001732 * IZTST2 LNJ \$B1,<RDATA READ RAM
 001733 07C9 9380 1098 DC <RTB RECEIVE BUFFER
 001734 07CE 2BAA DC X'20'
 001735 07CF 0020 0000 IZTST4 DC 0 RAM ADDRESS
 001736 07D0 0000 DC U START ON EVEN
 001737 07D1 0000
 001738 07D2 B800 14CD * LDR \$R3,<TPR
 001739 07D3 0000
 001740 07D4 8756 * CHECK ZERO'S WERE READ IN
 001741 07D5 2C01 CL =\$R6
 001742 07D6 D820 2BAA LDV \$R2,=1 START WITH WORD 2
 001743 07D7 D820 2BAA IZTST1 LDR \$R5,<RTB,\$R2 GET LCT CONTENT
 001744 07D8 D956 CMR \$R5,=\$R6 IS LCT CONTENT=0
 001745 07D9 0903 BE >\$+2+\$AF CONTINUE IF ZERO
 001746 07DA F380 125C LNJ \$B7,<ERROR OR ELSE REPORT ERROR
 001747 07DB 8AD2 IZTST7 INC =\$R2 INCREMENT COUNTER
 001748 07DD AF57 STR \$R2,=\$R7
 001749 07DE 3B90 BUDD \$R3,>1ZTST3 BRANCH IF TRANSMIT CHANNEL
 001750 07DF A970 0002 CMR \$R2,X'0002'
 001751 07E1 0901 FFFA BE IZTST7
 001752 07E2 A970 0004 CMR \$R2,X'0004'
 001753 07E3 0901 FFF6 BE IZTST7
 001754 07E4 A970 000A CMR \$R2,X'000A'
 001755 07E5 0901 FFF2 BE IZTST7
 001756 07E6 A970 000B CMR \$R2,X'000B'
 001757 07E7 0901 FFF1 BE >IZTST7 OR CONTROL WORD
 001758 07E8 A970 000B CMV \$R2,X'10'
 001759 07E9 0901 FFF0 BNE >IZTST1
 001760 07EE 2D10 JMP \$B4 ALL DONE
 001761 07EF 09E7
 001762 07F0 8384
 001763 *
 001764 *
 001765 * DO TWO INPUT NEXT STATUS TO GET THE INPUT
 001766 * NEXT STAT PTR AWAY FROM IT'S INITIAL STATE
 001767 07F1 C380 1161 ADV2 LNJ \$B4,<INXT INPUT NEXT STATUS
 001768 07F2 C380 1161 LNJ \$B4,<INXT INPUT NEXT STATUS
 001769 07F3 8381 JMP \$B1
 001770 07F4 *
 001771 * READ RANGE TO INSURE INPUT NEXT STATUS PTR IS RESET
 001772 *
 001773 07F5 8381 CHKRNG LDR \$R4,<CONT4 INPUT NEXT STATUS
 001774 07F6 C800 155F LNJ \$B4,<CGSCH MODIFY FOR CHANNEL
 001775 07F7 C380 1173 I0 TEMP,=\$R4 INPUT NEXT TO GET TO FIRST CCB
 001776 07FA 8040 0CD3 07FC 0054
 001777 07FD 0703 BIOT >\$+2+\$AF
 001778 07FE F380 125C LNJ \$B7,<ERROK
 001779 0800 4EF2 * NOW INPUT RANGE. SHOULD BE = 1 IF PTR WAS RESET
 001780 0801 8055 ADV \$R4,=-X'1'
 001781 0802 0054 I0 =\$R5,=\$R4 INPUT RANGE
 001782 0803 0703 BIOT >\$+2+\$AF
 001783 0804 F380 125C LNJ \$B7,<ERROK COMMAND WAS NAKED
 001784 0805 6C01 LDV \$R6,=1
 001785 0807 D956 CMR \$R5,=\$R6
 001786 0808 0903 BE >\$+2+\$AF
 001787 0809 F380 125C LNJ \$B7,<ERROK INPUT STAT PTR NOT..
 001788 080B 8381 * EPOINTING AT FIRST CCB
 001789 080C 8381 JMP \$B1
 001790 *
 001791 * SET UP 4 CCB'S *
 001792 *
 001793 080C 4C01 SET4 LDV \$R4,1
 001794 080D C380 1189 SETA LNJ \$B4,<MCCB MAKE CCB
 001795 080E 2BAA DC <RTB
 001796 0810 0040 DC X'40'
 001797 0811 8AD4 INC =\$R4 VALID CCB BIT
 001798 0812 4D05 CMV \$R4,5
 001799 0813 09FA . BNE >SETA
 001800 0814 8381 JMP \$B1
 001801 *
 001802 *
 001803 *
 001804 *
 001805 *
 001806 *
 001807 *
 001808 0815 B870 4B20 STIURC LDR \$R3,A'K'
 001809 0817 BF00 1581 STR \$R3,<ERMG+1
 001810 CALL ZV\$T.ZV\$TC.TSTK
 001811 0819 FBC0 0003 X
 001812 081B D380 0000
 001813 081D 0F80
 001814 081E 1606
 001815 081F C380 10FC LNJ \$B4,<GENITZ R3 TRACKS CHANNEL
 001816 081G 8753 CL =\$R3 FIND ACTIVE LINE
 001817 0822 C380 10EC RPSIO LNJ \$B4,<FLN

001814 0824 8382 * SET UP CONDITIONS FOR STOP IO TEST
 001815 0825 C380 0489 LNJ SB4,<LCTGRB NO MORE LINES TO TEST
 001816 0827 B380 0FCC LNJ SB4,<SETLCT FILL LCT AREA WITH DATA
 001817 0829 1EBB DC <SDB SEND OUT LCT BYTES
 001818 082A 4C01 LDV \$R4,=1 RANGE
 001819 082B C380 1189 LNJ SB4,<MCCB DUMMY LCB
 001820 082D 2BAA DC <RTB
 001821 082E 0400 DC X'400' RAM ADDRESS
 001823 * GIVE STOP I/O
 001824 082F C380 1177 LNJ SB4,<CHCT DO CHANNEL CONTROL
 001825 0831 0F82 B >\$+2
 001826 0832 2000 DC Z'2000' STOP I/O
 001827 * LNJ SB4,<DLAY DELAY
 001828 * CHECK THAT STOP I/O OPERATED CORRECTLY
 001829 * LNJ SB4,<INXT INPUT NEXT STATUS
 001830 0833 C380 1205 LDR \$R6,=X'1000' STAT COMPLETE BIT
 001831 0835 C380 1161 LB =\$R5,=X'1000'
 001832 0837 E870 1000 BBT >\$+2+\$AF
 001833 0839 82D5 LNJ SB7,<ERROR STOP I/O DIDN'T CAUSE STATUS COMPLETE
 001834 083A 1000
 001835 083B 5053
 001836 083C F380 125C
 001840 * INPUT LCT STATUS
 001841 083E C380 084E LNJ SB4,<ILCTST
 001842 0840 8756 CL =\$R6
 001843 0841 D956 CMR \$R5,=\$R6
 001844 0842 0903 BE >\$+2+\$AF
 001845 0843 F380 125C LNJ SB7,<ERROR STOP I/O DIDN'T CLEAR LCT STATUS
 001846 * DO A CHANNEL INITIALIZE IN PREPARATION FOR THE NEXT TEST
 001847 0845 C380 1177 LNJ SB4,<CHCT
 001848 0847 0F82 B >\$+2
 001849 0848 0000 DC 0 CHANNEL INITIALIZE
 001850 0849 C380 1205 LNJ SB4,<DLAY TIME DELAY TO ALLOW INITIALIZE TO COMPLETE
 001851 084B 8AD3 INC R3
 001852 084C 0F81 FFD5 B =\$R3 RPS10 SET FOR NEXT CHANNEL NUMBER
 001853 *-----
 001854 * SUBROUTINES FOR STOP IO
 001855 *-----
 001856 * INPUT LCT STATUS TO R5
 001857 084E 8F40 0CA8 ILCIST SAVE SAV2,Z'AA88' R4,B4
 001858 0850 AA88
 001859 0851 E870 1137 LDR \$R6,=Z'1137'
 001860 0853 A870 1037 LDR \$R2,=Z'1037'
 001861 0855 3B00 085B BEVN \$R3,<ILC1 BRANCH IF RCV CHAN
 001862 0857 A870 2000 ADD \$R2,=Z'2000'
 001863 0859 E870 2000 ADD \$R6,=Z'2000'
 001864 085B 9852 ILC1 LDR \$R1,=\$R2 GET BYTE ADDRESS OF MSB OF STAT
 001865 085C C380 0E0C LNJ SB4,<OUTLCT OUTPUT BYTE
 001866 085D C380 11A8 LNJ SB4,<INBYTE INPUT BYTE TO R5
 001867 0860 DF00 14CD STR \$R5,<TPR
 001868 0861 9856 LDR \$R1,=\$R6
 001869 0863 C380 0E0C LNJ SB4,<OUTLCT OUTPUT BYTE
 001870 0865 C380 11A8 LNJ SB4,<INBYTE INPUT LSB OF STAT
 001871 0866 5048 SOR \$R5,8 MOVE OVER
 001872 0867 D400 14CD OR \$R5,<TPR FORM WHOLE STAT WORD
 001873 0868 8FC0 0C8C KSTR SAV2,Z'AA88'
 001874 086C AA88
 001875 086D 8384 JMP SB4 EXIT SUBROUTINE
 001876 *-----
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 001892 086E 9870 4C31 RC0 LDR \$R1,A'L1' RECEIVING TEST
 001893 0870 9F00 1581 STR \$R1,<ERMG+1 END RETURN
 001894 0872 FBC0 0003 CALL ZV\$1.ZV\$TC,TSTL1
 001895 0874 D380 0000 X
 001896 0876 0F80
 001897 0877 160E
 001898 0878 C380 090A LNJ SB4,<RCVTST
 001899 087A 0F86 B >RC1 EVEN BYTE BOUNDARY
 001900 087B 0000 DC 0 CCB RANGE
 001901 087C 0006 DC 6 CCB CONTROL WORD
 001902 087D 0040 DC X'40' LCT TABLE
 001903 087E 0AB2 DC <LCT2 NUMBER OF BYTES TO XFER
 001904 087F 0006 DC 6
 001905 *-----
 001906 *-----
 001907 0880 9870 4C32 RC1 LDR \$R1,A'L2'
 001908 0882 9F00 1581 STR \$R1,<ERMG+1
 001909 0884 C380 090A LNJ SB4,<RCVTST
 001910 0886 0F86 B >RC2
 001911 0887 0001 DC 1 ODD BYTE BOUNDARY
 001912 0888 0006 DC 6 CCB RANGE
 001913 0889 0040 DC X'40' CCB CONTROL
 001914 088A 0AB2 DC <LCT2 LCT TABLE
 001915 088B 0006 DC 6 NUMBER OF BYTES TO XFER
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001920 088C 9870 4C33
 001921 088E 9780 1581
 001922 0890 C380 090A
 001923 0892 0F86
 001924 0893 0000
 001925 0894 0008
 001926 0895 0040
 001927 0896 0AB1
 001928 0897 0006

RC2 LDR \$R1,=A'L3'
 STR \$R1,<ERMG+1
 LJN \$B4,<RCVTST
 B >RC3
 DC 0
 DC 8
 DC X'40'
 DC <LCT2A
 DC 6
 RECEIVE TEST
 DONE
 EVEN BYTE
 CCB RANGE
 CCB CONTROL WORD
 LCT
 NUMBER OF BYTES TO XFER

*
 * 4. EVEN START, ODD RANGE, GNB TERMINATION

RC3 LDR \$R1,=A'L4'
 STR \$R1,<ERMG+1
 LJN \$B4,<RCVTST
 B >RC4
 DC 0
 DC 5
 DC X'40'
 DC <LCT2A
 DC 3
 RECEIVE TEST
 DONE
 EVEN BYTE
 CCB RANGE
 CCB CONTROL WORD
 LCT
 NUMBER OF BYTES TO XFER

*
 * RECEIVE TESTS

* 5. EVEN START, ODD RANGE, EOR TERMINATION

RC4 LDR \$R1,=A'L5'
 STR \$R1,<ERMG+1
 LJN \$B4,<RCVTST
 B >RC5
 DC 0
 DC 5
 DC X'40'
 DC <LCT12
 DC 5
 RECEIVE TEST
 RETURN
 EVEN BYTE START
 CCB RANGE
 CCB CONTROL
 NUMBER OF BYTES TO XFER

*
 * CCB OVER RUN TEST

RC5 LDR \$R1,=A'L6'
 STR \$R1,<ERMG+1
 LJN \$B4,<RCVTST
 JMP \$B2
 DC 0
 DC 3
 DC X'1040'
 DC <LCT2
 DC 5
 EXIT TEST
 CCB RANGE
 BIT 3 SET FOR FLAG FOR CCB SERVICE ERROR
 NUMBER OF BYTES TO XFER

*
 **
 *
 * TRANSMIT TESTS

* 1. EVEN START, EVEN RANGE, EOR TERM

X TRAN1 LDR \$R1,=A'M1'
 STR \$R1,<ERMG+1
 CALL ZV\$T.ZV\$TC.TSTM1

001980 08BC 9870 4D31
 001981 08BE 9F00 1581
 001982 08C0 FBC0 0003
 001983 08C2 D380 0000
 001984 08C4 0F80
 001985 08C5 1615
 001986 08C6 C3C0 00DA
 001987 08C8 0F86
 001988 08C9 0000
 001989 08CA 0006
 001990 08CB 0UAB
 001991 08CD 0006

LJN \$B4,XTST
 B >TRAN2
 DC 0
 DC 6
 DC X'60'
 DC <LCT1
 DC 6
 TRANSMIT TEST
 RETURN
 EVEN BYTES
 CCB RANGE
 CCB CONTROL, LAST BLOCK
 LCT
 NUMBER OF BYTES TO XFER

*
 * 2. ODD START,<EVEN RANGE

TRAN2 LDR \$R1,=A'M2'
 STR \$R1,<ERMG+1
 LJN \$B4,XTST
 B >TRAN3
 DC 1
 DC 6
 DC X'60'
 DC <LCT1
 DC 6
 TRANSMIT TEST
 ODD BYTE
 CCB RANGE
 CCB CONTROL WORD, LAST BLOCK, VALID
 LCT
 NUMBER OF BYTES TO XFER

*
 * 3. EVEN START, EVEN RANGE, GNB TERMINATION

TRAN3 LDR \$R1,=A'M3'
 STR \$R1,<ERMG+1
 LJN \$B4,XTST
 B >TRAN4
 DC 0
 DC 8
 DC X'0060'
 DC <LCT1A
 DC 6
 TRANSMIT TEST
 RETURN
 EVEN BYTE
 CCB RANGE
 LAST BLOCK, VALID
 LCT
 NUMBER OF BYTES TO XFER

*
 * 4. EVEN START, ODD RANGE, GNB TERMINATION

TRAN4 LDR \$R1,=A'M4'
 STR \$R1,<ERMG+1
 LJN \$B4,XTST
 B >TRAN5
 TRANSMIT TEST
 RETURN

002029 08ED 0000 DC 0
 002030 08EE 0008 DC 8 CCB RANGE
 002031 08EF 0060 DC X'60' CCB CONTROL, LAST BLOCK
 002032 08F0 0AAA DC ZLCTIA LCT
 002033 08F1 0005 DC 5 NUMBER OF BYTES TO XFER
 *-----
 *-----
 * 5. EVEN START, ODD RANGE, END TERMINATION
 *-----
 002037 TRANS LDR \$R1=A'M5'
 002038 08F2 9870 4D35 STR \$R1,<ERMG+1
 002040 08F4 9F00 1581 LNJ SB4,XTST
 002041 08F6 C3C0 00AA B >TRAN6 TRANSMIT TEST
 002042 08F8 0F86 DC 0 EVEN START
 002043 08F9 0000 DC 5 RANGE (CCB)
 002044 08FA 0005 DC X'60' CCB CONTROL , LAST BLOCK
 002045 08FB 0060 DC ZLCT1 LCT
 002046 08FC 0AAB DC 5 NUMBER OF BYTES TO XFER
 002047 08FD 0005 DC 5
 *-----
 *-----
 * CCB UNDER-RUN TEST
 *-----
 002051 08FE 9870 4D36 TRAN6 LDR \$R1=A'M6'
 002052 0900 9F00 1581 STR \$R1,<ERMG+1
 002053 0902 C3C0 009E LNJ SB4,XIST
 002054 0904 8382 JMP SB2 TRANSMIT
 002055 0905 0000 DC 0 EXIT TEST
 002056 0906 0005 DC 5 EVEN START
 002057 0907 1040 DC X'1040' CCB RANGE
 002058 0908 0AAB DC ZLCT1 NOT LAST CCB
 002059 0909 0006 DC 6 LCT XMIT 5
 002061
 *-----
 *-----
 * RECEIVE TEST
 *-----
 002063 * LNJ SB4,<RCVTST
 002064 * B >NEXT
 002065 * DC A 1= ODD BYTE
 002066 * DC B RANGE IN BYTES
 002067 * DC C CCB CONTROL WORD
 002068 * DC D POINTS TO LCT TABLE
 002069 * DC E NUMBER OF BYTES TO XFER
 *-----
 002074 RCVTST SAVE SAV4,=X'547C' R1,3,4, B1,2,3,4,5
 002075 090A 8F40 0C1C
 002076 090C 547C 0C1C
 002077 090D 9870 0200 LDR \$R1=X'200' P COUNTER START
 002078 090F 9F00 0A5A STR \$R1,<RCWR1
 002079 0911 9874 LDR \$R1,+\$B4 DUMMY TO INCREMENT B4
 002080 0912 9874 STR \$R1,+\$B4 ODD/EVEN FLAG
 002081 0913 9F00 095F STR \$R1,<KCV1
 002082 0915 9F00 0966 STR \$R1,<KCV2
 002083 0917 9F00 096F STR \$R1,<KCV3
 002084 0919 9F00 098B STR \$R1,<RCV4
 *-----
 002085 091B 9874 LDR \$R1,+\$B4 PICK UP RANGE
 002086 091C 9F00 096B STR \$R1,<RCV7
 002087 091E 9874 LDR \$R1,+\$B4 PICK UP CCB CONTROL WORD
 002088 091F 9F00 0970 STR \$R1,<RCV9
 002089 0921 9CF4 LDB \$B1,+\$B4 LCT POINTER
 002090 0922 9F80 096E STR \$B1,<RCV10
 *-----
 002092 0924 9874 LDR \$R1,+\$B4 PICK UP NUMBER OF BYTES
 002093 0925 9F00 098C STR \$R1,<RCV8
 002094 0927 9F00 095E STR \$R1,<RCV5
 002095 0929 9F00 0964 STR \$R1,<RCV6
 002096 092D 8700 14CC CL \$FLAG
 002097 092D 9900 096B CMR \$R1,<RCV7
 002098 092F 900C BE >RCV13
 002100 0930 0208 BL >RCV14
 002101 0931 D800 096B LDR \$R5,<RCV7
 002102 0933 DF00 095E STR \$R5,<RCV5
 002103 0935 DF00 098C STR \$R5,<RCV8
 002104 0937 0F84 RCV14 LDV \$R4=X'20' FLAG SET FOR NON ZERO RANGE RESIDUE
 002105 0938 4C20 STR \$R4,<FLAG
 002106 0939 CF00 14CC RCV13 SOL \$R1,8 FORM RANGE
 002107 093B 1008 RCV13 XOR \$R1=X'3F'
 002108 093C 9670 003F STR \$R1,<LCT2
 002109 093E 9F00 0A82 *-----
 002110 0940 8753 CL =\$R3 FIND ACTIVE LINE
 002111 0941 C380 10EC NXRCV LNJ \$B4,<FLN
 002112 0943 0F82 B >\$+2
 002113 0944 0F83 B >\$+2+\$AF
 002114 0945 0F81 0057 RLEN
 002115 0945 0F81 0057 BEVN \$R3,>RCV12 NO MORE LINES
 002116 0947 3B03 INC =\$R3 BRANCH MEANS RCV CHAN
 002117 0948 8AD3 B >NXRCV TRY NEXT
 002118 0949 0FF8 RCV12 LDR \$R4=\$R3
 002119 094A C853 MLV \$R4=X'20'
 002120 094B 4F20 ADV \$R4,=57 FORMS LCI START
 002121 094C 4E39 STR \$R4,<RC11
 002122 094D CF00 0965 STR \$R3,<RCDATA RAM ADDRESS WHERE DATA GOES
 002123 094F BF00 095D *-----
 *-----
 * FILL RECEIVE BUFFER WITH DEFAULT
 *-----
 002124 0951 FBC0 0003 CALL ZVSF,RTB,DFLT,BUFRNG
 002125 0953 D380 0000 X
 002126 0955 0F80
 002127 0956 2BA
 002128 0957 14BE
 002129 0958 14C6
 00212A 0959 C380 10FC
 *-----
 00212B 0959 LNJ \$B4,<GENITZ MLCC GENERAL INITIALIZE
 00212C 0959 LNJ \$B4,<FDTA FILL ASCENDING DATA
 002130 095B C380 0AAB RCADATA DC 0 STARTING BYTE
 002131 095D 0000 RCV5 DC 0 RANGE IN BYTES
 002132 095E 0000 RCV1 DC 0 1 = START ON RIGHT BYTE
 002133 095F 0000
 002134

002135
 002136
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 002138
 002139 0960 8AD3 * BLOCK WRITE DATA TO RAM. DURING RECEIVE THIS DATA WILL BE TRANSFERED TO CPU BY THE CHANNEL PROGRAM.
 002140 * INC = \$R3 GET XMIT HALF OF LINE
 002141 0961 9380 1069 LNJ \$B1,<SDATA SEND DATA
 002142 0963 1EBB DC <SDO FROM HERE
 002143 0964 0000 RCV6 DC 0 RANGE IN BYTES
 002144 0965 0000 KC11 DC 0 RAM ADDRESS
 002145 0966 0000 KCV2 DC 0 I = START ON RIGHT BYTE
 002146 * DEC = \$R3 GET BACK TO RECEIVE
 002147 0967 88D3 * RECEIVE A BLOCK OF DATA
 002148 * LNJ \$B4,<RCV READ TO HERE
 002149 * RCV7 DC 0 RANGE
 002150 096A C380 0A36 RCV7 DC 0 CHANNEL PROGRAM ADDRESS
 002151 096B 2BAA RCV7 DC <RCDT1 CHANNEL PROGRAM RANGE
 002152 096C 0000 RCV10 DC (RCRT1-RCDT1)*2 LCT PARAMETER TABLE
 002153 096D 13D7 RCV10 DC <DUMMY I = START ON RIGHT BYTE
 002154 096E 00A6 RCV3 DC 0 CCB CONTROL WORD
 002155 096F 14C7 RCV9 DC 0
 002156 0970 0000 *
 002157 0971 C380 1161 * READ AND CHECK STATUS
 002158 0972 E870 1000 LNJ \$B4,<INXT INPUT NEXT STATUS
 002159 0973 E600 14CC LDR \$K6,=X*1000* CCB COMPLETE BIT
 002160 0974 E600 14CC XUR \$K6,<FLAG NON ZERO RANGE
 002161 0975 D956 CMR \$K5,=\$K6 RESIDUE BIT
 002162 0976 0903 BE >\$Z+\$AF
 002163 0977 F380 125C LNJ \$B7,<ERROR STATUS ERROR
 002164 0978 8756 CL = \$R6 GET FLAG FOR LCB ERROR
 002165 0979 8756 LB RCV9,=X*1000*
 002166 097A 82C0 FFF3 * SET CCB SERVICE ERROR BIT
 002167 097B 1000 LBS = \$R6,=X*800*
 002168 097C 8A56
 002169 097D 0800
 002170 097E 097F * READ LCT STATUS
 002171 097F 8A56 *
 002172 0980 0800 *
 002173 0981 C380 084E *
 002174 0982 D570 0B00 LNJ \$B4,<ILCTST READ LCT STATUS
 002175 0983 D570 0B00 AND \$R5,=X*800* STRIP TO SERVICE ERROR
 002176 0984 D956 CMR \$R5,=\$R6
 002177 0985 0903 BE >\$Z+\$AF
 002178 0986 F380 125C LNJ \$B7,<ERROR CCB SERVICE ERROR BIT IN WRONG STATE
 002179 * CHECK DATA
 002180 *
 002181 0987 D380 0A7F RCV4 LNJ \$B5,<CHKBW CHECK ROUTINE
 002182 0988 0000 RCV8 DC 0 I = START ON RIGHT BYTE
 002183 0989 0000 *
 002184 098A 0000 *
 002185 098B 0000 *
 002186 098C 0000 *
 002187 098D 9870 0537 * CHECK DATA BUFFER POINTER CLEARED TO 0
 002188 098E C380 0EDC LDR \$R1,=X*0537*
 002189 098F 9874 LNJ \$B4,<OUTLC1 OUTPUT BYTE
 002190 0990 C380 11A8 LNJ \$B4,<INBYTE INPUT BYTE
 002191 0991 D570 C000 AND \$R5,=Z*CO00* STRIP TO DATA PTR
 002192 0992 0993 CMR \$R5,=\$R6
 002193 0993 6756 CL = \$R6
 002194 0994 0903 BE >RCV11
 002195 0995 F380 125C LNJ \$B7,<ERROR GNB DIDN'T RESET DATA PTR'S
 002196 0996 8AD3 RCV11 INC = \$R3
 002197 0997 0903 *
 002198 0998 0F81 FFA5 B NXRCV DO NEXT LINE
 002199 0999 0F81 FFA5 *
 002200 099A 0FC0 0B89 RCEND RSTR SAV4,=X*547C*
 002201 099B 547C *
 002202 099C 8384 JMP \$B4
 002203 *-----*
 002204 * DATA TRANSMIT TEST
 002205 *
 002206 * LNJ \$B4,<XTST
 002207 * B >NEXT EXIT BRANCH
 002208 * DC 0 I = ODD BYTE
 002209 * DC X RANGE IN BYTES
 002210 * DC Y CCB CONTROL WORD
 002211 * DC Z POINTER TO LCT PARAMETER TABLE
 002212 * DC W NUMBER OF BYTES TO XFER
 002213 *
 002214 *
 002215 09A1 8F40 0B85 XTST SAVE SAV4,=X*547C* R1,3,4, B1,2,3,4,5
 002216 09A2 547C *
 002217 09A3 9874 LDR \$R1,+\$B4 DUMMY TO INCREMENT B4
 002218 09A4 9874 LDR \$R1,+\$B4 PICK UP EVEN, ODD FLAG
 002219 09A5 9874 STR \$R1,<CHST1 STORE FLAG FOR ODD OR EVEN BYTE
 002220 09A6 9F00 09E4 STR \$R1,<CHST2
 002221 09A7 9F00 09EC STR \$R1,<CHST3
 002222 09A8 9F00 0A1C STR \$R1,<CHST4
 002223 09A9 9F00 0A20 LDR \$R1,+\$B4
 002224 09AA 9F00 09E8 STR \$R1,<CHRG2 PICK UP RANGE
 002225 09AB 9872 CL = \$R2
 002226 09BC 9874 LDR \$R1,+\$B4
 002227 09BD 82D1 LB = \$R1,X*40* GET LAST BLOCK BIT
 002228 09BE 0040 *
 002229 09BF 0503 XTST2 BBT >XTST2
 002230 09C0 0800 LDR \$R2,=X*800* BRANCH IF NO UNDER-RUN
 002231 09C1 09ED STR \$R1,<CCBSAV UNDER-RUN EXPECTED
 002232 09C2 AF00 14CC STR \$R2,<FLAG 0 MEANS NO CCB UNDER-RUN
 002233 09C3 9C4 LDB \$B1,+\$B4
 002234 09C4 9F80 09EB STB \$B1,<BWLCT
 002235 09C5 9874 LDR \$R1,+\$B4
 002236 09C6 9874 STR \$R1,<CHRG4
 002237 09C7 9F00 0A21 CMR \$R1,<CHRG2
 002238 09C8 9900 09E8 BLE >XTST3
 002239 09C9 0383 LDR \$R1,<CHRG2
 002240 09CA 9800 09E8 XTST3 STR \$R1,<CHRG3
 002241 09CB 9F00 0A1A STR \$R1,<CHRG1
 002242 09CC 9F00 09E3

002243 09CB 9800 0A21 LDR \$R1,<CHRG4
 002244 09CU 1008 SOL \$R1,B
 002245 09CE 9670 003F XOR \$R1,=X'3F'
 002246 09D0 9F00 0AAB STR \$R1,<LCT1
 *
 002247 09D2 8753 10EC NXWRT CL =\$R3 FIND ACTIVE LINE
 002249 09D3 C380 B
 002250 09D5 0F82 B
 002251 09D6 0F83 B
 002252 09D7 0F81 005A B
 002253 09D9 3B83 B
 002254 09DA 8AD3 INC
 002255 09DB 0FF8 B
 002256 09DC BF00 09E2 XTST1 STR >NXWRT TRY NEXT
 002257 09DE C380 10FC LNJ \$B4,<EODATA GENERAL INITIALIZE
 *
 002258 09E0 C380 0A88 EODATA LNJ \$B4,<FDTA FILL SDB WITH ASCENDING DATA
 002260 09E2 0000 DC 0 FIRST DATA WORD
 002261 09E3 0005 DC 5 RANGE IN BYTES
 002262 09E4 0000 DC 0 1= START ON RIGHT BYTE
 *
 002263 09E5 C380 0A74 LNJ \$B4,<WRT
 002265 09E7 1EBB DC <SDB SEND BUFFER
 002266 09E8 0005 DC 5 RANGE
 002267 09E9 1381 DC <TRANM1 CHANNEL PROGRAM
 002268 09EA 00AC DC <(RCDC1-TRANM1)*2 RANGE OF XMIT PROGRAM
 002269 09EB 14C7 BWLCT DC <DUMMY LCT PROTOTYPE
 002270 09EC 0000 CHST2 DC 0 0 = EVEN START
 002271 09ED 0000 CCBSAV DC 0 CCB CONTROL WORD STORED HERE
 *
 ** READ AND CHECK STATUS
 *
 002274 09EE 8756 CL =\$R6 INPUT NEXT STATUS
 002275 09EF C380 1161 LNJ \$B4,<INXT GET LAST BLOCK BIT
 002276 09F1 B2C0 FFFF LB CCBSAV,=X'0020'
 002277 09F3 0200 LBS =\$R6,Z'0020' SET CCB LAST BIT TO EXPECTED STATE
 002278 09F4 8A58 LB <CCBSAV,=X'1000'
 002279 09F5 0020 BBF >NXT1 BRANCH IF NOT UNDERUN TEST
 002280 09F6 8280 09ED AND \$R5,Z'1000'
 002281 09F9 0583 OK \$R6,Z'1000'
 002282 09FC D570 1000 CMR \$R5,\$R6
 002283 09FE D956 BE >\$2+\$AF IS VS SB
 002284 09FF 0903 LNJ \$B7,<ERROR STATUS ERR AFTER XMIT
 002285 0A00 F380 125C LB CCBSAV,=X'1000'
 002286 0A02 B2C0 FFEA CL =\$R6 GET CCB UNDERRUN FLAG
 002287 0A04 1000 LBS =\$R6,X'800'
 002288 0A06 8A56 CCB SERVICE ERROR BIT
 0A07 0800
 002289 ***
 002290 ***
 002291 ***
 002292 ***
 002293 ***
 0A08 C380 084E LNJ \$B4,<ILCTST INPUT LCI STATUS
 002294 0A0A D570 0800 AND \$R5,Z'0800'
 002295 0A0C D956 CMR \$R5,\$R6
 002296 0A0D 0903 BE >\$2+\$AF
 002297 0A0E F380 125C LNJ \$B7,<ERROR CCB SERVICE ERROR BIT IN WRNG STATE
 002298
 002300
 002301
 002302 0A10 C853 LDR \$R4,=\$R3
 002303 0A11 88D4 DEC =\$R4
 002304 0A12 4F20 MLV \$R4,X'20'
 002305 0A13 4E39 ADV \$R4,=57
 002306 0A14 CF00 0A1B STR \$R4,<EOADD
 002307 0A16 88D3 DEC =\$R3
 002308 0A17 9380 1098 LNJ \$B1,<RDATA
 002309 0A19 ZBAA CHRG3 DC 5
 002310 0A1A 0005 EOADD DC 0
 002311 0A1B 0000 CHST3 DC 0
 002312 0A1C 0000 *
 002313 0A1D 8AD3 INC =\$R3
 002314 ***
 002315 ***
 002316 ***
 002317 0A1E D380 0A7F LNJ \$B5,<CHKBW CHECK ROUTINE
 002318 0A20 0000 CHST4 DC 0 0=START LEFT BYTE
 002319 0A21 0005 CHRG4 DC 5 5 BYTES
 *
 ** CHECK DATA POINTERS ARE RESET
 *
 002324 0A22 9870 2537 LUR \$R1,X'2537'
 002325 0A24 C380 0E0C LNJ \$B4,<OUTLCT OUT X'25' TO LOC X'37'
 002326 0A26 C380 11A8 LNJ \$B4,<INBYTE INPUT BYTE
 002327 0A28 D570 C000 AND \$R5,Z'C000'
 002328 0A2A 8756 CL STRIP TO DATA PTR
 002329 0A2B D956 CMR \$R5,\$R6
 002330 0A2C 0903 BE >NXT2
 002331 0A2D F380 125C LNJ \$B7,<ERROR GNB DIDN'T RESET LR25, BITS 0,1, XMIT
 002332 0A2F 8AD3 INC =\$R3 SET FOR NEXT CHANNEL
 002333 0A30 0F81 FFA2 B NXWRT
 *
 002334 0A32 8FC0 0A4F BWEND RSTR SAV4,X'547C'
 002335 0A34 547C JMP \$B4
 002336 0A35 8384
 *
 002337 ***
 002338 ***
 002339 ***
 002340 ***
 002341 ***
 002342 ***
 002343 ***
 002344 ***
 002345 ***
 002346 ***
 002347 ***
 002348 ***
 002349 ***
 0A36 8F40 0AE0 RCV SAVE SAV3,=Z'ECB4' SAVE REGS.

002350 UA38 ECB4
 002351 UA39 9870 0200
 002352 UA3D ACF4
 002353 UA3E C874
 002354 UA3F D870 5555
 002355 UA41 DF02
 002356 UA42 CF52
 002357 UA43 8AD2
 002358 UA44 2041
 002359 UA45 DF22
 002360
 002361 UA46 BCF4
 002362 UA47 BF80 0A58
 002363 UA49 AF80 0A67
 002364 UA4B 9874
 002365 UA4C 9F00 0A59
 002366 UA4E BCF4
 002367 UA4F BF80 0A60
 002368 UA51 CFD5
 002369 UA52 BF00 14CD
 002370 UA54 3B82
 002371 UA55 8AD3
 002372 UA56 9880 1069
 002373 UA58 14C7
 002374 UA59 0000
 002375 UA5A 0200
 002376 UA5B 0000
 002377
 002378 UA5C B880 14CD
 002379 UA5E B380 0FC0
 002380 UA60 14C7
 002381
 002382 UA61 9875
 002383 UA62 A875
 002384 UA63 AF00 0A68
 002385 UA65 C880 1189
 002386 UA67 14C7
 002387 UA68 0010
 002388
 002389 UA69 89D2
 002390 UA6A 0803
 002391 UA6B B380 0628
 002392
 002393 UA6D C380 1205
 002394
 002395 UA6F CCF5
 002396 UA70 8FC0 UAA6
 002397 UA72 ECB4
 002398 UA73 8384
 *
 * ELMJE\$B4,<WKT
 * EDCECPU ADDRESS OF BUFFER
 * EDCE RANGE VALUE
 * EDCECPU ADDRESS OF CHANNEL PROGRAM
 * DC RANGE OF CHANNEL PROGRAM
 * DC LCT PARAMETER TABLE
 * EDCEO OR 1000 OR EVEN BYTE BUFFER ADDRESS START
 * DC CCB CONTROL WORD.
 *
 * WRT SAVE SAV3,=Z*ECB4*
 * LDR \$B2,+\$B4
 * LDR \$R4,+\$B4
 * STR \$R1,<RCWR1
 * B WRT1
 *
 * CHECK SEND VS. RCV BUFFERS ON A BYTE BY BYTE BASIS.
 * LNJ \$B5,<CHKBW
 * DC 0 = START ON RIGHT BYTE
 * DC X RANGE IN BYTES
 * CHKBW LDR \$R1,+\$B5
 * LDR \$R2,+\$B5
 * NEG =\$R2
 * LAB \$B3,<SDR
 * LAB \$B6,<RTB
 * CHK1 LDH \$R5,\$B6,\$R1
 * LDH \$R6,\$B3,+\$R1
 * CMR \$R5,=\$R6
 * BE >\$+2+\$AF
 * LNJ \$B7,<ERROR
 * BINC \$R2,<CHK1
 *
 * DATA IS GOOD, CHECK 1 BYTE MORE TO SEE IF INPUT EXCEEDED RANGE
 * LDH \$R5,\$B6,\$R1
 * LDV \$R6,=X*55*
 * CMR \$R6,=\$R5
 * BE >\$+2+\$AF
 * LNJ \$B7,<ERROR
 *
 * READ BACK P COUNTER VALUE
 * LNJ \$B4,<RPVLU
 * LDR \$R3,<TPR
 * BEVN \$R3,<CHK2
 * LDR \$R6,=X*405,
 * LD <CCDSAV,=Z*1000*
 * BBF >CHK3
 * LDR \$R6,=X*U4A2*
 * B >CHK3
 * CHK2 LDR \$R6,=X*205*
 * CHK3 CMR \$R5,=\$R6
 * BE >\$+2+\$AF
 * LNJ \$B7,<ERROR

LOAD \$B2 WITH ADDRESS
 LOAD \$R4 WITH NUMBER OF BYTES
 LOAD \$R5 WITH INITIAL PATTERN
 STORE AT TOP AND BOTTOM OF BUFFER
 FIND INDEX TO LAST WORD
 R2 HAS WORDS + 1

LOAD \$B3 WITH ADDRESS OF CHANNEL PROG.
 RANGE OF CHANNEL PROGRAM
 PICK UP ADDRESS OF LCT TABLE
 FIND WRITE CHANNEL
 WRITE OUT CHANNEL PROGRAM
 CHAN PROG
 RANGE OF CHANNEL PROGRAM
 RAM ADDRESS
 RIGHT BYTE START

SEND OUT LCT BYTES
 LCT TABLE ADDRESS

LOAD \$R1 WITH INDEX
 GET CCB CONTROL WORD

FORM CCB FOR NORMAL OPERATION
 CPU DATA ADDRESS
 CCB CONTROL WORD

BRANCH MEANS FLAG SET TO BYPASS START IO

TIME DELAY

SAVE REGS. R1,2,4,5, B2,3,5 I,<
 LOAD \$B2 WITH CPU ADDRESS OF BUFFER
 LOAD \$R4 WITH NUMBER OF BYTES
 STORE P COUNTER START

SET BYTE INDEX
 RANGE
 ADDRESS OF SEND BUFFER
 ADDRESS OF RECEIVE BUFFER

GET SENT DATA
 DATA ERROR

WHAT SHOULD BE IN NEXT BYTE
 INPUT EXCEEDED RANGE

READ P VALUE
 BRANCH IF RECEIVE CHAN
 SHOULD BE FOR TRANSMIT SIDE

P VALUE FOR GOOD COMPLETION
 COMPARE IS VS SB

INCORRECT OPERATION OF CHAN PROGRAM

002560 UB1E EF00 0B37
 002567 UB20 6C08
 002568 UB21 9380 0B3B
 002569
 002570 UB23 0F81 000D
 002571 UB25 5C05
 002572 UB26 8700 14E1
 002573 UB28 EB80 14A7
 002574 UB2A E870 4402
 002575 UB2C EF00 0B37
 002576 UB2E 6C06
 002577 UB2F 9380 0B3B
 002578
 002579 UB31 3E02
 002580 UB32 0F80 0AEE
 002581
 002582
 002583 UB34 0206
 002585 UB35 0226
 002586 UB36 0003
 002587 UB37 C002
 002588 UB38 C022
 002589 UB39 0004
 002590 UB3A 0000
 002591
 002592
 002593 UB3B C380 1113
 002595
 002596 UB3D A870 0100
 002597 UB3F DBA8 14C5
 002598 UB41 DF80 14C5
 002599 UB43 9870 FF00
 002600 UB45 BB96 0000 X
 002601 UB47 0F00 UB3B
 002602 UB49 LDD3
 002603 UB4A 0386
 002604 UB4B BB80 1EBB
 002605 UB4D BF80 14C5
 002606 UB4F 0FF0
 002607 UB50 DF80 0B6D
 002608 UB52 UF80 UB9E
 002609 UB54 UF80 UB64
 002610 UB56 A870 00F0
 002611 UB58 DBA8 14C5
 002612 UB5A UF80 0B94
 002613 UB5C B380 UFCC
 002614 UB5E UB34
 002615
 002616 UB5F FBC0 0003 X
 002617 UB61 0360 0000 X
 002618 UB63 0F80
 002619 UB64 UB5F
 002620 UB65 14BE
 002621 UB66 14D6
 002622
 002623
 002624
 002625
 002626
 002627
 002628
 002629
 002630
 002631
 002632
 002633
 002634 UB67 7CF0
 002635 UB68 C870 0100
 002636 UB6A 8751
 002637 UB6B C380 1189
 002638 UB6D 14C7
 002639 UB6E 0040
 002640
 002641 UB6F 4041
 002642 UB70 9854
 002643 UB71 BB98 0B6D
 002644 UB73 BF80 0B6D
 002645 UB75 7780 UB6A
 002646
 002647 UB77 7CFB
 002648 UB78 8756
 002649
 002650 UB79 B380 0628
 002651
 002652
 002653 UB7B C380 1161
 002654 UB7D 0F88
 002655 UB7E C800 1561
 002656 UB80 C380 1173
 002657 UB82 8755
 002658 UB83 8055
 002659 UB84 0054
 002660
 002661 UB85 82D5
 002662 UB86 1000
 002663 UB87 0506
 002664 UB88 0F00 UB79
 002665 UB8A 67F4
 002666 UB8B F380 125C
 002667 UB8D 7DFB
 002668 UB8E 0988
 002669 UB8F C870 0020
 002670 UB91 8751

* STR \$R6,<CPT3A
 LDV \$R6,=8
 LNJ \$B1,<TESC GO DO TEST FOR LRC8
 * B CRZR SKIP CRC12
 LDV \$R5,=5
 CL <MASK RESET MASK
 LAB \$B6,<CRC12 SET CONF. IN CPT FOR CRC12
 LDR \$R6,<Z'4402
 STR \$R6,<CPT3A NUMBER OF BITS IN CHARACTER
 LDV \$R6,=6
 LNJ \$B1,<TESC DO TEST FOR CRC12
 * CRZR ADV \$R3,=2 INCREMENT TO NEXT INPUT CHANNEL
 B <NXCHC
 * * LCT FOR ABOVE TEST
 * CPT3 DC Z'0206* P, MSB, RCV
 DC Z'0226* P, XMII, MSB
 TESCA DC Z'0003* PAUSE DISABLE, RCV
 DC Z'0002* CRC TYPE, RCV
 DC Z'0022* CRC, XMII
 DC Z'0004* PAUSE DISABLE, XMIT
 DC 0 END OF LCT
 * *
 * TESC LNJ \$B4,<CRC DO SIMULATED CRC
 * TESCA LDR \$R2,=X'100 FORM ADDRESS FOR RECEIVE DATA
 LAB \$B5,*CADD\$,\$R2
 STB \$B5,*CADD\$
 LDR \$R1,=X'100
 LAB \$B3,*ZV\$HR,\$R1
 NOP <TESC
 CMB \$B5,\$B3
 BLE >TESC
 LAB \$B3,<SDB
 STB \$B3,*CADD\$
 B >TESCA
 TESCC STB \$B5,<CP1
 STB \$B5,*COP+4+2*\$AF
 STB \$B5,*CP2+4+\$AF
 LDR \$R2,=X'FO
 LAB \$B5,*CADD\$,\$R2
 STB \$B5,<CP3
 LNJ \$B3,<SETLCT
 DC <CP13 SEND LCT OUT FOR THIS LINE
 * CP2 CALL ZV\$P,\$,DFLT,RANGE1
 * * * IN THIS TEST 4 CCB'S ARE INITIALLY SET UP AND ONE ADDITIONAL IS SET UP AFTER DATA TRANSFERS ARE STARTED. THE BASE OF THE CCB'S IS MOVED UP WITH EACH NEW BLOCK XFER SO THAT ALL AVAILABLE MEMORY WILL HAVE DATA TRANSFERRED TO IT. THE CCB'S ARE AS FOLLOWS:
 * * CCB CPU ADD RANGE (WORDS)
 * * 1 B 80
 * * 2 B+80 40
 * * 3 B+C0 20
 * * 4 B+E0 10
 * * 5 B+F0 10
 * * * IN THE ABOVE B = @ADD\$5. B STARTS AT 1900(HEX) AND INCREASES BY 100(HEX) AFTER EACH SET OF 5 CCB'S.
 * * TESB LDV \$R7,=-4 FORM CCB FOR CHANNEL RUN
 LDR \$R4,X'1000
 CL =\$R1
 CP1 LNJ \$B4,<MCCB CPU DATA ADDRESS
 DC <DUMMY SET FOR NO RUPT
 DC X'0040*
 * SUR \$R4,1 SHIFT OVER RANGE
 LDR \$R1,=\$R4
 LAB \$B3,*<CP1,\$R1
 STB \$B3,<CP1
 BINC \$R7,<TESB SET FOR NEXT CCP
 * LDV \$R7,=-5
 CL =\$R6
 * TESF LNJ \$B3,<STRTRIO START I/O
 * TESG LNJ \$B4,<INXT INPUT NEXT STATUS
 B >TESE
 TESH LDR \$R4,<CONT6
 LNJ \$B4,<CGSCH
 CL =\$R5
 IO =\$R5,=\$R4 INPUT STATUS
 * * TESE LB =\$R5,=Z'1000* SEE IF STATUS COMPLETE
 BBT >TESD
 NOP <TESF
 BINC \$R6,>TESH TRY AGAIN
 * TESD LNJ \$B7,<ERROR CCB NOT COMPLETE AFTER CHANNEL PROG. EXECUTIO
 CMV \$R7,=-5
 BNE >TESJ
 LDR \$R4,X'20* RANGE
 CL =\$R1 CLEAR INDEX

002671 0B92 C380 1189
 002672 0B94 14C7
 002673 0B95 0060
 002674 0B96 7780 0B7B
 002675
 002676
 002677 0B98 FBC0 0003
 002678 0B9A D380 0000
 002679 0B9C 0F80
 002680 0B9D 1EBB
 002681 0B9E 0B98
 002682 0B9F 14E1
 002683 0BA0 14D6
 002684 0BA1 14E3
 002685 0BA2 89C0 0940
 002686 0BA4 090F
 002687 0BA5 DC80 0B9E
 002688 0BA7 D800 14E5
 002689 0BA9 E800 14E4
 002690 0BAB F800 14E3
 002691 0BAD F380 125C
 002692
 002693
 002694
 002695
 002696
 002697
 002698
 002699
 002700 0BAC F870 0100
 002701 0BBD 8870 5020
 002702 0BEE F800 1581
 002703
 002704 0BC0 FBC0 0003
 002705 0BC1 D380 0000
 002706 0BC2 0F80
 002707 0BC3 162A
 002708 0BC4 C380 10FC
 002709 0BC5 8753
 002710 0BC6 8751
 002711 0BC7 C380 10EC
 002712 0BC8 3880 10FC
 002713 0BC9 8870 0100
 002714 0BCA 8870 0100
 002715 0BCB 8870 0100
 002716 0BCD 8870 0100
 002717 0BCD 9380 1069
 002718 0BCD 145F
 002719 0BCD 003C
 002720 0BCD 0200
 002721 0BCD 0000
 002722
 002723
 002724
 002725
 002726
 002727 0BCD 0B80 1EBB
 002728 0BCD EB80 14A4
 002729 0BCD 5C0A
 002730 0BCD 6C0B
 002731 0BCD F870 01F4
 002732 0BCD C380 1113
 002733
 002734 0BCD b380 0FCC
 002735 0BCD 0B34
 002736
 002737 0BCD C870 01F4
 002738 0BCD C380 1189
 002739 0BCD 1EBB
 002740 0BCD 0040
 002741
 002742 0BCD C870 00C8
 002743 0BCD C380 1189
 002744 0BCD 1FB5
 002745 0BCD 0040
 002746
 002747 0BCD C870 0064
 002748 0BCD C380 1189
 002749 0BCD 2019
 002750 0BCD 0040
 002751
 002752 0BCD C870 0064
 002753 0BCD C380 1189
 002754 0BCD 204B
 002755 0BCD 0040
 002756
 002757
 002758 0BCD B380 0628
 002759
 002760
 002761
 002762 0BCF C380 11C8
 002763 0C01 UF82
 002764 0C02 003C
 002765
 002766 0C03 82D5
 002767 0C04 1000
 002768 0C05 0500 0C09
 002769 0C07 F380 125C

CP3 LNJ \$B4,<MCCB
 DC <DUMMY
 DC X'0060'
 TESJ BINC \$R7,<TESG
 COMP CALL ZV\$C,SDB,\$,MASK,RANGE1,ERAR
 X

CMZ ERAK
 BE \$NOERC
 LDB \$B5,<COMP+4+2*\$AF
 LDR \$R5,<ERAR+2
 LDR \$R6,<ERAR+1
 LDR \$R7,<ERAR
 LNJ \$B7,<ERROR
 * OF CRC+1. \$B5 POINTS TO START OF CALL ZV\$C0
 *-----

NOERC LNJ \$B4,<CHCT
 B >\$+2
 DC 0
 * LDR \$R7,=X'100'
 LAB \$B3,<SDB
 JMP \$B1
 *-----

DATA OUTPUT TEST

UTPT1 LDR \$R3,=A*P '
 STK \$R3,<ERMG+1
 CALL ZV\$1.ZV\$TC,TSTP
 X

CL =SR3
 CL =SR1
 LNJ \$B4,<GENITZ
 *-----

UTPT2 LNJ \$B4,<FLN
 JMP \$B2
 BODD \$R3,>UTPT4
 INC \$R3
 B >UTPT2
 UTPT4 CL <MASK
 *-----

LOAD AND PACK RAM INSTRUCTIONS

LNJ \$B1,<SDATA
 DC <R01
 DC (VALTST-R01)*2
 DC X'200'
 DC 0
 *-----

NUMBER OF RAM INSTRUCTIONS
 RAM STARTING ADDRESS
 EVEN START ADDRESS

*-----

FORM RANDOM DATA TABLE TO BE OUTPUT

LAB \$B3,<SDB
 LAB \$B6,<CRC16
 LDV \$R5,10
 LDV \$R6,=11
 LDR \$R7,=500
 LNJ \$B4,<CRC
 *-----

LOAD \$B3 WITH ADDRESS OF SDB
 LOAD \$B6 WITH ADDRESS OF CRC16
 INITIAL CHARACTER
 NUMBER OF BITS IN CHAR.
 THE NUMBER OF WORDS MADE IN TABLE

UTPT5 LNJ \$B3,<SETLCT
 DC <CPT3
 *-----

LDR \$R4,=500
 LNJ \$B4,<MCCB
 DC <SDB+250
 DC X'0040'
 *-----

RANGE OF BYTES TO BE LOADED FOR CRC
 FORM CCB FOR CHANNEL RUN
 XMIT BUFFER ADDRESS
 NO RUP1

LDR \$R4,=200
 LNJ \$B4,<MCCB
 DC <SDB+250
 DC X'40'
 *-----

MAKE CCB

LDR \$R4,=100
 LNJ \$B4,<MCCB
 DC <SDB+350
 DC X'40'
 *-----

MAKE CCB
 CPU ADDRESS
 VALID BIT ONLY

LDR \$R4,=100
 LNJ \$B4,<MCCB
 DC <SDB+400
 DC X'40'
 *-----

MAKE CCB
 CPU ADDRESS
 VALID

LNJ \$B3,<STRT10
 *-----

START I/O

*-----

WAIT FOR FIRST CCB TO GET READY AND THEN GIVE 5TH CCB

LNJ \$B4,<TEST
 B >\$+2
 DC 60
 *-----

WAIT FOR STATUS COMPLETE
 RETURN
 1/2 SEC TIMEOUT

LB =\$R5,=Z'1000'
 BBT <UTPT7
 LNJ \$B7,<ERROR
 *-----

GET STAT COMPLETE BIT
 IF TRUE GO SET UP CCB
 NO CCB COMPLETE ON TRANSMIT

002769 UC09 C870 0064 * OTPT7 LDR \$R4,=100 RANGE
 002770 UC0B C380 1189 LNJ \$B4,<MCB MAKE CCB
 002771 UC0D 2070 DC \$SDB+450 CPU ADDRESS
 002772 UC0E 0060 DC X'60' VALID LAST BLOCK
 002773 UC0F C380 1205 * LNJ \$B4,<DLAY TIME DELAY
 002774 UC11 E853 LDR \$R6,=\$R3 CALCULATE LCT CRC RESIDUE ADDRESS
 002775 UC12 6F20 MLY \$R6,=X'20'
 002776 UC13 6E03 ADV \$R6,=3
 002777 UC14 EF40 0010 STR \$R6,OTPT3
 002781 UC16 5CFF * ACCUMULATE CRC OF RANDOM TABLE FORMED EARLIER
 002782 UC17 BB80 1EBB LDV \$R5,=-1
 002783 UC19 E680 14A4 LAB \$B3,<SDB
 002784 UC1B 6C08 LAB \$B6,<CRC16
 002785 UC21 F870 01F4 LDR \$R6,8 8 BIT CHARACTER
 002786 UC23 C380 1113 LNJ \$B4,<CKC NUMBER IN TABLE
 002787 UC24 0002 * DEC =\$R3 GET READ CHANNEL
 002788 UC25 0000 LNJ \$B1,<RDATA DO BLOCK READ
 002789 UC26 0000 DC <DUMMY TO HERE
 002790 UC27 88D3 14C7 OTPT3 DC 2 RANGE =2
 002791 UC28 9380 1098 DC 0 RAM ADDRESS GUS HERE
 002792 UC29 5018 DC 0 EVEN BYTE BOUNDARY
 002793 UC30 14C7 * LNJ \$B7,<DUMMY
 002794 UC31 3E03 SCL \$R5,8 ARE CRC'S EQUAL
 002795 UC32 C380 10EC CMR \$R5,<CRCAC YES DO NEXT CHANNEL
 002796 UC33 8382 >PRFR1 * 'IS' IN \$R5 '\$B' IN \$R6
 002797 UC34 8382 OTPT1 LDR \$R6,<CRCAC
 002798 UC35 3B80 0BE2 PRFR1 ADV \$R3,=3 INCREMENT TO NEXT CHANNEL
 002799 UC36 8AD3 OTPT1 LNJ \$B4,<FLN
 002800 UC37 0FFA OTPT1 JMP \$B2
 002801 UC38 10EC BUDD \$R3,<OTPT5
 002802 UC39 10EC INC \$R3
 002803 UC40 10EC =\$R3
 002804 UC41 10EC B >OTPT1
 *-----
 002805 UC42 F380 125C
 002806 UC43 3E03 * LNJ \$B7,<ERROR OUTPUT ERROR
 002807 UC44 10EC PRFR1 ADV \$R3,=3
 002808 UC45 10EC OTPT1 LNJ \$B4,<FLN
 002809 UC46 10EC >PRFR1
 002810 UC47 10EC JMP \$B2
 002811 UC48 10EC BUDD \$R3,<OTPT5
 002812 UC49 10EC INC \$R3
 002813 UC50 10EC =\$R3
 002814 UC51 10EC B >OTPT1
 *-----
 002815 UC52 10EC
 002816 UC53 10EC * INTERRUPT DESCRIPTION
 002817 UC54 10EC
 002818 UC55 10EC ***
 002819 UC56 10EC BEFORE RUNNING UNDER INTERRUPT MODE THE FOLLOWING
 002820 UC57 10EC TABLES MUST BE SET UP. ALL TABLES CONTAIN 16 ENTITIES
 002821 UC58 10EC CORRESPONDING TO THE 16 CHANNELS IN SEQUENCE. AN ENTITY
 002822 UC59 10EC IS EITHER A DATA OR ADDRESS CONSTANT.
 002823 UC60 10EC
 002824 UC61 10EC
 002825 UC62 10EC * INMB - SET UP TO CONTAIN THE NEG. NUMBER OF
 002826 UC63 10EC INTERRUPTS EXPECTED PER CHANNEL.
 002827 UC64 10EC IF IT IS SET TO A POS. NUMBER IT WILL
 002828 UC65 10EC BE ASSUMED THAT NO INTERRUPTS ARE EXPECTED
 002829 UC66 10EC ON THAT CHANNEL.
 002830 UC67 10EC
 002831 UC68 10EC * ILV - SET UP TO CONTAIN THE LEVEL ASSIGNED
 002832 UC69 10EC TO EACH CHANNEL.
 002833 UC70 10EC
 002834 UC71 10EC * IST - SET UP TO CONTAIN THE STATUS EXPECTED
 002835 UC72 10EC ON EACH CHANNEL AFTER INTERRUPTS.
 002836 UC73 10EC
 002837 UC74 10EC
 002838 UC75 10EC * AFTER AN INTERRUPT TEST HAS BEEN TERMINATED (NORMALLY OR OTHERWISE)
 002839 UC76 10EC THERE IS ONE OTHER TABLE CONTAINING USEFUL INFORMATION
 002840 UC77 10EC
 002841 UC78 10EC * PRIST - CONTAINS ORDER IN WHICH FIRST 16
 002842 UC79 10EC INTERRUPTS OCCURRED.
 002843 UC80 10EC
 002844 UC81 10EC * THE PROGRAM RUNS AT 5 LOGICALLY DISTINCT LEVELS:
 002845 UC82 10EC
 002846 UC83 10EC * LEVEL 0 - USED FOR SETUPS AND DATA CHECKING.
 002847 UC84 10EC IT IS EXITED BY SCHEDULING LEVEL 63
 002848 UC85 10EC BY MEANS OF A "LEV" INSTRUCTION.
 002849 UC86 10EC
 002850 UC87 10EC * LEVEL 3 - REAL TIME CLOCK TIMEOUT. MAJOR TESTS
 002851 UC88 10EC WITH INTERRUPTS TERMINATE BY A RTC
 002852 UC89 10EC TIMEOUT. THIS LEVEL EXITS BY SCHEDULING
 002853 UC90 10EC LEVEL 0.
 002854 UC91 10EC
 002855 UC92 10EC
 002856 UC93 10EC * LEVEL 4 - CCB DISPATCHER AND INTERRUPT MONITOR.
 002857 UC94 10EC THIS ROUTINE DOES STATUS CHECKING AND
 002858 UC95 10EC DISPATCHES A NEW CCB FOR A CHANNEL
 002859 UC96 10EC IF NEEDED. LEVEL 4 EXITS TO LEV 63 BY
 002860 UC97 10EC A "LEV" INSTRUCTION.
 002861 UC98 10EC
 002862 UC99 10EC * LEVELS 5-62 - 16 OF THESE LEVELS (VARIABLE) ARE USED
 002863 UC100 10EC FOR THE 16 CHANNELS. EACH LEVEL
 002864 UC101 10EC POINTS TO THE SAME RE-ENTRANT
 002865 UC102 10EC CHANNEL INTERRUPT HANDLER.
 002866 UC103 10EC
 002867 UC104 10EC * LEVEL 63 - A "DO NOTHING-WAIT" ROUTINE
 002868 UC105 10EC RESIDES AT THIS LEVEL.
 002869 UC106 10EC
 002870 UC107 10EC * TO USE THE PROGRAM INTERRUPT STRUCTURE THE FOLLOWING
 002871 UC108 10EC STEPS MUST BE TAKEN
 002872 UC109 10EC
 002873 UC110 10EC 1. USE ROUTINE INTITZ. THIS INITIALIZES ALL
 002874 UC111 10EC TABLES.
 002875 UC112 10EC
 002876 UC113 10EC 2. FILL TABLES WITH INFORMATION.
 002877 UC114 10EC
 002878 UC115 10EC 3. SETUP INTERRUPT VECTORS. (INITITZ PUTS
 002879 UC116 10EC VECTORS FOR ERROR HANDLER IN LEVELS 5-62. IT
 002880 UC117 10EC SETS UP VECTORS FOR LEV 0,3,4, AND 5 TO THEIR
 002881 UC118 10EC CORRECT VALUE.)

* USE ROUTINE "RTC" TO SET UP RTC TO
 DESIRED TIMEOUT VALUE AND TO START RTC.
 * TURN ON DESIRED CHANNELS.
 * SCHEDULE AND DISPATCH LEVEL 63.
 * WHEN THE RTC TIMEOUT IS COMPLETE THE PROGRAM
 WILL RESUME AT THE INSTRUCTION FOLLOWING THE "LEV"
 WHICH DISPATCHED LEVEL 63.
 * *****
 * FIRST INTERRUPT TEST
 *
 UC39 B870 5220
 UC3D BF00 1581
 UC3F D380 0003
 UC41 0F60 X
 UC42 1633
 UC43 8753
 UC44 B870 0080
 UC46 C380 10FC
 UC48 C380 10EC
 UC4A 8382
 UC4B 9380 0F18
 UC4D C380 0E18
 UC4F C380 1205
 UC51 B380 0FCC
 UC53 UD15
 UC54 9870 5020
 UC56 9F30 2690
 UC58 1C06
 UC59 9F30 26A0
 UC5B BF00 14CD
 UC5D 8953
 UC5E 0001
 UC5F 9380 1069
 UC61 0CD0
 UC62 0024
 UC63 0200
 UC64 0000
 UC65 B800 14CD
 UC67 C870 0004
 UC69 8751
 UC6A C380 1189
 UC6C 2BAA
 UC6D 0060
 UC6E C380 0DF6
 UC70 C380 10DB
 UC72 C800 1550
 UC74 C380 1173
 UC76 CF00 UC86
 UC78 9880 UC87
 UC7A 9F80 0EEC
 UC7C C380 124C
 UC7E 000A
 UC7F 8F00 0EEE
 UC81 7884
 UC82 B870 803F
 UC84 0F80 UC92
 UC86 0000
 UC87 B380 10B0
 UC89 4000
 UC8A 0000
 UC8B E870 00BF
 UC8D 8055
 UC8E 0000 UC86
 UC90 0F80 UC8B
 UC92 0005
 UC93 C380 1161
 UC95 82D9
 UC96 1000
 UC97 0503
 UC98 F380 125C
 UC9A B700 14CC
 UC9C 9380 0F18
 UC9E 9870 9020
 UC9F 8980 14CC
 UC9A 0983
 * USE ROUTINE "RTC" TO SET UP RTC TO
 DESIRED TIMEOUT VALUE AND TO START RTC.
 * TURN ON DESIRED CHANNELS.
 * SCHEDULE AND DISPATCH LEVEL 63.
 * WHEN THE RTC TIMEOUT IS COMPLETE THE PROGRAM
 WILL RESUME AT THE INSTRUCTION FOLLOWING THE "LEV"
 WHICH DISPATCHED LEVEL 63.
 * *****
 * FIRST INTERRUPT TEST
 *
 INST1 LUR \$R3=>A'R'
 STR \$R3,<ERMG+1
 CALL ZV\$T,ZV\$TC,TSTR
 CL = \$R3
 LEV = Z'0080'
 INLOOP LNJ \$B4,<GENITZ
 LNJ \$B4,<FLN
 JMP \$B2
 LNJ \$B1,<INTIZ
 LNJ \$B4,<CHINTZ
 LNJ \$B4,<DELAY
 LNJ \$B3,<SETLCT
 LNJ \$B3,<INITLCT
 * SET UP EXPECTED STATUS TABLE
 LUR \$R1=>'5020'
 STR \$R1,<IST,\$R3
 LDV \$R1,>6
 STR \$R1,<ILV,\$R3
 OUT CHANNEL PROGRAM
 STR \$R3,<IPR
 LB1 = \$R3,>X'1'
 LDR \$B1,<SDATA
 DC <GNOPRG
 DC 36
 DC = X'200'
 DC 0
 LDR \$R3,<TPR
 OUT CCB. IN THIS FIRST TEST THE "1" BIT (BIT 0) IS
 SET TO A "0". NO INTERRUPT SHOULD OCCUR FOR THIS FIRST TEST
 LDR \$R4,>4
 CL = \$R1
 LNJ \$B4,<MCCB
 DC <RTD
 DC = X'0060'
 SEND CCB
 ANY ADDRESS
 LAST BLOCK BIT ONLY
 * SEND OUT INTERRUPT LEVEL 6 + RETURN CHANNEL NUMBER TO LCT
 LNJ \$B4,<LEVRC
 OUTPUT LEVEL + RETURN CHANNEL
 * SET UP INTERRUPT VECTOR IN CP
 LNJ \$B4,<SETVEC
 LDR \$R4,<CONT2
 LNJ \$B4,<CGSCH
 STR \$R4,<RNFC
 SETS UP VECTOR FOR LEVEL
 SET UP IN TABLE ILV
 GET INPUT RANGE FC
 PUT IN ADDRESS
 * TURN ON CHANNEL
 LAB \$B1,<LV63TA
 STB \$B1,<L635SV2
 LNJ \$B4,<RTIC
 DC X'000A'
 SAVE <L635SV3,X'7884'
 GET ADDRESS TO CONTINUE
 PUT IN LEVEL 63 SAVE AREA
 SETUP RTC TO INTERRUPT
 ***** X'000A' AFTER 50 MSEC
 LEV = Z'803F'
 B <BKMR
 KNFC DC 0
 LV63TA LNJ \$B3,<TRNON
 DC X'4000'
 DC 0
 IDLP LEV = Z'00BF'
 IO = \$R5,<RNFC
 TURN ON CHANNEL
 CHANNEL CONTROL WORD - START IO
 WORKS FOR XM1 + RECEIVE
 DUMB LEV TO GENERATE RINI
 INPUT RANGE
 B <IDLP
 * PROGRAM RETURNS TO LEVEL 0 HERE IF NO ERRORS
 BKMR RTCF TURN OFF REAL TIME CLOCK
 * CHECK STATUS IS COMPLETE
 LNJ \$B4,<INXT
 LB = \$R5,Z'1000'
 INPUT NEXT STATUS TO R5
 BB1 >S+2+SAF
 LNJ \$B7,<ERROR
 STATUS NOT COMPLETE AFTER
 "GNB" ISSUED
 * SET UP FOR NEXT TEST. SET UP CCB WITH I BIT = 1 (PERMITTING
 * INTERRUPTS)
 *
 INST2 CL <FLAG
 LNJ \$B1,<INTIZ
 LUR \$R1,>'9020'
 CMZ <FLAG
 BNE >INST4
 FLAG CLEARED FOR FIRST TIME THROUGH
 INITIALIZE PROGRAM FOR INTERRUPTS
 BRANCH IF SECOND TIME THRU

002987 UCA3 9870 5020
 002988 UCA5 0F11 0003
 002989 UCA7 9670 0020
 002990 UCA9 9F30 2690
 002991 *
 002992 UCLB 1C06
 002993 UCAF 9F30 26A0
 002994 *
 002995 UCAF 1CFF
 002996 UCAF 9F30 2680
 002997 UCD1 C380 10DB
 002998 *
 002999 * SEND OUT CCB WITH 1 BIT SET
 003000 * THIS CCB NOT USED SECOND TIME THRU LOOP
 003001 *
 003002 UCB3 4C04
 003003 UCB4 8751
 003004 UCB5 C380 1189
 003005 UCB7 2BA0
 003006 UCB8 00E0
 003007 UCB9 98B0 0C87
 003008 UCBB 9F80 0EEC
 003009 UCBD C380 124C
 003010 UCBF 0006
 003011 UCC0 8F00 0EEE
 003012 UCC2 7884
 003013 UCC3 8E70 803F
 003014 *
 003015 * RETURN HERE AFTER INTERRUPTS HAVE OCCURED
 003016 UCC5 0005
 003017 *
 003018 RTCF
 003019 * CHECK THAT INTERRUPT DID OCCUR
 003020 UCC6 9380 0FB9
 003021 UCC8 9800 14CC
 003022 UCCA 198F
 003023 UCCB 8A80 14CC
 003024 UCCD 4C01
 003025 UCE5 C380 1189
 003026 UCD0 2BA0
 003027 UCD1 0060
 003028 *
 003029 UCD2 B380 0FCC
 003030 UCD4 UCD7
 003031 UCD5 UF80 UC9C
 003032 *
 003033 UCD7 0317
 003034 UCD8 0000
 003035 *
 003036 UCD9 8AD3
 003037 UCDA 0F80 UC46
 003038 *
 003039 * CHANNEL PROGRAM FOR ABOVE TEST
 003040 *
 003041 UCDC BD31
 003042 UCDG 20BD
 003043 UCDI 3100
 003044 UCDF C605
 003045 UCE0 0101
 003046 UCE1 5A01
 003047 UCE2 5D26
 003048 UCE3 FBBD
 003049 UCE4 3120
 003050 UCE5 BD31
 003051 UCE6 00BD
 003052 UCE7 3180
 003053 UCE8 01BD
 003054 UCE9 3120
 003055 UCEA BD31
 003056 UCEB 0001
 003057 UCEC BD31
 003058 UCED 0001
 003059 UCEE 0000
 003060 *
 003061 *
 003062 * * * * * * *
 003063 * DATA SET SCAN TEST
 003064 *
 003065 *-----
 003066 UCF1 B870 5320
 003067 UCF1 BF00 1581
 003068 UCF3 FBC0 0003
 UCF5 D380 0000
 UCF7 UF80
 UCF8 163E
 003069 UCF9 8753
 003070 UCFA C380 10EC
 003071 UCFC 8382 10EC
 003072 UCFD C380 10FC
 003073 Ucff C380 1205
 003074 *
 003075 UDD1 BF00 14CD
 003076 UDD3 8953
 003077 UDD4 0001
 003078 0005 9380 1069
 003079 UDD7 0DF1
 003079 UDD8 000A
 003080 UDD9 0200
 003081 UDDA 0000
 003082 UDDB B800 14CD
 003083 UDDC B800 14CD
 003084 *
 003085 *
 003086 *
 003087 * DATA SCAN MASK = ALL ZEROS (0)
 003088 * MAKE CLA STATUS = ALL ONES (F)
 003089 *
 003090 UDD0 B380 0FCC
 003091 UDDF UD11
 003092 UDI0 OF90
 003093 *
 INST4 LDR \$R1,=Z'5020'
 XUR \$EX14
 NEXT4 STR \$R1,<IST,\$R3
 *
 LDV \$R1,=6
 STK \$R1,<ILV,\$R3
 *
 LDV \$R1,=-1
 STR \$R1,<INMB,\$R3
 LNJ \$B4,<SETVEC
 *
 * SEND OUT CCB WITH 1 BIT SET
 * THIS CCB NOT USED SECOND TIME THRU LOOP
 *
 LDV \$R4,=4
 CL =>R1
 LNJ \$B4,<MCCB
 DC <RTD
 DC =Z'0UE0'
 LAB \$B1,<LV63TA
 STB \$B1,<L635V2
 LNJ \$B4,<RTC
 DC 6
 SAVE <L635V3,=X'7884'
 LEV =Z'803F'
 *
 * RETURN HERE AFTER INTERRUPTS HAVE OCCURED
 *
 RTCF
 *
 * CHECK THAT INTERRUPT DID OCCUR
 *
 LNJ \$B1,<CHLV
 LDR \$R1,<FLAG
 BNEZ \$R1,<INST3
 INC <FLAG
 LDV \$R4,=1
 LNJ \$B4,<MCCB
 DC <RIB
 DC X'6U'
 *
 LNJ \$B3,<SETLCT
 DC <SFLG
 B <INST2
 *
 SFLG DC X'0317'
 DC 0
 *
 INST3 INC =>R3
 B <INLOOP
 *
 * CHANNEL PROGRAM FOR ABOVE TEST
 *
 GNBPRG DC =Z'BD31'
 DC =Z'20BD'
 DC =Z'3100'
 DC =Z'605'
 DC =Z'U101'
 DC =Z'5A01'
 DC =Z'5D26'
 DC =Z'FBBD'
 DC =Z'3120'
 DC =Z'BD31'
 DC =Z'U0BD'
 DC =Z'3180'
 DC =Z'U1BD'
 DC =Z'3120'
 DC =Z'BD31'
 DC =Z'0001'
 DC =Z'BD31'
 DC =Z'0001'
 DC 0
 *
 *-----
 DSST LDR \$R3,A'S'
 STR \$R3,<ERMG+1
 CALL ZV\$1,ZV\$TC,TSTS
 X
 DSSTLP CL =>R3
 LNJ \$B4,<FLN
 JMB \$B2
 LNJ \$B4,<GENITZ
 LNJ \$B4,<DLAY
 * LOAD CHANNEL PROGRAM FOR DATA SET SCAN
 STR \$R3,<TPR
 LBT =>R3,X'1'
 LNJ \$B1,<DATA
 DC <SCNPORG
 DC =Z'000A'
 DC =X'200'
 DC 0
 LDR \$R3,<TPR
 *
 * OUTPUT LCT DATA
 *
 * DATA SCAN MASK = ALL ZEROS (0)
 * MAKE CLA STATUS = ALL ONES (F)
 *
 LNJ \$B3,<SETLCT
 DC <SCTEST
 B >SCTST1
 *

003109 0D11 000F
 0031095 0D12 002F
 0031096 0D13 FF0E
 0031097 0D14 FF2E
 0031098 0D15 0206
 0031099 0D16 0007
 003100 0D17 0226
 003101 0D18 0027
 003102 0D19 0010
 003103 0D1A 0011
 003104 0D1B 0030
 003105 0D1C 0031
 003106 0D1D 0008
 003107 0D1E 0028
 003108 0D1F 0000

SCTEST DC =Z'000F'
 SCTEST DC =Z'002F'
 SCTEST DC =Z'FF0E'
 INTLCT DC =Z'0206'
 INTLCT DC =Z'0007'
 INTLCT DC =Z'0226'
 INTLCT DC =Z'0027'
 INTLCT DC =Z'0010'
 INTLCT DC =Z'0011'
 INTLCT DC =Z'0030'
 INTLCT DC =Z'0031'
 INTLCT DC =Z'0008'
 INTLCT DC =Z'0028'
 INTLCT DC 0

RECEIVE MASK
 XMIT MASK
 RECEIVE CLA STATUS SET TO FF
 XMIT CLA STATUS SET TO FF
 RCV P COUNTER,<MSB
 RCV P COUNTER,<LSB
 XMIT P COUNTER,<MSB
 XMIT P COUNTER,<LSB
 RCV STAT (LCT)

XMIT SIAIUS (LCT)
 RCV SCAN CONTROL
 XMIT SCAN CONTROL

* START SCAN AND CHECK THAT MASK OF 0 INHIBITS IS OPERATION
 SCTSTI LDR \$R1,=Z'0008'
 BEVN \$R3,>\$+2'
 ADV \$R1,=X'20'
 LNJ \$B4,<OUTLCT
 LNJ \$B4,<DELAYLG
 LNJ \$B4,<ILCTST
 LB =\$R5,=X'10'

DATA SCAN = 1, SET LCT STAT = 1
 MODIFY FOR TRANSMIT
 SEND OUT CHANNEL COMMAND BYTE
 DELAY 250MS
 INPUT LCI STATUS TO R5
 GET DATA SET CHANGE BIT

003110 0D20 9870 0008
 003111 0D22 3B02
 003112 0D23 1E20
 003113 0D24 C380 0E0C
 003114 0D25 C380 1215
 003115 0D28 C380 084E
 003116 0D2A 82D5
 003117 0D2C 0583
 003118 0D2D F380 125C
 003119
 003120
 003121
 003122 0D2F 9870 000E
 003123 0D31 3B02
 003124 0D32 1E20
 003125 0D33 9F57
 003126 0D34 C380 0E6C
 003127 0D36 D570 FF00
 003128 0D38 D657
 003129 0D39 DF00 14CF
 003130 0D3B 8655
 003131 0D3C D570 FF00
 003132 0D3E D657

BBF >\$+2+\$AF
 LNJ \$B7,<ERROR

DATA SET SCAN BIT SET WITH
 MASK OF 0

* READ CLA STATUS BY INPUT DATA SET STATUS
 LDR \$R1,=X'1E'
 BEVN \$R3,>\$+2'
 ADV \$R1,=X'20'
 STR \$R1,=\$R7
 LNJ \$B4,<DSSTA
 AND \$R5,=Z'FF00'
 XOR \$R5,=\$R7
 STR \$R5,<TEMP1
 CPL \$R5
 AND \$R5,=Z'FF00'
 XOR \$R5,=\$R7
 XOR \$R5,=\$R7

BRANCH IF RECEIVE CHAN
 FORM CLA ADDRESS
 READ DATA SET STATUS
 STRIP OFF UNUSED BYTE
 FORM STATUS WITH ADDRESS
 FORM COMPLEMENT OF STATUS
 STRIP ADDRESS
 OR IN ADDRESS

* OUTPUT COMPLEMENT OF NOMINAL CLA STATUS. DATA SCAN IS STILL
 * PROGRESSING.
 STR \$R5,<TEMP1
 LNJ \$B4,<OUTLCT
 LNJ \$B4,DELAY

FIRST STORE AWAY DATA
 OUTPUT BYTE TO LCT
 WAIT FOR FIRMWARE TO FINISH

* INPUT LCT STATUS AND CHECK DATA SET CHANGE IS RESET
 *
 LNJ \$B4,<ILCTST
 LB =\$R5,=X'10'

INPUT LCI STATUS TO R5
 GET DATA SET CHANGE BIT

*
 LNJ \$B7,<ERROR

DATA SET SCAN BIT SET WITH
 MASK OF 0

* START OF LOOP WHICH WILL TEST THE DATA SET MASK
 *
 LDR \$R1,=X'400'
 STR \$R1,<MASK

*
 DSLOOP LNJ \$B3,<SETLCT
 DC <SCTEST
 LDK \$R1,<MASK
 SUL \$R1,1
 BCT <SCITIA
 STR \$R1,<MASK
 XOR \$R1,=X'FF'
 XOR \$R1,=\$R7
 BEVN \$R3,>\$+2'
 ADV \$R1,=X'20'
 LNJ \$B4,<OUTLCT

SEND OUT LCT
 EXIT IF NO MORE BITS TO TEST
 STORE MASK
 OR IN ADDRESS FOR RCV MASK

*
 * OUTPUT BYTE TO CLA POSITION IN LCT. THIS BYTE WILL TEST
 * ONE BIT POSITION CORRESPONDING TO THE MASK WHICH HAS ONE BIT SET.
 * EACH BIT POSITION IN THE STATUS MASK IS CHECKED ONCE
 *
 LNJ \$B4,<DELAY
 LDR \$R1,<TEMP1
 XOR \$R1,<MASK
 LNJ \$B4,<OUTLCT
 LNJ \$B4,<LEVRCH
 LNJ \$B1,<INITIZ
 LDV \$R1,=6
 STR \$R1,<ILV,\$R3

DELAY 25 MSEC
 GET CLA NORMAL STAT + ADDRESS
 WITH LCI ADDRESS
 COMPLEMENT BIT TO TEST
 OUTPUT TO CLA STATUS

*
 * SET UP FOR ERROK INTERRUPT
 * SET UP FOR ERROK INTERRUPT
 LNJ \$B4,<LEVRCH
 LNJ \$B1,<INITIZ
 LDV \$R1,=6
 STR \$R1,<ILV,\$R3

SET LCI BYTES FOR LEVEL = 6 + RETURN CHAN
 INITIALIZE INTERRUPTS

*
 * SWITCH TO LEVEL 63
 * DATA SET SCAN WILL TURN ON WITH CONTROL = "DO"
 LAB \$B1,<LV63TB
 STB \$B1,<LV63V2
 LNJ \$B4,<RTC
 DC 6
 SAVE <LV63V3,=X'7884'

SET PRIORITY IN LEVEL 63 SAVE
 SET RTC TO INTERRUPT
 AFTER 50 MSEC
 PASS ALONG REGISTERS

*
 LEV =Z'803F'
 B >DOL2

SUSPEND CURRENT AND GO TO 63
 RETURN HERE AFTER RTC RUPT

*
 * LEVEL 63 CODE. OUTPUT A DATA SET SCAN CUNROL WORD
 * WITH BIT 0 = 1 (SCAN), BIT 1 =1 (SET STATUS), BIT 2 = 0
 * (INTERRUPT),<BIT 3 = 1 (START CHANNEL PROGRAM)
 *
 LV63TB LNJ \$B3,<SETLCT
 DC <SCTESTVA
 NOER20 NUP >DOL2
 B >NOER20

OUTPUT DATA SCAN BYTE
 WAIT FOR RTC INTERRUPT

*
 * RETURN HERE VIA RTC INTERRUPT. THEN INPUT LCT STATUS
 *
 DOL2 RTCF
 LNJ \$B4,<ILCTST
 LB =\$R5,=X'10'

TURN OFF REAL TIME CLOCK
 INPUT LCI STATUS TO R5
 GET DATA SET CHANGE BIT

003188
 003189
 003190
 003191
 003192
 003193 0D7C B380 0FCC
 003194 0D7E 0E09
 003195 0D7F 0F02
 003196 0D80 0FFF
 003197
 003198
 003199
 003200 0D81 0005
 003201 0D82 C380 084E
 003202 0D84 82D5
 003203 0D85 0010

003203 0D86 0503
 003204 0D87 F380 125C
 003205
 003206
 003207
 003208 0D89 1C0D
 003209 0D8A C380 0E0C
 003210
 003211 0D8C C380 0E25
 003212 0D8E E870 0204
 003213 0D90 D956
 003214 0D91 0903
 003215 0D92 F380 125C
 003216 0D94 UF80 0D50
 003217
 003218
 003219
 003220
 003221 0D96 C380 0E18
 003222 0D98 C380 1205
 003223
 003224 0L9A B3C0 0231
 003225 0D9C 0D11
 003226
 003227
 003228
 003229
 003230
 003231 0D9D B380 UFCC
 003232 0D9F 0DA1
 003233 0DA0 UF84
 003234
 003235 0DA1 FF0F
 003236 0DA2 FF2F
 003237 0DA3 0000
 003238 0DA4 C380 0DF6
 003239 0DA6 9380 UF18
 003240 0DAB 1C06
 003241 0DA9 9F30 26A0
 003242 0DAB C380 10D8
 003243 0DAE 1CF0
 003244 0DAE 9F30 2680
 003245 0DB0 1C0F
 003246 0DB1 9F00 14D1
 003247
 003248
 003249
 003250 0DB3 9800 14D0
 003251 0DB5 C380 0E0C
 003252
 003253
 003254
 003255 0DB7 9B80 0DC4
 003256 0DB9 9F80 0EEC
 003257 0DBB C380 124C
 003258 0DBD 0006
 003259 0DBE 8F00 0EEE
 003260 0DC1 0F00 0DC4
 003261 0DC3 0F8B
 003262
 003263
 003264
 003265
 003266 0DC4 9870 A008
 003267 0DC6 3B02
 003268 0DC7 1E20
 003269 0DC8 C380 0E0C
 003270 0DCA 0F00 0DC4
 003271 0DCC 0F58
 003272 0DCE 0FFD
 003273
 003274
 003275
 003276
 003277 0DCE 0005
 003278 0DCE 0F81 000F
 003279 0DD1 C3C0 0433
 003280 0DD3 C380 084E
 003281 0DD5 82D5
 003282 0DD7 0563
 003283 0DD8 F380 125C
 003284
 003285 0DDA 82D5
 003286 0DDC 0503
 003287 0DDD F380 125C
 003288
 003289
 003290
 003291 0DDE 1C0D
 003292 0DE0 3B02
 003293 0DE1 1E20
 003294 0DE2 C380 0E0C
 003295
 003296
 003297
 003298 0DE4 C380 0E25
 003299 0DE6 E870 0200
 003300 0DE8 D956
 003301 0DE9 0903
 003302 0DEA F380 125C
 003303
 003304
 003305
 003306
 003307
 003308 0DEC 9380 0FB9
 003309
 003310
 003311
 003312 0DEE 8AD3

BBT LNJ >\$+2+\$AF
 * LNJ \$B7,<ERROR DATA SET SCAN BIT NOT SET
 * RESET LEVEL TO ZERO SO BLOCK READ WON'T CAUSE INTERRUPT.
 * LDV LNJ \$R1,<X'D'
 * LNJ \$B4,<OUTLCT ADDRESS OF LEVEL
 * CMR LDR \$R6,<X'204'
 * BE \$R5,<SH6
 * LNJ >\$+2+\$AF
 * B \$B7,<ERROR READ P VALUE TO TEMP
 * B <DSLLOOP CHANNEL PROGRAM NOT STARTED BY
 * SCAN WITH BIT 3 OF CHAN COMMAND = 1.
 * TEST NEXI MASK BIT
 * TEST THAT DATA SET SCAN WILL CAUSE INTERRUPT
 * SCITIA LNJ SB4,<CHINTZ INITIALIZE CHANNEL
 * LNJ \$B4,<DLAY DELAY 25 MS.
 * LNJ \$B3,<SETLCT SEND OUT LCT
 * DC <SCTEST
 * OUTPUT LCT DATA
 * MASK = ALL ONES (F)
 * LNJ \$B3,<SETLCT SET LCI TABLE ADDRESS
 * DC <SCIT1
 * DC <SCIT2
 * SCIT1 DC =Z'FF0F'
 * DC =Z'FF2F'
 * SCIT2 LNJ \$B1,<LEVRCH RECEIVE MASK
 * LNJ \$B1,<INTIZ XMIT MASK
 * LDR \$R1,<6 END OF TABLE
 * STR \$R1,<ILV,\$R3 SET FOR LEVEL 6 IN LEVEL TABLE
 * LNJ \$B4,<SETVEC
 * LDR \$R1,<=-1 SET UP VECTOR FOR LEVEL
 * STR \$R1,<INMB,\$R3 SET FOR ONE RUPT EXPECTED
 * LDR \$R1,<XF1 SET FLAG FO SCAN
 * STR \$R1,<SCFLG
 * SEND OUT COMPLEMENT OF NORMAL STATUS
 * LDR \$R1,<TEMPST
 * LNJ \$B4,<OUTLCT OUT PUI VALUE
 * SWITCH TO LEVEL 63
 * LAB \$B1,<SCIT3 SET P VECTOR IN LEVEL 63 SAVE
 * STB \$B1,<L63SV2
 * LNJ \$B4,<RTC SET RTC TO INTERRUPT
 * DC 6 AFTER 50 MS
 * SAVE <L63SV3,<X'78B4'
 * LEV =Z'803F' SUSPEND CURRENT AND GO TO 63
 * B >DOL3
 * SCIT3 LDR \$R1,<A008' BRANCH IF RECEIVE
 * BEVN \$R3,>\$+2
 * ADV \$R1,<X'20' MODIFY FOR XMIT
 * LNJ \$B4,<OUTLCT OUTPUT BYTE
 * WRUPT NOP
 * NOP <SCIT3
 * B >WRUPT WAIT FOR INTERRUPT
 * RETURN HERE VIA RTC INTERRUPT. CHECK LCI STATUS TO INSURE
 * THAT DATA SET SCAN BIT IS RESET.
 * DOL3 RTCF TURN OFF REAL TIME CLOCK
 * B DOLBB TEMPORARY BYPASS *****
 * LNJ \$B4,<DELAY WAIT FOR CHANNEL PROGRAM TO FINISH
 * LNJ \$B4,<ILCTS1 INPUT LCI STATUS TO R5
 * LB =S\$R5,<X'10' GET DATA SET CHANGE BIT
 * BBF LNJ >\$+2+\$AF
 * LNJ \$B7,<ERROR DATA SET SCAN BIT IS SET WHEN
 * LB =S\$R5,<X'4000' IT SHOULDN'T BE. GET INTERRUPT STATUS BIT
 * BBT LNJ >\$+2+\$AF
 * LNJ \$B7,<ERROR DATA SET SCAN DIDN'T SET RUPT BIT
 * RESET LEVEL IN LCT SO FOLLOWING BLOCK READ WILL NOT CAUSE AN INTERRUPT
 * DOLBB LDV \$R1,<X'D'
 * BEVN \$R3,>\$+2
 * ADV \$R1,<X'20'
 * LNJ \$B4,<OUTLCT ADDRESS OF LEVEL
 * * CHECK P COUNTER TO INSURE THAT THE CHANNEL PROGRAM
 * * DIDN'T START.
 * LNJ \$B4,<RPVLU READ P VALUE TO TEMP
 * LDR \$R6,<X'200'
 * CMR \$R5,<R6
 * BE >\$+2+\$AF
 * LNJ \$B7,<ERROR CHANNEL PROGRAM STARTED
 * BY DATA SET SCAN WHEN IT SHOULD NOT HAVE.
 * * CHECK THAT INTERRUPT OCCURED
 * LNJ \$B1,<CHLV CHECK INTERRUPT OCCURED ON CHANNEL
 * TEST FOR NEXT CHANNEL
 * INC =S\$R3

003313 0DEF 0F80 OCFA * B <DSSTLP GO TO NEXT CHANNEL

003314 * * CHANNEL PROGRAM FOR SCAN TEST

003315 SCNPRG DC =Z'01BD' NOP, <WAIT

003316 DC =Z'3100' WAIT, WAIT

003317 UDF1 01BD DC =Z'BD31'

003318 UDF2 3100 DC =Z'00BD'

003319 UDF3 BD31 DC =Z'3100'

003320 UDF4 00BD DC

003321 UDF5 3100 DC

003322 * * SUBROUTINES FOR INTERRUPT TESTS LEVRCH SWB \$B4,=\$B5 SAVE B4

003323 STS =\$R1 GET S REG

003324 UDF6 CED5 AND SR1,=Z'03C0' STRIP TO CPU ID

003325 UDF7 8C51 STS =\$R1

003326 UDF8 9570 03C0 AND SR1,<RTCHN

003327 UDF9 9F00 1548 STR SR1,<RTCHN

003328 UDFC 9670 0006 XOR SR1,=6

003329 UDFE C800 1565 LDR SR4,<CONT11 PUT IN LEVEL 6

003330 UEO0 C380 1173 LNJ SB4,<CGSCH FUNCTION CODE FOR OUTPUT INT CONT

003331 UEO2 8051 IO =\$R1,=\$R4 MODIFY FOR CHANNEL

003332 UEO3 0054 DC

003333 UEO4 0703 BIOT >\$+2+\$AF OUTPUT INTERRUPT CONTROL

003334 UEO5 F380 125C LNJ SB7,<ERROR RESTORE B4

003335 UEO7 CED5 SWB \$B4,=\$B5

003336 UEO8 8384 JMP \$B4

003337 * LCT CONTROL FOR SCAN TEST.

003338 UEO9 D008 SCTSVA DC =Z'D008' RCV SCAN CONTROL

003339 UEOA D028 DC =Z'D028' =ZMIT SCAN CONTROL

003340 UEOB 0000 DC 0

003341 * * OUTPUT LCT BYTE CONTAINED IN R1 OUILCT SWB \$B4,=\$B5

003342 UEOC CED5 LDR SR4,<CONT5 OUTPUT LCT BYTE FUNC CODE

003343 UEOD C800 1560 LNJ SB4,<CGSCH MODIFY FOR CHANNEL

003344 UEOF C380 1173 IO =\$R1,=\$R4 OUTPUT BYTE

003345 UEOE 8051

003346 UEE1 8051

003347 UEE2 0054

003348 UEE3 0703 BIOT >\$+2+\$AF

003349 UEE4 F380 125C LNJ SB7,<ERROR COMMAND WAS NAKED

003350 UEE5 CED5 SWB \$B4,=\$B5 RESTORE B4

003351 UEE6 8384 JMP \$B4

003352 * * ISSUE CHANNEL INITIALIZE CHINTZ SWB \$B4,=\$B5

003353 UEE7 CED5 LDR SR4,<CONT7 CHANNEL CONTROL

003354 UEE8 C800 1562 LNJ SB4,<CGSCH MODIFY FOR CHANNEL

003355 UEE9 C380 1173 IO =Z'80001,=\$R4

003356 UEEA 8000

003357 UEEB 0054

003358 UEEC 0703 BIOT >\$+2+\$AF

003359 UEEF F380 125C LNJ SB7,<ERROR COMMAND WAS NAKED

003360 UEE1 8384 SWB \$B4,=\$B5 RESTORE B5

003361 * * READ P VALUE FROM RAM

003362 * *

003363 RPVLU LDV \$R1,=6

003364 UEE2 1C06 STR \$R3,<TPR

003365 UEE3 BF00 14CD BEVN \$R3,>DOL4

003366 UEE4 3B03 DEC =\$R3

003367 UEE5 88D3 LDV \$R1,=38

003368 UEE6 8D03 DOL4 LDR \$R4,=\$R3

003369 UEE7 C853 MLV \$R4,=X'20'

003370 UEE8 4F20 ADD \$R1,=\$R4

003371 UEE9 9A54 STD \$R1,<PVLU1 FORM START OF LCT AREA

003372 UEEA 9F00 0E34 LNJ SB1,<RDATA FORM ADDRESS OF P COUNTER

003373 UEEB 9380 1098 DC <TEMP DO BLOCK READ

003374 UEEC 14CE DC Z TO HERE

003375 UEEF 0002 DC 2 BYTES

003376 UEE1 0000 DC RAM ADDRESS

003377 UEE3 0000 DC START AT EVEN CPU BYTE

003378 * *

003379 UEE4 0800 14CE LDV \$R5,<TEMP

003380 UEE5 D570 FFFF AND \$R5,=Z'FFFF'

003381 UEE6 DF00 14CE STR \$R5,<TEMP

003382 UEE7 B800 14CD LDR \$R3,<TPR

003383 UEE8 8384 JMP \$B4 GET BACK TESTED CHANNEL

003384 * *

003385 * * 2ND INTERRUPT TEST.

003386 * * DO BLOCK WRITES AND READS AND CHECK INTERRUPTS.

003387 * * 3 DEFERRED INTERRUPTS ARE ACCUMULATED ON ALL CHANNELS

003388 * * - LEVELS 5 - 62 ARE USED

003389 * *

003390 * *

003391 * *

003392 UEE1 0E3F B870 5420 SITST LDR \$R3,=A'T'

003393 UEE2 BF00 1581 STR \$R3,<ERMG+1

003394 UEE3 CALL ZV\$1.ZV\$TC,TSTT X

003395 UEE4 0E43 FBC0 0003. * *

003396 UEE5 D380 0000 LEV =Z'0080'

003397 UEE6 0E47 0F80 LDV \$R5,=5 SUSPEND + QUICK CHANGE TO LEV 0

003398 UEE7 0E48 1648 STR \$R5,<LVSTR STARTING LEVEL NUMBER

003399 UEE8 0E49 B870 0080 PRILP LAB \$B3,<SD8 GET ADDRESS OF SEND DATA BUFFER

003400 UEE9 0E50 BF80 0E64 STB \$B3,<ADD

003401 UEEA 0E51 C870 0200 LDR \$R4,=X'200'

003402 UEEB 0E52 CF00 0E65 STR \$R4,<RAD RAM ADDRESS

003403 UEEC 0E53 8753 CL =\$R3 R3 IS CHANNEL COUNTER

003404 UEEF 0E54 2CF0 LDV \$R2,=-16 RANGE OF TWO BYTES

003405 UEE1 0E55 4C02 LDV \$R4,=2

003406 * *

003407 * * INITIALIZE CONTROLLER

003408 * *

003409 UEE2 0E59 C380 10FC LNJ \$B4,<GENITZ MLCC GENERAL INITIALIZATION

003410 UEE3 0E5B 9380 0F18 LNJ \$B1,<INTIZ INITIALIZE INTERRUPTS

003411 * *

003412 * * SET UP 3 CCP'S FOR EACH CHANNEL

003413 * *

003414 UEE4 0E5D C380 10EC CCBLP LNJ \$B4,<FLN FIND ACTIVE LINE

003415 UEE5 0E5F UF92 B >RTI ALL HAVE BEEN FOUND

003416 * *

003417 UEE6 0E60 8751 CL =\$R1 R1=0 MEANS LEFT BYTE BOUNDARY

003418 UEE7 0E61 5CFD LDV \$R5,=-3

003419
 003420 OE62 C380 1189
 003421 OE64 14C7
 003422 OE65 0000
 003423
 003424 OE66 57FC
 003425 OE67 8AD3
 003426 OE68 8A80 OE64
 003427 OE6A 8A80 OE65
 003428 OE6C 8A80 OE65
 003429 OE6E AF48 FFF5
 003430 OE70 27ED
 003431
 003432 OE71 D800 0E7E
 003433 OE73 A870 8000
 003434 OE75 9852
 003435 OE76 9500 14B0
 003436 OE78 1900 OE80
 003437
 003438 OE7A 9F00 0E7F
 003439 OE7C 9380 0FA8
 003440 OE7E 0000
 003441 OE7F 0000
 003442 OE80 26A0
 003443 OE81 8AD5
 003444 OE82 5D3F
 003445 OE83 0905
 003446 OE84 DF00 0E7E
 003447 OE85 2041
 003448 OE87 29EE
 003449
 003450
 003451
 003452 OE88 1CF0
 003453 OE89 8752
 003454
 003455 OE8A B810 26B0
 003456 OE8C 8A11
 003457 OE8D 2011
 003458 OE8E 3903
 003459 OE8F A670 0001
 003460 OE91 1879
 003461
 003462 OE92 AF00 0E9B
 003463 OE94 AF00 0EA0
 003464 OE96 AF00 14E1
 003465
 003466
 003467
 003468
 003469 OE98 9380 0FA8
 003470 OE9A FFFD
 003471 OE9B 0000
 003472 OE9C 26B0
 003473
 003474 OE9D 9380 0FA8
 003475 OE9F 5000
 003476 OEAO 0000
 003477 OEAI 2690
 003478
 003479
 003480
 003481 OEAA C380 10DB
 003482
 003483
 003484
 003485 OEAA B380 10CA
 003486 OEAB 8753
 003487 OEAC C380 10EC
 003488 OEAD 0000
 003489
 003490
 003491
 003492 OEAB 1CFD
 003493 OEAD B380 10B0
 003494 OLA0 0400
 003495 OEAE 0001
 003496
 003497 OEAF B380 10B0
 003498 OEB1 0800
 003499 OEB2 0002
 003500
 003501 OEB3 C380 1205
 003502 OEB5 1780 0EAB
 003503
 003504
 003505
 003506 OEB7 C380 124C
 003507 OEB9 000C
 003508
 003509 OEB8 8F00 0EEE
 003510 OEBD 8E70 803F
 003511
 003512
 003513 OEBF. 0005
 003514
 003515 OEC0 9380 0FB9
 003516
 003517
 003518
 003519 OEC2 5D3F
 003520 OEC3 0200 0E4E
 003521 OEC5 8382
 003522
 003523
 003524
 003525 OEC6 8F40 0650
 003526 OEC8 0008
 003527 OEC9 C800 1566
 003528 OECD 8055
 003529 OEEC 0054

* NEXT5 LNJ \$B4,<MCCB
 ADD DC <DUMMY
 RAD DC =X'U'
 * BINC \$K5,>NEXT5
 INC =\$R3
 INC <ADD+\$AF-1
 INC <RAD
 INC <RAD
 STR \$K2,*ADD
 BINC \$R2,>CCBLP
 * SET UP LEVELS
 RT1 LDR \$R5,<LVSTK
 LUR \$R2,=Z'8000'
 RT6 LDR \$R1,=\$R2
 AND \$R1,<IMASK
 BEZ \$R1,<RT7A
 * STR \$R1,<CHAN
 LNJ DC 0
 LVSTK CHAN DC 0
 INC <ILV
 CMV =\$R5
 BEZ \$R5,=63
 STR \$R5,<LVSTK
 SOR \$R2,1
 BNEZ \$R2,>RT6
 * FORM MASK FOR LINES TO BE TESTED
 RT5 LDV \$R1,=-16
 CL =\$R2
 * RT7 LDR \$R3,<ILV+16,\$R1
 INC =\$R1
 SCL \$R2,1
 BEZ \$R3,>RT8
 XOR \$R2,=Z'0001'
 RT8 BLZ \$R1,>RT7
 * STR \$R2,<RT2
 STR \$R2,<RT3
 STR \$R2,<MASK
 * SET UP INTERRUPT INFORMATION
 * LNJ \$B1,<FTBL
 DC -3
 RT2 DC 0
 DC <INMB
 * LNJ \$B1,<FTBL
 DC =Z'5000'
 RT3 DC 0
 DC <IST
 * SET INTERRUPT VECTORS
 * LNJ \$B4,<SETVEC
 * SEND OUT LEVEL AND CHANNEL
 * LNJ \$B3,<SETV
 CL =\$R3
 LNJ \$B4,<FLN
 HLT
 * TURN ON ALL CHANNELS
 * RT10 LDV \$R1,=-3
 LNJ \$B3,<TRNON
 DC =Z'0400'
 DC 1
 * LNJ \$B3,<TRNON
 DC =Z'0800'
 DC 2
 * LNJ \$B4,<DLAY
 BINC \$R1,<RT10
 * SET UP RTC FOR 100 MILLISEC
 * LNJ \$B4,<RTC
 DC =12
 * NOW SWITCH LEVELS TO 63 AND WAIT FOR SMOKE TO CLEAR
 SAVE <L635V3,=X'7884'
 LEV =Z'803F'
 * RETURN HERE AFTER INTERRUPTS HAVE OCCURRED
 RTCF
 * CHECK ALL INTERRUPTS OCCURRED
 LNJ \$B1,<CHLV
 * END OF 1 LOOP THRU
 * CMV \$R5,=63
 BL <PRILP
 JMP \$B2
 * READ DATA SET STATUS
 DSSTA SAVE SAV3,=Z'0008'
 LDR \$R4,<CONT12
 LNJ \$B4,<CGSCH
 IO =\$R5,=\$R4
 * FORM CCB
 RAM ADDRESS
 SET FOR NEXT CHANNEL
 BUMP CPU ADDRESS
 BUMP RAM ADDRESS
 BUMP AGAIN
 SET UP DATA - ASCENDING PATTERN -16 TO -1
 BRANCH IF MORE TO GO
 SET LEVEL START
 STRIP IO CHANNEL BIT
 INITIALIZE CHANNEL
 SET LEVEL
 LEVEL
 CHANNEL
 LEVEL TABLE
 CHECK IF LAST LEVEL
 BRANCH IF LAST LEVEL
 SET FOR NEXT LEVEL
 DO NEXT CHANNEL
 GET LEVEL FOR CHANNEL
 PUT IN BIT FOR CHANNEL
 MORE TO GO
 SET EXPECTED INTERRUPT COUNT
 3 INTERRUPT/CHANNEL
 MASK FOR TESTED CHANNELS
 TABLE ADDRESS
 SET EXPECTED STATUS
 WHAT SAI SHOULD BE
 MASK FOR TESTED CHANNELS
 STATUS TABLE
 FIND LINE NUMBER
 NO ACTIVE LINE NUMBERS
 3 BLOCK OPERATIONS/CHAN
 TURN ON ALL CHANNELS TO BE TESTED
 START BLOCK WRITE CHANNEL CONTROL
 BLOCK WRITE OPERATION
 TURN ON CHANNELS
 BLOCK READ OPERATION
 BLOCK READ
 DELAY 25 MS
 BRANCH IF NOT LAST TIME
 120'S OF A SEC
 SUSPEND CURRENT AND DISPATCH 63
 SHUT OFF RTC
 CHECK ALL LEVELS INTERRUPTED
 CHECK FOR LAST LEVEL
 MORE TO GO
 EXIT TEST
 SAVE B4
 GET FUNCTION CODE
 OR IN CHANNEL NUMBER
 INPUT DATA SET STATUS

003529 0EFC 0703 BIOT >\$+2+\$AF
 003530 0ED0 F380 125C LNJ \$B7,<ERROR
 003531 0ED2 8FC0 0644 RSTR SAV3,=Z'0008'
 003532 0ED4 0008 JMP \$B4 COMMAND WAS NAK'ED
 003533 0ED5 8384
 *
 * INTERRUPT SAVE AREA - LEV 4
 * ISAV4 RESV \$AF,0 TRAP SAVE POINTER
 003540 0ED6 0000 RESV 1,0 CHAN, LEVEL
 003541 0ED7 0000 RESV 1,X'0' SAVE NO REGISTERS
 003542 0ED8 0000 RESV 1,0 RFU
 003543 0ED9 0000 RESV 1,0 CCB SCHEDULAR
 003544 0EDA 105C DC <IMUN SET PRIVILEGE BIT
 003545 0EDB FFFF DC -1
 * INTERRUPT SAVE AREA - ERROR INTERRUPT
 * ERRINT RESV \$AF,0 TRAP SAVE AREA POINTER
 003549 0EDC 0000 RESV 1,0 DEVICE ID
 003550 0EDD 0000 RESV 1,0 RFU
 003551 0EE0 0000 DC Z'0000' SAVE
 003552 0EEF 0000 DC <ERUPT ERROR RUPT ROUTINE
 003553 0EE0 0FFE DC -1 SET PRIVILEGE
 003554 0EE1 FFFF
 * RTC SAVE AREA
 * RTCSAV RESV \$AF,0 DEVICE ID GETS STORED HERE
 003557 0EE2 0000 RESV 1,0 RFU
 003558 0EE3 0000 RESV 1,0 SAVE
 003560 0EE4 0000 DC Z'0000' POINTS TO INTERRUPT ROUTINE
 003561 0EE5 0000 DC <RTCHDL SET PRIVILEGE BIT
 003562 0EE6 0F08
 003563 0EE7 FFFF
 * INTERRUPT SAVE AREA - LEVEL 63
 * L63SV1 RESV \$AF,0 TRAP SAVE POINTER
 003565 0EE8 0000 RESV 1,0 CHAN, LEVEL
 003566 0EE9 0000 RESV 1,X'7884' SAVE R1,R2,R3,R4,I,B5
 003568 0EEA 7884 RESV 1,0 RFU
 003569 0EEB 0000 DC <L63HDL POINTS TO INT. ROUTINE
 003570 0EEC 0EF4 DC -1 SET PRIVILEGE BIT
 003572 0EED FFFF L63SV3 RESV 5+\$AF SAVE AREA
 *
 *
 * LEVEL 63 CONTAINS 'WAIT' LOOP
 * L63HDL NOP <DUMMY
 003579 0EEF 0F00 14C7 B >L63HDL
 003580 0EEF 0FFE
 * TRAP SAVE AREA IS AT LOC 2 - 9
 *
 * TRAP ERROR ROUTINE
 * TRPERR STS <LAST STORE STATUS
 003582 0EEF 8C00 0F05 LEV =X'0080' ERROR, SHOULD BE NO TRAPS
 003583 0EEF 8E70 0080 LNJ \$B7,<ERROR
 003584 0EEF 5380 125C HLT
 *
 * ERROR INTERRUPT
 * ERUPT STS <LAST STORE LAST STATUS
 003593 0EEF 8C00 0F05 LEV =X'0080' ERROR, BAD INTERRUPT OCCURED
 003594 0FO0 8E70 0080 LNJ \$B7,<ERROR
 003595 0FO0 8E70 0080 HLT
 003596 0FO2 5380 125C LAST DC 0 LAST STATUS STORED HERE
 003597 0FO4 0000
 003598 0FO5 0000
 003599 0000
 003600 0000
 003601 0UF06 8E70 8000 SETNEW LEV =Z'0000' SCHEDULE LEVEL = 0
 003602 0UF08 8A80 0F0C RTCHDL INC <RTCLFLG GO TO LEVEL 0
 003603 0FOA 83C0 FFFF JMP SETNEW REAL TIME CLOCK FLAG
 003604 0FOC 0000 RTCLFLG DC 0
 003605 0000
 003606 0000
 003607 0000
 003608 0UF00 0EE6 INTBL DC <RTCSV1 RTC SAVE POINTER
 003609 0F0E 0F08 DC <RTCHDL RTC RUPT PONIER
 003610 0F0F 0EEC DC <L63SV2 LEVEL 63 P SAVE
 003611 0F10 0EEF4 DC <L63HDL LEVEL 63 RUPT HANDLER
 003612 0F11 0EEF4 DC <IS4S LEVEL 4 SAVE
 003613 0F11 0EDA DC <IMUN
 003614 0F12 105C DC <STKPTR PRIORITY STACK POINTER
 003615 0F13 0F17 DC <PR1ST
 003616 0F14 26B0 DC <INLLST+SAF LAST IN LIST
 003617 0F15 0F16 INLLST DC <INLLST+16
 003618 0F16 26C0 PRIEND DC <PR1ST+16
 003619 0F17 26B0 STKPTR DC <PR1ST
 *
 *
 * INITIALIZATION FOR INTERRUPTS
 * ENTERED BY LNJ \$B1.<INTITZ
 *
 INTITZ SAVE SAV1,=Z'FFFF' SAVE ALL
 003624 0000 ITM1 LAB \$B5,<INTBL GET ADDRESS OF LIST
 003625 0000 LDB \$B2,+\$B5 B2 GETS POINTER
 003626 0000 CMB \$B5,<INLLST
 003627 0000 BE >ININX BRANCH MEANS WE GOT END OF LIST
 003628 UF18 8F40 05CE LDB \$B3,+\$B5 B3 GETS VECTOR
 003629 UF1A FFFF STB \$B3,\$B2 PUT IN VECTOR
 003630 UF1B D5B0 0F0D B >ITM1 DO NEXT IN LIST
 003631 UF1C ACF5
 003632 UF1E DD80 0F15
 003633 UF20 0904
 003634 UF21 BC5
 003635 UF22 BF82
 003636 UF23 OFFA
 003637 UF24 DB80 0E0D INTNX LAB \$B5,<ERRINT+\$AF GET ADDRESS OF ERROR SAVE
 003638 UF26 AB80 0000 LAB \$B2,<ZH1SAZ WHERE TO START STORING IT
 003639 UF28 ICCI LDV \$R1,=-63 63 VECTORS

003640 UF29 DFF2
 003641 UF2A 17FF
 003642
 003643
 003644
 003645 UF2B 1CEC
 003646 UF2C DB80 0000 X ITM2 STB \$B5+\$B2
 003647
 003648 UF2E AB80 0000 X * BINC SRI,>ITM2
 003649 UF30 DFE2
 003650 UF31 17F
 003651 UF32 AB80 0000 X * SET UP VECTORS FOR TRAPS
 003652 UF34 AB80 0000 X *
 003653 UF36 AB80 0000 X * LDV \$R1,=-20
 003654 UF38 DB80 266B X LAB \$B5,<ZHPFR
 003655 UF3A DF82
 003656
 003657 UF3B DB80 0EE3
 003658 UF3D DFC2 0003
 003659 UF3F DB80 0ED7
 003660 UF41 DFC2 0004
 003661 UF43 DB80 0EE9
 003662 UF45 DFC2 003F
 003663 UF47 8751
 003664 UF48 9700 0FOC
 003665 UF4A 9700 14D2
 003666 UF4C 9700 14D1
 003667 UF4E 9700 0001 X SVEC LAB \$B2,<ZH1SAZ
 003668 UF50 9700 0002 X STB \$B5,-\$B2
 003669 UF52 9700 0003 X BINC SRI,>SVEC
 003670 UF54 9870 8000 X LAB \$B2,<ZHTSA
 003671 UF56 9700 0000 X STB \$B2,<ZHNTSA
 003672
 003673 UF58 DB80 14DB
 003674 UF5A UF80 0000 X LAB \$B2,<TOPSAV
 003675
 003676
 003677
 003678 UF5C 8700 14E2
 003679 UF5E FBC0 0003
 UF60 D380 0000 X * PUT IN RTC, LEV 4 + 63 VECTORS
 UF62 UF80
 UF63 2690
 UF64 14E2
 UF65 14A9
 003680
 003681
 003682
 003683 UF66 FBC0 0003 X STB \$B5,<RTCSAV+\$AF
 UF68 D380 0000 X SVEC LAB \$B5,\$B2,3*\$AF
 UF6A UF80
 UF6B 26A0
 UF6C 14E2
 UF6D 14A9
 003684
 003685
 003686
 003687 UF6E 8AB0 14E2 X CL SRI,<RTCFLG
 003688 UF70 FBC0 0003 X STR SRI,<CCBFLG
 UF72 D380 0000 X STR SRI,<SCFLG
 UF74 UF80
 UF75 2680
 UF76 14E2
 UF77 14A9
 003689
 003690
 003691
 003692 UF78 FBC0 0003 X * INITAILIZE LEVEL TABLE
 UF7A D380 0000 X CALL ZV\$F,ILV,MASK1,C16
 UF7C UF80
 UF7D 26B0
 UF7E 14A8
 UF7F 14A9
 003693
 003694
 003695
 003696
 003697 UF80 8700 14E2 X * INITAILIZE COUNT TABLE
 003698 UF82 FBC0 0003 X INC MASK1
 UF84 D380 0000 X CALL ZV\$F,INMB,MASK1,C16
 UF86 UF80
 UF87 25AA
 UF88 14E2
 UF89 UF87
 003699 UF8A BB80 25AA
 003700 UF8C 8751 X * FILL PRIORITY LIST
 * CALL ZV\$F,PRIST,ALLONE,C16
 UF8D 9CF3
 UF8E 9873
 UF8F C870 7884
 UF91 CF73
 UF92 C873
 UF93 9BB0 1020
 UF95 9FF3
 UF96 4CF7
 UF97 CF73
 UF98 9CF3
 UF99 BBC3 0005
 UF9B BB80 14CA
 UF9D 09F0
 003701
 003702
 003703
 003704
 003705
 003706
 003707
 003708
 003709
 003710
 003711
 003712
 003713
 003714
 003715
 003716
 003717
 003718
 003719
 003720 UF9E 1CF7
 003721 UF9F 9F00 266C
 003722 UF01 9F00 266E
 * CREATE INTERRUPT SAVE AREAS
 * CL <MASK1
 * CALL ZV\$F,IS1,MASK1,CNFILL FILL SDB WITH 0'S
 * LAD \$B3,<IS1
 * CL =SRI
 * MISA LDB \$B1+\$B3
 LDR \$R1+\$B3
 LDR \$R4,X*7884
 STR \$R4+\$B3
 LDR \$R4+\$B3
 LAB \$B1,<IH01
 STB \$B1+\$B3
 LDV \$R4,=-1
 STR \$R4+\$B3
 * LDB \$B1+\$B3
 LAB \$B3,\$B3.5
 CMB \$B3,<ENDSAV
 BNE >MISA
 *
 * CREATE LEVEL 0 SAVE AREA
 * MISB LDV \$R1,=-1
 STR \$R1,<TOPSAV+1
 STR \$R1,<TOPSAV+2+\$AF

003723 OFA3 8FC0 0543 RSTR SAV1,=Z'FFFF' RESTORE ALL REG
 003724 OFA5 FFFF JMP \$B1
 003725 OFA6 8381
 003726 OFA7 00D6 *CNFILL DC (157+57*\$AF)
 003727 * ROUTINES TO SUPPORT INTERRUPT SERVICING
 003728 *
 003729 *
 003730 *
 003731 *
 003732 *
 003733 *
 003734 *
 003735 *
 003736 *
 003737 *
 003738 *
 003739 *
 003740 *
 003741 *
 003742 *
 003743 *
 003744 OFA8 8F40 053E FTBL SAVE SAV1,=Z'70B0' R1,R2,R3,B2,B3,I
 003745 OFA9 70B0 LDR \$R2,+\$B1 PICK UP NUMBER
 003746 OFAB A811 LDR \$R3,+\$B1
 003747 OFAD B811 LDV \$R1,=-16
 003748 OFAE ACF1 LDB \$B2,+\$B1
 003749 OFAF BBC2 0010 LAB \$B3,\$B2,16
 003750 OFB1 3882 SLVLP BGEZ \$R3,>ITM7 ADDRESS OF END OF TABLE
 003751 OFB2 AF13 STR \$R2,\$B3,\$R1 BRANCH IF BIT 0 = 0
 003752 OFB3 3011 ITM7 SCL \$R3,1 ROTATE R3
 003753 OFB4 17FD BINC \$R1,>SLVLP
 003754 OFB5 8FC0 0531 RSTR SAV1,=Z'70B0'
 003755 OFB6 70B0
 003756 OFB7 8381
 003757 OFB8 8381
 003758 *
 003759 OFB9 8F40 052D CHLV SAVE SAV1,=Z'3000'
 003760 OFBC B753 CL =SR3 WILL TRACK CHANNEL NUMBER
 003761 OFBD 2FC0 LDV \$R2,=-16
 003762 OFBE 69B0 26A0 CHLV1 CMZ <ILV,\$R3 CHECK IF CHANNEL WAS TO BE TESTED
 003763 OFCU 0906 BE >CHLV2
 003764 OFC1 89B0 2680 CMZ <INMB,\$R3
 003765 OFC3 0903 BE >CHLV2
 003766 OFC4 F380 125C LNJ \$B7,<ERROR WRONG NUMBER OF INTERRUPTS ON CHANNEL
 003767 OFC6 8A03 INC =SR3 BUMP CHANNEL NUMBER
 003768 OFC7 27F7 BINC \$R2,>CHLV1
 003769 OFC8 8FC0 051E RSTR SAV1,=Z'3000'
 003770 OFCA 3000
 003771 OFCB 8381
 003772 *
 003773 *
 003774 *
 003775 *
 003776 *
 003777 OFCC 8F40 052A SETLCT SAVE SAV2,=Z'E8E0' R1,<R2,<R4,<B2,<B1
 003778 OFCF E8E0 LDB \$B1,+\$B3 GET ADDRESS OF TABLE
 003779 OFD0 9CF3 CL =\$R1
 003780 OFD1 8751
 003781 OFD2 A811 LCT4 LDR \$R2,\$B1,\$R1 GET BYTE TO OUTPUT
 003782 OFD3 8A01 INC =\$R1
 003783 OFD4 2985 BNEZ \$R2,>LCT5 BRANCH IF NOT AT END OF TABLE
 003784 OFD5 8FC0 0522 KSTR SAV2,=Z'E8E0'
 003785 OFD6 E8E0
 003786 OFD7 6383
 003787 OFD8 C800 1560 LCT5 LDR \$R4,<CONT5 FUNCTION CODE FOR OUT LCT BYTE
 003788 OFD9 C800 1173 LNJ \$B4,<CGSCH FORM IN CONTROL WORD
 003789 OFDC 8052 LCT3 IO =\$R2,\$R4 OUTPUT BYTE
 003790 OFDE 0054
 003791 OFDF 07FE BTOF >LCT3
 003792 OFE0 0FF2 B >LCI4 CHECK IF TAKEN
 003793 *
 003794 *
 003795 *
 003796 OFE0 8F40 0526 SNDLCT SAVE SAVN,=Z'E8E0' SAVE REGISTERS
 003797 OFE2 E8E0 LDB \$B1,+\$B3 GET ADDRESS OF THE TABLE
 003798 OFE4 9873 LDR \$R1,+\$B3
 003799 *
 003800 OFE5 8A01 LCN4 INC =\$R1 IS IT LCI 0
 003801 OFE6 9970 0000 CMR \$R1,=0 DONT SEND BYTE IF LCT 0
 003802 OFE8 0901 FFFF BE LCN4
 003803 OFEA 9970 0001 CMR \$R1,=1
 003804 OFEC 0901 FFFF BE LCN4
 003805 OFEE 9970 0005 CMR \$R1,=5
 003806 OFF0 0901 FFFF BE LCN4
 003807 OFF2 9970 0009 CMR \$R1,=9
 003808 OFF4 0901 FFFF BE LCN4
 003809 OFF6 9970 0015 CMR \$R1,=21
 003810 OFF8 0901 FFEC BE LCN4
 003811 OFFA 9970 0016 CMR \$R1,=22
 003812 OFFC 0901 FFE8 BE LCN4
 003813 OFFE 9970 0020 CMR \$R1,=32
 003814 1000 0901 FFE4 BE LCN4
 003815 1002 9970 0021 CMR \$R1,=33
 003816 1004 0901 FFE0 BE LCN4
 003817 1006 9970 0025 CMR \$R1,=37
 003818 1008 0901 FFDC BE LCN4
 003819 100A 9970 0035 CMR \$R1,=53
 003820 100C 0901 FFD8 BE LCN4
 003821 100E 9970 0036 CMR \$R1,=54
 003822 1010 0901 FFD4 BE LCN4
 003823 1012 A811 LDR \$R2,\$B1,\$R1 GET THE BYTE
 003824 1013 2985 BNEZ \$R2,>LCN5
 003825 1014 8FC0 04F2 RSTR SAVN,=Z'E8E0'

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003826 1017 8383 * JMP $B3 RETURN TO CALLER
003827 1018 C800 1560 LCN5 LDR $R4,<CONT5 FUNCTION CODE FOR OUT LTCI
003828 1019 C380 1173 LCN3 I0 $B4,<CGSCH FORM I0 CONTROL WORD
003829 101C 8052
003830 101D 0054
003831 101E 07FE BIUF >LCN3
003832 101F 0FC6 B >LCN4
003833
003834
003835 ****
003836 *
003837 * INTERRUPT HANDLER ROUTINE
003838 *
003839 ****
003840 *
003841 1020 8C51 IHDI STS =$R1 STORE STATUS IN R1
003842 1021 9570 003F AND $R1,=X'3F'
003843 1023 B878 0000 LDR $R3,$IV,0 STRIP TO LEVEL
003844 1025 B570 FC00 AND $R3,=Z'FC00'
003845 1027 B900 14C3 CMR $R3,<MLCADD STRIP TO MLCP ADDRESS
003846 1029 0905 BE >IH05 BRANCH IF GOOD
003847 102A 8E70 0080 LEV =X'0080' QUICK CHANGE TO LEVEL 0
003848 102C F380 125C IHQ5 LDR $R3,$IV,0 MLCP GAVE CP WRONG ADDRESS AFTER INTERRUPT
003849 102E B878 0000 AND $R3,=X'3C0'
003850 1030 B570 03C0 STK $R3,=$R4
003851 1032 BF54 SUR $R3,6
003852 1033 3046 CMR <INMB,$R3 GETS SUBCHAN IN R3
003853 1034 8980 2680 BNE >IH01 COMPARE NUMBER OF INTERRUPTS
003854 1036 0989 LEV =X'0080' SWITCH TO LEVEL 0
003855 1037 8E70 0080 IHQ5 LNJ $B7,<ERROR TOO MANY INTERRUPTS ON THIS CHANNEL
003856 1039 F380 125C
003857
003858 103B 0801 0005 IHQ1 BAL IHQ2 BRANCH IF NEG
003859 103D 8E70 0080 LEV =X'0080' SWITCH TO LEVEL 0
003860 103F F380 125C LNJ $B7,<ERROR ERROR, UNEXPECTED INTERRUPT
003861 1041 9930 26A0 CMR $R1,<ILV,$R3 COMPARE LEVELS
003862 1043 0905 BE >IH03
003863 1044 8E70 0080 LEV =X'0080' SWITCH TO LEVEL 0
003864 1046 F380 125C LNJ $B7,<ERROR WRONG CHANNEL INTERRUPTED THIS LEVEL
003865 1048 8980 14D1 CMR <SCFLG CHECK SCAN FLAG
003866 104A 098F BNE >IH04 IF SET, NO VALID CCB STATUS
003867 104B C600 155F XOR $R4,<CONT4 FORMS STATUS
003868 104D 8052 I0 =$R2,=$R4 READ NEXT STATUS
003869 104E 0F01 FFF8 NUP IHQ3 DELAY
003870 1051 A930 2690 CMR $R2,<IST,$R3
003871 1053 0909 BE >IH04
003872 1054 8E70 0080 LEV =X'0080' SWITCH TO LEVEL 0
003873 1056 F380 125C LNJ $B7,<ERROR STATUS ERROR AFTER INTERRUPT
003874 1058 8E70 8004 IHQ4 B =Z'8004' SUSPEND, SCAN, AND DISPATCH LEV 4
003875 105A 0F81 FCC5 IHQ1
003876
003877
003878
003879 ****
003880 * INTERRUPT MONITOR ROUTINE - LEVEL 4
003881 105C DC80 0F17 IMON LDB $B3,<STKPTR GET PRIORITY STACK POINTER
003882 105E DD80 0F16 CMB $B3,<RIEND CHECK IF AT END
003883 1060 0284 BGE >IMUN2 STACK IS FULL
003884 *
003885 1061 BF75 STR $R3,+$B5 STORE CHANNEL ON STACK
003886 1062 DF80 0F17 STB $B5,<STKPTR STORE NEW STACK POINTER
003887 *
003888 1064 8A80 2680 IMUN2 INC <INMB,$R3 INCREMENT INTERRUPT COUNT
003889 *
003890 1066 8E70 803F IMUN4 LEV =Z'803F' SCHEDULE LEV 03 AND SUSPEND
003891 1068 OFF4 B >IMUN PICK UP NEXT TIME FROM HERE
003892
003893
003894
003895
003896
003897
003898
003899
003900
003901
003902
003903
003904
003905
003906 1069 8F40 048D
003907 106B FFBE SDATA SAVE SAV2,=Z'FFBF* SAVE ALL BUT $1
003908 106C DCF1 LDB $B5,+$B1 GET ADDRESS OF DATA
003909 106D C871 LDR $R4,+$B1 GET RANGE
003910 106E DF80 1079 STB $B5,<SPRG5
003911 1070 A871 AF00 107A LDR $R2,+$B1
003912 1071 9871 STR $R2,<SPRG2
003913 1073 9F57 LUR $R1,+$B1
003914 1075 9570 7FFF STK $R1,=$R7
003915 1077 C380 1189 AND $R1,=X'7FFF'
003916 1079 0000 LNJ $B4,<MCCB
003917 107A 0000 SPRG5 RESV $AF,0 FORM CCB
003918 107B C380 1177 SPRG2 DC CPU ADDRESS
003919 107D OF82 LNJ $B4,<CHCT RAM ADDRESS
003920 107E 0400 B >$+2 DO CHANNEL CONTROL
003921 DC Z'0400' BLOCK WRITE
003922
003923
003924 107F 7880 1086 SPRG3 BGEZ $R7,<SPRG7
003925 1081 C380 11C8 LNJ $B4,<TEST WAIT FOR STATUS COMPLETE
003926 1083 OF82 B >$+2
003927 1084 001F DC 30 250 MS TIMEOUT
003928 1085 OF85 B >SPRG8
003929 1086 C380 1215 SPRG7 LNJ $B4,<DELAYLG
003930 1088 C380 1161 LNJ $B4,<INXT
003931 108A 82D5 SPRG8 LB =$R5,=X'1000' INPUT NEXT STATUS
003932 108C 0503 BBT >+2+$AF GET STATUS COMPLETE BIT
003933 108D F380 125C LNJ $B7,<ERROR
003934 108F 82D5 LB =$R5,=7 STATUS COMPLETE NOT SET AFTER BLOCK WRITE

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003935 1090 0007
 003936 1091 0583
 003937 1092 F380 125C
 003938 1094 8F80 14F7
 003939 1096 FFBF
 003940 1097 8381

BBF >\$+2+\$AF
 LNJ \$B7,<ERROR
 KSTR <SAV2,Z!FFBF!
 JMP \$B1

 * BLOCK READ FROM RAM.- CHAN. NUMBER MUST BE IN R3.
 * LNJ \$B1,<RDATA
 * DC INBUFF
 * DC RANGE
 * DC RAMAD
 * DC EVEN
 INPUT BUFFER ADDRESS
 NUMBER OF BYTES
 RAM ADDRESS
 0 = EVEN BYTE CPU ADDRESS
 BIT 15 = 15 FOR ODD BYTE ADDRESS
 BIT 0 = 1 FOR 250 MS DELAY AFTER STARTING
 BIT 0 = 0 FOR 25 MS DELAY AFTER STARTING

RDATA SAVE SAV2,Z!FFBF
 GET IN BUFF ADD
 RDTA1 LDB \$B5,+\$B1
 STB \$B5,<RDTA1
 LDR \$R4,+\$B1
 LDR \$R1,+\$B1
 STK \$R1,<RDTA2
 LDR \$R1,+\$B1
 STK \$R1,=SR7
 AND \$R1,=X'7FFF'
 LNJ \$B4,<MCCB
 RDTA2 DC 0
 DC <DUMMY
 DC 0
 LNJ \$B4,<CHCT
 B >\$+2
 DC Z*0800*
 FORM CLB
 CPU ADDRESS
 RAM ADDRESS
 DO CHANNEL CONTROL
 * B SPRG3
 EXIT

003952 1098 8F40 045E
 003953 1099 FFBF
 003954 109C DF80 10A8
 003955 109E C871
 003956 109F 9871
 003957 10A0 9F00 10A9
 003958 10A2 9871
 003959 10A3 F957
 003960 10A4 9570 7FFF
 003961 10A6 C380 1189
 003962 10A8 14C7
 003963 10A9 0000
 003964 10AA C380 1177
 003965 10AC 0F82
 003966 10AD 0800
 003967 10AE 0F81 FF00

* TURN ON ALL CHANNELS TO BE TESTED
 * LNJ \$B3,<TRNON
 * DC CONT CHAN CONTROL WORD
 * DC XX 1= XMIT ONLY, 2= REC. ONLY, 0= BOTH
 TRNON SAVE SAV1,Z!FO00* R3,4,5,6
 LDR \$R5,+\$B3
 LDR \$R6,+\$B3
 LDV \$R3,=16
 ITM11 BDEC \$R3,>TALL
 RSTR SAV1,Z!FO00*
 GET CHANNEL CONTROL WORD
 GET TYPE FLAG
 R3 GETS CHANNEL NUMBERS
 BRANCH IF NOT DONE

003968 10B0 8F40 0436
 003969 10B2 1F00
 003970 10B3 D873
 003971 10B4 E873
 003972 10B5 3C10
 003973 10B6 3705
 003974 10B7 8FC0 042F
 003975 10B8 1F00
 003976 10B9 8383

JMP \$B3
 ALL DONE, EXIT

003977 10B0 8F40 0436
 003978 10B1 89B0 26A0
 003979 10B2 0977
 003980 10B3 6904
 003981 10B4 F856
 003982 10B5 F653
 003983 10B6 7BF3
 003984 10B7 C800 1562
 003985 10B8 89B0 26A0
 003986 10B9 0977
 003987 10C0 6904
 003988 10C1 F856
 003989 10C2 F653
 003990 10C3 7BF3

TALL LDR \$R4,<CONT7
 CMZ <ILV,\$R3
 BE >ITM11
 BEZ \$R6,>ITM11A
 LDR \$R7,=\$R6
 XOR \$R7,=\$R3
 BUDD \$R7,>ITM11
 CHECK IF LEVEL SET UP
 BRANCH IF NOT SET UP
 CHECK TYPE OF XFER
 BRANCH IF NOT RIGHT TYPE

003991 10C4 C380 1173
 003992 10C5 8055
 003993 10C6 0054
 003994 10C7 07FE
 003995 10C8 0FED
 003996 10C9 0FED

ITM11A LNJ \$B4,<CGSCH
 ITM12 IO \$R5,=\$R4
 FORM IO CONTROL WORD
 START IO

003997 10C4 C380 1173
 003998 10C5 8055
 003999 10C6 0054
 004000 10C7 07FE
 004001 10C8 0FED
 004002 10C9 0FED

BIOF B >ITM12
 >ITM11
 WAIT
 DO NEXT

* SET LEVEL + CHANNEL FOR ALL ACTIVE CHANNELS

004003 10D0 C3C0 00A2
 004004 10D1 8051
 004005 10D2 0054
 004006 10D3 0054
 004007 10D4 0703
 004008 10D5 F380 125C

SETV1 CL =\$R3
 SETV2 LDR \$R1,<ILV,\$R3
 BEZ \$R1,>SETV1
 LDR \$R4,<CONT11
 LNJ \$B4,CGSCH
 IO =\$R1,=\$R4
 BIOT >\$+2+\$AF
 LNJ \$B7,<ERROR
 GET LEVEL
 GET FUNCTION CODE
 OR IN CHANNEL
 COMMAND NEVER SHOULD BE NAKED

004009 10D6 8AD3
 004010 10D7 3D10
 004011 10D8 09F2
 004012 10D9 8383

SETV1 INC =\$R3
 CMV \$R3,=16
 BNE >SEIV2
 JMP \$B3
 * SET UP INTERRUPT VECTORS FOR ENTRIES IN LEVEL TABLE

004013 10D9 8383
 004014 10D9 8383
 004015 10D9 8383
 004016 10D9 8383
 004017 10D9 8383
 004018 10D9 8383
 004019 10D9 8383
 004020 10D9 8383
 004021 10D9 8383
 004022 10D9 8383
 004023 10D9 8383
 004024 10D9 8383
 004025 10D9 8383
 004026 10D9 8383
 004027 10D9 8383

SETVEC CL =\$R2
 LAB \$B5,<ISI+\$AF
 SET1 NOP >\$+4
 SAF4 LAB \$B3,\$B5,9+3*\$AF
 SAF5 LDR \$R1,<ILV,\$R2
 CMZ =\$R1
 BE >SET2
 STB \$B5,<ZHISAZ,\$R1
 SET2 INC =\$R2
 STB \$B3,=\$B5
 CMV \$R2,=17
 BL >SEII
 JMP \$B4
 BRANCH IF NO LEVEL SET FOR CHANNEL
 STORE VECTOR
 SET FOR NEXT CHANNEL

004028 10D9 8383
 004029 10D9 8383
 004030 10D9 8383
 004031 10D9 8383
 004032 10D9 8383
 004033 10D9 8383
 004034 10D9 8383
 004035 10D9 8383
 004036 10D9 8383
 004037 10D9 8383
 004038 10D9 8383
 004039 10D9 8383
 004040 10D9 8383

* FIND FIRST ACTIVE CHANNEL
 * SR3=START CHECK ON THIS CHANNEL AND
 * CYCLE THROUGH ALL CHANNEL IF
 * ONE FOUND ON CHANNEL NUMBER IS HERE
 *
 * LNJ \$B4,<FLN
 * <RETURNS> IF NO LINES ON
 * <RETURNS>

004041 10EC 8F40 03FA FLN SAVE SAV1,=Z*AOAO* SAVE REG. \$R2,\$B2
 004042 10EE A0A0 LDV \$R2,=-1 ADDRESS OF ACTIVE LINE TABLE
 004043 2CFF LAB \$B2,<AILT SEE IF AT BOTTOM OF TABLE
 004044 10F0 AB80 14AD CMV \$R3,=16
 004045 10F2 3D10 DGE >FLN3
 004046 10F3 0285 CMR \$R2,\$B2.+\$R3 SEE IF INDEX SCH. ON
 004047 10F4 A97E BE >FLN2 SET \$R3 BACK TO VALUE FOUND
 004048 10F5 097D DEC =\$R3 INCREMENT TO VALID RETURN
 004049 10F6 88D3 LDR \$R2,\$B4 RESTORE REG.
 004050 10F7 A874 RSTR SAV1,=Z*AOAO*
 004051 10FA A0A0
 004052 10FD 8384 JMP \$B4 JUMP OUT OF ROUTINE
 * * GENERAL INITIALIZE FOR MLCC
 * * GENITZ SAVE SAV3,=Z*1808* SAVE B4,R4
 004055 10FC 8F40 041A CL =\$R3 FIND ACTIVE LINE
 10FF 1808 LNJ \$B4,<FLN NO ACTIVE LINES
 004056 8753 HLT
 004057 1100 C380 10EC LDR \$R4,<CONT9 GET FUNCTION CODE FOR INITIALIZE
 004058 1102 0000 LNJ \$B4,<CGSCH
 004059 1103 C800 1563 IO =Z*8000*,=\$H4 MODIFY FOR CHANNEL
 004060 1105 C380 1173
 004061 1107 8070 8000
 1109 0054 BIOT >S+2+\$AF
 004062 110A 0703 LNJ \$B7,<ERROK
 004063 110B F380 125C LNJ \$B4,<DELAYL
 004064 110C C380 1215 RSTR SAV3,=Z*1808*
 004065 110F 8FC0 0407
 1111 1808
 004066 1112 8384 JMP \$B4 RETURN
 * * * * *
 * * * * * PSEUDO - RANDOM DATA GENERATION ROUTINE
 * * * * * DATA IS BASED ON CRC GENERATOR
 * * * * * 2 MODES OF OPERATION.
 * * * * * INPUT - TAKES INITIAL CHAR AS BASE AND FORMS
 * * * * * TABLE OF DATA BASED ON CRC SPECIFIED.
 * * * * * OUTPUT - TAKES TABLE OF DATA AND FORMS ACCUMULATED CRC.
 * * * * * ONLY FOR CRC16 AND CCITT.
 * * * * * TO USE, MUST SET:
 * * * * * R6 - NUMBER OF BITS IN CHAR.
 * * * * * R7 - RANGE IN BYTES
 * * * * * R3 - TABLE ADDRESS
 * * * * * R6 - ADDRESS OF CRC TYPE.
 * * * * * FOR INPUT SET:
 * * * * * R5 - INITIAL CHAR. TO GIVE TO CRC GEN.
 * * * * * FOR OUTPUT SET:
 * * * * * R5 - MUST BE SET NEGATIVE.
 * * * * * CRC CHECK AND FILL ROUTINE
 * * * * *
 * * * * * LNJ \$B4,<CRC
 * * * * *
 * * * * * \$B3 - ADDRESS WHERE CRC ACCUMULATOR IS STORED
 * * * * * OR CHARACTER IS LOADED IF \$R5<0
 * * * * * \$B6 - ADDRESS OF CRC TYPE
 * * * * *
 * * * * * \$R5 - IF >=0 CONTAINS INITIAL CHARACTER FOR FILLING TABLE
 * * * * * IF <0 NOT USED EXCEPT AS A FLAG FOR OUTPUT
 * * * * * \$R6 - CONTAINS NUMBER OF BITS IN CHARACTER
 * * * * * \$R7 - THE NUMBER OF CRC TO BE
 * * * * * GENERATED INTO/FROM THE TABLE AT \$B3
 * * * * *
 * * * * * THE ACCUMULATED CRC IS STORED IN LOCATION CRCA.
 * * * * *
 * * * * * CRC SAVE SAV4,=Z*F994*
 * * * * *
 004115 1115 F994 LDR \$R4,\$B6 PUT CRC TYPE INTO \$R4
 004116 1116 C806 CL =\$R1
 004117 1117 8751 CL <TEMP
 004118 1118 8700 14CE CL <TEMP1
 004119 111A 8700 14CF CL =\$R3
 004120 111C 8753 CL =\$R2
 004121 111E 8256 NEG =\$R6
 004122 111F EF00 14DA STR \$K6,<IMGA+(\$AF-1) STORE COUNTER
 004123 1121 5885 BGEZ \$K5,>CRCAA1 SEE IF OUTPUT OR INPUT
 004124 1122 DOUF SR5,\$B3+\$R3
 004125 1123 8A80 14CE INC <TEMP
 004126 1125 7001 SOL \$H7,1
 004127 1126 8257 CRCAA1 NEG =\$R7
 004128 1127 89D4 CRCAA CMZ =\$R4
 004129 1128 0901 0035 BE CRCF
 004130 112A C970 F010 CMR \$R4,=Z*F010* IF CRC12
 004131 112C 091A BE >CRCD BRANCH TO CRC
 004132 112D DF51 CRCA STR \$R5,=\$R1
 004133 112E 5041 BE >CRCD
 004134 112F 9652 SUR \$R5,1
 004135 1130 2041 XUR \$R1,=\$R2
 004136 1131 1B02 SUR \$R2,1
 004137 1132 A654 BEVN \$R1,>CRCB
 004138 1133 67FA XOR \$R2,=\$R4
 004139 1134 89C0 0399 CRCB BINC \$R6,>CRCA
 004140 1136 098E CMZ TEMP
 004141 1137 D852 BNE >CRCD
 004142 1138 5048 LDR \$R5,=\$R2
 004143 1139 88D5 SUR \$R5,8
 004144 113A AF73 DEC =\$R5
 004145 113B E800 14DA CRCC STR \$R2,+\$B3
 004146 113D 77EA BINC \$R6,<IMGA+(\$AF-1) RESET CHAR. COUNT
 004147 113E AF00 14D3 STR \$R7,>CRCAA DO NEW CHARACTER
 004148 113F 88D5 BINC \$R7,<CRCAC STORE LAST CRC ACCUMULATED

004148 1140 8FC0 03E6 RSTR SAV4,=Z'F994' RESTORE REGS.
 004149 1142 F994 *
 004150 1143 8384
 004151 1144 D0FF CRCDD LDH \$R5,\$B3,+\$R3
 004152 1145 OFF6 B >CRC2
 004153 1146 5004 *
 004154 1147 DF51 CRCDV SOL \$R5,4
 004155 1148 5045 STR \$R5,=\$R1
 004156 1149 9652 SUR \$R5,5
 004157 114A 2045 XUR \$R1,=\$R2
 004158 114B 2004 SUR \$R2,5
 004159 114C 82D1 SOL \$R2,4
 004160 114D 0010 LB =\$R1,=X'10'
 004161 114E 0582 BBF >CRCE
 004162 114F A654 XUR \$R2,=\$R4
 004163 1150 67F6 BINC \$R6,>CRCD
 004164 1151 30CA CRCE DOK \$R3,10 FORM GENERATED CRC
 004165 1152 3042 SUR \$R3,2 ACCUMULATOR
 004166 1153 2002 SOL \$R2,2
 004167 1154 3088 DOL \$R3,8
 004168 1155 AF73 STR \$R2,+\$B3
 004169 1156 D852 LDR \$R5,=\$R2
 004170 1157 30CB DOR \$R3,8
 004171 1158 3002 SOL \$R3,2
 004172 1159 2042 SUR \$R2,2
 004173 115A 308A DOL \$R3,10
 004174 115B 5048 SUR \$R5,8
 004175 115C 88D5 DEC =\$R5
 004176 115D UFDE B >CRC2
 004177 *
 004178 115E A655 CRCF XUR \$R2,=\$R5
 004179 115F 8AD5 INC =\$R5
 004180 1160 UFDA B >CRCC
 *
 * INPUT NEXT STATUS TO R5
 004185 1161 8F00 1535 INXT SAVE <SAV6,=Z'0008' B4
 004186 1162 0008 155F LDR \$R4,<CONT4 GET CONTROL WORD FOR INPUT NEXT STATUS
 004187 1163 C800 1173 LNJ \$B4,<CGSCH MODIFY FOR CHANNEL
 004188 1164 C380 1173 10 =\$R5,=\$R4 GET NEXT STATUS
 004189 1165 0054 BIOT >+\$2+\$AF
 004190 1166 F380 125C LNJ \$B7,<ERROR INPUT NEXT STATUS WAS NAK'ED
 004191 116D DF00 154D STR \$R5,<STAT STORE LAST STATUS READ
 004192 116F 8F80 1535 RSTR <SAV6,=Z'0008'
 004193 1171 0008 JMP \$B4
 *
 *
 * CHANGE CHANNEL
 *
 * \$R3 -CONTAINS CHANNEL WANTED
 * \$R4 -CONTAINS I/O CONTROL WORD TO BE CHANGED
 *
 * LNJ \$B4,<CGSCH
 * CGSCH SOL \$R3,6 SHIFT TO CHANNEL POSITION
 * OR \$R4,=\$R3 OR CHANNEL NUMBER INTO CONTROLWORD
 * SUR \$R3,6 SHIFT TO NORMAL POSITION
 * JMP \$B4 GO BACK
 *
 * OUTPUT CHANNEL CONTROL
 *
 * LNJ \$B4,CHCT
 * B >+\$2 RETURN
 * DC XX XX = CHANNEL CONTROL
 *
 * CHCT SAVE <SAV1,=Z'0C0D' R4,R5,B5,B7,B4
 004207 1173 3006 CGSCH
 004208 1174 C453 LDR \$R5+\$B4 DUMMY
 004209 1175 3046 LDX \$R5+\$B4 GET CONTROL WORD
 004210 1176 8384 LDR \$R4,<CONT7 FUN CODE FOR CCB CONTROL
 *
 * LNJ \$B4,<CGSCH FORM TO CONTROL WORD
 * 10 =\$R5,=\$R4 OUTPUT CCB CONTROL
 *
 * CHCT >CHZ
 * LNJ \$B7,<ERROR ERROR, IO WAS NAK'ED
 *
 * CHZ RSTR <SAV1,=Z'0C0D'
 *
 * JMP \$B4
 *
 *
 * CCB FORMATION
 *
 * \$R1 - CONTAINS INDEX OF CPU ADDRESS
 * \$R3 - CONTAINS CHANNEL WANTED
 * \$R4 - CONTAINS RANGE (NUMBER OF BYTES)
 *
 * LNJ \$B4,<MCCB
 * DC CPU ADDRESS
 * DC RAM ADDRESS NUMBER OR CHANNEL CONTROL WORD.
 *
 * MCCB SAVE SAV1,=Z'FDF4' SAVES \$B1,\$B3,\$B2,\$B5,\$R7,\$R5,\$R4,R2,6 \$R1
 004244 1189 8F40 035D LDB \$B2,+\$B4 LOAD \$B2 WITH CPU ADDRESS
 004245 1180 FDF4 LDR \$R2,+\$B4 PUT RAM ADDRESS IN \$R2
 004246 1181 ACF4 SWB \$B5,=\$B4 ALLOW \$B4 TO BE USE IN SUBR. CALL
 004247 1182 A874 DED4 LDR \$R5,=\$R4
 004248 1183 D854 LDR \$R4,<CONT1 LOAD \$R4 WITH I/O CONTROL WORD
 004249 1184 C800 155C LNJ \$B4,<CGSCH
 004250 1185 C380 1173 TOLD \$B2,\$R1,=\$R4,=\$R5 OUTPUT ADDRESS AND RANGE
 004251 1186 8192

1195 0054
 1196 0055
 004256 1197 0703 BIOT >\$+2+\$AF
 004253 1198 F380 125C LNJ \$B7,<ERROR
 004254 119A C800 155E LDR \$R4,<CONT3
 004255 119C C380 1173 LNJ \$B4,<CGSCH
 004256 119L 8052 IU =\$R2,=\$R4
 119F 0054
 004257 11A0 0703 BIOT >\$+2+\$AF
 004258 11A1 F380 125C LNJ \$B7,<ERROR
 004259 11A3 CED5 SWB \$B4,=\$B5
 004260 11A4 8FC0 0342 RSTR SAV1,=Z'FDF4'
 11A6 FDF4
 004261 11A7 8384 JMP \$B4
 * INPUT LCT BYTE
 * INBYTE SWB \$B5,=\$B4
 004266 11A8 UED4 LDR \$B4,<CONT10
 004267 11A9 C800 1564 LNJ \$B4,<CGSCH
 004268 11AD 8055 IU =\$R5,=\$R4
 11AE 0054
 004269 11AF 0703 BIOT >\$+2+\$AF
 004270 11B0 F380 125C LNJ \$B7,<ERROR
 004271 11B2 D570 FF00 AND \$R2,=Z'FF00'
 004272 11B4 UED4 SWB \$B5,=\$B4
 004273 11B5 8384 JMP \$B4
 *
 * PACK RAM INSTRUCTIONS
 * \$R5 - RANGE IN WORDS OF BUFFER WHERE PACKED INSTRUCTIONS GO.
 *
 * LNJ \$B4,<PKRIN
 * DC ADDRESS OF UNPACKED INSTRUCTION
 * DC ADDRESS OF PACKED INSTRUCTIONS
 *
 * PKRIN SAVE SAV1,=Z'CCD0'
 004287 11B6 8F40 0330 SAVE \$R1,\$R4,\$R5,\$B3,\$B1
 11B8 CCD0
 004288 11B9 8255 NEG =\$R5
 004289 11BA BC4F LDB \$B3,+\$B4
 004290 11BB 9CF4 LDB \$B1,+\$B4
 004291 11BC C873 LDR \$B4,+\$B3
 004292 11BD 4008 SOL \$R4,8
 004293 11BE 9873 LDR \$R1,+\$B3
 004294 11BF 9570 00FF AND \$R1,X#FF'
 004295 11C1 C651 XOR \$R4,=\$R1
 004296 11C2 CF71 STR \$R4,+\$B1
 004297 11C3 57F9 BINC \$R5,>PKR1
 004298 11C4 8FC0 0322 RSTR SAV1,=Z'CCD0'
 11C6 CCD0
 004299 11C7 8384 JMP \$B4
 * WAIT FOR STATUS COMPLETE
 * LNJ \$B4,<TEST
 * B \$+2 RETURN
 * DC XX TIMEOUT IN 120TH'S SEC.
 *
 * TEST SAVE <SAV1,=Z'4909'
 004307 11C8 8F00 14E7 R1,4,7,\$B4,7
 11CA 4909
 004308 11CB D880 0000 X LAB \$B5,<ZHPFR
 004309 11CD 8751 CL \$R1
 004310 11CE 9F00 0000 X STR \$R1,<ZHRTCI
 004311 11D0 1C02 LDV \$R1,=2
 004312 11D1 9F00 0000 X STR \$R1,<ZHRTCL
 004313 11D3 9874 LDR \$R1,+\$B4
 004314 11D4 9874 LDR \$R1,+\$B4
 004315 11D5 9F00 0000 X STR \$R1,<ZHRTCC
 004316 11D7 9F00 154B STR \$R1,<INTM
 004317 11D9 0004 RTCN LNJ \$B4,<INXT
 004318 11DA C380 1161 TESTZ LB =\$R5,=Z'1000'
 004319 11DC 82D5
 11DD 1000
 004320 11DE 505F BBT >TESTZ1
 004321 11DF 8980 0000 X CMZ <ZHRTCC
 004322 11E1 0F01 FFFF NOP \$
 004323 *
 004324 11E3 C800 1561 LDR \$R4,<CONT6
 004325 11E5 C380 1173 LNJ \$B4,<CGSCH
 004326 11E7 8055 IU =\$R5,=\$R4
 11E8 0054
 004327 11E9 0773 BIOT >TESTZ
 004328 11EA F380 125C LNJ \$B7,<ERROR
 004329 11EC 0000 TESTZ1 HLT
 004330 11ED 0005 RTCF
 004331 11EE 9800 154B LDR \$R1,<INTM
 004332 11F0 9200 0000 SUB \$R1,<ZHRTCC
 004333 11F2 C380 11F8 LNJ \$B4,<UPTM
 004334 *
 004335 11F4 8F80 14E7 RSTR <SAV1,=Z'4909'
 11F6 4909
 004336 11F7 8384 JMP \$B4
 004337 *
 004338 * UPTM ADD \$R1,<TTOT
 004339 11F8 9A00 154A STK \$R1,<TTOT
 004340 11FA 9F00 154A SUR \$R1,1
 004341 11FC 1041 CMR \$R1,=60
 004342 11FD 9970 003C BL >UPTM1
 004343 11FF 0205 CL <TTOT
 004344 1200 8700 154A INC <SEC
 004345 1202 8A80 1549 UPTM1 INC \$B4
 004346 1204 8384 *
 * TIME DELAY OF 25 MSEC (APPROX)
 * LNJ \$B4,<DLAY
 DLAY SAVE <SAV1,=Z'CE8C'
 1205 8F00 14E7
 1207 CE8C

004353	1208	9800	154E		LDR	\$R1,<HRTZ	GET FREQ	
004354	120A	1044			SOR	\$R1,4	SET FOR CLOCK/16	
004355	120B	0F90			B	>DLAY2		
004356				*	* LONG DELAY (APPROX 225 MS)			
004357				*				
004358				*				
004359				*				
004360	120C	8F40	02DA		DLAYLI	SAVE	SAV1,=Z'CE8C'	
	120E	CE8C			LDR	\$R1,<HRTZ	GET CLOCK	
004361	120F	9800	154E		SOR	\$R1,1	GET CLOCK/4	
004362	1211	1042		X	DLAY2	LAB	\$B5,<ZHPFR	
004363	1212	1F03			STB	\$B5,<ZHISAZ+\$AF	CLEAR B5	
004364	1213	0F81	0007		STR	\$R1,<HRTCC	ZERO OUT LEVI INTERRUPT VECTOR	
004365	1215	8F40	02D1		LNJ	\$B4,<UPTM	SET RTC CURRENT VALUE	
	1217	CE8C			CL	=\$R1	UPDATE TIME	
004366	1218	9800	154E		STR	\$R1,<HRTCI	RTC RESET VALUE	
004367	121A	1041			LDV	\$R1,=1		
004368	121B	DB80	0000	X	DLAY1	BDEC	\$R1,<DLAY4	
004369	121D	DF80	0001	X	LDV	\$R1,=10	TURN ON RTC TIMER	
004370	121F	9F00	0000	X	ADV	\$R4,=6	RESTORE COUNTER	
004371	1221	C380	11F8		*	* GIVE INPUT LCT BYTE TO TRY TO INDUCE INTERFERENCE		
004372	1223	8751			IO	=\$R5,\$R4		
004373	1224	9F00	0000	X	DLAY4	ADV	\$R4,=-6	GET BACK INPUT STAT FC
004374	1226	1C01			LDR	\$R6,<ERMG+1	GET TEST TABLE	
004375	1227	9F00	0000	X	CMR	\$R6,=A'SI'		
004376	1229	C800	1561		BNE	>DLAY3	BRANCH MEANS NOT TEST SI	
004377	122B	C380	1173		INC	=SRI		
004378					CMZ	<ZHRTCC		
004379	122D	1C0A			B	>DLAYS		
004380	122E	0004			*	* SET UP RTC AND START IT.		
004381	122F	1700	1236		DLAY3	CMZ	<ZHRTCC	
004382	1231	1C0A		X	IO	=\$R5,\$R4	INPUT STAT	
004383	1232	4E06			*	* HANG HERE IF MLCC DOES NOT RESPOND WITH A SECOND HALF READ		
004384					DLAY5	STR	\$R5,<STAT	STORE LAST STATUS READ
004385						BAG	<DLAY1	WAIT TILL DONE
004386	1233	8055		X		RTCF	RSTR	TURN OFF TIMER
	1234	0054					SAV1,=Z'CE8C'	
004387	1235	4EFA				JMP	\$B4	RETURN
004388	1236	E800	1581					
004389	1238	E970	5349					
004390	123A	0985						
004391								
004392	123B	8AD1						
004393	123C	8980	0000	X				
004394	123E	0F87						
004395								
004396	123F	8980	0000	X				
004397	1241	8055						
	1242	0054						
004398								
004399	1243	DF00	154D					
004400	1245	0A00	122F					
004401	1247	0005						
004402	1248	8FC0	029E					
004403	124A	CE8C						
004404	124B	8384						
004405								
004406								
004407								
004408								
004409								
004410	124C	9874						
004411	124D	9F00	0000	X	RTC	LDR	\$R1,+\$B4	GET DELAY DESIRED
004412	124F	9F00	0000	X		STR	\$R1,<ZHRTCI	STORE INITIAL VALUE
004413	1251	CF80	14CD			STR	\$R1,<ZHRTCC	STORE CURRENT VALUE
004414	1253	C380	11F8			STB	\$B4,<TPR	
004415	1255	CC80	14CD			LNJ	\$B4,<UPTM	UPDATE TIME
004416	1257	1C03				LDV	\$B4,<TPR	
004417	1258	9F00	0000	X		STR	\$R1,=3	LEVEL 3
004418	125A	0004				RTCN	\$R1,<ZHRTCL	FOR RTC
004419	125B	8384				JMP	\$B4	TURN ON RTC
004420								RETURN
004421								
004422								
004423								
004424								
004425	125C	8F40	02DA					
	125E	FFFF						
004426	125F	1CFO						
004427	1260	8753						
004428	1261	EBC7	FFFF					
004429	1263	EF80	126A					
004430								
	1265	FBC0	0003					
	1267	D380	0000	X				
	1269	0F80						
	126A	1265						
	126B	1580						
004431	126C	EB80	14C1					
004432	126E	CB80	14C2					
004433	1270	AB80	1282					
004440	1272	EF82						
004441	1273	8980	154C					
004442	1275	0900	128D					
004443	1277	BBB0	1537					
004444	1279	9880	1281					
004450	127B	BF81						
004458	127C	FBC0	0003					
	127E	D380	0000	X				
	1280	0F80						
	1281	127C						
	1282	127C						
004463	1283	8AD3						
004464	1284	CF82						
004465	1285	3D08						
004466	1286	0984						
004467	1287	EB80	14C1					
004468	1289	EF82						
004469	128A	1780	1277					
004470	128C	0000						
004471	128D	8F80	1537					

LDR \$R1,<HRTZ
 SOR \$R1,4
 B >DLAY2
 * LONG DELAY (APPROX 225 MS)
 DLAYLG SAVE SAV1,=Z'CE8C'
 LDR \$R1,<HRTZ
 SOR \$R1,1
 LAB \$B5,<ZHPFR
 STB \$B5,<ZHISAZ+\$AF
 STR \$R1,<HRTCC
 LNJ \$B4,<UPTM
 CL =\$R1
 STR \$R1,<HRTCI
 LDV \$R1,=1
 ADV \$R1,=10
 DLAY1 BDEC \$R1,<DLAY4
 LDV \$R1,=10
 ADV \$R4,=6
 * GIVE INPUT LCT BYTE TO TRY TO INDUCE INTERFERENCE
 IO =\$R5,\$R4
 DLAY4 ADV \$R4,=-6
 LDR \$R6,<ERMG+1
 CMR \$R6,=A'SI'
 BNE >DLAY3
 * IS SOFT INITIALIZE TEST. BYPASS STATUS INPUTS.
 INC =\$R1
 CMZ <ZHRTCC
 B >DLAYS
 * SET UP RTC AND START IT.
 LNJ \$B4,<RTC
 DC XX XX = 120*S OF A SECOND DELAY DESIRED
 RTC LDR \$R1,+\$B4
 STR \$R1,<ZHRTCI
 STR \$R1,<ZHRTCC
 STB \$B4,<TPR
 LNJ \$B4,<UPTM
 LDV \$B4,<TPR
 STR \$R1,=3
 RTCN \$R1,<ZHRTCL
 JMP \$B4
 * ERROR PRINT ROUTINE
 *EELNJ \$B7,<ERROR
 ERRUR SAVE SAV5,=Z'FFFF'
 ERRUR LDV \$R1,=-16
 CL =\$R3
 LAB \$B6,\$B7,-2*\$AF
 STB \$B6,<ERR1+4+\$AF
 ERR1 CALL ZV\$ER,\$,ERMG
 *EELNJ \$B7,<ERROR
 ERRUR SAVE ALL REGISTERS
 ERRUR LDV \$R1,=-16
 CL =\$R3
 LAB \$B6,\$B7,-2*\$AF
 STB \$B6,<ERR1+4+\$AF
 ERR1 CALL ZV\$ER,\$,ERMG
 *EELNJ \$B7,<ERROR
 ER11 STB \$B6,\$B2
 CMZ <ERR5
 BE <ERR5
 ERR2 LAB \$B3,<SAV5,\$R3
 LAB \$B1,<ERR3+4+\$AF
 ERR21 STB \$B3,\$B1
 ERR3 CALL ZV\$HZ,\$,\$
 *EELNJ \$B7,<ERROR
 ER34 INC =\$R3
 STB \$B4,\$B2
 CMV \$R3,=7*\$AF+1
 BNE >ERR4
 LAB \$B6,<CRLF
 STB \$B6,\$B2
 ERR4 BINC \$R1,<ERR2
 HLT
 RSTR <SAV5,=Z'FFFF'
 BUMP INDEX
 SEE IF CR-LF NEEDED
 PUT CR-LF INTO CALL
 DO NEXT REGISTER
 HALT AFTER ERROR
 RESTORE REGISTERS

004472 128F FFFF
 004483 1290 8387
 004484
 004485
 004486
 004487
 004488
 004489
 004490
 004491
 004492
 004493
 004494
 004495
 004496
 004497
 004498 1291
 004499
 004500 0200
 004501 1291 2003
 004505
 004506 1292 BD31
 004507
 004508 0205
 004509 1293 00C6
 004513 1294 0186
 004517 1295 0110
 004518 1296 9741
 004519
 004520 1297 9641
 004523 1298 2703
 004524
 004525 0210
 004526
 004527 1299 BD31
 004528
 004529 0213
 004530
 004531 129A 0096
 004532 129B 4126
 004535
 004536 0217
 004537
 004538 129C 0996
 004541 129D 1810
 004542 129E 9741
 004543
 004544 129F 9641
 004547 12A0 2603
 004548
 004549 0220
 004550
 004551 12A1 BD31
 004552
 004553 0223
 004554
 004555 12A2 0096
 004556 12A3 4127
 004559
 004560 12A4 E95A
 004561
 004562 12A5 5D27
 004565
 004566 022B
 004567
 004568 12A6 03BD
 004569 12A7 3100
 004570
 004571 022E
 004572
 004573 12A8 5D26
 004576
 004577 12A9 045A
 004578
 004579 12AA 5D26
 004582
 004583 0235
 004584
 004585 12AB 03BD
 004586 12AC 3100
 004587
 004588 0238
 004589
 004590 12AD DE63
 004593 12AE 2704
 004594
 004595 12AF DE63
 004598
 004599
 004600
 004601
 004602 12B1 BD31
 004603
 004604
 004605
 004606
 004607 12B2 00BD
 004608
 004609 12B4 DE63
 004612
 004613
 004614
 004615
 004618 12B6 9617
 004619 12B7 1097
 004620
 004621 12B8 4196
 004622 12B9 4127
 004625

SAFF JMP \$B7 RETURN

* * * -VD-VL-VC-VC -VI-VN-VS-VT-VR-VU-VC-VI-VI-V0-VN -VT-VE-VS-VT , -VC-VH-

* * * RAM INSTRUCTION TEST PROGRAM GENERATION

* * * THIS SECTION GETS LOADED AT HEX 200

* * * INSTR1 ORG X'200'

* * * ST LOC ST

* * * ST EQU X'200'

* * * BT1

* * * WAIT UNCONDITIONAL BRANCH ERROR

* * * LOC BT1

* * * LD EQU X'0205'

* * * =1

* * * LD =1

* * * C =1

* * * BET BT6

* * * BRANCH IF TRUE

* * * BT5 LOC BT5

* * * WAIT X'0210'

* * * BRANCH

* * * ON EQUAL TRUE OR *LD* OR *C* ERROR

* * * BT6 LOC BT6

* * * BEF EQU X'0213'

* * * BTB

* * * BRANCH IF FALSE

* * * RMOP LOC RMUP

* * * EQU C X'0217'

* * * 24

* * * COMPARE WITH LCT 24 (0)

* * * BEF BT6

* * * BTB LOC BTB

* * * EQU X'0220'

* * * BRANCH

* * * ON EQUAL FALSE ERROR

* * * BTC LOC BTC

* * * EQU X'0223'

* * * BT5

* * * BET

* * * GOES FROM 1 TO 0

* * * BZT BT12

* * * BRANCH IF ZERO TRUE

* * * BT11 LOC BT11

* * * EQU X'022B'

* * * BRANCH

* * * ON ZERO TRUE ERROR

* * * BT12 LOC BT12

* * * EQU X'022E'

* * * BT17

* * * BRANCH IF ZERO FALSE

* * * GOES FROM 0 TO FF

* * * DEC R

* * * BZF BT12

* * * BT18 LOC BT18

* * * EQU X'0238'

* * * ERZ1

* * * BLCT

* * * DEC R

* * * BZF BT18

* * * BT17 LOC BT17

* * * EQU X'0235'

* * * BRANCH

* * * ON ZERO FALSE ERROR

* * * BT18 LOC BT18

* * * EQU X'0238'

* * * ERZ1

* * * BLCT

* * * LOC ERZ1

* * * EQU X'0240'

* * * BRANCH

* * * ERZ1

* * * WAIT

* * * BT1D LOC BT1D

* * * EQU X'024A'

* * * 23

* * * COMPARE WITH LCT 23

* * * BET BT4F

* * * WAIT ERROR

* * * IN STORE INST.

004626	12BA	03BD					
004627	12BB	3100					
004628							
004629		0256	*	BT4F	LOC	BT4F	
004630			*		EQU	X'0256'	
004633	12BC	D417	*		AND	23	AND R (FF) WITH LCT 23 (FF)
004634			*		C	=X'FF'	C =X'FF'
004637	12BD	86FF					
004638	12BE	1097					
004639			*		BEF	BT4D	
004640	12BF	4196					
004641	12C0	4126	*		AND	24	AND R (FF) WITH LCT 24 (0)
004644			*		C	24	COMPARE R(0) WITH LCT 24 (0)
004645	12C1	16D4					
004646			*				
004649	12C2	1896	*				
004652	12C3	1810					
004653	12C4	9741	*				
004654			*		DEF	BT4D	
004655	12C5	9641					
004658	12C6	260B	*				
004659			*		AND	23	AND R (0) WITH LCT 23 (FF)
004662	12C7	D417	*		C	24	COMPARE R (0), LCT 24 (0)
004663			*				
004666	12C8	9618					
004667	12C9	1097	*		BET	BTAC	
004668			*				
004669	12CA	4196					
004670	12CB	4127	*				
004673			*		BT4D	LOC	
004674		0277	*		EQU	X'0277'	
004675			*		WAIT	AND	INSTRUCTION ERROR
004676	12CC	03BD					
004677	12CD	3100	*				
004678			*		BTAC	LOC	
004679		027A	*		EQU	X'027A'	
004680			*		AND	24	AND R (0) WITH LCT 24 (0)
004683	12CL	D418	*		B	TMP1	
004684			*				
004687	12CF	2002	*		TEMPA	LOC	
004688			*		EQU	X'027E'	
004689		027E	*		B	ST	BRANCH TO SUBROUTINE
004690			*				
004693	12D0	2080	*		BTAC	TEMPA	
004694			*		EQU	X'0280'	
004695		0280	*		C	24	
004696			*		DEF	BT4D	
004699	12D1	9618					
004700	12D2	1097	*				
004701			*		OR	24	OR R (0), LCT 24 (0)
004702	12D3	4196	*		C	24	COMPARE R WITH DATA BUFF
004703	12D4	4126	*				
004706			*		BET	BT4D	
004707	12D5	EEDA	*				
004710			*				
004711	12D6	1896	*		OR	23	OR R(0), LCT 23 (FF)
004714	12D7	1810	*		C	23	COMPARE R WITH LCT 23
004715	12D8	9741	*				
004716			*		DEF	BT4C	
004717	12D9	9641	*				
004720	12DA	2616	*				
004721			*		OR	23	
004724	12DB	DA17	*		C	23	
004725			*				
004728	12DC	9617	*		DEF	BT4C	
004729	12DD	1097	*				
004730			*				
004731	12DE	4196	*		OR	24	OR R (FF), LCT 24 (0)
004732	12DF	4126	*		C	24	COMPARE R (FF), LCT 23 (FF)
004735			*				
004736	12E0	0BDA	*		BET	BTAC	
004739			*				
004740	12E1	1896	*				
004743	12E2	1710	*				
004744	12E3	9741	*				
004745			*				
004746	12E4	9641	*				
004749	12E5	2703	*				
004750			*		BT4C	LOC	
004751		02AA	*		EQU	X'02AA'	
004752			*		WAIT	OR	INSTRUCTION ERROR
004753	12E6	BD31	*				
004754			*		BT4C	LOC	
004755		02AD	*		EQU	X'02AD'	
004756			*		OR	23	
004757	12E7	00DA	*				OR R (FF), LCT23 (FF)
004760			*		C	23	COMPARE R (FF), LCT 23
004761	12E8	1796	*				
004764	12E9	1710	*				
004765	12EA	9741	*				
004766			*		DEF	BT4C	
004767	12EB	9641	*				
004770	12EC	26F2	*				
004771			*				
004774	12ED	D818	*		XOR	24	EXCL. OR R (FF), DATA BUFF (0)
004775			*		C	23	COMPARE R(FF), LCT 23 (FF)
004778	12EE	9617	*				
004779	12EF	1097	*				
004780			*				
004781	12F0	4196	*		DEF	BT4B	
004782	12F1	4126	*				
004785			*				
004786	12F2	16D8	*		XOR	23	EXCLUSIVE OR R (FF), LCT 23 (FF)
004789			*		C	24	COMPARE R (0) WITH LCT 24 (0)
004790	12F3	1796	*				
004793	12F4	1810	*				
004794	12F5	9741	*				
004795			*		DEF	BT4B	
004796	12F6	9641	*				
004799	12F7	260B	*		XOR	24	EXCL. OR R (0) WITH LCT 24 (0)
004800			*		C	24	
004803	12F8	D818	*				
004804			*				

004807	12F9	9618			
004808	12FA	1097			
004809					
004810	12FB	4196	*	BET	BTAC
004811	12FC	4127			
004814			*	BT4B	LOC X#02D9*
004815		02D9	*	EQU WAIT	XOR
004816					INSTRUCTION ERROR
004817	12FD	03BD	*	BTAC	X#02DC*
004818	12FE	3100	*	EQU XOR	23
004819			*	C	23
004820		02DC	*	BTAC	EXCL. OR R (U), LCT 23 (FF)
004821			*	EQU XOR	COMPARE R (FF), LCT 23 (FF)
004824	12FF	0817	*	C	
004825			*	RET	
004826	1300	9617	*	BT4B	
004829	1301	1097	*	EQU LD	X#E7*=X#AA*
004830			*	BT4J	
004831	1302	4196	*	BT4J	
004832	1303	4126	*	EQU LD	LD =X#AA*
004835			*	BT4J	
004836		02E7	*	EQU LD	
004837			*	BT4J	
004838	1304	F2C6	*	EQU LD	
004841			*	BT4J	
004842	1305	AADE	*	EQU LD	
004843	1306	52DE	*	BTAC	
004844			*	EQU LD	
004845	1307	0001	*	BTAC	
004846			*	EQU LD	
004847			*	BTAC	
004848			*	EQU LD	
004849			*	BTAC	
004850		1308	*	INSTR2 EQU \$	
004851			*	LOC INS2A	
004852		02EE	*	EQU INS2A	X#02EE*
004853			*	JUMP IN56	
004856	1308	3F00	*	ORG BTYD	X#02F1*
004857			*	LOC BTYD	X#02F1*
004858		02F1	*	EQU NOP	DUMMY
004860			*	BTAC	
004861	1309	EF01	*	BS TEMP	BRANCH STORE TO X#200*
004862			*	BTAC	
004863	130A	BD30	*	BS TEMP	
004864	130B	8020	*	C =X#AA*	COMPARE R(AA), =AA
004867	130C	8786	*	BTAC	
004871	130D	AA10	*	BTAC	
004872	130E	9741	*	BTAC	
004873	130F	9641	*	BTAC	
004877	1310	2703	*	BTAC	
004878			*	BTAC	
004879	1310	0300	*	BTAC	
004880			*	BTAC	
004881	1311	BD31	*	BTAC	AND STORE ERROR
004882			*	BTAC	
004883	1311	0303	*	BTAC	
004884			*	BTAC	BRANCH STORE TO X#306*
004885	1312	00BD	*	BTAC	
004886	1313	3080	*	BTAC	
004889	1314	2062	*	C =X#55*	C =X#55*
004890			*	BTAC	
004893	1315	8655	*	BTAC	
004894	1316	1097	*	BTAC	
004895			*	BTAC	
004896	1317	4196	*	BTAC	
004897	1318	4126	*	BTAC	
004900			*	BTAC	
004901			*	HERE FOLLOWS THE TABLE LOOKUP TEST	
004902			*	BTAC	
004903			*	BTAC	
004904	1319	EFC6	*	LD =0	LD =0
004907			*	TLU 55	TABLE LOOKUP, POINTER AT LCT 55
004908	131A	00DE			
004911	131B	37BD			
004912	131C	30E0			
004913			*	C =3	FIRST ENTRY OF TABLE IS 3
004916	131D	8603	*	BTAC	
004917	131E	1097	*	BTAC	
004918			*	BTAC	
004919	131F	4196	*	BTAC	
004920	1320	4127	*	WAIT TABLE	LOOKUP ERROR 24 1ST ENTRY
004923	1321	03BD	*	BTAC	
004925	1322	3100	*	BTAC	
004926			*	NEXT LOC LD	NEXT X#0324*=2
004927		0324	*	BTAC	LD =2, GET 3D ENTRY
004928			*	TLU 55	TABLE LOOKUP, LCT 55
004931	1323	C602	*	BTAC	
004932	1324	DE37	*	BTAC	
004936	1325	BD30	*	BTAC	
004937			*	BTAC	
004938	1326	E0BD	*	BTAC	
004939	1327	3100	*	BTAC	
004940			*	BTAC	
004941	1328	BD31	*	BTAC	
004942			*	BTAC	
004943	1329	00BD	*	BTAC	
004944	132A	3100	*	BTAC	
004945			*	BTAC	
004946	132B	BD31	*	BTAC	ERROR 24 INCORRECT BRANCH
004947			*	BTAC	GOOD
004948	132C	0020	*	BTAC	TLU BRANCH
004951			*	BTAC	
004952	132D	09BD	*	BTAC	
004953	132E	3100	*	BTAC	
004954			*	BTAC	
004955	132F	BD31	*	BTAC	
004956	1330	00BD	*	BTAC	

004958	1331	3100			
004959		0342	*	NEXT1 EQU C	NEXT1 X'0342' =X'2'
004960			*		COMPARE R WITH =X'2'
004961	1332	8602	*	BET	NEXT2
004962	1333	1097	*	WAIT	TLU
004963			*		ERROR 24 WRONG DATA CAME BACK
004964	1334	4196	*	WAIT	TLU
004965	1335	4127	*		ERROR 24 WRONG DATA CAME BACK
004966			*	LOC	NEXT2 X'034E'
004967			*	EQU	LD =1
004968			*	TLU	55
004969			*	C	=X'1A'
004970			*	BET	NEXT3
004971			*	WAIT	WRUNG
004972			*	LOC	NEXT3 X'0361'
004973	1337	3100	*	EQU	WAIT
004974			*	NOP	
004975			*	INSTRUCTION TEST, PART 3, GETS LOADED AT 306	
004976			*	INSTR3 EQU \$	
004977	1340	BD31	*	ORG	X'036A'
004978			*	BTAG EQU BTAG	
004979			*	LD	X'036A'
004980			*	C	=X'FF'
004981	1339	DE37	*	BET	BTB5
004982	133A	BD30	*	WAIT	
004983			*	LOC	BTAG
004984			*	EQU	X'0380'
004985	133B	E086	*	LD	LOAD
004986			*	C	
004987	133C	1A10	*	BET	
004988			*	LOC	BTB5
004989	133D	9741	*	EQU	X'0383'
004990			*	LD	44
004991			*	C	=X'55'
004992	133E	9641	*	BET	BTAG
004993	133F	2703	*	WAIT	
004994			*	LOC	BTAG
004995			*	EQU	X'0380'
004996	1340	BD31	*	LD	LOAD
004997			*	C	
004998			*	BET	
004999	0361		*	LOC	BTAG
005000			*	EQU	X'0383'
005001	1341	00BD	*	LD	28
005002	1342	3100	*	C	=X'AA'
005003			*	BET	BTAG
005004			*	WAIT	
005005			*	LOC	BTAG
005006			*	EQU	X'0380'
005007			*	LD	LOAD
005008	1343		*	C	
005009	1343	0100	*	BET	
005010			*	LOC	BTAG
005011			*	EQU	X'036A'
005012	036A		*	LD	3
005013			*	C	=X'FF'
005014	1344	D603	*	BET	BTAG
005015			*	WAIT	
005016	1345	86FF	*	LOC	BTAG
005017	1346	1097	*	EQU	X'0380'
005018			*	LD	LOAD
005019	1347	4196	*	C	
005020	1348	4126	*	BET	
005021			*	LOC	BTAG
005022			*	EQU	X'0383'
005023			*	LD	44
005024	1349	0BD6	*	C	=X'AA'
005025			*	BET	BTAG
005026	134A	1C86	*	WAIT	
005027	134B	AA10	*	LOC	BTAG
005028	134C	9741	*	EQU	X'0380'
005029			*	LD	LOAD
005030	134D	9641	*	C	
005031	134E	2703	*	BET	
005032			*	LOC	BTAG
005033	0380		*	EQU	X'0380'
005034			*	LD	LOAD
005035			*	C	
005036	134F	BD31	*	BET	
005037			*	LOC	BTAG
005038			*	EQU	X'0383'
005039			*	LD	44
005040	1350	00D6	*	C	=X'55'
005041			*	BET	BTAG
005042	1351	2C86	*	WAIT	
005043	1352	5510	*	LOC	BTAG
005044	1353	9741	*	EQU	X'0383'
005045			*	LD	44
005046			*	C	=X'55'
005047			*	BET	
005048	1354	9641	*	LOC	BTAG
005049	1355	26F2	*	EQU	X'0380'
005050			*	LD	LOAD
005051	1356	D63F	*	C	
005052			*	BET	
005053	1357	86AA	*	LOC	BTAG
005054	1358	1097	*	EQU	X'0383'
005055			*	LD	44
005056	1359	4196	*	C	=X'55'
005057	135A	4126	*	BET	BTAG
005058			*	WAIT	
005059			*	LOC	BTAG
005060	135B	E7C6	*	EQU	X'0380'
005061			*	LD	LOAD
005062	135C	8054	*	C	
005063			*	BET	
005064	135D	8640	*	LOC	BTAG
005065	135E	1097	*	EQU	X'0383'
005066			*	LD	44
005067			*	C	=X'55'
005068	135F	4196	*	BET	BTAG
005069	1360	4126	*	WAIT	
005070			*	LOC	BTAG
005071	1357	86AA	*	EQU	X'0383'
005072			*	LD	44
005073	1358	1097	*	C	=X'55'
005074			*	BET	BTAG
005075	1359	4196	*	WAIT	
005076	135A	4126	*	LOC	BTAG
005077			*	EQU	X'0380'
005078			*	LD	LOAD
005079	135B	E7C6	*	C	
005080			*	BET	
005081			*	LOC	BTAG
005082	135C	8054	*	EQU	X'0383'
005083			*	LD	44
005084	135D	8640	*	C	=X'55'
005085	135E	1097	*	BET	BTAG
005086			*	WAIT	
005087			*	LOC	BTAG
005088	135F	4196	*	EQU	X'0380'
005089	1360	4126	*	LD	LOAD
005090			*	C	
005091			*	BET	
005092	1361	1F54	*	LOC	BTAG
005093			*	EQU	X'0383'
005094			*	LD	44
005095	1362	8620	*	C	=X'55'
005096	1363	1097	*	BET	BTAG
005097			*	WAIT	
005098	1364	4196	*	LOC	BTAG
005099	1365	4126	*	EQU	X'0383'
005100			*	LD	44
005101	1366	1596	*	C	=X'55'
005102	1367	6585	*	BET	BTAG
005103	1368	2027	*	WAIT	
005111			*	LOC	BTAG
005112	03B5		*	EQU	X'03B5'
005113			*	LD	BLCF
005114	1369	03BD	*	C	
005115	136A	3100	*	BET	
005116			*	LOC	BTAG
005117	03B8		*	EQU	X'03B8'
005118			*	LD	BT4H

OR BLCT FAILURE

BRANCH IF LAST BLOCK TRUE

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005119 136B 9665
005120 136C 8520
005123 136D 26F7
005124 * LD =X'550*
005127 136E C655 * RET
005128 136F DE52
005130 1370 6E00
005131 * LOC ERM6
005132 03C4 EQU X'03C4'
005133
005134 1371 BD31
005135 1372 0001 * NOP
005136
005137
005138
005139 * INSTRUCTION TEST - PART 4 - LOADED AT X'5FD0*
005140
005141 1373 INSTR4 EQU $ WAIT
005142
005143 1373 BD31
005144 * WAIT
005145 1374 00BD
005146 1375 3100
005147 * LOC INS4
005148 03CE EQU X'03CE' JUMP
005149
005150 1376 3F00 INS5
005151
005152 * PART 5 - LOADED AT AFE
005153
005154
005155 INSTR5 EQU $ WAIT
005156
005157
005158 1377 06BD
005159 1378 3100
005160 * WAIT
005161 1379 BD31
005162 * LOC INS5
005163 03D7 EQU X'03D7' JUMP BYTD
005164
005165 137A 003F
005166 137B FF17 DC BYTD-X'03DA'
005167
005168
005169 * INSTRUCTION TEST - PART 6, LOADED AT DFB
005170
005171
005172 137C INSTR6 EQU $ WAIT
005173
005174 137C BD31
005175 * WAIT
005176 137D 00BD
005177 137E 3100
005178 * LOC INS6
005179 03E0 EQU X'03E0' JUMP
005180
005181 137F 3FFF INS4
005182
005183 * TRANSMIT DATA CHANNEL PROGRAM
005184 * GETS LOADED AT X'400' IN MLCC RAM
005185
005186
005187
005188
005189
005190
005191 1380 EB00 * ORG X'400'
005192
005193 0400 * XMDT LOC XMUT
005194 1381 EQU X'0400' TRANM1 EQU $ RTD1
005195
005196 1381 2003 * RTD LOC RTD
005197 0402 EQU X'0402' END
005201
005202 1382 BD31 * RTD1 LOC RTD1
005203 0405 EQU X'0405' LOAD AND CHECK COUNTER
005204
005205
005206 1383 00D6 * LD 55
005207
005208 1384 3786 * C =5 IF 5 STORE IN 57
005209 1385 0510
005210 1386 9741
005211
005212 1387 9641 * BET R57
005213 1388 275A
005214
005215 1389 8604 * C =4 IF 4 STORE IN 58
005216 138A 1097
005217
005218 138B 4196 * BET R58
005219 138C 4127
005220
005221 138D 5886 * C =3 IF 3 STORE IN 59
005222 138E 0310
005223 138F 9741 * BET R59
005224
005225 1390 9641
005226 1391 2756
005227 1392 8602 * C =2 IF 2 STORE IN 60
005228 1393 1097
005229 1394 4196 * BET R60
005230 1395 4127 * C =1 IF 1 STORE IN 61
005231
005232 1396 5486
005233 1397 0110
005234 1398 9741 * BET R61
005235 1399 9641
005236 139A 2752 * LD, IS ZERO
005237 139B BD30 * ST 62 STORE IN 62
005238 139C A0D7

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005268						
005269	0439	*	KTD2	EQU	X#0439*	
005270		*	LD		56	CHECK FLAG
005271	139D	3ED6	*	C	=0	
005274						
005275	139E	3886	*	BET	R70	BRANCH IF EOR
005278	139F	0010				
005279	13A0	9741				
005280						
005281	13A1	9641	*	BLCF	R67	
005284	13A2	2710	*	WAIT	GNB	BLCF DID NOT BRANCH
005285						
005286	13A3	DE63	*	LOC	R67	
005289	13A4	2603	*	EQU	X#044B*	
005290			*	BLCT	R68	
005291	13A5	BD31	*			
005292						
005293		044b	R67			
005294			*			
005295	13A6	00DE	*			
005296	13A7	6327	*			
005299			*			
005300	13A8	15BD	*			
005301	13A9	3120	*			
005302			*			
005305	13AA	20AE	*			
005306			*			
005307			*			
005308			*			
005309			*			
005310		0454	R70	LOC	R70	
005311			*	EQU	X#0454*	
005312	13AB	DE63	*	BLCF	R69	
005315	13AC	260F	*			
005316			*			
005317	13AD	DE63	*	BLCT	R71	
005320	13AE	2703	*			
005321			*			
005322	13AF	BD31	*	WAIT	WAIT	BLCF DID NOT BRANCH
005323			*			
005324		045F	R71	LOC	R71	
005325			*	EQU	X#045F*	
005326	13B0	00BD	*	GNB		
005327	13B1	3120	*			
005328			*			
005331	13B2	209E	*	B	RTD	
005332			*			
005333			*			
005334			*			
005335			*			
005336		0464	R68	LOC	R68	
005337			*	EQU	X#0464*	
005338	13B3	BD31	*	BLCT		DID BRANCH
005339			*			
005340		0467	R69	LOC	R69	
005341			*	EQU	X#0467*	
005342	13B4	00BD	*	WAIT	BLCF	DID BRANCH
005343	13B5	3100	*			
005344			*			
005345			*			
005346			*			
005347			*			
005348		046A	R57	LOC	R57	
005349			*	EQU	X#046A*	
005350	13B6	BD30	*	LD		
005351	13B7	A0D7	*	ST	57	
005352			*			
005355			*			
005356	13B8	3920	*	B	R50	
005359			*			
005360		0471	R58	LOC	R58	
005361			*	EQU	X#0471*	
005362	13B9	1ABD	*	LD		
005363	13BA	30A0	*			
005364			*			
005367	13Bd	073A	*	ST	58	
005368			*			
005371	13BC	2013	*	B	R56	
005372			*			
005373		0478	R59	LOC	R59	
005374			*	EQU	X#0478*	
005375	13BD	BD30	*	LD		
005376			*			
005377	13BE	A0D7	*	ST	59	
005380			*			
005381	13BF	3B20	*	B	R56	
105384			*			
05385		047F	R60	LOC	R60	
15386			*	EQU	X#047F*	
5387	13C0	0CBD	*	LD		
5388	13C1	30A0	*			
389			*			
392	13C2	D73C	*	ST	60	
93			*			
96	13C3	2005	*	B	R56	
17			*			
8		0486	R61	LOC	R61	
13C4			*	EQU	X#0486*	
	BD30		*	LD		
			*			
13C5	A0D7		*	ST	61	
			*			
	048B		R56	LOC	R56	
			*	EQU	X#048B*	
3C6	3DD6		*	LD	55	
C7	375A		*	DEC		
8	U737		*	ST	55	
17			*	LD	63	
8	U63F		*	DEC		
			*	ST	63	

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005423 13CA 5AD7
005426
005427 13CB 3F5D
005430 13CC 27A1
005431
005432 13CD DE63
005435 13CE 2703
005436
005439 13CF 3FFF
005440
005441
005442 049F
005443
005444 13D0 66BD
005445 13D1 3120
005446
005447 13D2 BD30
005448
005449 13D3 A03F
005452 13D4 FF5A
005453
005454
005455 13D5 0101
005456
005457
005458
005459
005460
005461
005462
005463
005464
005465 13D6 0100
005466
005467
005468 0200
005469
005472 13D7 2003
005473
005474 0202
005475
005476 13D8 BD31
005477
005478 0205
005479
005480 13D9 00D6
005483
005484 13DA 3786
005487 13DB 0510
005488 13DC 9741
005489
005490 13DD 9641
005493 13DE 2755
005494
005498 13E0 1097
005499
005500 13E1 4196
005501 13E2 4127
005504
005505 13E3 5386
005508 13E4 0310
005509 13E5 9741
005510
005511 13E6 9641
005514 13E7 2751
005515
005518 13E8 8602
005519 13E9 1097
005520
005521 13EA 4196
005522 13EB 4127
005525
005526 13EC 4F86
005529 13ED 0110
005530 13EE 9741
005531
005532 13EF 9641
005535 13F0 274D
005536
005539 13F1 D63E
005540
005541 13F2 BD30
005542
005543 0239
005544
005545 13F3 C0D6
005548
005549 13F4 3886
005552 13F5 0010
005553 13F6 9741
005554
005555 13F7 9641
005558 13F8 2710
005559
005560 13F9 DE63
005563 13FA 2603
005564
005565 13FB BD31
005566
005567 024B
005568
005569 13FC 00DE
005570 13FD 6327
005573
005574 024F
005575
005576 13FE 10BD
005577 13FF 3120
005578
005581 1400 20AE
005582
005583

*       BZT      RTD2
*       BLCT      R55
*       JUMP      RTD1
*       COME HERE FOR UNDERRUN TEST ONLY
*       LOC      R55
*       K55      EQU      X'049F'
*       GNB
*       LD
*       JUMP      RTD
*       DC      RTD-X'04A8'
*       NOP
*       NOP
*       NOP
*       *
*       *
*       *
*       RECEIVE DATA CHANNEL PROGRAM
*       *
*       ORG      X'200'
*       RCDTC1      EQU      $
*       LOC      RCDT
*       RCDT      EQU      X'0200'
*       B      XMD1
*       XMD      LOC      XMD
*       EQU      X'0202'
*       WAIT      NORMAL
*       XMD1      LOC      XMD1
*       LD      55
*       C      =5
*       BET      XM57
*       C      =4
*       BET      XM58
*       C      =3
*       BET      XM59
*       C      =2
*       BET      XM60
*       C      =1
*       BET      XM61
*       LD      62
*       ST,      PUT
*       LOC      XM62
*       EQU      X'0239'
*       LD      56
*       C      =0
*       BET      XM70
*       BLCF      XM67
*       WAIT      GNB
*       LOC      XM67
*       EQU      X'024B'
*       BLCT      XM68
*       LOC      XM71
*       EQU      X'024F'
*       GNB
*       B      XMD

```

005584		*		
005585		*		
005586	0254	*	LOC EQU X'M70'	
005587		*	BLCF	X'M69'
005588	1401	DE63		
005591	1402	260A		
005592			*	BLCT XM71
005593	1403	DE63		
005596	1404	27F3		
005597			*	WAIT WAIT
005598	1405	BD31		BLCT DID NOT BRANCH
005599			*	
005600			*	
005601			*	
005602			*	
005603	025F		XM68 LOC EQU X'M68'	
005604			*	WAIT BLCT
005605	1406	00BD		
005606	1407	3100		
005607			*	LOC XM69
005608		0262	*	EQU X'M0262'
005609			*	WAIT BLCF
005610	1408	BD31		DID BRANCH
005611			*	
005612			*	
005613			*	
005614		0265	*	XM57 LOC X'M57'
005615			*	EQU LD
005616			*	
005617	1409	00D6		
005620			*	ST
005621	140A	39BD		
005622	140B	30C0		
005623			*	B XM56
005626	140C	201A		
005627			*	LOC XM58
005628		026C	*	EQU X'M026C'
005629			*	LD 58
005632	140D	D63A		
005633	140E	BD30		
005634			*	ST
005635	140F	C020	*	B XM56
005636			*	
005639		0273	*	LOC XM59
005640			*	EQU X'M0273'
005641			*	LD 59
005642	1410	13D6		
005645			*	ST
005646	1411	3BBD		
005647	1412	30C0		
005648			*	
005651	1413	200C	*	B XM56
005652			*	LOC XM60
005653		027A	*	EQU X'M027A'
005654			*	LD 60
005657	1414	D63C		
005658			*	ST
005659	1415	BD30	*	B XM56
005660			*	
005661	1416	C020	*	LOC XM61
005664			*	EQU X'M0281'
005665		0281	*	LD 61
005666			*	
005667	1417	05D6		
005670			*	ST
005671	1418	3DBD		
005672	1419	30C0		
005673			*	
005674		0286	*	LOC XM56
005675			*	EQU X'M0286'
005678	141A	D637	*	LD 55
005679			*	
005680			*	DEC ST 55
005681	141B	5AD7	*	
005684			*	LD 63
005685	141C	37D6	*	
005688			*	DEC
005689	141D	3F5A	*	
005690			*	ST 63
005693	141E	D73F	*	
005694			*	BZT XMD2
005695	141F	5D27	*	
005698			*	BLCT XM55
005699	1420	A6DE		
005700	1421	6327		
005703			*	JUMP XMD1
005704	1422	033F		
005707	1423	FF6B	*	DC XMD1-X'M029A'
005708			*	LOC XM55
005709		029A	*	EQU X'M029A'
005710			*	GNB
005711	1424	BD31		FOR CCB OVERRUN TEST ONLY
005712			*	ST
005713	1425	20BD		
005714	1426	30C0		
005715			*	
005718	1427	3FFF	*	JUMP XMD
005719			*	NOP
005720	1428	5F01	*	NOP
005721			*	NOP
005722			*	
005723	1429	0101	*	
005724			*	
005725			*	
005726			*	
005727			*	
005728			*	MLCC INPUT DATA PROGRAM WITH
005729			*	CRC TO GENERATE RANDOM DATA
005730			*	
005731			*	
005732			*	
005733			*	
005734	142A		KCRT1 ORG X'200'	
			EQU \$	

005735			*	RMCRT	LUC	RMCRT1		
005736	0200		*	EQU	LD	X'0200'		CHECK CUNF. IS LRC8
005737			*			2		
005740	142A	0602	*		C	=X'C6'		
005741			*					
005744	142D	86C6	*					
005745	142C	1097	*		BTF	RMCRT13		BRANCH IF NOT LRC8
005746			*					
005747	142D	4196	*					
005748	142E	4126	*		LD	=1		
005751			*					
005752	142F	15C6	*					
005755			*		LUC	RMCRT15		
005756		020D	*	RMCRT5	EQU	X'020D'		
005757			*		ST	24		
005758	1430	01D7	*		CCH			
005761			*					
005762	1431	18BD	*					
005763	1432	3160	*					
005764			*		BS	RMCRT14		
005765	1433	BD30	*					
005766	1434	8020	*					
005769			*		LD	24		
005770	1435	3006	*					
005773			*		XOR	=X'FF'		
005774	1436	18C8	*					
005777			*		DEC			
005778	1437	FF5A	*					
005779			*		XOR	=X'FF'		
005782	1438	C8FF	*					
005783			*		B	RMCRT15		
005786	1439	20ED	*					
005787			*	RMCRT3	LUC	RMCRT3		
005788		0220	*		EQU	X'0220'		
005789			*		C	=X'44'		SEE IF CRC12
005792	143A	8644	*					
005793	143B	1097	*					
005794			*		LD	=5		
005795	143C	41C6	*					
005798			*		BET	RMCRT7		
005799	143D	0596	*					
005800	143E	4127	*					
005803			*	RMCRT1	LUC	RMCRT1		
005804		022B	*		EQU	X'022B'		
005805			*		CCH			
005806	143F	BD30	*					
005807	1440	3160	*		BS	RMCRT14		
005808			*					
005809	1441	BD30	*					
005810	1442	8020	*		LD	4		
005813			*					
005814	1443	14D6	*		DEC			
005817			*					
005818	1444	045A	*					
005819			*		B	RMCRT1		
005822	1445	20F3	*					
005823			*					
005824		0238	*	RMCRT7	LUC	RMCRT7		
005825			*		EQU	X'0238'		
005826			*		CCH			
005827	1446	BD31	*					
005828			*		BS	RMCRT4		
005829	1447	60BD	*					
005830	1448	3080	*					
005833	1449	2007	*					
005834			*		LD	4		
005837	144A	D604	*					
005838			*		DEC			
005840	144B	5AC4	*		AND	=X'3F'		
005843			*					
005844	144C	3F20	*		B	RMCRT7		
005847			*					
005848		0247	*	RMCRT4	LUC	RMCRT4		
005849			*		EQU	X'0247'		
005850	144D	F1D6	*		LD	4		
005853			*					
005854	144E	04BD	*		ST			
005855	144F	30C0	*					
005856			*		LD	3		
005859	1450	D603	*					
005860			*		ST			
005861	1451	BD30	*					
005862			*		BLCF	RMCRT8		
005863	1452	CODE	*					
005864	1453	6326	*					
005867			*		GNB			
005868	1454	03BD	*					
005869	1455	3120	*					
005870			*					
005871		0258	*	RMCRT8	LUC	RMCRT8		
005872			*		EQU	X'0258'		
005875	1456	D617	*		LD	23		
005876			*					
005877			*		DEC			
005878	1457	5AD7	*		ST	23		
005881			*					
005882	1458	175D	*		BZT	RMCRT2		
005885	1459	2704	*					
005886			*		RET			
005887	145A	DE52	*					
005888	145B	6E00	*					
005889			*					
005890		0264	*	RMCRT2	LUC	RMCRT2		
005891	145C	BD31	*		EQU	X'0264'		
005893			*					
005894	145D	0020	*		B	RMCRT		
005897			*					
005898	145E	9701	*		NOP			
005899			*					
005900			*					
005901			*					
			*		MLCC OUTPUT DATA PROGRAM			

005902
 005903
 005904
 005905 145F * ROD1 ORG X'200'
 005906 0200 * LOC RMUPT \$
 005907 * EQU X'0200'
 005908 * LD =4
 005909 145F C604 * ST 23
 005910 1460 D717 * LOC RMUPTA
 005911 0204 * EQU X'0204'
 005912 * LD =125
 005913 1461 C67D * ST 24
 005914 1462 D718 * LOC RMUPT1
 005915 0208 * EQU X'0208'
 005916 * LD
 005917 1463 BD30 * CCH
 005918 0218 * LD
 005919 1464 A0BD * BLCF RMUPT2 CHECK IF LAST CHARACTER
 005920 1465 3160 * CCH
 005921 1466 BD30 * GNB
 005922 0218 * BLCF RMUPT2
 005923 1467 A0DE
 005924 1468 6326
 005925 * CCH
 005926 1469 03BD * GNB
 005927 146A 3120 * LD
 005928 0218 * BLCF RMUPT2
 005929 * CCH X'0218'
 005930 146B BD31 * LD 24
 005931 146C 60D6 * DEC
 005932 146D 185A * C =0
 005933 146E 8600 * ST 24
 005934 146F 1097 * BEF RMUPT1
 005935 1470 41D7 * LD 23
 005936 1471 1896 * DEC
 005937 1472 4126 * BEF RMUPTA
 005938 1473 DFD6 * LD =0
 005939 1474 175A * WAIT
 005940 1475 8600 * ST 23
 005941 1476 1097 * BEF
 005942 1477 41D7 * WAIT
 005943 1478 1796 * TEST
 005944 1479 4126 * NOP
 005945 147A C0BD * NOP
 005946 147B 3100 * NOP
 005947 147C 0101 * NOP
 005948 147D * BVT TEST COUNTER FOR 4 CCB'S
 005949 VALIST EQU \$
 005950 006000 * ORG X'200'
 005951 * LD =4
 005952 147D C604 * BVT TEST
 005953 0202 * LOC BVT
 005954 006005 * EQU X'0202'
 005955 * BVFF
 005956 147E 9665 * BVFF
 005957 147F 8540 * BVBF
 005958 1480 2606 * BWT
 005959 1481 BD31 * WAIT ERROR
 005960 1482 00BD * WAIT BVT SHOULD BRANCH
 005961 1483 3100 * BWT
 005962 1484 020E * LOC BVFF
 005963 1485 9665 * EQU X'020E'
 005964 1486 8540 * BVBF
 005965 1487 2006 * BWT
 005966 1488 0216 * LOC BVFT
 005967 1489 00BD * EQU X'0216'
 005968 148A 3100 * WAIT ERROR, BVF SHOULDNT BRANCH
 005969 148B BD31 * BWT
 005970 148C 205A * LOC
 005971 148D 5D26 * EQU X'021C'
 005972 148E DFBD * DEC
 005973 148F 3100 * BZF BVI BRANCH IF MORE CCB'S
 005974 * WAIT WAIT FOR NEXT START TO
 005975 * CCB LIST RESET HAS NOW RESET VALID BITS
 005976 * LD =4 COUNTER

006053	1490	C604				
006054			* BVF	LOC	BVF	
006055		0228	*	EQU	X'0228'	BVR
006056				BVBF		
006057	1491	9665				
006058	1492	8540				
006061	1493	2706				
006062			*		WAIT	
006063	1494	BD31	*		WAIT	
006064						
006065	1495	00BD				
006066	1496	3100				
006067		0234		* BVK	LOC	BVK
006069			*	EQU	X'0234'	BVK
006070	1497	9665				
006071	1498	8540				
006074	1499	2602				
006075			*	B	BVL	
006078	149A	2006		*	LOC	BVK
006079			*	EQU	X'023C'	
006080		023C		*	WAIT	
006081						
006082	149B	BD31	*		WAIT	
006083						
006084	149C	00BD				
006085	149D	3100				
006086			*	* BVL	LOC	
006087		0242	*	EQU	X'0242'	GND
006088						
006089	149E	BD31	*		DEC	
006090						
006091	149F	205A	*	BZF	BVF	MORE TO GO
006092			*			
006093	14A0	5D26	*			
006096			*		WAIT	DONE
006097	14A1	DFBD				
006098	14A2	3100				
006099			*		WAIT	
006100	14A3	BD31				
006101		14A4	ECP	EQU	\$	
006102			*		DEFINED CONSTANTS	
006103			*			
006104			*			
006105			*			
006106			*			
006107			*			
006108	14A4	A001	CKC16	DC	Z'A001'	
006109	14A5	8408	CCITT	DC	Z'8408'	
006110	14A6	0000	LRC8	RESV	1,0	
006111	14A7	F010	CRC12	DC	Z'F010'	
006112	14A8	FFFF	ALLONE	DC	Z'FFFF'	
006113	14A9	0010	C16	DC	X'10'	
006114	14AA	0000	FRST	DC	0	
006115	14AB	0000	LOOP	DC	0	
006116	14AC	0000	PASS	DC	0	
006117	14AD	FFFF	ATLT	RESV	16,-1	
006118	14BE	0000	IMASK	RESV	1,0	
006119	14BF	5555	DFLT	DC	X'5555'	
006120	14C0	0101	PNOB	DC	X'0101'	
006121	14C0	0000	RNG	DC	0	
006122	14C1	805C	CRLF	DC	Z'805C'	
006123	14C2	2020	SPACE	DC	Z'2020'	
006124	14C3	0000	MLCADD	DC	0	
006125	14C4	0000	FW-REV	DC	0	
006126			*			
006127			*			
006128			*		STORAGE CONTANTS	
006129			*			
006130			*			
006131			*			
006132			*			
006133			*			
006134	14C5	14C7	ADDS	DC	<UMMY	
006135	14C6	0400	BFRNG	DC	X'400'	ADDRESS STORAGE LENGTH OF SEND, RECEIVE BUFFER
006136	14C7	0000	DUMMY	RESV	1,0	
006137	14C8	0000	COUNT	RESV	1,0	COUNTER
006138	14C9	0000	CHCT	DC	0	CHANNEL COUNT
006139	14CA	266A	ENDSAV	DC	<LSI	
006140	14CB	0000	TIMEIN	DC	0	INITIAL VALUE FOR PASS TIME
006141	14CC	0000	FLAG	DC	0	FLAG
006142	14CD	0000	TPR	RESV	S\$AF,0	TEMPORARY REGISTER
006143	14CE	0000	TEMP	RESV	S\$AF,0	
006144	14CF	0000	TEMP1	RESV	S\$AF,0	
006145	14D0	0000	TEMPST	RESV	1,0	
006146	14D1	0000	SCFLG	RESV	1,0	STATUS STORED HERE
006147	14D2	0000	CCBFLG	RESV	1,0	SCAN FLAG
006148	14D3	0000	CRCAC	RESV	1,0	SET CCB AFTER RUPT FLAG
006149	14D4	0000	IRG	RESV	1,0	CRC ACCUMULATOR
006150	14D5	0000	RANGE	RESV	1,0	RANGE INPUT VALUE
006151	14D6	0000	RANGE1	RESV	1,0	RANGE VALUE
006152	14D7	0000	RANGEW	RESV	1,0	SECONDARY RANGE
006153	14D8	0000	KAMAD	RESV	1,0	RANGE IN WORDS
006154	14D9	0000	RAMAD2	RESV	1,0	RAM ADDRESS IN MLCC
006155	14DA	0000	IMGA	RESV	S\$AF,0	SECONDARY RAM ADDRESS
006156	14DB	0000	TSAZ	DC	0	DUMMY CCB ADDRESS
006157	14DC	1547	SBCK	DC	<RMINST	O TO END TRAP SAVE AREA
006158	14DD	0000	SBCCB	DC	0	GETS SBCK WITH BYTES REVERSED
006159	14DE	0000	SBCCP	DC	Z'0000'	
006160	14DF	08AA		DC	Z'08AA'	
006161	14E0	5500		DC	Z'5500'	
006162	14E1	0000	MASK	RESV	1,0	
006163	14E2	0000	MASK1	RESV	1,0	MASK WORD
006164	14E3	0000	ERAR	RESV	4,0	SECONDARY MASK WORD
006165	14E7	0000	SAV1	RESV	9+7*\$AF,0	ERROR ARRAY
006166	14F7	0000	SAV2	RESV	9+7*\$AF,0	SUB-PROGRAM REGS. SAVE AREA
006167	1507	0000	SAVN	RESV	9+7*\$AF,0	
006168	1517	0000	SAV3	RESV	9+7*\$AF,0	
006169	1527	0000	SAV4	RESV	9+5*\$AF,0	
006170	1535	0000	SAV6	RESV	2*\$AF,0	
006171	1537	0000	SAV5	RESV	9+7*\$AF,0	
006172	1547	0000	RMINST	RESV	1,0	
006173	1548	0000	KTCHN	DC	0	RAM INSTRUCTION TO TEST INTERRUPT RETURN CHANNEL

006174 1549 0000 SEC DC 0 SECONDS STORED HERE
 006175 154A 0000 TTOT DC 0 TICK TOTAL
 006176 154B 0000 INTM DC 0 TEMPORARY TIME VALUE
 006177 154C 002D ERF DC 45 ERROR FLAG 0 = SHORT PRINTOUT
 006178 154D 0000 STAT DC 0 LAST STATUS INPUT STORED HERE
 006179 154E 003C HRTZ DC =60 PWR FREQ
 006180 154F 2020 2020 454E D0N TEXT * END-OF-PASS DCMC1\$!
 1552 442D 4F46 2050
 4153 5320 2020
 2020 4443 4D43
 3124

006181 *
 006182 *
 006183 *
 006184 155C 0009 I/O CONTROL WORDS
 006185 155D 0000
 006186 155E 000F
 006187 155F 001A
 006188 1560 000B
 006189 1561 0018
 006190 1562 0005
 006191 1563 0001
 006192 1564 001E
 006193 1565 0003
 006194 1566 001C

CONT1 DC Z'0009'
 CONT2 DC Z'000C'
 CONT3 DC Z'000F'
 CONT4 DC X'1A'
 CONT5 DC Z'000B'
 CONT6 DC Z'0018'
 CONT7 DC X'5'
 CONT9 DC Z'0001'
 CONT10 DC X'1E'
 CONT11 DC X'3'
 CONT12 DC X'1C'
 * OLD FUNCTION CODE
 * INPUT RANGE FUNCTION CODE
 * OUTPUT CCB CONTROL FUNCTION CODE
 * INPUT NEXT STATUS FUNCTION CODE
 * OUTPUT BYTE INTO LCT FUNCTION CODE
 * INPUT STATUS FUNCTION CODE
 * OUTPUT CHANNEL CONTROL FUNCTION CODE
 * OUTPUT MLCC CONTROL FUNCTION CODE
 * INPUT LCT STATUS
 * OUTPUT INTERRUPT CONTROL FUNCTION CODE
 * INPUT DATA SET STATUS FC

006195 *
 006196 *
 006197 *
 006198 *
 006199 *
 006200 *
 006201 *
 006202 *
 006203 1567 2020 2044 4C43 *ECONSOLE MESSAGES
 156A 5020 4348 414E
 4E45 4C20 2400
 006204 1570 204C 494E 4520
 1573 2400
 006205 1574 2049 4420 3D20
 1577 2024
 006206 1578 2020 2065 6E64
 157B 2062D 7465
 7374 2400
 006207 1580 4D42 2020 2024
 006208 1583 444C 4343 204E
 1586 4F54 204F 4E20
 5448 4953 2043
 4841 4E4E 454C
 2400

MESG1 TEXT * DLCP CHANNEL \$!
 IDMSG TEXT * LINE \$!
 EQ TEXT * ID = \$!
 DONE TEXT * END-OF-TEST\$!
 ERMG TEXT *MB \$!
 ERMG1 TEXT *DLCC NOT ON THIS CHANNEL\$!

006209 1590 5241 4E47 4520
 1593 5445 5354 2020
 2400
 006210 1597 4343 4220 424C
 159A 4F43 4B20 5245
 4144 2054 4553
 5420 2400

006211 15A2 4D45 4D4F 5259
 15A5 2054 4553 5420
 2400
 006212 15A9 554E 4156 4149
 15AC 4C41 424C 4520
 5245 534F 5552
 4345 2054 4553
 5420 2400

006213 15B7 4C43 5420 4F55
 15BA 5450 5554 2F49
 4E50 5554 2042
 5954 4520 5445
 5354 2024

006214 15C5 534F 4654 2049
 15C8 4E49 5449 414C
 495A 4520 5445
 5354 2024

006215 15D0 2348 414E 4E45
 15D3 4C20 5052 4F47
 5241 4D20 5445
 5354 2024

006216 15DB 494E 5354 5255
 15DE 4354 494F 4E20
 5445 5354 2024

006217 15E4 4F55 5450 5554
 15E7 2049 4E54 4552
 5255 5054 2043
 4C42 5452 4F4C
 2054 4553 5420
 2400

006218 15F4 494E 4954 4941
 15F7 4C49 5A45 2041
 4E44 2043 4841
 4E4E 454C 2052
 4553 4554 2054
 4553 5420 2400

006219 1606 5352 4F50 2049
 1609 2F4F 2054 4553
 5420 2400

006220 160E 5249 4345 4956
 1611 4520 5445 5394
 2024

006221 1615 5452 414E 5340
 1618 4954 2054 4553
 5420 2400

006222 161D 4441 5441 2049
 1620 4E50 5554 2041
 4E44 2043 5243
 2054 4553 5420
 2400

006223 162A 4441 5441 204F
 1620 5554 5055 5420
 5445 5354 2024

006224 1633 4649 5253 5420
 1636 494E 5445 5252
 5550 5420 5445
 5354 2024

TSTA TEXT * RANGE TEST \$!
 TSTB TEXT * CCB BLOCK READ TEST \$!
 TSTC TEXT * MEMORY TEST \$!
 TSTD TEXT * UNAVAILABLE RESOURCE TEST \$!
 TSTE TEXT * LCT OUTPUT/INPUT BYTE TEST \$!
 TSTS1 TEXT * SOFT INITIALIZE TEST \$!
 TSTF TEXT * CHANNEL PROGRAM TEST \$!
 TSTG TEXT * INSTRUCTION TEST \$!
 TSTH TEXT * OUTPUT INTERRUPT CONTROL TEST \$!
 TSTJ TEXT * INITIALIZE AND CHANNEL RESET TEST \$!
 TSTK TEXT * STOP I/O TEST \$!
 TSTL1 TEXT * RECEIVE TEST \$!
 TSTM1 TEXT * TRANSMIT TEST \$!
 TSTN TEXT * DATA INPUT AND CRC TEST \$!
 TSTP TEXT * DATA OUTPUT TEST \$!
 TSTR TEXT * FIRST INTERRUPT TEST \$!

006225	163E	4441	5441	2053	TSTS	TEXT	*DATA SET SCAN TEST \$*
	1641	4554	2053	4341			
		4E20	5445	5354			
			2024				
006226	1648	5345	43F	4E44	TSTT	TEXT	*SECOND INTERRUPT TEST \$*
	164B	2049	4E54	4552			
		5255	5054	2054			
		4553	5420	2400			
006227			*				
006228			* DUMPER ROUTINE -USES CHANNEL 2 TO READ				
006229			*				
006230	1654	3C02	DMP	LDV	\$R3,=2	USE CHANNEL 2	
006231	1655	8752		CLV	=SR2		
006232	1656	9B80	0000	LAB	=\$B1,<RTB	RAM ADDRESS	CPU ADDRESS
006233	1658	0000		HLT			
006234	1659	AFF0	1661	STR	\$R2,<DMP2		
006235	165B	9F80	165F	STB	=\$B1,<DMP1		
006236	165D	9380	1098	LNJ	=\$B1,<RDATA	READ	
006237	165F	2BA0	DMP1	DC	<RTB	TO HERE	
006238	1660	0200		DC	X'200'	RANGE	
006239	1661	0000	DMP2	DC	X'0'	RAM ADDRESS	
006240	1662	8000		DC	Z'8000'		
006241	1663	0FF1	*				
006242		1664	V\$LB	B	>DMP	DO NEXI	
006243				EWU	\$		
006244	1EBB	1EBB		IFZ	(\$AF-2),LAF99		
006245				ORG	ZER0+X'1EBB'		
006248			SDB	EWU	\$	SEND BLOCK AREA	
006249				IFZ	(\$AF-2),LAF98		
006250	2BAA	2BAA		ORG	ZER0+X'2BAA'		
006253	25AA	2BAA	RTB	EWU	\$	RETURN BLOCK AREA	
006257	25AA	25AA	SAF9	ORG	ZER0+X'25AA'		
006258			LAF2	EWU	\$		
006259						* INTERRUPT SAVE AREA FOLLOWS. THIS AREA WILL BE OVER WRITTEN BY	
006260						* DATA DURING DATA TRANSFERS BUT THE FOLLOWING SAVE AREAS ARE	
006261						* RE-CREATED BEFORE TESTING INTERRUPTS	
006262						*	
006263						*	
006264						*	
006265						*	
006266						*	
006267	25AA	0000	1SI	RESV	\$AF,0	TRAP SAVE POINTER	
006268	25AB	0000		RESV	1,0	CHAN,LEVEL	
006269	25AC	7884		RESV	1,X'7884'	SAVE R1,R2,R3,R4,I,B5	
006270	25AD	0000		RESV	1,0	RFU	
006271	25AE	1020		DC	<IH01	MAKE SURE PRIVILEGE BIT IS SI	
006272	25AF	FFFF		DC	-1		
006273	25B0	0000		RESV	5+\$AF,0		
006274			*	CHANNEL 1			
006275	25B6	0000		RESV	\$AF,0	TRAP SAVE POINTER	
006276	25B7	0000		RESV	1,0	CHAN,LEVEL	
006277	25B8	7884		RESV	1,X'7884'	SAVE R1,R2,R3,R4,I,B5	
006278	25B9	0000		RESV	1,0	RFU	
006279	25BA	1020		DC	<IH01	SET PRIVILEGE BIT	
006280	25BB	FFFF		DC	-1		
006281	25BC	0000		RESV	5+\$AF,0		
006282			*	CHANNEL 2			
006283	25C2	0000		RESV	\$AF,0	TRAP SAVE POINTER	
006284	25C3	0000		RESV	1,0	CHAN,LEVEL	
006285	25C4	7884		RESV	1,X'7884'	SAVE R1,R2,R3,R4,I,B5	
006286	25C5	0000		RESV	1,0	RFU	
006287	25C6	1020		DC	<IH01	SET PRIVILEGE	
006288	25C7	FFFF		DC	-1		
006289	25C8	0000		RESV	5+\$AF,0		
006290			*	CHANNEL 3			
006291	25CE	0000		RESV	\$AF,0	TRAP SAVE POINTER	
006292	25CF	0000		RESV	1,0	CHAN,LEVEL	
006293	25D0	7884		RESV	1,X'7884'	SAVE R1,R2,R3,R4,I,B5	
006294	25D1	0000		RESV	1,0	RFU	
006295	25D2	1020		DC	<IH01	SET PRIVILEGE BIT	
006296	25D3	FFFF		DC	-1		
006297	25D4	0000		RESV	5+\$AF,0		
006298			*	CHANNEL 4			
006299	25DA	0000		RESV	\$AF,0	TRAP SAVE POINTER	
006300	25DB	0000		RESV	1,0	CHAN,LEVEL	
006301	25DC	7884		RESV	1,X'7884'	SAVE R1,R2,R3,R4,I,B5	
006302	25DD	0000		RESV	1,0	RFU	
006303	25DE	1020		DC	<IH01	SET PRIVILEGE BIT	
006304	25DF	FFFF		DC	-1		
006305	25E0	0000		RESV	5+\$AF,0		
006306			*	CHANNEL 5			
006307	25E6	0000		RESV	\$AF,0	TRAP SAVE POINTER	
006308	25E7	0000		RESV	1,0	CHAN,LEVEL	
006309	25E8	7884		RESV	1,X'7884'	SAVE R1,R2,R3,R4,I,B5	
006310	25E9	0000		RESV	1,0	RFU	
006311	25EA	1020		DC	<IH01	SET PRIVILEGE BIT	
006312	25EB	FFFF		DC	-1		
006313	25EC	0000		RESV	5+\$AF,0		
006314			*	CHANNEL 6			
006315	25F2	0000		RESV	\$AF,0	TRAP SAVE POINTER	
006316	25F3	0000		RESV	1,0	CHAN,LEVEL	
006317	25F4	7884		RESV	1,X'7884'	SAVE R1,R2,R3,R4,I,B5	
006318	25F5	0000		RESV	1,0	RFU	
006319	25F6	1020		DC	<IH01	SET PRIVILEGE BIT	
006320	25F7	FFFF		DC	-1		
006321	25F8	0000		RESV	5+\$AF,0		
006322			*	CHANNEL 7			
006323	25FE	0000		RESV	\$AF,0	TRAP SAVE POINTER	
006324	25FF	0000		RESV	1,0	CHAN,LEVEL	
006325	2600	7884		RESV	1,X'7884'	SAVE R1,R2,R3,R4,I,B5	
006326	2601	0000		RESV	1,0	RFU	
006327	2602	1020		DC	<IH01	SET PRIVILEGE BIT	
006328	2603	FFFF		DC	-1		
006329	2604	0000		RESV	5+\$AF,0		
006330			*	CHANNEL 8			
006331	260A	0000		RESV	\$AF,0	TRAP SAVE POINTER	
006332	260B	0000		RESV	1,0	CHAN,LEVEL	
006333	260C	7884		RESV	1,X'7884'	SAVE R1,R2,R3,R4,I,B5	
006334	260D	0000		RESV	1,0	RFU	
006335	260E	1020		DC	<IH01	SET PRIVILEGE BIT	
006336	260F	FFFF		DC	-1		
006337	2610	0000		RESV	5+\$AF,0		
006338			*	CHANNEL 9			
006339				RESV	\$AF,0	TRAP SAVE POINTER	
006340				RESV	1,0	CHAN,LEVEL	
006341				RESV	1,X'7884'	SAVE R1,R2,R3,R4,I,B5	
006342				RESV	1,0	RFU	
006343				RESV	<IH01	SET PRIVILEGE BIT	
006344				RESV	-1		
006345				RESV	5+\$AF,0		
006346			*	CHANNEL 10			
006347				RESV	\$AF,0	TRAP SAVE POINTER	
006348				RESV	1,0	CHAN,LEVEL	
006349				RESV	1,X'7884'	SAVE R1,R2,R3,R4,I,B5	
006350				RESV	1,0	RFU	
006351				RESV	<IH01	SET PRIVILEGE BIT	
006352				RESV	-1		
006353				RESV	5+\$AF,0		

006339	2616	0000		KESV	\$AF,0	TRAP SAVE POINTER
006340	2617	0000		KESV	1,0	CHAN,LEVEL
006341	2618	7884		KESV	1,X*7884*	SAVE R1,R2,R3,R4,I,B5
006342	2619	0000		KESV	1,0	RFU
006343	261A	1020		DC	<IHDL	
006344	261B	FFFF		DC	-1	SET PRIVILEGE BIT
006345	261C	0000		KESV	5+\$AF,0	
006346	2622	0000	*	* CHANNEL 11	KESV \$AF,0	TRAP SAVE POINTER
006348	2623	0000		KESV	1,0	CHAN,LEVEL
006349	2624	7884		KESV	1,X*7884*	SAVE R1,R2,R3,R4,I,B5
006350	2625	0000		KESV	1,0	RFU
006351	2626	1020		DC	<IHDL	
006352	2627	FFFF		DC	-1	SRT PRIVILEGE BIT
006353	2628	0000		KESV	5+\$AF,0	
006354	262L	0000		KESV	\$AF,0	TRAP SAVE POINTER
006356	262F	0000		KESV	1,0	CHAN,LEVEL
006357	2630	7884		KESV	1,X*7884*	SAVE R1,R2,R3,R4,I,B5
006358	2631	0000		KESV	1,0	RFU
006359	2632	1020		DC	<IHDL	
006360	2633	FFFF		DC	-1	SET PRIVILEGE BIT
006361	2634	0000		KESV	5+\$AF,0	
006362	263A	0000	*	* CHANNEL 13	KESV \$AF,0	TRAP SAVE POINTER
006364	263B	0000		KESV	1,0	CHAN,LEVEL
006365	263C	7884		KESV	1,X*7884*	SAVE R1,R2,R3,R4,I,B5
006366	263D	0000		KESV	1,0	RFU
006367	263E	1020		DC	<IHDL	
006368	263F	FFFF		DC	-1	SET PRIVILEGE BIT
006369	2640	0000		KESV	5+\$AF,0	
006370	2646	0000	*	* CHANNEL 14	KESV \$AF,0	TRAP SAVE POINTER
006372	2647	0000		KESV	1,0	CHAN,LEVEL
006373	2648	7884		KESV	1,X*7884*	SAVE R1,R2,R3,R4,I,B5
006374	2649	0000		KESV	1,0	RFU
006375	264A	1020		DC	<IHDL	
006376	264B	FFFF		DC	-1	SET PRIVILEGE BIT
006377	264C	0000		KESV	5+\$AF,0	
006378	2652	0000	*	* CHANNEL 15	KESV \$AF,0	TRAP SAVE POINTER
006380	2653	0000		KESV	1,0	CHAN,LEVEL
006381	2654	7884		KESV	1,X*7884*	SAVE R1,R2,R3,R4,I,B5
006382	2655	0000		KESV	1,0	RFU
006383	2656	1020		DC	<IHDL	
006384	2657	FFFF		DC	-1	SET PRIVILEGE BIT
006385	2658	0000		KESV	5+\$AF,0	
006386	265E	0000	*	* CHANNEL 16	KESV \$AF,0	TRAP SAVE POINTER
006388	265F	0000		KESV	1,0	CHAN,<LEVEL
006389	2660	7884		KESV	1,X*7884*	SAVE R1,<R2,<R3,<R4,<I,B5
006390	2661	0000		KESV	1,0	RFU
006391	2662	1020		DC	<IHDL	
006392	2663	FFFF		DC	-1	SET PRIVILEGE BIT
006393	2664	0000		KESV	5+\$AF,0	
006394	266A		*	LSI EQU \$		
006395			*			
006396			*			
006397			*			
006398	266A	0000	*			
006399	266B	0000		TOPSAV KESV \$AF,0		TRAP SAVE POINTER
006400	266C	FFFF		TOPSAV KESV 1,0		CHAN, LEVEL
006401	266D	0000		DC Z'FFFF'		SAVE ALL REG
006402	266E	14C7		RESV 1,0		RFU
006403	266F	FFFF		DC <DUMMY		SUPPLIED AT RUPT TIME
006404	2670	0000		DC -1		SET PRIVILEGE BIT
006405	2671	0000		DC 0		FILLED AT SAVE TIME
006406				KESV 8+7*\$AF,0		ROOM FOR SAVE
006407			*			
006408			*			
006409	2680	0000	*			
006410			*			
006411			*			
006412			*			
006413	2690	0000	*			
006414			*			
006415			*			
006416			*			
006417	26A0	0000	*			
006418			*			
006419			*			
006420	26B0	0000	*			
006421			*			
006422			*			
006423			*			
006424	26C0	8F00 1EBB	*			
006425	26C3	C380 10FC		LNJ \$B4,<GENITZ		GENERAL INITAILIZE
006426	26C5	9380 1098		LNJ \$B1,<RDAT		BLOCK READ
006427	26C7	2BAA		DC <RTB		TO HERE
006428	26C8	0002		DC 2		RANGE
006429	26C9	0000		DC 0		EVEN BYTE START
006430	26CA	0000	*			
006431			*			
006432			*			
006433			*			
006434	26D1	9800 2BAA	X	CALL ZV\$TC,FREV		PRINT FIRMWARE REV
006435	26D3	9570 00E0		LDR \$R1,<RTB		GET REV NUMBER
006436	26D5	1045		AND \$R1,=Z'00E0'		ALIGN INTERIM REV NUMBER
006437	26D6	9F00 14CE		SUR \$R1,5		BYPASS INTERIM REV IF 0
006438	26D8	1900 26E0		STR \$R1,<TEMP		TYPE INTERIM
006439	26DA	FBC0 0003	X	BEZ \$R1,<SIT12		
006440	26DC	D380 0000		CALL ZV\$TD,TEMP		
006441	26E0	9800 2BAA		SIT12 LDR \$R1,<RTB		
006442	26E2	9570 001F		AND \$R1,=Z'001F'		GET REV NUMB STRIP TO REV

				STR	\$R1,<FW-REV		PUT BACK
				CALL	ZV\$TD,FW-REV		PRINT NUMBER
006443	26E4	9F00	14C4				
006444	26E6	FBC0	0003				
	26E8	D380	0000	X			
	26EA	0F80					
	26EB	14C4					
006445	26EC	6C01			LDV	\$R6,=1	
006446	26ED	D800	14C4		LDK	\$R5,<FW-REV	
006447	26EF	D956			CMR	\$R5,=\$R6	
006448	26F0	0284			BGE	>BLAP	
006449	26F1	F380	125C		WFW	\$B7,<ERROR	WRONG FIRMWARE REV, REV C OF PROGRAM DOESN'T SUPPORT
006450	26F3	0FFE		*	LNJ		
006451	26F4	8F80	1EBB		BLAP	B	>WFW <SDB,Z=0008*
006452	26F6	0008			RSTR		RESTORE B4
006453	26F7	8384					
006454	26F8	2020	2046 4952		REV	JMP	\$B4 FIRMWARE REV.\$
	26F9	4057	4152 4520				
006455	2701	2020	2020 2044		MESG	TEXT	* DLCP TEST *
	2704	4C43	5020 2054				
		4553	5420				
006456	2709	4443	4D43 312C			IFZ	(SAF-2),LAF8
006457	270L	2053	4146 2D45			TEXT	*DCMC1, SAF-E *
	2020						
006460	2710	2040	4159 2020		DATE	TEXT	* MAY 11, 1978\$
006461	2713	3131	2C20 2031				
	3937	3824					
006462	2718	2020	2050 4F57		MESG5	TEXT	* POWER FRE (HZ) \$
	271B	4552	2046 5245				
		5120	2848 5A29				
	2024						
006463	2722	434T	5059 5249				
006464	2725	4748	5420 3139				
006465		3736	2042 5920				
006466		484F	4E45 5957				
006467	1664	454C	4C20 494E				
006468	1664	464F	524D 4154				
	0100	494F	4E20 9359				
		5354	454D 5320				
		494E	432E				
0000	EKR COUNT			*			
TITLE DCMC1 *REV. A*					ORG	VSLB	
\$AF					END	DCMC1,STRT	
406B	507C	524B	564B	568B,	575B	591B	596B
610B	622B	641	682B	692B	925B	930B	1095B
1320	1349B	1358B	1517B	1537B	1562B	1566B	1570B
1655B	1711B	1747B	1777B	1782B	1786B	1838B	1846B
2177B	2251B	2284B	2297B	2432B	2441B	2457B	2608C
2974B	3117B	3143B	3203B	3214B	3282B	3286B	3301B
3357B	3426C	3529B	3540	3549	3558	3567	3573
3657	3658C	3659	3660C	3661	3662C	3722C	3726
3935B	4005B	4016	4018	4062B	4122C	4145	4189B
4269B	4369C	4428	4429C	4433	4434	4438	4444
4451	4456C	4459	4465	4473	4474	5142	6143
6165	6166	6167	6168	6169	6170	6171	6244
6251	6254	6256	6267	6273	6275	6281	6283
6297	6299	6305	6307	6313	6315	6321	6323
6337	6339	6345	6347	6353	6355	6361	6363
6377	6379	6385	6387	6393	6398	6405	6456
502B	755B	769B	777B	784B	789B	796B	799B
840B	843B	850B	863B	870B	873B	880B	887B
1036C	1041C	1065B	1091	1093	1145B	1153B	1169
1201B	1271B	1278B	1396B	1403B	1410B	1416B	1422B
1462B	1528B	1600B	1602B	1618B	1644B	1645B	1646B
1673B	1680B	1692B	1733B	1770B	1789B	1800B	2090
2233	2234C	2308B	2372B	2488	2495C	2498C	2500
2558B	2568B	2577B	2692B	2717B	2792B	2906B	2922B
2982B	3007	3008C	3020B	3077B	3175B	3181	3182C
3256C	3308B	3373B	3410B	3439B	3469B	3474B	3515B
3708C	3712	3724B	3745	3746	3748	3755B	3770B
3797	3823	3907	3908	3910	3912	3938B	3953
3958	4290	4296C	4444	4449	4450C	4478C	6235C
6426B							
\$B1							
457B	458B	459B	460B	461B	462B	463B	464B
467B	468B	469B	470B	471B	472B	473B	474B
732B	917B	944B	961B	1118B	1134B	1215B	1263B
1509B	1553B	1814B	1964B	2055B	2352	2355C	2389C
2536B	2709B	2809B	2905B	3071B	3521B	3630	3634C
3648	3649C	3651	3652C	3653	3655C	3658C	3660C
3749	4043	4046	4245	4251	4433	4437C	4440C
4468C	4474	4477C					
757	760C	1042B	1046	1047	1075	1076	1078C
1161B	1167B	1285B	1289B	1316B	1318B	1334B	1360B
1595B	1688B	1694B	1702B	1817B	2361	2362C	2366
2391B	2426	2430	2552	2600	2602	2604	2605C
2644C	2650B	2691	2727	2734B	2758B	2785	2910B
3090B	3154B	3193B	3224B	3231B	3399	3400C	3485B
3633	3634C	3699	3702	3703	3705C	3706	3708C
3713	3713	3714	3749	3751C	3778	3785B	3797
3977	3978	3982B	4011B	4018	4024C	4124	4144C
4289	4291	4293	4443	4450C	4455C	4456C	4478C
\$B2							
361	362C	363	364C	404B	425B	504B	510B
536B	554B	558B	562B	585B	589B	594B	604B
649B	667B	674B	680B	687B	689B	707B	726B
916B	923B	928B	933B	937B	939B	957B	960B
1051B	1052B	1081B	1105B	1130B	1133B	1168B	1172B
1202B	1207B	1214B	1260B	1262B	1288B	1297B	1301B
1340B	1341B	1343B	1356B	1380B	1384B	1448B	1454B
1515B	1552B	1555B	1560B	1573B	1579B	1587B	1605B
1637B	1653B	1664B	1674B	1678B	1695B	1696B	1703B
1762B	1768B	1769B	1775B	1794B	1811B	1813B	1816B
1830B	1834B	1843B	1851B	1854B	1872B	1873B	1876B
1895B	1908B	1922B	1935B	1950B	1963B	1985B	1999B
2041B	2054B	2077	2078	2084	2087	2090	2093
2130B	2151B	2162B	2174B	2189B	2190B	2201B	2216
2226	2233	2236	2249B	2257B	2259B	2264B	2276B

2326B	2336B	2352	2353	2361	2364	2366	2368C	2385B	2393B
2395	2397B	2411	2412	2446B	2491	2492	2494	2505B	2516B
2526B	2594B	2637B	2653B	2656B	2671B	2686B	2706B	2708B	2732B
2738B	2743B	2748B	2753B	2762B	2771B	2775B	2789B	2808B	2903B
2904B	2907B	2908B	2933B	2939B	2943B	2946B	2953B	2972B	2997B
3004B	3009B	3025B	3070B	3072B	3073B	3113B	3114B	3115B	3126B
3136B	3137B	3141B	3163B	3168B	3172B	3174B	3183B	3201B	3209B
3211B	3221B	3222B	3238B	3242B	3251B	3257B	3269B	3279B	3280B
3294B	3298B	3324B	3330B	3334C	3335B	3343C	3345B	3349C	3350B
3353C	3355B	3359C	3360B	3383B	3409B	3414B	3420B	3481B	3487B
3501B	3506B	3527B	3532B	3788B	3829B	3915B	3918B	3925B	3929B
3930B	3961B	3964B	3992B	4003B	4027B	4049	4051B	4057B	4060B
4064B	4066B	4149B	4187B	4193B	4210B	4219	4220	4222B	4228B
4245	4246	4247C	4250B	4255B	4259C	4261B	4265C	4267B	4272C
4273B	4289	4290	4299B	4313	4314	4318B	4325B	4333B	4336B
4346B	4371B	4377B	4403B	4410	4413C	4414B	4415	4419B	4432
4439	4464C	4476	4477C	6425B	6453B	6459B	6463B	6467B	6470B
\$B5	432	436	438C	2182B	2318B	2368C	2382	2383	2395
2424	2459B	2520	2521C	2597	2598B	2602	2607C	2608C	2609C
2611	2612C	2679	3324C	3334C	3343C	3349C	3353C	3359C	3629
3630	3631	3633	3637	3640C	3646	3649C	3654	3655C	3657
3658C	3659	3660C	3661	3662C	3673	3674C	3881	3882	3885C
3886C	3907	3909C	3953	3954C	4016	4018	4022C	4024C	4247C
4259C	4265C	4272C	4308	4368	4369C	4436	4437C	4440C	4446C
\$B6	739B	741B	743B	745B	788B	2421	2429	2438	2551
2573	2728	2786	4115	4428	4429C	4431	4440C	4446	4468C
\$B7	407B	520B	525B	531B	539B	549B	565B	569B	576B
597B	601B	607B	611B	624B	683B	693B	703B	899B	924
926B	931B	943B	1056B	1096B	1112B	1175B	1186B	1306B	1311B
1350B	1359B	1474B	1518B	1538B	1563B	1567B	1571B	1608B	1629B
1640B	1656B	1712B	1748B	1778B	1783B	1787B	1839B	1847B	2167B
2178B	2195B	2285B	2299B	2331B	2433B	2442B	2458B	2666B	2683B
2768B	2805B	2975B	3118B	3144B	3204B	3215B	3283B	3287B	3302B
3333B	3348B	3358B	3530B	3589B	3596B	3766B	3848B	3856B	3860B
3864B	3873B	3933B	3936B	4006B	4063B	4190B	4225B	4253B	4258B
4270B	4328B	4428	4472B	6449B					
\$IV	3843	3849							
\$R1	349	350C	353	354C	359	360	365	368	369
394	402	424	434	435C	437	438C	446	447C	482
483	489	490	499	500C	506	507C	508C	560	572B
587	615B	635	636C	641	642C	643C	722	723C	737C
829	830B	918C	974C	977	978C	979C	984C	985	986C
993	994C	995	996C	1030	1036C	1038	1041C	1047	1048C
1049	1050	1089C	1092	1093	1097C	1098	1126	1127C	1171
1192	1200C	1266C	1294	1296B	1319	1321B	1387C	1575	1576C
1629	1631B	1871	1875	1892	1893C	1906	1907C	1920	1921C
1933	1934C	1948	1949C	1961	1962C	1982	1983C	1997	1998C
2011	2012C	2025	2026C	2039	2040C	2052	2053C	2075	2076C
2077	2078	2079C	2080C	2081C	2082C	2084	2085C	2087	2088C
2093	2094C	2095C	2096C	2098	2107	2108	2109C	2188	2216
2217	2218C	2219C	2220C	2221C	2222	2223C	2226	2227	2230C
2236	2237C	2238	2240	2241C	2242C	2243	2244	2245	2246C
2324	2350	2351C	2364	2365C	2382	2413	2414C	2423	2429C
2430	2438	2494	2495C	2498C	2499	2503B	2531	2532C	2533C
2534	2538C	2599	2600	2636C	2642	2643	2670C	2705C	2914
2915C	2917	2918C	2931C	2983	2987	2989	2990C	2992	2993C
2995	2996C	3003C	3021	3022B	3110	3112	3122	3124	3125C
3149	3150C	3156	3157	3159C	3160	3162	3169	3171	3176
3177C	3208	3240	3241C	3243	3244C	3245	3246C	3250	3266
3268	3291	3293	3325	3327C	3328	3331	3346	3364	
3368	3371	3372C	3417C	3434	3435	3436B	3438C	3452	3456C
3456C	3460B	3492	3502B	3639	3641B	3645	3650B	3663C	3664C
3665C	3666C	3667C	3668C	3669C	3670	3671C	3700C	3703	3720
3721C	3722C	3747	3751C	3753B	3779C	3781	3782C	3798	3800C
3801	3803	3805	3807	3809	3811	3813	3815	3817	3819
3821	3823	3841	3842	3861	3912	3913C	3914	3956	3957C
3958	3959C	3960	4000	4001B	4004	4019	4020	4022C	4116C
4132C	4134	4136B	4156C	4155C	4157	4160	4251	4293	4294
4309C	4310C	4311	4312C	4313	4314	4315C	4316C	4331	4332
4339	4340C	4341	4342	4353	4354	4361	4362	4363	4366
4367	4370C	4372C	4373C	4374	4375C	4379	4381B	4382	4392C
4410	4411C	4412C	4416	4417C	4426	4469B	6434	6435	6436
\$R2	6437C	6438B	6441	6442	6443C				
431	436	438C	439B	555	563	570C	574	582	598
613C	750	975	976	980	985	987B	988C	1033C	1034
1035	1036C	1040C	1041C	1045	1047	1057C	1058	1074	1076
1078C	1079B	1170C	1171	1176	1175C	1565	1626	1627	1630
1744	1745	1749C	1750C	1752	1754	1756	1758	1760	1867
1869	1871	2225C	2229	2231C	2356C	2357C	2358	2359C	2383
2384C	2389	2424	2425C	2434B	2491	2495C	2496C	2501	2502C
2522	2523C	2596	2597	2610	2611	3404	3429C	3430B	3433
3434	3447	3448B	3453C	3457	3459	3462C	3463C	3464C	3745
3751C	3761	3768B	3781	3783B	3789	3823	3824B	3830	3868
3870	3910	3911C	4015C	4019	4023C	4025	4042	4046	4049
4120C	4134	4135	4137	4141	4144C	4147C	4157	4158	4159C
4162	4166	4168C	4169	4172	4178	4246	4256	6231C	6234C
391	392	393	395C	396C	408C	409B	411	412C	414
420	421C	422	509C	546C	557C	577C	584C	626C	638C
651C	652C	653C	654C	656	659C	660B	661C	663C	672
678	694	709C	730C	734B	735C	746C	764C	765C	766B
768C	776C	788C	798C	814	828C	842C	862C	872C	882
910	911C	913C	954	955C	959C	962C	963B	964C	1104B
1114	1115C	1116	1132C	1141C	1142B	1143C	1166	1188	1189B
1190C	1206C	1212	1213C	1256	1257C	1259C	1264B	1265C	1267C
1325C	1333C	1375	1376C	1378C	1382C	1383C	1385B	1386C	1456
1460C	1475	1476C	1504	1505C	1507C	1520	1524C	1525B	1526C
1534	1539C	1548	1549C	1551C	1580	1611C	1612	1613	1614C
1615	1616	1632	1713C	1726	1729C	1730B	1731C	1739	17518
1808	1809C	1812C	1856C	1868B	2111C	2116B	2117C	2119	2123C
2139C	2147C	2196C	2248C	2253B	2254C	2256C	2302	2307C	2314C
2332C	2369C	2370B	2371C	2378	2448	2449B	2492	2493C	2497B
2513	2514C	2524C	2528B	2529C	2539C	2550C	2579	2701	2702C
2704C	2710B	2711C	2777	2791C	2807	2810B	2811C	2898	2899C
2901C	2915C	2918C	2920C	2921	2927	2990C	2993C	2996C	3036C
3066	3067C	3069C	3075C	3076	3083	3111B	3123B	3161B	3177C
3241C	3244C	3267B	3292B	3312C	3365C	3366B	3367C	3369	3382
3392	3393C	3403C	3425C	3455	3458B	3486C	3746	3750B	3752
3760C	3762	3764	3767C	3843	3844	3845	3849	3850	3851C
3852	3853	3861	3870	3886C	3888C	3979	3980B	3985	3989
3999C	4000	4008C	4009	4044	4046	4048C	4056C	4119C	4124
4151	4164	4165	4167	4170	4171	4173	4207	4208	4209
\$R4	4427C	4443	4446	4454C	4455	4463C	4465	6230	437

513	514	515C	516	518	522	523	527	529	535
537	561	563	566	567	573	574	588	590	593
595	603	605	619	621	645	646C	665	673	679
681	699	728	729C	738C	740	742	744	760C	761
785	786C	814	815	816C	817C	818	819C	820C	821
822C	823C	824C	825	826C	858	859	860C	895	922C
924	927	929	976	977	978C	983	988C	1031	1033C
1035	1037C	1060	1061B	1062	1063	1064C	1076	1077	1078C
1293C	1347C	1355	1357	1388	1389C	1447	1456	1457	1458
1459C	1514	1516	1520	1521	1522	1523C	1556	1558C	1559
1561	1569	1588	1589C	1590C	1591C	1592C	1593C	1594C	1604
1606	1636	1638	1652	1654	1671	1672C	1726	1727	1728C
1774	1776	1780	1781	1793	1797C	1798	1819	2105	2106C
2119	2120	2121	2122C	2302	2303C	2304	2305	2306C	2353
2356C	2412	2489	2490C	2498C	2635	2641	2642	2655	2658
2669	2737	2742	2747	2752	2770	2930	2945	2947C	3002
3024	3329	3331	3344	3346	3354	3356	3369	3370	3371
3401	3402C	3405	3526	3528	3704	3705C	3706	3709	3710C
3787	3789	3828	3830	3851C	3867	3868	3908	3955	3984
3993	4002	4004	4059	4061	4115	4128	4130	4137	4162
4186	4188	4208	4221	4223	4248	4249	4251	4254	4256
4266	4268	4291	4292	4295	4296C	4324	4326	4376	4383
4386	4387	4397							
505	370	371	372	378C	390	395C	397	405	408C
416	417	418C	426	537	540	595	599	605	609
621	647	648C	656	657	658	662	664C	691	700
852	853	896	919	920C	924	941	973	981B	1054
1080	1092	1094	1109	1174B	1184	1304	1309	1348	1469
1470	1472	1535	1536	1627	1628B	1638	1710	1745	1746
1781	1785	1831	1845	1874C	1878	1879	2101	2102C	2103C
2165	2175	2176	2191	2193	2281	2283	2295	2296	2327
2329	2354	2355C	2359C	2429	2431	2438	2440	2456	2553
2561	2571	2657C	2658	2661	2680	2729	2766	2784	2799
2600	2801	2964	2973	3116	3127	3128	3129C	3130C	3131
3132	3135C	3142	3202	3213	3281	3285	3300	3379	3380
3381C	3396	3397C	3418	3424B	3432	3444	3446C	3519	
3528	3931	3934	3977	3993	4123B	4124	4132C	4133	4141
4142	4143C	4151	4154	4155C	4156	4169	4174	4175C	4178
4179C	4188	4191C	4219	4220	4223	4248	4251	4268	4271
4288C	4297B	4319	4326	4386	4397	4399C	6446	6447	
503	518	520	544	598	599	608	609	690	691
701	751	752C	758	759C	762B	897	1048C	1053	1054
1093	1094	1108C	1109	1183	1184	1268	1269C	1292	1304
1308	1309	1346	1348	1471	1472	1510	1511	1512	1516
1536	1709	1710	1743C	1746	1784	1785	1836	1844C	1845
1866	1870	1875	2163	2164	2165	2168C	2170	2176	2192C
2193	2279C	2278	2282	2283	2287C	2288	2296	2328C	2329
2430	2431	2439	2440	2450	2453	2455	2456	2518	2519C
2554	2555C	2556	2563	2564C	2565	2566C	2567	2574	2575C
2576	2648C	2664B	2681	2730	2777	2778	2779	2780C	2787
2803	3212	3213	3299	3300	3978	3987B	3988	4121C	4122C
4138B	4145	4163B	4388	4389	6445	6447			
556	567	571	583	608	614	702	898	1750C	2957
2634	2645B	2647	2667	2674B	2682	2690	2731	2788	3125C
3128	3132	3133C	3924B	3959C	3988	3989	3990B	4126	4127C
4146B									
3421	ADD	3400C	3426C	3429C					
6134	ADDS	2521C	2597	2598C	2605C	2611			
1768	ADV2	1602B	1646B						
433	ALL	439B							
6112	ALLUNE	386	395C	408C	4043				
2968	BKKH	2957B	440B	451B					
457	BKK1								
467	BKK10								
458	BKK3	456	485B						
459	BKK4								
470	BKK4A								
460	BKK4B								
471	BKK5								
463	BKK7								
474	BKK7A								
464	BKK7B								
468	BKK7C								
469	BKK7D								
473	BKK9								
466	BKK9B								
6452	BLAP	6448B							
708	BLUG	706B							
1726	BRCLCR	1579B	1674B	4504					
4508	BT1	4502	4503						
4566	BT11								
4571	BT12	4563	4564	4568					
4583	BT17	4574	4575	4577					
4588	BT18	4580	4581	4585					
4614	BT1L								
4815	BT4B	4783	4784	4786	4797	4798	4799	4833	4834
4751	BT4C	4718	4719	4720	4733	4734	4736	4768	4770
4674	BT4U	4642	4643	4645	4656	4657	4658	4704	4707
4629	BT4F	4623	4624	4626					
5117	BT4G	5109	5110	5114					
5112	BT4H	5121	5122	5123					
4836	BT4J								
4525	BT5	4557	4558	4560					
4529	BT6	4521	4522	4523					
5012	BT6	4887	4888	4889					
4683	BT7	4875	4876	4877					
5043	BT8	5025	5026	5028	5060	5061	5062	5075	5076
4679	BTAC	4671	4672	4676					
4549	BTB	4533	4534	4538					
5047	BTB5	5039	5040	5041					
4879	BTB7	4898	4899	4904					
4553	BTC	4545	4546	4547					
4755	BTCL	4747	4748	4749					
4820	BTXC	4812	4813	4817					
4859	BTYL	5166	5167	5168					
6135	BUFKNG	1138	2127						
6055	BVF	6094	6095	6097					
6018	BVF	6009	6010	6011					
6080	BVK	6072	6073	6074					
6087	BVL	6076	6077	6078					
6068	BVR	6059	6060	6061					
6005	BVT	6044	6045	6047					

	704B	900B	2685B									
ZV\$CO	375B	4430B		1138B	1270B	1577B	2127B	2616B	3679B	3683B	3688B	
ZV\$EK	3692B	3698B										
ZV\$F	921B	2600										
ZV\$HR	321											
ZV\$ID	356B											
ZV\$IH	358B											
ZV\$PCH	486B											
ZV\$QC	355B	357B										
ZV\$RD	322B	348B	921B									
ZV\$T	415B	475B	477B	501B	637B	724B	912B	956B	1128B	1258B		
	1377B	1506B	1550B	1810B	1894B	1984B	2515B	2703B	2900B	3068B		
ZV\$TC	3394B	4462B	475B	487B	501B	637B	724B	912B	956B	1128B		
	410B	1258B	1377B	1506B	1550B	1810B	1894B	1984B	2515B	2703B	2900B	
	3068B	3394B	6433B									
ZV\$TD	413B	6439B	6444B									
ZV\$TH	419B	4457B										
ZV\$THZ	4458B											

715 LABELS
4247 REFERENCES
6468 RECORDS

1 U FLAGS
0 M FLAGS
38 N FLAGS

6 CROSS REF VERSION L - 24 SEPT. 1976
RS LINKER VERSION 5.00 05/10/78 2055.8 EDT WED

LINK MAP FOR DCMC1

START 0100
LOW 0000
HIGH 2BA9
CURRENT 1F31

*LOC DEFS

ZHCOMM 0000

*DCMC1 0000

ZHPFK 0000

ZHTSA 0002

ZHNNSA 0010

ZHRTC1 0014

ZHRTCC 0015

ZHRTCL 0016

ZHWTC 0017

ZHMERC 001F

ZHIAFB 0020

ZHTH29 0063

ZHTH28 0064

ZHTH27 0065

ZHTH26 0066

ZHTH25 0067

ZHTH24 0068

ZHTH23 0069

ZHTH22 006A

ZHTH21 006B

ZHTH20 006C

ZHTH19 006D

ZHTH18 006E

ZHTH17 006F

ZHMEMP 006F

ZHTH16 0070

ZHLERK 0070

ZHTH15 0071

ZHNRES 0071

ZHTH14 0072

ZHPMEM 0072

ZHTH13 0073

ZHP-OP 0073

ZHTH12 0074

ZHTH11 0075

ZHTH10 0076

ZHTH9 0077

ZHTH8 0078

ZHTH7 0079

ZHTH6 007A

ZHOVFL 007A

ZHTH5 007B

ZHOP-N 007B

ZHTH4 007C

ZHTH3 007D

ZHSN-N 007D

ZHTH2 007E

ZHJRC 007E

ZHTH1 007F

ZHMCL 007F

ZHTSAZ 0080

ZHTVB5 0080

ZHTVB5 0080

*ZV\$TH 1664

ZV\$TU 1699

ZV\$TH 1664

ZV\$THZ 168C

*ZV\$IH 16B4

ZV\$IU 16D9

ZV\$IH 16B4

ZV\$IAL 16BE

ZV\$--2 16D6

ZV\$--3 16E8

*ZV\$EK 174D

ZV\$EK 174D

ZV\$TA 1779

ZV\$--0 1760

*ZV\$F 17BD

ZV\$F 17BD

*ZV\$T 17CB

ZV\$UC 17E8

ZV\$TC 17D4

ZV\$T 17CB

ZV\$W 17DD

*ZV\$PCH 17FC

ZV\$PCH 17FC

*ZV\$C 18FE

ZV\$C 18FE

REV. 5

ZV\$CU 1921

REV. 5.0

REV. 5.0

*ZV\$GP	1932
ZV\$GP	1932
ZV\$--4	1952
*ZV\$HA	195E
ZV\$HA	195E
ZV\$H2	1968
ZV\$HS	1963
*ZV\$HD	1997
ZV\$HU	1997
*ZV\$IA	19C9 REV. 7
ZV\$IA	19CC
ZV\$ADF	1A7D
ZV\$AKG	1A7B
ZV\$--1	1A38
ZV\$IAV	19CA
*ZV\$BK	1A88
ZV\$DKK	1A88
*ZV\$RD	1AA2 REV. 7
ZV\$KD	1AA2
ZV\$HM	1B18
ZV\$HK	1AD1
ZV\$SV1	1C77
ZV\$SV3	1C97
ZV\$AF	1AB3
ZV\$TTY	1AB5
ZV\$SY4	1C87
ZV\$UTP	1B49
ZV\$BF	1ACA
ZV\$T1D	1AB4
ZV\$CF2	1ABE
ZV\$TK	1ABA
ZV\$RAK	1ABD
ZV\$SI1	1ABF
ZV\$RCC	1AC0
ZV\$BUU	1AB6
ZV\$ULD	1AC2
ZV\$RCD	1AC3
ZV\$NSK	1AC7
ZV\$SIK	1AC5
ZV\$RKS	1AC9
ZV\$IZ	1ADC
ZV\$LR	1ACE
ZV\$DA1	1AD1
ZV\$HCU	1ACB
ZV\$HKL	1ACC
ZV\$LKR	1ACD
ZV\$LKL	1ACE
ZV\$HBU	1ACF
ZV\$LF1	1ADD
ZV\$--5	1AD4
ZV\$RMD	1AB2
ZV\$MCN	1AD0
HIBAUU	1ACF
ZV\$RAW	1ABC
ZV\$RDT	1CD3
ZV\$CTL	1AB9
ZV\$B1	1B44
ZV\$TST	1D29
ZV\$MDC	1CFD
ZV\$K99	1EFB
ZV\$ISA	1AD7
ZV\$UH	1AD2
ZV\$CKU	1B56
ZV\$BSH	1B58
ZV\$CPU	1AB8
ZV\$R50	1B36
ZV\$R60	1B41
ZV\$RT	1E38
ZV\$ALL	1AB7
*MLCHPG	1FU0 T+V
MLCHPG	1FU0
ENDCHPG	1F31
*UNLINK MODULE(S)	
ZV\$QC	
ZV\$ID	
ZV\$IC	
ZV\$TU	
ZV\$CO	
ZV\$TH	

