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TITLE SMDS2, *REV C*, STORAGE MODULE T&D, FEB 18, 1978

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*
*
* DESCRIPTION:
*
* THIS T&D PROGRAM TESTS THE MASS STORAGE DEVICES LISTED BELOW
* ON A LEVEL 6 SYSTEM. IT ASSUMES THAT PREVIOUS TESTS HAVE
* INDICATED THE FAILURE IS LOCATED IN THE MSD. THE TEST IS
* DESIGNED TO RUN SEQUENTIALLY FROM START TO FINISH OR UNTIL
* A FAILURE IS DETECTED.
*
* THE SUBSYSTEM ITEMS SUPPORTED BY THIS PROGRAM ARE:
*
* MSU9101 40 MB STORAGE MODULE DRIVE
* MSU9102 80 MB STORAGE MODULE DRIVE
* MSU9103 150 MB STORAGE MODULE DRIVE
* MSU9104 300 MB STORAGE MODULE DRIVE
* MSU9105 40 MB STORAGE MODULE DRIVE(2ND UNIT)
* MSU9106 80 MB STORAGE MODULE DRIVE(2ND UNIT)
*
*
* REVISION HISTORY:
*
* REV DATE
* A NOV 77 PRERELEASE(SMTDS1)
* B DEC 77 ORIGINAL RELEASE
* C FEB 78

*
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* SYSTEMS INC. IT IS MADE AVAILABLE ONLY TO HONEY-
* WELL AUTHORIZED RECIPIENTS FOR THEIR USE SOLELY IN
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* PRODUCTS. THIS DOCUMENT AND INFORMATION MUST BE
* MAINTAINED IN STRICTEST CONFIDENCE; IT MUST NOT
* BE REPRODUCED IN WHOLE OR IN PART; AND IT SHALL
* NOT BE DISCLOSED TO ANY OTHER PARTY WITHOUT THE
* PRIOR WRITTEN CONSENT OF HONEYWELL.

 * PROGRAM PREPARATION:
 * THE ROOT SOURCE OF THIS PROGRAM, AFTER THE ADDITION OF APPROPRIATE
 * TITLE AND END STATEMENTS, WAS PROCESSED BY THE HOST RESIDENT ASSEMBLER
 * TO CREATE EITHER SHORT OR LONG ADDRESS FORM (SAF OR LAF) OBJECT TEXT
 * AND LISTING. THE OBJECT TEXT WAS FURTHER PROCESSED BY THE HOST RESIDENT
 * LINKER USING THE APPROPRIATE CONSOLE ZV\$LIB LIBRARY TO CREATE A PUNCH
 * SEGMENT CONTAINING AN EXECUTABLE MODULE. THE ASSEMBLY LISTING WAS
 * AUGMENTED WITH CROSS REFERENCE DATA PLUS THE LOAD MAP FROM THE LINKER
 * TO CREATE A LIST SEGMENT.
 *
 * ROOT SAF LAF
 * NAME SMDX2 SMDS2 SMDL2
 * DOCUMENT 60134667-002 60134668-002 60134669-002
 *
 * PROGRAM DISTRIBUTION:
 * THE ELEMENTARY ITEMS SUBMITTED TO THE T&V PROGRAM DISTRIBUTION CENTER
 * WERE THE EXECUTABLE LINKED IMAGES ON DISKETTE OF SMDX2 AND SMDL2,
 * AND MAGNETIC TAPE IMAGES OF THE AUGMENTED LISTINGS.
 *
 * REPRODUCTIONS OF THE EXECUTABLE LINKED IMAGES MAY BE AS CARD DECKS
 * OR AS MEMBER "SW"/"LW" OF A MULTIPLE MEMBER FILE. MOST FREQUENTLY
 * IT WILL BE FOUND AS MEMBER "SW"/"LW" WITHIN FILE "PROGFILE" OF A DISK-
 * ETTE VOLUME ENTITLED "DIAGS".
 *
 * DISTRIBUTION OF THE LISTINGS, WHICH SHOULD BE AVAILABLE IF ANY COMPLEX
 * MAINTENANCE OR REPAIR IS TO BE PERFORMED, IS NORMALLY MADE AS A
 * PRINTED COPY.
 *
 * MAIN MEMORY REQUIREMENT:
 * THIS PROGRAM REQUIRES 8K WORDS OF MAIN MEMORY IN SAF MODE OR 12K WORDS
 * IN LAF MODE.
 *
 * OPERATION:
 * LOAD THE PROGRAM AND START (OR RESTART) AT LOCATION
 * 0100 HEX. IF A CONSOLE IS PRESENT, VERIFY CORRECT
 * PROGRAM IDENTIFICATION FROM THE LISTING OF THE I/O
 * EQUIPMENT BY CHANNEL NUMBER AND ID-CODE. THIS LISTING WILL
 * BE OMITTED ON RESTARTS.
 *
 * THE CONSOLE SEARCH RULES ARE: FIND THE CONSOLE WITH THE LOWEST CHANNEL
 * NUMBER CONNECTED THRU AN MDC CONTROLLER. IF THERE IS NO CONSOLE ON AN
 * MDC, THEN SEARCH FOR A TERMINAL WITH THE HIGHEST CHANNEL NUMBER ASSIGNED
 * TO AN ACLA ADAPTER ON AN MLC CONTROLLER. IF NO ASYNC ADAPTER IS FOUND,
 * THEN GO TO THE FULL CONTROL PANEL.
 *
 * THERE ARE THREE CONSOLE CHANNEL OPTIONS DETERMINED BY THE VALUE OF LO-
 * CATION "ZV\$TTY".
 *
 * IF ZV\$TTY EQUALS (0000), SEARCH FOR A CONSOLE.
 * IF ZV\$TTY EQUALS (FFFF), ASSUME THERE IS NO CONSOLE.
 * IF ZV\$TTY EQUALS NEITHER (0000), NOR (FFFF), THEN IT IS THE CONSOLE CHAN-
 * NEL NUMBER. NOTE: DEFAULT IS TO SEARCH FOR A CONSOLE.
 *
 * ALL CONSOLE I/O IS EVEN PARITY. IF CONSOLE IS ON MLC, IT MUST BE ASYNC
 * AND THE BAUD RATE SET AT 1200 TO MATCH THE PROGRAM SUPPLIED RATE. IF IT
 * IS NECESSARY TO CHANGE THE PROGRAM BAUD RATE, THEN THE NEW BAUD RATE
 * CODE SHOULD BE PUT INTO LOCATION "ZV\$BUD" IN HEX. THE TERMINAL BAUD RATE
 * MUST BE SET TO MATCH THIS NEW BAUD RATE. THE CORRECT HEX VALUE MAY BE
 * OBTAINED FROM THE FOLLOWING TABLE.
 *-----
 * BAUD RATE TABLE
 *-----
 * ACLA I.D. (2118) (2110) (2108)
 * BAUD-RATE
 *-----
 * 50 0 1
 * 75 1 2
 * 110 2 3
 * 134 3 4
 * 150 4 5
 * 200 5 6
 * 300 6 7
 * 600 7 8
 * 900 ---
 * 1050 8 ---
 * 1200 9 9
 * 1800 10 (A) 10 (A)
 * 2000 11 (B) ---
 * 2400 12 (C) 11 (B)
 * 3600 --- 12 (C)
 * 4800 13 (D) 13 (D)
 * 7200 --- 14 (E)
 * 9600 14 (E) 15 (F)
 * 19200 15 (F) ---
 *
 * TO MAKE ANY OF THE ABOVE CHANGES, LOAD AND HALT THE PROGRAM BEFORE EX-
 * ECUTION. INSERT CHANGE THEN EXECUTE. MEMORY LOCATIONS OF "ZV\$TTY" AND
 * "ZV\$BUD" MAY BE FOUND IN MAP AT END OF LISTING.
 * CONSULT LEVEL-6 T&V MANUAL "AW94" FOR DETAILS ON HOW TO LOAD THE TESTS.
 *
 * THE FOLLOWING IS A TYPICAL RESULT OF LOADING THE PROGRAM
 * AND STARTING TO RUN.
 *
 * SMDX2 REV X, STORAGE MODULE T&D, MM DD YY
 * ZV\$LIB REV. X.X
 *
 * WDT CHAN DEV C ID
 * 0400 DSKT 2010
 * 0480 DSKT 2010
 *

000162 * 0500 CONS 2018
 000163 * 1200 DISK 2361
 000164 * 1280 DISK 2363
 000165 * 0580 CDR 2008
 000166 * MEMORY LOW 0000XXXX
 000167 * MEMORY HIGH 00007FFF 32K
 000168 * READ BUFF ADDR(HEX): XXXX
 000169 * WRITE BUFF ADDR(HEX): XXXX
 000170 *
 000171 *
 000172 *
 000173 * THE FIRST EXECUTION OF THE PROGRAM WILL ASK THE QUESTION:
 000174 * POWER FREQ, HZ ?: 60 C/R (ON A 6/30 SYSTEM ONLY)
 000175 * RESPOND WITH THE POWER LINE FREQUENCY IN HERTZ,
 000176 * USUALLY 60 IN THE UNITED STATES AND 50 ELSEWHERE. THE
 000177 * PROGRAM WILL THEN CALIBRATE THE CPU CLOCK AGAINST THE
 000178 * REAL-TIME-CLOCK. DEFAULT VALUE IS 60 HZ. WITH A 6/40.
 000179 * THIS QUESTION WILL NOT BE ASKED.
 000180 *
 000181 * THE PROGRAM WILL THEN ASK:
 000182 * CHANNEL ?: 1200 C/R
 000183 *
 000184 * FIRMWARE REV XX
 000185 *
 000186 *
 000187 *
 000188 *
 000189 *
 000190 * THE RESPONSE SHOULD BE THE FOUR DIGIT HEX CHANNEL NUMBER ASSIGNED
 000191 * TO ANY DEVICE IN THE SUBSYSTEM. THE PROGRAM WILL USE
 000192 * THE CONTROLLER PORTION OF THE CHANNEL NUMBER TO ADDRESS
 000193 * ALL DEVICES. DEFAULT VALUE IS HEX 1200.
 000194 *
 000195 * THE ABOVE SEQUENCE WILL ONLY OCCUR ON THE FIRST START AFTER
 000196 * A FRESH LOAD OF THE PROGRAM. SUBSEQUENT RESTARTS WILL START
 000197 * AT TEST 0101.
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 * TEST DESCRIPTION:
 * TEST 0101: LOGIC PLUG "0" IS INSTALLED IN THE DEVICE TO BE TESTED.
 * IF THIS DEVICE IS OTHER THAN "0" SWAP SELECTION PLUGS
 * WITH DEVICE "0".
 * THE DEVICE IS SELECTED AT LOGICAL ADDRESS "0", THEN ATTEMPT
 * TO SELECT WITH ADDRESS PLUG REMOVED.
 * TEST 0102: REINSTALL NORMAL LOGIC PLUG IN DEVICE AND SELECT AT THIS
 * ADDRESS.
 * TEST 0200: ALL COMBINATIONS OF BUS-OUT INFORMATION ARE WRAPPED
 * BACK ON BUS-IN. SOLID ERROR PRINTOUTS ARE TERMINATED
 * BY THE "BREAK" KEY.
 * TEST 0300: CYCLE UP AND CYCLE DOWN SEQUENCES ARE EXERCISED.
 * TEST 0400: RFU
 * TEST 0500: RTZ SEQUENCES ARE TESTED.
 * TEST 0601: OFFSETS (+8-), ZERO SEEK AND ILLEGAL SEEK OPERATIONS
 * ARE TESTED.
 * TEST 0602: SINGLE TRACK SEEKS FROM 0 TO MAX. CYLINDER AND
 * RETURN ARE MONITORED.
 * TEST 0603: RANDOM SEEKS ARE TIMED FOR A MAXIMUM OF 10,000
 * OPERATIONS. THE SEQUENCE MAY BE TERMINATED AT
 * ANY TIME BY HITTING THE "BREAK" KEY. THE AVER-
 * AGE SEEK TIME IS THEN CALCULATED.
 * TEST 0604: A DIRECT SEEK FROM 0 TO AN OPERATOR SELECTED CYL-
 * INDER(DECIMAL) AND RETURN IS MONITORED. THE TEST
 * MAY BE LOOPED AT THE OPERATORS DISCRETION AND TERM-
 * INATED BY THE "BREAK" KEY.
 * TEST 0700: MAXIMUM CYLINDER SEEKS ARE PERFORMED FOR THE NUMBER
 * OF OPERATOR SPECIFIED CYCLES(32K DECIMAL MAX.) OR UNTIL
 * TERMINATED BY THE "BREAK" KEY. MAX. FWD, REV AND
 * AVERAGE SEEK TIMES ARE DISPLAYED AS AN AID TO COURSE
 * VELOCITY GAIN ADJUSTMENT.
 * TEST 0800: THIS TEST IS NOT PERFORMED ON LEVEL 6.
 * TEST 0900: THE SECTOR COUNTER IS TESTED SEQUENTIALLY FROM
 * SECTOR 0 TO SECTOR 63.
 * TEST 1000: WRITE PROTECT FUNCTION IS CHECKED.
 * TEST 1001: IF WRITE OPERATIONS ARE PERMITTED ALL TRACKS ARE
 * FORMATTED ON AN OPERATOR SELECTED CYLINDER. AFTER
 * CHECKING THE FORMAT INFORMATION, A WORSE CASE PATT-
 * ERN IS WRITTEN ON AN OPERATOR SELECTED RECORD ON ALL
 * TRACKS AND THEN READ. ALL OPERATOR INPUTS ARE DECIMAL.
 * SOLID ERROR PRINTOUTS ARE TERMINATED BY THE "BREAK" KEY.
 * THE LAST TRACK NUMBER PRINTED IS THE CURRENT TRACK.
 *
 * ERROR REPORTING:
 * DETECTED ERRORS ARE REPORTED AS FOLLOWS:
 *
 * ERROR DICTIONARY ENTRY XXXX <TAG XX IS YY, SB ZZ>
 * YYY TEST FAILED *****
 *
 * REGISTER DUMP:
 * B7 B6 B5 B4
 * B3 B2 B1 I
 * R7 R6 R5 R4
 * R3 R2 R1 M
 *
 * THESE REGISTERS WERE SAVED PRIOR TO ENTRY TO THE REPORTING ROUTINE.
 * THE ERROR REPORTING RETURN LOCATION IS CONTAINED IN B5.
 * THE PROGRAM WILL THEN ASK THE QUESTION "RESTART ?". REPLY (Y) OR (N).

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* THE TROUBLE SHOOTING AND REPAIR GUIDE (AV-0026-015-N) CONTAINS
* THE FAULT DICTIONARY. REFERENCE TO THE DICTIONARY ENTRY
* INDICATES THE FAILED ORU(S) AND AN EXTENDED PROCEDURE
* WHERE APPLICABLE.

* STATUS WORDS:
* THE TWO STATUS WORDS HAVE THE FOLLOWING SIGNIFICANCE:
* FIRST STATUS WORD
* BIT STATUS

* 0 READY
* 1 ATTENTION
* 2 OVERRUN@UNDERUN
* 3 DEVICE FAULT

* 4 READ ERROR
* 5 ILLEGAL SEEK
* 6 MISSED DATA SYNC
* 7 UNSUCCESSFUL SEARCH

* 8 MISSING CLOCK PULSE
* 9 SUCCESSFUL RECOVERY
10 RFU
11 RFU

12 CORRECTED MEMORY ERROR
13 NON-EXISTANT RESOURCE ERROR
14 BUS PARITY ERROR
15 UNCORRECTED MEMORY ERROR

SECOND STATUS WORD
BIT STATUS

0 CORRECTED READ ERROR
1 SUCCESSFUL RETRY
2 OVERRUN/UNDERUN RECOVERY
3 DEVICE SEIZED

4 DEVICE RESERVED
5 RFU
6 RFU
7 RFU

8 NO HEAD SELECT
9 WRITE FAULT
10 (W+R)OFF CYL
11 WR FAULT

12 VOLTAGE FAULT
13 HEAD SELECT FAULT
14 SEEK ERROR
15 WRITE PROTECTED

* BUS-IN BYTES REPORTED AS "IS" / "SB".
* THE BUS IN BYTES HAVE THE FOLLOWING SIGNIFICANCE:
*
* TAG 1 BYTE
* BIT STATUS
*-----
* 0-7 BUS-OUT BITS WRAPPED ON BUS-IN
*
* TAG 2 BYTE
* BIT STATUS
*-----
* 0 NO HEAD SELECT
* 1 WRITE FAULT
* 2 (W+R) OFF CYL.
* 3 W.R FAULT
*-----
* 4 VOLTAGE FAULT
* 5 HEAD SELECT FAULT
* 6 SEEK ERROR
* 7 WRITE PROTECTED
*-----
* TAG 3 BYTE
* BIT STATUS
*-----
* 0 START
* 1 SPEED
* 2 LOAD**+RTZ*
* 3 DIBIT FAULT
*-----
* 4 HEADS LOADED
* 5 SLOPE
* 6 FINE
* 7 RFU
*-----
* TAG 5 BYTE
* BIT STATUS
*-----
* 0-7 RPS COUNTER
*-----
*
* FLAG BIT 15 - PASS INDICATOR
* BIT 14 - BYPASS INDICATOR
* BIT 13 - 10 MS TIME INDICATOR
* BIT 12 - SLOW SEEK INDICATOR
* BIT 11 - TIMER OVERFLOW

```

0000384          ****
0000385          0000  ZER0  EQU  $
0000386          XLCU  ZHISAZ,ZHRTCI,ZHRTCL,ZHNTSA,ZV$HR,ZHTH15,ZHCOMM,ZV$BKF
0000387          XLUC  ZV$SV1,ZV$SV2,ZV$FRB,ZHIAFB,ZV$LR
0000388          ****
0000389          0100  START  EQU  ZER0+X'100'
0000390          *      UKG  START
0000391          0100  CL    =SR3
0000392          0100  8753  CALL  ZV$RD,MSNAME   PRINT TITLE AND RESOURCES
0000393          0101  FBC0  0003
0000394          0102  D380  0000  X     LD    <GOFLAG,=Z'0002'
0000395          0103  UF80
0000396          0104  0FF0
0000397          0105  8280  0FC3
0000398          0106  0002
0000399          0107  0500  01E2
0000400          0108  8280  0FC3
0000401          0109  0001  X     LD    <GOFLAG,=Z'0001'
0000402          0110  0500  017A
0000403          0111  CC80  0000
0000404          0112  CF80  UFC7
0000405          0113  0001  RBUF
0000406          0114  8756
0000407          0115  F800  UFC7
0000408          0116  FA00  UFC9
0000409          0117  8E00
0000410          0118  EA00  UFC7
0000411          0119  EF00  UFC6
0000412          0120  EF00  UFC6
0000413          0121  EC80  UFC6
0000414          0122  FBC0  0003
0000415          0123  FCD4
0000416          0124  D380  0EE2
0000417          0125  UF80
0000418          0126  11B8  X     CALL  ZV$1,LNGAD+$AF-1  ADDRESS
0000419          0127  FBC0  0003
0000420          0128  D380  0000
0000421          0129  UF80
0000422          0130  01C9  X     LD    $B7,=$B6
0000423          0131  UFCA
0000424          0132  FCB6
0000425          0133  D380  0EE2
0000426          0134  FBC0  0003
0000427          0135  D380  0000  X     LD    $B5,<LNGASC
0000428          0136  UF80
0000429          0137  11C9
0000430          0138  11C9
0000431          0139  11C9
0000432          0140  UFCA
0000433          0141  8C51
0000434          0142  82D1
0000435          0143  2000
0000436          0144  0500  0152
0000437          0145  FBC0  0003
0000438          0146  D380  0000
0000439          0147  UF80
0000440          0148  11B0
0000441          0149  FBC0  0003
0000442          0150  D380  0000  X     CALL  ZV$1D,RTCHZ  GET RTC FREQUENCY (DEFAULT = 60)
0000443          0151  UF80
0000444          0152  9B80  015D
0000445          0153  9F80  121A
0000446          0154  9B80  1217
0000447          0155  9F80  000F
0000448          0156  8E70  800F
0000449          0157  0000  X     LEVX  LAB  $B1,<LEV-15  SET UP FOR NORMAL RUNNING AT LEVEL 15
0000450          0158  HLT
0000451          0159  0000
0000452          0160  0000
0000453          0161  0000
0000454          0162  0000
0000455          0163  0005  X     STB  $B1,<SA15P
0000456          0164  9B80  120B
0000457          0165  9F80  0005
0000458          0166  9800  UF01
0000459          0167  1F02
0000460          0168  1E01
0000461          0169  9F00  0000  X     ADV  $R1,=1
0000462          0170  9F00  0000
0000463          0171  D380  0E26
0000464          0172  0005
0000465          0173  0000
0000466          0174  0000
0000467          0175  UF80  0174
0000468          0176  B    <$-1
0000469          0177  8900  UFC3
0000470          0178  0001  X     STR  $R1,<ZHRTCI
0000471          0179  0001  X     STR  $R1,<ZHRTCC
0000472          0180  0000  X     DEC  =$R1
0000473          0181  2C05
0000474          0182  88D1
0000475          0183  LDV  $R2,=5
0000476          0184  AF00  0000  X     STR  $R2,<ZHRTCL
0000477          0185  D380  0E26
0000478          0186  0005
0000479          0187  LNJ  $B5,<SYNCH
0000480          0188  RTCF
0000481          0189  HLT
0000482          0190  B    <$-1
0000483          0191  ****
0000484          0192  ****
0000485          0193  ****
0000486          0194  ****
0000487          0195  ****
0000488          0196  ****
0000489          0197  ****
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0000491          0199  ****
0000492          0200  ****
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0000681          0389  ****
0000682          0390  ****
0000683          0391  ****
0000684          0392  ****
0000685          0393  ****
0000686          0394  ****
0000687          0395  ****
0000688          0396  ****
0000689          0397  ****
0000690          0398  ****
0000691          0399  ****
0000692          0400  ****
0000693          0401  ****
0000694          0402  ****
0000695          0403  ****
0000696          0404  ****
0000697          0405  ****
0000698          0406  ****
0000699          0407  ****
0000700          0408  ****
0000701          0409  ****
0000702          0410  ****
0000703          0411  ****
0000704          0412  ****
0000705          0413  ****
0000706          0414  ****
0000707          0415  ****
0000708          0416  ****
0000709          0417  ****
0000710          0418  ****
0000711          0419  ****
0000712          0420  ****
0000713          0421  ****
0000714          0422  ****
0000715          0423  ****
0000716          0424  ****
0000717          0425  ****
0000718          0426  ****
0000719          0427  ****
0000720          0428  ****
0000721          0429  ****
0000722          0430  ****
0000723          0431  ****
0000724          0432  ****
0000725          0433  ****
0000726          0434  ****
0000727          0435  ****
0000728          0436  ****
0000729          0437  ****
0000730          0438  ****
0000731          0439  ****
0000732          0440  ****
0000733          0441  ****
0000734          0442  ****
0000735          0443  ****
0000736          0444  ****
0000737          0445  ****
0000738          0446  ****
0000739          0447  ****
0000740          0448  ****
0000741          0449  ****
0000742          0450  ****
0000743          0451  ****
0000744          0452  ****
0000745          0453  ****
0000746          0454  ****
0000747          0455  ****
0000748          0456  ****
0000749          0457  ****
0000750          0458  ****
0000751          0459  ****
0000752          0460  ****
0000753          0461  ****
0000754          0462  ****
0000755          0463  ****
0000756          0464  ****
0000757          0465  ****
0000758          0466  ****
0000759          0467  ****
0000760          0468  ****
0000761          0469  ****
0000762          0470  ****
0000763          0471  ****
0000764          0472  ****
0000765          0473  ****
0000766          0474  ****
0000767          0475  ****
0000768          0476  ****
0000769          0477  ****
0000770          0478  ****
0000771          0479  ****
0000772          0480  ****
0000773          0481  ****
0000774          0482  ****
0000775          0483  ****
0000776          0484  ****
0000777          0485  ****
0000778          0486  ****
0000779          0487  ****
0000780          0488  ****
0000781          0489  ****
0000782          0490  ****
0000783          0491  ****
0000784          0492  ****
0000785          0493  ****
0000786          0494  ****
0000787          0495  ****
0000788          0496  ****
0000789          0497  ****
0000790          0498  ****
0000791          0499  ****
0000792          0500  ****
0000793          0501  ****
0000794          0502  ****
0000795          0503  ****
0000796          0504  ****
0000797          0505  ****
0000798          0506  ****
0000799          0507  ****
0000800          0508  ****
0000801          0509  ****
0000802          0510  ****
0000803          0511  ****
0000804          0512  ****
0000805          0513  ****
0000806          0514  ****
0000807          0515  ****
0000808          0516  ****
0000809          0517  ****
0000810          0518  ****
0000811          0519  ****
0000812          051A  ****
0000813          051B  ****
0000814          051C  ****
0000815          051D  ****
0000816          051E  ****
0000817          051F  ****
0000818          051G  ****
0000819          051H  ****
0000820          051I  ****
0000821          051J  ****
0000822          051K  ****
0000823          051L  ****
0000824          051M  ****
0000825          051N  ****
0000826          051O  ****
0000827          051P  ****
0000828          051Q  ****
0000829          051R  ****
0000830          051S  ****
0000831          051T  ****
0000832          051U  ****
0000833          051V  ****
0000834          051W  ****
0000835          051X  ****
0000836          051Y  ****
0000837          051Z  ****
0000838          051A  ****
0000839          051B  ****
0000840          051C  ****
0000841          051D  ****
0000842          051E  ****
0000843          051F  ****
0000844          051G  ****
0000845          051H  ****
0000846          051I  ****
0000847          051J  ****
0000848          051K  ****
0000849          051L  ****
0000850          051M  ****
0000851          051N  ****
0000852          051O  ****
0000853          051P  ****
0000854          051Q  ****
0000855          051R  ****
0000856          051S  ****
0000857          051T  ****
0000858          051U  ****
0000859          051V  ****
0000860          051W  ****
0000861          051X  ****
0000862          051Y  ****
0000863          051Z  ****
0000864          051A  ****
0000865          051B  ****
0000866          051C  ****
0000867          051D  ****
0000868          051E  ****
0000869          051F  ****
0000870          051G  ****
0000871          051H  ****
0000872          051I  ****
0000873          051J  ****
0000874          051K  ****
0000875          051L  ****
0000876          051M  ****
0000877          051N  ****
0000878          051O  ****
0000879          051P  ****
0000880          051Q  ****
0000881          051R  ****
0000882          051S  ****
0000883          051T  ****
0000884          051U  ****
0000885          051V  ****
0000886          051W  ****
0000887          051X  ****
0000888          051Y  ****
0000889          051Z  ****
0000890          051A  ****
0000891          051B  ****
0000892          051C  ****
0000893          051D  ****
0000894          051E  ****
0000895          051F  ****
0000896          051G  ****
0000897          051H  ****
0000898          051I  ****
0000899          051J  ****
0000900          051K  ****
0000901          051L  ****
0000902          051M  ****
0000903          051N  ****
0000904          051O  ****
0000905          051P  ****
0000906          051Q  ****
0000907          051R  ****
0000908          051S  ****
0000909          051T  ****
0000910          051U  ****
0000911          051V  ****
0000912          051W  ****
0000913          051X  ****
0000914          051Y  ****
0000915          051Z  ****
0000916          051A  ****
0000917          051B  ****
0000918          051C  ****
0000919          051D  ****
0000920          051E  ****
0000921          051F  ****
0000922          051G  ****
0000923          051H  ****
0000924          051I  ****
0000925          051J  ****
0000926          051K  ****
0000927          051L  ****
0000928          051M  ****
0000929          051N  ****
0000930          051O  ****
0000931          051P  ****
0000932          051Q  ****
0000933          051R  ****
0000934          051S  ****
0000935          051T  ****
0000936          051U  ****
0000937          051V  ****
0000938          051W  ****
0000939          051X  ****
0000940          051Y  ****
0000941          051Z  ****
0000942          051A  ****
0000943          051B  ****
0000944          051C  ****
0000945          051D  ****
0000946          051E  ****
0000947          051F  ****
0000948          051G  ****
0000949          051H  ****
0000950          051I  ****
0000951          051J  ****
0000952          051K  ****
0000953          051L  ****
0000954          051M  ****
0000955          051N  ****
0000956          051O  ****
0000957          051P  ****
0000958          051Q  ****
0000959          051R  ****
0000960          051S  ****
0000961          051T  ****
0000962          051U  ****
0000963          051V  ****
0000964          051W  ****
0000965          051X  ****
0000966          051Y  ****
0000967          051Z  ****
0000968          051A  ****
0000969          051B  ****
0000970          051C  ****
0000971          051D  ****
0000972          051E  ****
0000973          051F  ****
0000974          051G  ****
0000975          051H  ****
0000976          051I  ****
0000977          051J  ****
0000978          051K  ****
0000979          051L  ****
0000980          051M  ****
0000981          051N  ****
0000982          051O  ****
0000983          051P  ****
0000984          051Q  ****
0000985          051R  ****
0000986          051S  ****
0000987          051T  ****
0000988          051U  ****
0000989          051V  ****
0000990          051W  ****
0000991          051X  ****
0000992          051Y  ****
0000993          051Z  ****
0000994          051A  ****
0000995          051B  ****
0000996          051C  ****
0000997          051D  ****
0000998          051E  ****
0000999          051F  ****
0001000          051G  ****
0001001          051H  ****
0001002          051I  ****
0001003          051J  ****
0001004          051K  ****
0001005          051L  ****
0001006          051M  ****
0001007          051N  ****
0001008          051O  ****
0001009          051P  ****
0001010          051Q  ****
0001011          051R  ****
0001012          051S  ****
0001013          051T  ****
0001014          051U  ****
0001015          051V  ****
0001016          051W  ****
0001017          051X  ****
0001018          051Y  ****
0001019          051Z  ****
0001020          051A  ****
0001021          051B  ****
0001022          051C  ****
0001023          051D  ****
0001024          051E  ****
0001025          051F  ****
0001026          051G  ****
0001027          051H  ****
0001028          051I  ****
0001029          051J  ****
0001030          051K  ****
0001031          051L  ****
0001032          051M  ****
0001033          051N  ****
0001034          051O  ****
0001035          051P  ****
0001036          051Q  ****
0001037          051R  ****

```

000462 U17F 1194
 CALL ZV\$IH,CHAN GET CHANNEL (DEFAULT = 1200)
 000463 U180 FBC0 0003 X
 000464 U182 D380 0000
 000465 U184 0F80
 000466 U185 11F5
 000467 U186 9800 11F5
 000468 U187 9B80 11F6
 000469 U188 A680 120A
 000469 U189 9AF1
 000470 U18D FE00
 000471 U18E 9DD2
 000472 U18F 0200 018C
 MODEIA LDR \$R1,<CHAN
 LAB \$B1,<IOREAD
 LAB \$B2,<CHANZ
 SRM \$R1,+\$B1,Z=FE00*
 CMB \$B1,=\$B2
 BL <MODEIA
 *
 ** NOW CHECK THAT DEVICE ADAPTER IS ON LINE BY
 ** CHECKING ID CODES, BUT FIRST SET UP FOR NON-EXISTANT DEVICE
 ** TRAP.
 *
 LAB \$B1,<TSA1
 STB \$B1,<ZHNTSA
 STB \$B1,<TS1
 LBF <GOFLAG,Z=0002*
 SO WE DON'T GET TSA FULL RUPT
 WILL SET IF GOOD ID FOUND
 LAB \$B2,<MODEIF
 STB \$B2,<ZHTH15
 TRAP HANDLER
 STORE IN TRAP 15 VECTOR
 *
 LUU \$R1,=-4
 MODEIB LDR \$R2,<DRIVE0+4,\$R1
 SRM \$R2,<INIDEN,Z=0180*
 GET DEVICE ADDRESS
 SET UP INPUT ID
 MODEIC CL
 =\$R5
 =\$R5,<INIDEN
 GET ID IN R5
 BLOF <MODEIC
 STR \$R5,<DEVID+4,\$R1
 AND \$R5,Z=FFF0*
 CMR \$R5,Z=2360*
 BNE <MODEIE
 LDT <GOFLAG,Z=0002*
 SET FLAG, ID OK
 MODEIE BINC \$R1,<MODEIB TRY NEXT ADDRESS
 * ALL FOUR DEVICE ADDRESSES CHECKED, TEST THE FLAG
 *
 LAB \$B1,<TH15
 STB \$B1,<ZHTH15
 LB <GOFLAG,Z=0002*
 SET UP FOR FUTURE...
 ...MISSING RESOURCE TRAPS.
 000495 U1B7 9880 0E01
 000496 U1B9 9F80 0000 X
 000497 U1B9 8280 UFC3
 000498 U1BD 0002 T
 000499 U1BE 0500
 000500 U1BF FBC0 0003
 000501 U1C1 D380 0000 X
 000502 U1C3 0F80
 000503 U1C4 119C
 000504 U1C5 0100
 000505 U1C7 8051
 000506 U1CA 07FD
 000507 U1C8 0000 1200
 000508 U1C9 9570 0UFF
 000509 U1C0 9F00 0FE7
 000510 U1CF FBC0 0003
 000511 U1D1 D380 0000 X
 000512 U1D3 0F80
 000513 U1D4 1045
 000514 U1D5 FBC0 0003
 000515 U1D7 D380 0000 X
 000516 U1D9 0F80
 000517 U1DA 0FE7
 000518 U1DB 0F80 020D
 000519 U1D9 9B80 01B5
 000520 U1D9 9F80 1222
 000521 U1E1 0003
 000522 U1E2 9B80 0E01
 000523 U1E4 9F80 0000 X
 000524 U1E6 8000 11F3
 000525 U1E8 0000 11FB
 000526 U1EA 07FC
 000527 U1EB CC80 UFC7
 000528 U1EC EC80 UFC6
 000529 U1EF 9B80 120B
 000530 U1F1 9B80 0005
 000531 U1F3 9B80 01FF
 000532 U1F5 9B80 121A
 000533 U1F7 9B80 1217
 000534 U1F9 9F80 000F
 000535 U1FB 8E70 800F
 000536 U1FD 0000
 000537 U1FE 0FFF
 MODEIF B <TO101 GO TO 1ST TEST
 LAB \$B1,<MODEIE RESUME SCAN AT MODEIE
 STB \$B1,<TS1P
 KIT RETURN FROM TRAP
 ***** RESTORE TRAPS *****
 * RESTORE TRAPS
 *
 MODEIH LAB \$B1,<TH15 MISSING RESOURCE TRAP
 STB \$B1,<ZHTH15
 SA IU <INZBDC,<OTCONT INITIALIZED
 BLOF >-\$A
 LDB \$B4,<KKPT
 LDB \$B6,<JJPT
 LAB \$B1,<SA5DV
 STB \$B1,<ZH1SAZ+5*\$AF
 LAB \$B1,<RESTR
 STB \$B1,<SA15P
 LAB \$B1,<SA15DV
 STB \$B1,<ZH1SAZ+15*\$AF
 LEY =Z'8000'+15
 HLT
 B >-\$1
 ***** ALL NOW INITIALIZED *****
 * RESTRT CALL ZV\$1.ZV\$QC,MSRSTR
 000538 U1FF FBC0 0003
 000539 U201 D380 0000 X
 000540 U203 0F80
 000541 U204 11C5
 000542 U205 D380 000B
 000543 U207 0000
 000544 U208 020D
 000545 U209 0000
 000546 U20A 0FFE
 000547 U20B 0F80 0F87
 LNJ \$B5,<YSNO RESTART ?
 DC <+\$A 'N' REPLY
 DC <TO101 'Y' REPLY
 SA HLT
 B >-\$A FREEZE HERE
 B <PCN XXXXXXXXDEBUG TOOL

```

***** TEST 0100 SELECTION TEST PART 1 *****
* TEST 0101 DEVICE 0
*
* T0101 CL <FLAG
*          CL <ERFL
*          CL <BITE
*          LDR $R1,Z"0101"
*          STR $R1,<TEST
*          LD  <IPFL,X"1"
*          BDT <T0101A
*          LINJ $B5,<TDST
*          CALL ZV$1.ZV$TC,MSILP   CLEAR ERROR FLAG
*                                SET DEVICE 0
*                                LOAD TEST NUMB
*                                INHIBIT PRINT ?
*
* 0200 8700 0FC2
* 020F 8700 0FC1
* 0211 8700 0FAC
* 0213 9870 0101
* 0215 9F00 0FEB
* 0217 8280 0FC5
* 0219 0001
* 021A 0500 0238
* 021C D380 0C60
* 021E FBC0 0003
* 0220 D380 0000
* 0222 0F80
* 0223 109B
* 0224 FBC0 0003
* 0226 D380 0000
* 0228 0F80
* 0229 0FAC
* 022A FBC0 0003
* 022C D380 0000
* 022E 0F80
* 022F 10EA
* 0230 D380 0D0B
* 0232 022A
* 0233 0234
* 0234 D800 0FAC
* 0236 D380 0UF5
* 0238 A800 0FFB
* 023A AF00 0FE7
* 023C D380 UC50
* 023E D000
* 023F 9800 0FE7
* 0241 8900 0FC2
* 0243 0001
* 0244 0500
* 0245 9952
* 0246 0980
* 0247 D380 UC85
* 0249 0102
* 024A FBC0 0003
* 024C D380 0000
* 024E 0F80
* 024F 115B
* 0250 FBC0 0003
* 0252 D380 0000
* 0254 0F80
* 0255 0FAC
* 0256 FBC0 0003
* 0258 D380 0000
* 025A 0F80
* 025D 10EA
* 025E D380 0D0B
* 025F 0258
* 0260 0F80 U238
* 0262 9952
* 0263 0900
* 0264 D380 UC85
* 0266 0106
* 0267 D380 UCDD
* 0269 0101
* 026A FBC0 0003
* 026C 8700 0FC2
* 026E 8700 0FC1
* 026F 8700 0FAC
* 0270 9870 0102
* 0272 9F00 0FEB
* 0274 D380 0C60
* 0276 FBC0 0003
* 0278 D380 0000
* 027A 0F80
* 027B 1038
* 027C FBC0 0003
* 027E D380 0000
* 0280 0F80
* 0281 0FA0
* 0282 9800 0FAC
* 0284 1042
* 0285 1900
* 0286 FBC0 0003
* 0288 D380 0000
* 028A 0F80
* 028B 1042
* 028C 0FF0
* 028D FBC0 0003
* 028F D380 0000
* 0291 0F80
* 0292 10E1
* T0101A LINJ $B5,<YSNO
*          DC <T0101B
*          DC <$1,$AF
*          LDR $R3,<BITE
*          LINJ $B5,<CDCH
*          LDR $R2,<X-FFFF
*          STR $R2,<TEMPA
*          LINJ $B5,<TAG1
*          DC Z"DU00"
*          LDR $R1,<TEMPA
*          BDT <FLAG,Z"0001"
*          BDT >+$B
*          CMK $R1,$R2
*          BNE $B
*          LINJ $B5,<TDER
*          DC Z"0102"
*          CALL ZV$1.ZV$TC,MSRLP   !N! REPLY
*                                !Y! REPLY
*                                INDEX SET TO DEVICE NUMBER
*                                SET CHANNELS FOR CURRENT DEVICES
*                                MAKE TEMPA UNIQUE
*                                TAG CODE IN
*                                GET BUS IN DATA
*                                1ST PASS DONE
*          $A          PLUG MISSING
*          BNE $B           BUS IN CHANGED?
*          LINJ $B5,<TDER           YES- DEVICE WAS SELECTED
*          DC Z"0102"           ERROR
*          CALL ZV$1.ZV$TC,MSRLP           MODULE NOT SELECTED
*                                         REMOVE PLUG
*
* 000577          CALL ZV$TH,BITE
* 000578          CALL ZV$TH,BITE
* 000579          CALL ZV$TH,BITE
* 000580          CALL ZV$TH,BITE
* 000581          CALL ZV$TH,BITE
* 000582          CALL ZV$TH,BITE
* 000583          CALL ZV$TH,BITE
* 000584          CALL ZV$TH,BITE
* 000585          CALL ZV$TH,BITE
* 000586          CALL ZV$TH,BITE
* 000587          CALL ZV$TH,BITE
* 000588          CALL ZV$TH,BITE
* 000589          CALL ZV$TH,BITE
* 000590          CALL ZV$TH,BITE
* 000591          ***** TEST 0100 SELECTION TEST PART 2 *****
* 000592          * TEST 0101 DEVICE N
* 000593          *
* 000594          *
* 000595          *
* 000596          *
* 000597          *
* 000598          *
* 000599          T0102 CL <FLAG
*          CL <ERFL
*          CL <BITE
*          LDR $R1,Z"0102"
*          STR $R1,<TEST
*          LINJ $B5,<TDST
*          CALL ZV$1.ZV$QC,MSDVNO   CLEAR ERROR FLAG
*                                DEFAULT IS DEVICE 0
*                                LOAD TEST NUMB
*                                !START TEST!
*                                ASK FOR DRIVE NUMB
*
* 0260 8700 0FC2
* 0261 8700 0FC1
* 0262 8700 0FAC
* 0263 9870 0102
* 0264 9F00 0FEB
* 0265 D380 0C60
* 0276 FBC0 0003
* 0278 D380 0000
* 027A 0F80
* 027B 1038
* 027C FBC0 0003
* 027E D380 0000
* 0280 0F80
* 0281 0FA0
* 0282 9800 0FAC
* 0284 1042
* 0285 1900
* 0286 FBC0 0003
* 0288 D380 0000
* 028A 0F80
* 028B 1042
* 028C 0FF0
* 028D FBC0 0003
* 028F D380 0000
* 0291 0F80
* 0292 10E1
* 000606          $A          CALL ZV$1.D,BITE   INPUT DRIVE NUMB
* 000607          T          LDR $R1,<BITE
*          SUK $R1,2
*          BEZ $R1,>+$B
*          CALL ZV$1.ZV$QC,MSBLNK   GET DEV NUMB
*                                DUMP 0->3
*                                DEV NUMB 0 -> 3
*                                ?
* 000611          $B          CALL >+$A   RETRY
* 000612          $B          CALL ZV$1.ZV$QC,MSDVID

```

000613	U293	UF80	T		b	>+\$b		
000614	U294	FBC0 0003		T0102A	CALL	ZV\$1.ZV\$UC,MSBLNK	? WRONG INPUT	
	U295	D380 0000	X					
	U298	UF80						
	U299	1042						
000615	U29A	FBC0 0003			\$b	CALL	ZV\$1H,IDEN	INPUT DEVICE ID
	U29C	D380 0000	X					
	U29E	UF80						
	U29F	UFC4						
000616	U2A0	9800 UFC4			LDR	\$R1,<IDEN		GET MODEL
000617	U2A2	1AB0 0294			BLEZ	\$R1,<T0102A		WRONG MODEL
000618	U2A4	9970 2363			CMR	\$R1,<Z'2363'		
000619	U2A6	0300 0294			BG	<T0102A		TOO BIG
000620	U2A8	9970 2360			CMR	\$R1,<Z'2360'		
000621	U2AA	0980	T		BNE	>+\$A		
000622	U2AD	9870 0000			LDR	\$R1,<Z'0000'		LOAD DEVICE ID FOR 40 MB
000623	U2AD	9970 2361			CMR	\$R1,<Z'2361'		
000624	U2AF	0980	T		BNE	>+\$A		
000625	U2B0	9870 8000			LDR	\$R1,<Z'8000'		ID FOR 80 MB
000626	U2B2	9970 2362			CMR	\$R1,<Z'2362'		
000627	U2B4	0980	T		BNE	>+\$A		
000628	U2B5	9870 4000			LDR	\$R1,<Z'4000'		ID FOR 150 MB
000629	U2B7	9970 2363			CMR	\$R1,<Z'2363'		
000630	U2B9	0980	T		BNE	>+\$A		
000631	U2BA	9870 C000			LDR	\$R1,<Z'C000'		ID FOR 300 MB
000632	U2BC	82D1			LD	=SR1,<Z'3FFF'		INVALID ID
000633	U2BD	3FFF						
000634	U2C0	0500 0294			BBT	<T0102A		
000635	U2C0	9F00 UFC4			STR	\$R1,<IDEN		
	U2C2	FBC0 0003			CALL	ZV\$1.ZV\$TC,MSILP		IDEN OK, STORE IT
	U2C4	D380 0000	X					
	U2C6	UF80						
	U2C7	1090						
000636	U2C8	FBC0 0003			CALL	ZV\$1H,BITE		CURRENT DRIVE
	U2CA	D380 0000	X					
	U2CC	UF80						
	U2CD	UFAC						
000637	U2CE	FBC0 0003		T0102C	CALL	ZV\$1.ZV\$TC,MSDONE	*Y* WHEN DONE	
	U2D0	D380 0000	X					
	U2D2	UF80						
	U2D3	10EA						
000638	U2D4	D380 0D00			LNJ	\$B5,<YSNO		
000639	U2D6	U2CE			DC	<T0102C		
000640	U2D7	02D8			DC	<\$+\$AF		*N* REPLY
000641	U2D8	B800 UFAC			LDR	\$R3,<BITE		SET INDEX TO DEVICE
000642	U2DA	D380 0DF5			LNJ	\$B5,<CDCH		SET CHANS FOR THIS DEV
000643	U2DC	BAB0 02E5			SKM	\$R3,<T0102B,=Z'0003'		IN TAG COMMAND
000644	U2DE	0003						
000645	U2DF	A800 0FFB			SB	LDR	\$R2,<X'FFFF	
000646	U2E1	AF00 0FE7			STR	\$R2,<TEMPA		INITIALIZE
000647	U2E3	D380 UC50			LNJ	\$B5,<TAG1		
000648	U2E5	D000		T0102B	DC	Z'DU00'		TAG COMMAND
000649	U2E6	9800 0FE7			LDR	\$R1,<TEMPA		GET BUS IN DATA
000650	U2E8	9952			CMR	\$R1,<SR2		ALL F'S MEANS NO TRANSFER
000651	U2E9	0980	T		BNE	>+\$A		OK
000652	U2EA	D380 UC85			LNJ	\$B5,<TDER		MODULE NOT SELECTED
000653	U2EC	0108			DC	Z'0108'		
000654	U2ED	9600 UFC4			SA	XOR	\$R1,<IDEN	TEST ID BITS
	U2F0	E000			LD	=SR1,<Z'E000'		
000655	U2F1	0580	T		BBF	>+\$A		OK
000656	U2F2	D380 UC85			LNJ	\$B5,<TDER		
000657	U2F4	0114			DC	Z'0114'		WRONG ID
000658	U2F5	9800 0FE7			SA	LDR	\$R1,<TEMPA	GET BUS IN AGAIN
000659	U2F7	1048			SUR	\$R1,<B		GET DEV NUMB
000660	U2F8	9600 UFAC			XOR	\$R1,<BITE		AND COMPARE WITH SELECTED
000661	U2FA	82D1			LD	=SR1,<Z'000F'		
000662	U2FB	000F						
000663	U2FC	0580	T		BBF	>+\$A		OK
000664	U2FD	D380 UC85			LNJ	\$B5,<TDER		
000665	U2FF	0116			DC	Z'0116'		ADDRESS ERROR
000666	U300	D380 UCDD			SA	LNJ	\$B5,<TDTE	END OF TEST
000667	U302	0102			DC	Z'0102'		
*	*	*	*	*	*	*	*	*
000669	*	*	*	*	*	*	*	*
000670	*	*	*	*	*	*	*	*
000671	*	*	*	*	*	*	*	*
000672	*	*	*	*	*	*	*	*
000673	U303	8700 UFC2		T0200	CL	<FLAG		
000674	U305	8700 UFC1			CL	<ERFL		CLEAR ERROR FLAG
000675	U307	9870 0200			LDR	\$R1,<Z'0200'		
000676	U309	9F00 0FEB			STR	\$R1,<TEST		LOAD TEST NUMB
000677	U30B	D380 UC6D			LNJ	\$B5,<TDST		START TEST
000678	U30D	9870 D100			SA	LDR	\$R1,<Z'D100'	INITIAL DATA WRAP
000679	U30F	9F00 0313		T0200A	STR	\$R1,<T0200B		TASK WORD
000680	U311	D380 UC50		T0200B	LNJ	\$B5,<TAG1		WRAP TEST
000681	U313	D100			LDR	\$R7,<TEMPA		GET *IS* DATA
000682	U314	F800 0FE7			SUR	\$R7,8		
000683	U316	7048			LDR	\$R6,<T0200B		SAVE *SB* DATA
000684	U317	E800 0313			AND	\$R6,<X'FF'		
000685	U319	E570 0UFF			CMR	\$R6,<SR7		
000686	U31B	E957			BE	<T0200C		DATA IS OK
000687	U31C	0900 0337			LBT	<IPFL,X'2'		SET FLAG FOR *IS*/*SB* PRINT
000688	U31E	8900 UFC5						
	U320	0002						
000689	U321	4048			SOR	\$R4,8		
000690	U322	C570 000F			AND	\$R4,<X'0F'		GET TAG
000691	U324	CF00 0FE9			STR	\$R4,<TEMPC		GET TAG NUMBER
000692	U326	L380 UF7B			LNJ	\$B5,<CRLF		
000693	U328	D380 UF33			LNJ	\$B5,<PSBC		PRINT "IS" / "SB" DATA
000694	U32A	8900 UFC1			LBT	<ERFL,X'01'		SET ERROR FLAG
000695	U32C	0001						
000696	U32D	8800 UFC5			LBF	<IPFL,X'02'		CLR IS/SB FLAG
000697	U32F	0002						
000698	U330	D380 UD33			LNJ	\$B5,<QUIT		HIT BREAK TO QUIT
000699	U332	0337			DC	<T0200C		CONTINUE
000699	U333	0334			DC	<+\$AF		GET OUT

000699	U334	D380	0C85	\$D	LNJ	\$B5,<TDER		
000700	U336	D206			DC	Z'D0206'	DATA WRAP ERROR	
000701	U337	8A80	0313	T0200C	INC	<T0200B	NEXT DATA FOR WRAP	
000702	U339	8280	0313		LD	<T0200B,=Z'0100'	CHECK FOR END	
000703	U33C	8500	0311					
000704	U33E	D380	099E		BBT	<T0200A	NO-GO AGAIN	
000705	U340	D200			LNJ	\$B5,TDE	END TEST 0200	
000706					DC	Z'D0200'		
000707	*							
000708	*							
000709	*							
000710	*							
000711	*							
000712	*							
000713	*							
000714	U341	8700	0FC2	T0300	CL	<FLAG		
000715	U343	8700	0FC1		CL	<ERFL		
000716	U345	9870	0300		LDK	\$R1,<Z'0300'		
000717	U347	9F00	0FEB		SIR	\$R1,<TEST	STORE TEST NUMB	
000718	U349	D380	0C6D		LNJ	\$B5,<TDET	'START TEST'	
000719	U34D	D380	0C50		LNJ	\$B5,<TAGI	TAG IN	
000720	U34U	8700			DC	Z'D/00'	CONTROL TAG IN	
000721	U34L	8280	0FE7		LD	<TEMPA,=Z'0100'	CHECK DIAGNOSTIC BIT	
000722	U351	8580	036E		BBF	<T0300A	OK DO SOMETHING ELSF	
000723	U353	D380	0C52		LNJ	\$B5,<TAGO	TAG OUT	
000724	U355	D27C			DC	Z'D27C'	TRY TO CLEAR DIAG STATUS	
000725	U356	D380	0C50		LNJ	\$B5,<TAG1	TAG IN	
000726	U358	D200			DC	Z'D200'	READ DIAG STATUS	
000727	U359	8280	0FE7		LD	<TEMPA,=Z'8'	TEST HEAD SELECT	
000728	U35C	8580						
000729	U35D	U380	0C52		BBF	>+\$A		
000730	U35F	8300			LNJ	\$B5,<TAGO		
000731	U360	D380	0C50		DC	Z'D3'		
000732	U362	D200			LNJ	\$B5,<TAGI		
000733	U363	8280	0FE7		DC	Z'D2'		
000734	U365	F600			SA	LD	<TEMPA,=Z'FE'	
000735	U366	8580	036E		BBF	<T0300A	DIAG BITS CLEARED	
000736	U368	D380	0D46		LNJ	\$B5,<SBIS	PRINT 'IS/SB'	
000737	U36H	0001			DC	X'01'	BIT 7 DONT CARE	
000738	U36D	D380	0C85		LNJ	\$B5,<TDER	BAD COMPARE	
000739	U36D	8300			DC	Z'0308'	DIAG STATUS FAILED TO CLEAR	
000739	U36E	D380	0C50		LNJ	\$B5,<TAGI	INPUT TASK	
000740	U370	D300			DC	Z'D300'	TEST HEADS LOADED	
000741	U371	8280	0FE7		LB	<TEMPA,=Z'08'	TEST HEADS LOADED	
000742	U373	8800						
000743	U374	8580	03B7		BBF	<T0300D	OK, GO ON	
		FBC0	0003		CALL	ZV\$1.ZV\$TC,MSIND	OPERATOR MESSAGES	
	U376	D380	0000	X				
	U378	UF80						
	U37A	104D						
000744	U37C	FBC0	0003		CALL	ZV\$1.ZV\$TC,MSIND1		
	U37E	D380	0000	X				
	U380	UF80						
	U381	1062						
000745	U382	FBC0	0003		CALL	ZV\$1.ZV\$TC,MSIND2		
	U384	D380	0000	X				
	U386	UF80						
	U387	1085						
000746	U388	D380	0D0B		SA	LNJ	\$B5,<YSNU	'Y' OR 'N' RESPONSE
000747	U38A	036C			DC	<T0300B	'N'	
000748	U38D	0396			DC	<T0300C	'Y'	
000749	U38E	8280	0FE7		T0300B	LB	<TEMPA,=Z'80'	CHECK START BIT
000750	U38F	8580			BBF	>+\$A	START BIT IS OFF	
000751	U390	D380	0C85		LNJ	\$B5,<TDER	START LIGHT OFF	
000752	U392	8312			DC	Z'D312'		
000753	U393	D380	0C85		SA	LNJ	\$B5,<TDER	HEADS LOADED & NO START
000754	U395	0314			DC	Z'D314'	GET STATUS WORD 1	
000755	U396	D380	0F12		T0300C	LNJ	\$B5,<STAT1	TEST READY BIT
000756	U398	8280	0FE5		LB	<STAT1,=Z'8'		
000757	U399	8000						
000758	U39C	8580	0E41		BBF	>T0300C	WAIT TO CLEAR	
000759	U39E	0019			LNJ	\$B5,<TISEC	TIMEOUT	
000760	U39F	0F80			DC	25	FOR 25 SECS	
000761	U3A0	UF80			T	\$A	B	LOOP WHILE WAITING
000762	U3A1	UFFE				B	>+\$C	TIMEOUT FINISHED
000763	U3A2	D380	0C50		SC	LNJ	\$B5,<TAGI	
000764	U3A4	8300				DC	Z'D3'	
000765	U3A5	9080	0FE7			LDH	\$R1,<TEMPA	
000766	U3A7	9570	4800			AND	\$R1:=Z'48'	
000767	U3A9	9970	4800			CMR	\$R1:=Z'48'	
000768	U3AB	0980				BNE	>+\$C	TEST BITS 1 & 4
000769	U3AC	D380	0C85			LNJ	\$B5,<TDER	1 & 4 ARE SET
000770	U3AE	8316				DC	Z'0316'	
000771	U3AF	8280	0FE7		SC	LB	<TEMPA,=Z'C8'	TEST 0,1,64
	U3B1	C800						
000772	U3B2	8580	03B7		BBF	<T0300D	OK, NONE SET	
000773	U3B4	D380	0C85		LNJ	\$B5,<TDER		
000774	U3B6	0310			DC	Z'0310'	AT LEAST ONE IS SET	
000775	U3B7	FBC0	0003		T0300D	CALL	ZV\$1.ZV\$TC,MSSTR	DEPRESS START SWITCH
	U3B9	D380	0000	X				
	U3BD	UF80						
	U3C0	112D						
000776	U3BD	FBC0	0003		CALL	ZV\$1,MSTIME	WITHIN 15 SECS	
	U3BF	D380	0000	X				
	U3C1	UF80						
	U3C2	114E						
000777	U3C3	D380	0E41		LNJ	\$B5,<T1SEC		
000778	U3C5	000F			DC	15	15 SECOND TIMEOUT	
000779	U3C6	0F80			P	>+\$A	TIME THIS PROCESS	
000780	U3C7	D380	0C85		LNJ	\$B5,<TDER		
000781	U3C9	8318			DC	Z'0318'	TIMEOUT ON START BIT	
000782	U3CA	D380	0C50		SA	LNJ	\$B5,<TAGI	
000783	U3CC	D300			DC	Z'D3'		

000784	U3CF	8280	0FE7		LB	<TEMPA,=Z*8*	TEST START BIT
000785	U3CF	8000			BBF	>-SA	KEEP TRYING
000786	U3D0	05FA			RTCF	OK-TURN CLOCK OFF	
000787	U3D1	0005			T0300E	\$B5,<T1SEC	RESET CLOCK...
000788	U3D2	D380	0E41	1	DC	35	TO 35 SECS
000789	U3D4	0023			B	>+\$D	THEN DO THIS
000790	U3D5	0F80			LNJ	\$B5,<TDER	SPEED BIT TIMEOUT
000791	U3D6	D380	UC85		DC	Z*0320*	
000792	U3D8	0320			LNJ	\$B5,<TAGI	
000793	U3D9	D380	OC50		DC	Z*D3*	
000794	U3D0	D300			LB	<TEMPA,=Z*4*	
000795	U3DE	8280	0FE7		BBF	>-SD	
000796	U3DF	05FA			RTCF	OK-TURN THE CLOCK OFF	
000797	U3E0	0005			T0300F	\$B5,<TIME	DELAY FOR...
000798	U3E1	D380	0E38		DC	1	...1 MILLISECOND
000799	U3E3	0001			LNJ	\$B5,<TAGI	
000800	U3E4	0380	OC50		DC	Z*D3*	
000801	U3E5	D300			LB	<TEMPA,=Z*4*	
000802	U3E7	8280	0FE7		BBF	<T0300G	
000803	U3E9	2000			LNJ	\$B5,<SBIS	
000804	U3EC	0580	0406		DC	X*01*	
000805	U3EE	D380	UC50		LNJ	\$B5,<TDER	
000806	U3EF	8280	0FE7		DC	Z*D3*	
000807	U3F1	FE00			LB	<TEMPA,=Z*FE*	
000808	U3F2	0580		T	BBF	>+\$L	
000809	U3F3	D380	UD46		LNJ	\$B5,<SBIS	
000810	U3F5	0001			DC	Z*0324*	
000811	U3F6	D380	UC85		LNJ	\$B5,<TDER	
000812	U3F8	0322			DC	Z*0322*	
000813	U3F9	D380	UC50		LNJ	\$B5,<TAGI	
000814	U3FC	D300			DC	Z*D3*	
000815	U3FF	8280	0FE7		LB	<TEMPA,=Z*1*	
000816	U400	D380	UC85	T	BBF	>+\$D	
000817	U402	0324			LNJ	\$B5,<TDER	
000818	U403	D380	OC65		DC	Z*0324*	
000819	U405	0326			SD	\$B5,<TDER	
000820	U406	D380	0E38		DC	Z*0326*	
000821	U408	0019			T0300G	\$B5,<TIME	
000822	U409	D380	UC50		DC	25	
000823	U40B	D300			LNJ	\$B5,<TAGI	
000824	U40C	8280	0FE7		DC	Z*D3*	
000825	U40E	0800			LB	<TEMPA,=Z*08*	
000826	U40F	0500	041E		BBT	<T0300H	
000827	U411	D380	UC50		LNJ	\$B5,<TAGI	
000828	U413	D200			DC	Z*D2*	
000829	U414	8280	0FE7		LB	<TEMPA,=Z*08*	
000830	U416	0800			BBF	<T0300H	
000831	U417	0580		T	LNJ	\$B5,<TDER	
000832	U418	D380	UC85		DC	Z*0328*	
000833	U419	0350			SD	\$B5,<TDER	
000834	U420	0014			T0300H	\$B5,<T10MS	
000835	U421	0F80			DC	Z*0330*	
000836	U422	0F80		T	B	20	
000837	U423	D380	UC50		B	>+\$A	
000838	U425	D300			SD	\$B5,<TDER	
000839	U426	8280	0FE7		DC	Z*D3*	
000840	U428	2000			LB	<TEMPA,=Z*2*	
000841	U429	05FA			BBF	>+\$L	
000842	U42A	0005			LNJ	\$B5,<TDER	
000843	U42B	0F7F			DC	Z*0328*	
000844	U42C	0F80	0460		NUP	>-\$1	
000845	U431	8280	0FE7		BBF	<T0300K	
000846	U432	0800		T	LB	<TEMPA,=Z*08*	
000847	U433	0580			BBF	>+\$L	
000848	U435	D380	UC85		LNJ	\$B5,<TDER	
000849	U436	0332			DC	Z*0332*	
000850	U437	D380	OC85		SD	\$B5,<TDER	
000851	U438	0334			DC	Z*0334*	
000852	U43C	8280	0FE7		LB	<TEMPA,=Z*1*	
000853	U43E	1000			BBF	>+\$L	
000854	U43F	0580		T	LNJ	\$B5,<TDER	
000855	U440	D380	UC85		DC	Z*0336*	
000856	U442	0336			SD	\$B5,<TAGI	
000857	U443	D380	UC50		DC	Z*D2*	
000858	U445	D200			LB	<TEMPA,=Z*08*	
000859	U446	8280	0FE7		BBF	>+\$L	
000860	U447	0800			LNJ	\$B5,<TDER	
000861	U448	0580			DC	Z*0338*	
000862	U449	D380	0E41		SD	\$B5,<T1SEC	
000863	U44F	0005			DC	5	
000864	U450	0F80		T	A	>+\$D	
000865	U451	0F80			B	>+\$L	
000866	U452	0FFE			SD	>-\$A	
000867	U453	D380	UC50		DC	\$B5,<TAGI	
000868	U455	D300			LB	Z*D3*	
000869	U456	8280	0FE7		BBF	<TEMPA,=Z*4*	
000870	U458	4000			LNJ	>+\$L	
000871	U459	0500		T	DC	\$B5,<TDER	
000872	U45A	D380	UC85		LNJ	Z*0340*	
000873	U45C	0340			DC	\$B5,<TDER	
000874	U45F	D380	UC85		SD	Z*0342*	
000875	U460	0342			T0300K	\$B5,<TIME	
000876	U462	002C			DC	44	
000877	U463	D380	0E41		LNJ	\$B5,<T10MS	
000878	U465	0003			DC	3	
000879	U466	0F80		T	B	>+\$D	
000880	U467	D380	UC85		LNJ	\$B5,<TDER	
000881	U469	0352			DC	Z*0352*	
000882	U46A	D380	UC50		SD	\$B5,<TAGI	
000883	U46C	D300			DC	Z*D300*	

000684	0460	8280	UFE7		LD	<TEMPA,=Z'02'	TEST FINE BIT
000685	046F	0200		T	BBT	>+\$C	
000686	0470	0500			B	>-\$D	
000687	0471	0FF9			RTCF		TURN CLOCK OFF
000688	0472	0005		\$C	LB	<TEMPA,=Z'04'	TEST SLOPE BIT
000689	0473	8280	UFE7		BBF	>+\$D	
000690	0475	0400			LNJ	\$B5,<TDER	
000691	0476	0580		T	DC	Z'0354'	
000692	0477	0380	UC85		LNJ	\$B5,<TIME	SLOPE ACTIVE
000693	0478	U354		\$D	DC	20	20MS TIME DLY
000694	0479	U380	UL38		LNJ	\$B5,<TAGI	
000695	047A	014			DC	Z'D7'	
000696	047B	8280	UC50		LB	<TEMPA,=Z'2'	ON CYL ?
000697	047C	U700			BBT	>+\$A	
000698	047D	8280	UFE7	T	LB	<TEMPA,=Z'1'	READY ?
000699	047E	0500			BBT	>+\$E	
000700	047F	1000		T	LNJ	\$B5,<TDER	UNIT NOT READY
000701	0480	0380	UC85		DC	Z'0344'	
000702	0481	0344		\$E	LNJ	\$B5,<TDER	READY & ONCYL*
000703	0482	0380	UC85		DC	Z'0346'	READY ?
000704	0483	8280	UFE7		LB	<TEMPA,=Z'1'	
000705	0484	1000			BBT	>+\$A	
000706	0485	0380	UFE7	T	LB	<TEMPA,=Z'1'	
000707	0486	0500			BBT	>+\$E	
000708	0487	1000		T	LNJ	\$B5,<TDER	UNIT NOT READY
000709	0488	0380	UC85		DC	Z'0348'	STOP DEVICE
000710	0489	0344		\$E	CALL	ZV\$1.ZV\$TC,MSSTR	
000711	0490	0380	UC85		BBT	>+\$D	
000712	0491	0500		T	LNJ	\$B5,<TDER	
000713	0492	1000			DC	Z'0348'	
000714	0493	0380	UC85		BBT	>+\$A	
000715	0494	0348		\$D	LNJ	\$B5,<TDER	
000716	0495	8280	UFE7		DC	Z'0346'	
000717	0496	1000			LB	<TEMPA,=Z'1'	
000718	0497	FBC0	0003	X	BBT	>+\$D	
000719	0498	D380	0000		LDR	\$R5,=6	
000720	0499	0F80			BBT	SR5,=Z'0358'	
000721	049A	1120			BBT	>-\$A	
000722	049B	0380	UC50		LNJ	\$B5,<TAGI	
000723	049C	U700			DC	Z'D7'	
000724	049D	8280	UFE7		BBT	<TEMPA,=Z'10'	WAIT FOR READY*
000725	049E	1000			LNJ	BB5,<TDER	
000726	049F	0380	U41		DC	Z'0356'	
000727	04A0	000F		T	BBT	>+\$A	
000728	04A1	0F80			LNJ	\$B5,<TSEC	15 SEC TIMEOUT
000729	04A2	0380	UL41		DC	15	
000730	04A3	0344			BBT	>+\$C	TIMEOUT FAULT
000731	04A4	000F			LNJ	\$B5,<TDER	
000732	04A5	0F80			DC	Z'0356'	
000733	04A6	0380	UC85		BBT	>+\$A	
000734	04A7	0356			LNJ	\$B5,<TAGI	
000735	04A8	0380	UC50		DC	Z'DU'	
000736	04A9	U380	0000		BBT	<TEMPA,=Z'4'	40/80 OR 150/300 MBS ?
000737	04AA	8280	UFE7		LB	<TEMPA,=Z'8'	
000738	04AB	4000			BBT	>+\$D	
000739	04AC	0500		T	LDR	SR5,=6	6 SECS FOR 40/80
000740	04AD	5C06			BBT	SR2,=Z'0358'	DICT ENTRY FOR 40/80
000741	04AE	A570	U358		BBT	>+\$E	
000742	04AF	0380	UC85	T	LDR	SR5,=11	11 SECS FOR 150/300
000743	04B0	0343			BBT	SR2,=Z'0362'	DICT ENTRY FOR 150/300
000744	04B1	0F80			BBT	>+\$C	COUNTER FOR 1 SEC TIMFR
000745	04B2	0380	U362		BBT	=\$R5	STORE DICT ENRY
000746	04B3	0F80			BBT	<T0300J	
000747	04B4	0380	U362		BBT	>+\$D	
000748	04B5	A870	U362		BBT	=\$R5	
000749	04B6	8280	U4CF		BBT	<T0300J	
000750	04B7	8280	UC50		BBT	>+\$E	
000751	04B8	U300			BBT	=\$R5	
000752	04B9	8280	UFE7		BBT	<TEMPA,=Z'4'	
000753	04BA	4000			BBT	>+\$F	TEST START BIT
000754	04BB	0500			BBT	<TEMPA,=Z'8'	WAIT FOR IT TO DROP
000755	04BC	0380	0000	X	BBT	<TEMPA,=Z'8'	OK TURN CLOCK OFF
000756	04BD	0F80			BBT	<TEMPA,=Z'4'	DELAY 1 SEC
000757	04BE	1120			BBT	<TEMPA,=Z'8'	LOOP FOR 6/11 TIMES
000758	04BF	0380	UF12		BBT	<TEMPA,=Z'8'	TEST SPEED
000759	04C0	8280	UFE5		BBT	>+\$D	
000760	04C1	0005			BBT	=\$B5,<TDER	
000761	04C2	0380	UE38		BBT	0	
000762	04C3	0348			BBT	0	
000763	04C4	0380	UC85		BBT	0	
000764	04C5	57F7			BBT	0	
000765	04C6	0380	UC50		BBT	0	
000766	04C7	0300			BBT	0	
000767	04C8	0380	UFE7		BBT	0	
000768	04C9	4000			BBT	0	
000769	04CA	0580		T	BBT	>+\$D	
000770	04CB	0380	UC85		BBT	=\$B5,<TDER	
000771	04CC	0000			BBT	0	
000772	04CD	FBC0	0003		BBT	0	
000773	04CE	D380	0000	X	BBT	0	
000774	04CF	0F80			BBT	0	
000775	04D0	1120			BBT	0	
000776	04D1	0380	UF12		BBT	0	
000777	04D2	8280	UFE5		BBT	0	
000778	04D3	8000			BBT	0	
000779	04D4	05F5			BBT	0	
000780	04D5	0380	UCDV		BBT	0	
000781	04D6	U300			BBT	0	
000782	04D7	0380	UF12		BBT	0	
000783	04D8	0000			BBT	0	
000784	04D9	0F80			BBT	0	
000785	04DA	1120			BBT	0	
000786	04DB	0380	UF12		BBT	0	
000787	04DC	8280	UFE5		BBT	0	
000788	04DD	8000			BBT	0	
000789	04DE	05F5			BBT	0	
000790	04DF	0380	UCDV		BBT	0	
000791	04E0	U300			BBT	0	
000792	04E1	0380	UF12		BBT	0	
000793	04E2	8280	UFE5		BBT	0	
000794	04E3	8000			BBT	0	
000795	04E4	05F5			BBT	0	
000796	04E5	0380	UFEB		BBT	0	
000797	04E6	8280	UFE7		BBT	0	
000798	04E7	8000			BBT	0	
000799	04E8	05F5			BBT	0	
000800	04E9	0380	UC6D		BBT	0	
000801	04EA	8280	UFE7		BBT	0	
000802	04EB	8000			BBT	0	
000803	04EC	05F5			BBT	0	
000804	04ED	0380	UC50		BBT	0	
000805	04EE	0300			BBT	0	
000806	04EF	0380	UF12		BBT	0	
000807	04F0	8280	UFE7		BBT	0	
000808	04F1	8000			BBT	0	
000809	04F2	05F5			BBT	0	
000810	04F3	0380	UFEB		BBT	0	
000811	04F4	8280	UFE7		BBT	0	
000812	04F5	8000			BBT	0	
000813	04F6	05F5			BBT	0	
000814	04F7	0380	UF12		BBT	0	
000815	04F8	8280	UFE7		BBT	0	
000816	04F9	8000			BBT	0	
000817	04FA	05F5			BBT	0	
000818	04FB	0380	UC85		BBT	0	
000819	04FC	0380	UFEB		BBT	0	
000820	04FD	8280	UFE7		BBT	0	
000821	04FE	8000			BBT	0	
000822	04FF	05F5			BBT	0	
000823	04F0	0380	UFEB		BBT	0	
000824	04F1	8280	UFE7		BBT	0	
000825	04F2	8000			BBT	0	
000826	04F3	05F5			BBT	0	
000827	04F4	0380	UFEB		BBT	0	
000828	04F5	8280	UFE7		BBT	0	
000829	04F6	8000			BBT	0	
000830	04F7	05F5			BBT	0	
000831	04F8	0380	UFEB		BBT	0	
000832	04F9	8280	UFE7		BBT	0	
000833	04FA	8000			BBT	0	
000834	04FB	05F5			BBT	0	
000835	04FC	0380	UFEB		BBT	0	
000836	04FD	8280	UFE7		BBT	0	
000837	04FE	8000			BBT	0	
000838	04FF	05F5			BBT	0	
000839	04F0	0380	UFEB		BBT	0	
000840	04F1	8280	UFE7		BBT	0	
000841	04F2	8000			BBT	0	
000842	04F3	05F5			BBT	0	
000843	04F4	0380	UFEB		BBT	0	
000844	04F5	8280	UFE7		BBT	0	
000845	04F6	8000			BBT	0	
000846	04F7	05F5			BBT	0	
000847	04F8	0380	UFEB		BBT	0	
000848	04F9	8280	UFE7		BBT	0	
000849	04FA	8000			BBT	0	
000850	04FB	05F5			BBT	0	
000851	04FC	0380	UFEB		BBT	0	
000852	04FD	8280	UFE7		BBT	0	
000853	04FE	8000			BBT	0	
000854	04FF	05F5			BBT	0	
000855	04F0	0380	UFEB		BBT	0	
000856	04F1	8280	UFE7		BBT	0	
000857	04F2	8000			BBT	0	
000858	04F3	05F5			BBT	0	
000859	04F4	0380	UFEB		BBT	0	
000860	04F5	8280	UFE7		BBT	0</td	

000976	0506	D300		DC	<TEMPA,=Z*8*		
000977	0507	8280	0FE7	LB	<TEMPA,=Z*8*	TEST START BIT	
	0509	8000		BBT	>+\$A	OK	
000978	050A	0500		LNJ	\$B5,<TDER		
000979	050B	D380	0C85	DC	Z*0510*	NO START BIT	
000980	050D	0510		LB	<TEMPA,=Z*1*	TEST DIBIT	
000981	050E	8280	0FE7	\$A	<TEMPA,=Z*4*		
	0510	1000		BBF	>+\$B	OK	
000982	0511	0580		LNJ	\$B5,<TDER		
000983	0512	D380	0C85	DC	Z*0512*	DIBIT FAULT	
000984	0514	0512		LB	<TEMPA,=Z*4*	TEST SPEED BIT	
000985	0515	8280	0FE7	\$B	<TEMPA,=Z*4*		
	0517	4000		BBT	>+\$C	OK	
000986	0518	0500		LNJ	\$B5,<TDER		
000987	0519	D380	0C85	DC	Z*0514*		
000988	051B	0514		LB	<TEMPA,=Z*4*	TEST HEADS LOADED	
000989	051C	8280	0FE7	\$C	<TEMPA,=Z*08*		
	051E	0800		BBT	>+\$D		
000990	051F	0500		LNJ	\$B5,<TDER		
000991	0520	D380	0C85	DC	Z*0516*	HEADS NOT LOADED	
000992	0522	0516		LB	<TEMPA,=Z*2*	TEST BIT 2	
000993	0523	8280	0FE7	\$D	<TEMPA,=Z*2*		
	0525	2000		BBT	>+\$E		
000994	0526	0500		LNJ	\$B5,<TDER		
000995	0527	D380	0C85	DC	Z*0518*	LOAD/RTZ ACTIVE	
000996	0529	0518		LB	<TEMPA,=Z*2*		
000997	052A	D380	0C85	\$E	LNJ	<B5,<TDER	
000998	052C	0520		DC	Z*0520*		
000999	052D	D380	0C52	T0500B	LNJ	<B5,<TAGO	
001000	052F	D280		DC	Z*D280*		
001001	0530	D380	0C50	LNJ	<B5,<TAGI		
001002	0532	D300		DC	Z*D3*		
001003	0533	8280	0FE7	LB	<TEMPA,=Z*2*		
	0535	2000		BBF	>+\$A		
001004	0536	0580		LNJ	\$B5,<TDER		
001005	0537	D380	0C85	DC	Z*0522*	BIT 2 OFF AFTER I/O CMND	
001006	0539	0522		LB	<TEMPA,=Z*2*		
001007	053A	D380	0C50	\$A	LNJ	<B5,<TAG1	
001008	053C	D700		DC	Z*D7*		
001009	053D	8280	0FE7	LB	<TEMPA,=Z*2*	TEST ON CYL BIT	
	053F	2000		BBF	>+\$B		
001010	0540	0560		LNJ	\$B5,<TDER		
001011	0541	D380	0C85	DC	Z*0526*	ON CYLINDER AFTER RTZ	
001012	0543	0526		LB	<TEMPA,=Z*2*	CALL MILLISEC TIMER	
001013	0544	D380	0E3E	\$B	LNJ	400 MILLISECOND TIMEOUT	
001014	0546	0028		DC	40	WHILE TIMING	
001015	0547	UF84		B	>T0500C		
001016	0548	D380	0C85	LNJ	<B5,<TDER		
001017	054A	0546		DC	Z*0546*	FINE BIT TIMEOUT	
001018	054B	D380	0C50	LB	<B5,<TAGI		
001019	054D	D300		DC	Z*D3*		
001020	054E	8280	0FE7	LB	<TEMPA,=Z*2*		
	0550	0200		BBF	>+\$C		
001021	0551	05FA		KTCF	>T0500C		
001022	0552	0005		LB	<TEMPA,=Z*04*	WAIT FOR IT	
001023	0553	8280	0FE7			TURN OFF CLOCK	
	0555	0400		BBF	<TEMPA,=Z*04*	TEST SLOPE BIT	
001024	0556	0580		T	BBF		
001025	0557	D380	0C85	LNJ	>+\$C		
001026	0559	0548		DC	\$B5,<TDER	SLOPE SET	
001027	055A	D380	0E38	LB	Z*0548*		
001028	055C	0019		\$C	LNJ	<B5,<TIME	
001029	055D	D380	0C50	DC	Z*0530*	25MS TIMEOUT	
001030	055F	D700		LNJ	<B5,<TAGI		
001031	0560	8280	0FE7	DC	Z*D7*	ON CYL ?	
	0562	2000		LB	<TEMPA,=Z*2*		
001032	0563	0500	0572	BBT	<T0500G		
001033	0565	D380	0C50	LNJ	<B5,<TAGI	TIMEOUT EXIT	
001034	0567	D300		DC	Z*D3*		
001035	0568	8280	0FE7	LB	<TEMPA,=Z*2*		
	056A	2000		BBT	>+\$C		
001036	056B	0500		LNJ	\$B5,<TDER		
001037	056C	D380	0C85	DC	Z*0528*	NO SEEK COMPL AND BIT 2 LOW	
001038	056E	0528		LB	<TEMPA,=Z*2*		
001039	056F	D380	0C85	\$C	LNJ	<B5,<TDER	
001040	0571	0530		DC	Z*0530*	NO SEEK COMPL WIT BIT 2 HIGH	
001041	0572	D380	0C52	T0500G	LNJ	<B5,<TAGO	
001042	0574	D280		DC	Z*D280*	TAG OUT	
001043	0575	D380	0E38	LNJ	<B5,<TIME	RTZ	
001044	0577	0007		DC	Z*D3*		
001045	0578	D380	0C50	LB	<B5,<TAGI	WAIT 7 MILLSECS	
001046	057A	D700		DC	Z*D7*		
001047	057B	8280	0FE7	LB	<TEMPA,=Z*2*	ON CYLINDER ?	
	057D	2000		BBF	<T0500D		
001048	057E	0580	059E	LB	<TEMPA,=Z*01*	DIAGNOSTIC BIT ?	
001049	0580	8280	0FE7				
	0582	0100		BBT	>+\$A		
001050	0583	0500		LB	<TEMPA,=Z*2*		
001051	0584	8280	0FE7	T	BBT		
	0586	2000		LB	<TEMPA,=Z*2*		
001052	0587	0580		BBF	>+\$U		
001053	0588	D380	0C85	LNJ	\$B5,<TDER		
001054	058A	0536		DC	Z*0536*	ON CYL. NOT CLEARED	
001055	058B	D380	0C85	LB	<B5,<TDER		
001056	058D	0540		\$D	LNJ	<B5,<TDER	
001057	058E	D380	0C50	DC	Z*0540*		
001058	0590	D300		SA	LNJ	<B5,<TAGI	
001059	0591	8280	0FE7	DC	Z*D3*		
	0593	FE00		LB	<TEMPA,=Z*FE*		
001060	0594	0580		BBF	>+\$D		
001061	0595	D380	0D46	LNJ	\$B5,<SBIS		
001062	0597	0001		DC	X*01*	GET SB/IS DATA	
001063	0598	D380	0C85	LB	<B5,<TDER	DONT CARE MASK	
001064	059A	0544		DC	Z*0544*		
001065	059B	D380	0C85	LB	<B5,<TDER	DIAGNOSTIC BIT SET	
001066	059D	0542		\$B	DC	Z*0542*	DIAG STATUS AND NO FAULT BITS
001067	059E	D380	0C50	T0500D	LNJ	<B5,<TAGI	
001068	05A0	D300		DC	Z*D3*		
001069	05A1	8280	0FE7	LB	<TEMPA,=Z*2*		
	05A3	2000		BBF	>+\$C		
001070	05A4	0580		LNJ	\$B5,<TDER		
001071	05A5	D380	0C85	DC	Z*0532*	BIT 2 RESET TOO SOON	
001072	05A7	0532					

001073	05A8	9870	0005	\$C	LDR	\$R1,=5 <IDEN,=Z*4*	SET 5 MS TIME DELAY		
001074	05AA	8280	UFC4		LB		19 SURFACE ?		
001075	05AC	4000					NO		
001076	05AE	9A70	0002		BBF	>+\$U	MAKE 7MS FOR 19 SURFACE		
001077	05B0	9F00	05B4		ADD	\$R1,=2			
001078	05B2	D380	0E38		STR	\$R1,<T0500E			
001079	05B4	0000			LNJ	\$B5,<TIME	FILL IN MILLISECS		
001080	05B5	U380	UC50		DC	0			
001081	05B7	D300			LNJ	\$B5,<TAGI			
001082	05B8	8280	UFE7		DC	Z*D3*			
	05BA	2000			LB	<TEMPA,=Z*2*	BIT 2 ?		
001083	05B9	0500			BBT	>+\$E			
001084	05BC	D380	UC85		LNJ	\$B5,<TDER			
001085	05BE	U534			DC	Z*0534*	ON CYL NOT RESET BY RTZ		
001086	05BF	9870	000A		SE	STR	SET 10 MS DELAY		
001087	05C1	8280	UFC4		LDK	\$R1,=10	19 SURFACE ?		
	05C3	4000			LB	<IDEN,=Z*4*			
001088	05C4	0580			BBF	>+\$F	NO		
001089	05C5	9A70	0003		ADD	\$R1,=3	MAKE 13MS FOR 19 SURF.		
001090	05C7	9F00	05CB		STR	\$R1,<T0500F			
001091	05C9	D380	0E38		LNJ	\$B5,<TIME	DELAY TIMER		
001092	05CA	0000			DC	0	TIME DELAY		
001093	05CB	D380	UC50		LNJ	\$B5,<TAGI			
001094	05CE	U700			DC	Z*D7*			
001095	05CF	8280	UFE7		LB	<TEMPA,=Z*2*	ON CYLINDER ?		
	05D1	2000			BBT	>+\$A			
001096	05D2	0500			LNJ	\$B5,<TDER			
001097	05D3	D380	UC85		DC	Z*0538*	ON CYL TIMEOUT		
001098	05D5	0538			SE	STR	END TEST 0500		
001099	05D6	D380	UCDD		LNJ	\$B5,<TDTE	END TEST 0500		
001100	05D8	0500			DC	Z*0500*			
001101	*	*							
001102	*	*							
001103	*	*							
001104	*	*							
001105	*	*							
001106	*	*							
001107	*	*							
001108	*	*							
001109	*	*							
001110	*	*							
001111	*	*							
001112	05D9	8700	UFC2		T0601	CL	<FLAG		
001113	05D0	8700	UFC1			CL	<ERFL		
001114	05D1	9870	0601			LDK	\$R1,=Z*0601*	LOAD TEST NUMBER	
001115	05D2	9F00	UFE8			STR	\$R1,<TEST	*START TEST*	
001116	05E1	D380	UC6D			LNJ	\$B5,<TDST	FAULT RESET/READY CHECK	
001117	05E3	D380	UC27			LDV	\$B5,<FRRC		
001118	05E5	5C01				LNJ	\$R5,=1	SET LOOP COUNT = 2	
001119	05E6	D3C0	066B			LDV	\$B5, TAGO	TAG OUT	
001120	05E8	D120				DC	Z*D120*	SET OFFSET +	
001121	05E9	D380	UC50			T0601B	LNJ	<TEMPA,=Z*20*	ON CYL ?
001122	05ED	U700				DC	Z*D7*		
001123	05EC	8280	UFE7			LB			
	05EE	2000				BBT	>+\$A	NO	
001124	05EF	0580				LB	<ERFL,=X*02*	SET CONTINUE FLAG	
001125	05F0	8900	UFC1			BBF			
001126	05F2	0002				LNJ	\$B5,<TDER	ON CYL	
001127	05F3	D380	UC85			DC	Z*0512*		
001128	05F5	0612				SA	LNJ	\$B5,<TAGI	
001129	05F6	D380	OC50			DC	Z*D7*		
001130	05F8	D700				LB	<TEMPA,=Z*02*	OFFSET ?	
	05FB	0200				BBT	>+\$D	YES	
001131	05FC	0500				LNJ	\$B5,<TDER1		
001132	05FD	D380	UC81			DC	Z*0614*	NO OFFSET	
001133	05FF	0614				SE	LB	<TEMPA,=Z*20*	ON CYL ?
001134	0600	8280	UFE7			BBF			
	0602	2000				LNJ	>+\$H	NO	
001135	0603	0580				DC	\$B5,<TDER1	ONCYL	
001136	0604	D380	UCB1			SE	Z*0616*	CLEAR CONT. FLAG	
001137	0606	0616				LB	<ERFL,=X*02*	NORMAL PATH	
001138	0607	8800	UFC1			BBF			
	0609	0002				LNJ	>+\$C		
001139	060A	0580				DC	\$B5,<TDTE		
001140	060B	D380	UCDD			SE	Z*0601*		
001141	060D	0601				B	<START		
001142	060E	0F80	0100			LNJ	\$B5,<TIME	WAIT 3 MILLISECS	
001143	0610	D380	0E38			DC	3		
001144	0612	0003				LNJ	\$B5,<TAGI		
001145	0613	D380	UC50			DC	Z*D7*		
001146	0615	U700				LB	<TEMPA,=Z*02*	OFFSET ?	
001147	0616	8280	UFE7			BBT	>+\$U	YES	
	0618	0200				LNJ	\$B5,<TDER		
001148	0619	0500				DC	Z*0620*	NO OFFSET	
001149	061A	D380	UC85			SE	<TEMPA,=Z*20*	ON CYL ?	
001150	061C	0620				BBF			
001151	061D	8280	UFE7			LNJ	>+\$E	YES	
	061F	2000				DC	\$B5,<TDER	NOT ON CYL	
001152	0620	0500				SE	Z*0622*	COUNT = 2 ?	
001153	0621	D380	UC85			B	\$R5,<+\$G	LEAVE LOOP	
001154	0623	0622				LNJ	<T0601C	NO	
001155	0624	5700	0000			SG	\$B5,<TAGO	CLR ERROR RECV LATCHES	
001156	0626	0F80	063A			DC	Z*D208*		
001157	0628	D380	UC52			LNJ	\$B5,<TAGI		
001158	062A	D208				DC	Z*D7*		
001159	062B	D380	UC50			LB	<TEMPA,=Z*02*	OFFSET ?	
001160	062D	D700				BBF		NO	
001161	062E	8280	UFE7			LNJ	>+\$F	OFFSET ACTIVE	
	0630	0200				DC	\$B5,<TDER		
001162	0631	0580				SE	Z*0624*		
001163	0632	D380	UC85			B	<T0601B	SET OFFSET-	
001164	0634	0624				LNJ	>-\$1	REPEAT WITH - OFFSET	
001165	0635	D380	UC52			DC	\$B5,<SKCYS	SEEK	
001166	0637	D110				DC	0	CYL ZERO	
001167	0638	0F80	05E9			SA	IOLD	SETUP INPUT TASK	
001168	063A	0F7F							
001169	063B	D380	0DC3						
001170	063D	0000							
001171	063E	8180	UFE7						
0640	0000	11F0							
0642	0000	0FAF							

001172	U644	07FA		\$B	BIOF IO	>-\$A =Z'D6',<OTTASK	TAG 6	
001173	U645	8070	D600		BIOF L NJ	>-\$B \$B5,<ISTAT1	STALL	
	U647	0000	11FD		LB	<TEMPA,=Z'02'	OFFSET ?	
001174	U649	07FC					NO	
001175	U64A	D380	0F12		BBF L NJ	>+\$A \$B5,<TDER	OFFSET STILL ACTIVE	
001176	U64C	8280	0FE7		DC	Z'0626'	ON CYL ?	
	U64E	0200			LB	<TEMPA,=Z'20'		
001177	U64F	0580		T	BBT L NJ	>+\$A \$B5,<TDER	YES	
001178	U650	D380	0C85		DC	Z'0632'	ON CYL NOT ACTIVE	
001179	U652	0626			L NJ	\$B5,<TAGO	SET OFFSET+	
001180	U653	8280	0FE7-		DC	Z'D120'	CONTROL TAG	
	U655	2000			L NJ	\$B5,<TAGI	OFFSET ?	
001181	U656	0500		T	DC	Z'D7'		
001182	U657	D380	0C85		LB	<TEMPA,=Z'02'		
001183	U659	0632			BBT L NJ	>+\$C \$B5,<TDER		
001184	U65A	D380	0C52		DC	Z'0632'		
001185	U65C	D120			L NJ	\$B5,<TAGO		
001186	U65D	D380	0C50		DC	Z'D120'		
001187	U65F	D700			L NJ	\$B5,<TAGI		
001188	U660	8280	0FE7		DC	Z'D7'		
	U662	0200			LB	<TEMPA,=Z'02'		
001189	U663	0500		T	BBT L NJ	>+\$U \$B5,<TDER	OFFSET NOT ACTIVE	
001190	U664	D380	0C85		DC	Z'0634'	RTZ	
001191	U666	0634			L NJ	\$B5,<TAGO	DELAY	
001192	U667	D380	0C52		DC	Z'D280'	4MS	
001193	U669	D280			L NJ	\$B5,<TIME	CONTROL TAG	
001194	U66A	D380	0E38		DC	4	OFFSET ?	
001195	U66C	0004			L NJ	\$B5,<TAGI		
001196	U66D	D380	0C50		DC	Z'D7'		
001197	U66F	D700			LB	<TEMPA,=Z'02'		
001198	U670	8280	0FE7					
	U672	0200						
001199	U673	0580		T	BBF L NJ	>+\$A \$B5,TDER	OFFSET STILL ACTIVE	
001200	U674	D3C0	0610		DC	Z'0636'	450 MS DELAY	
001201	U676	0636			L NJ	\$B5,<TIME	SEEK	
001202	U677	D380	0E38		DC	450	CYL 823 (ILLEGAL)	
001203	U679	01C2			L NJ	\$B5,<SKCYS	SETUP FOR INPUT	
001204	U67A	D380	0DC3		DC	823		
001205	U67C	0337			IOLD	<TEMPA,<IOREAD,<D-1		
001206	U67D	8180	0FE7					
	U67F	0000	11F6					
001207	U681	0000	UFAF					
001208	U683	07FA						
001209	U684	8070	D600		\$A	BIOF IO	>-\$F =Z'D6',<OTTASK	
001210	U686	0000	11FD				TAG 6	
	U688	07FC						
001211	U689	8000	0FE8		\$B	BIOF IO	>-\$A <TEMPB,<INSTW1	
001212	U68D	0000	1204				STALL	
	U68E	8280	0FE7					
	U690	0100						
001213	U691	0500		T	BBT L NJ	>+\$J \$B5,<TDER	DIAG NOT ACTIVE	
001214	U692	D380	0C85		DC	Z'0640'		
001215	U694	0640			L NJ	\$B5,<TAGI		
001216	U695	D380	0C50		DC	Z'D6'		
001217	U697	D600			LB	<TEMPA,=Z'20'	ON CYL ?	
001218	U698	8280	0FE7					
	U69A	2000						
001219	U69B	0500		T	BBT L NJ	>+\$K \$B5,<TDER	NOT ON CYL	
001220	U69C	D380	0C85		DC	Z'0644'		
001221	U69E	0644			L NJ	\$B5,<TAGO	RTZ	
001222	U69F	D380	0C52		DC	Z'D280'	END TEST	
001223	U6A1	D280			L NJ	\$B5,<TDTE		
001224	U6A2	D380	0CDD		DC	Z'0601'		
001225	U6A4	0601						
001226	*	*	*	*	*	*	*	
001227	*	*	*	*	*	*	*	
001228	*	*	*	*	*	*	*	
001229	*	*	*	*	*	*	*	
001230	*	*	*	*	*	*	*	
001231	*	*	*	*	*	*	*	
001232	*	*	*	*	*	*	*	
001233	*	*	*	*	*	*	*	
001234	*	*	*	*	*	*	*	
001235	U6A5	8700	0FC2	X	T0602	CL	<FLAG	
001236	U6A7	8700	0FC1			CL	<ERFL	
001237	U6A9	9870	0602			LDR	SRI,=Z'0602'	
001238	U6AB	9F00	0FEB			STR	SRI,<TEST	
001239	U6AD	D380	0C6D			L NJ	\$B5,<IDST	
001240	U6AF	9B80	1211			LAB	\$B1,<SA10DV	
001241	U6B1	9F80	000A			STB	\$B1,<ZHISAZ+10*\$AF	
001242	U6B3	D380	0C52			L NJ	\$B5,<TAGO	
001243	U6B5	D238			DC	Z'D238'	CLR DIAG,FAULT,ERR RECOVERY	
001244	U6B6	D380	0C50			L NJ	\$B5,<TAGI	
001245	U6B8	D700			DC	Z'D7'	CONTROL TAG	
001246	U6B9	8280	0FE7			LB	<TEMPA,=Z'01'	UNIT READY ?
	U6B9	1000						
001247	U6BC	0500		T	BBT L NJ	>+\$A \$B5,<TDER	UNIT NOT READY	
001248	U6BD	D380	0C85		DC	Z'0608'	DIAG CHECK	
001249	U6BF	0608			LB	<TEMPA,=Z'01'		
001250	U6C0	8280	0FE7					
	U6C2	0100						
001251	U6C3	0580		T	BBF L NJ	>+\$B \$B5,<TAGI	NO	
001252	U6C4	D380	0C50		DC	Z'D2'	GET DIAG DATA	
001253	U6C6	D200			L NJ	\$B5,<SBIS	GET IS/SB DATA	
001254	U6C7	D380	0D46		DC	X'1'	TEST ALL BITS EXCEPT PROTECT	
001255	U6C9	0001			L NJ	\$B5,<TDER	DIAG FAULTS	
001256	U6CA	D380	0C85		DC	Z'0610'	SETUP LV10 RUPT	
001257	U6CC	0610			L NJ	\$B5,<LV10SU	TIMING PROCESS	
001258	U6CD	D380	0E91		DC	<LV10TI		
001259	U6CF	0E82			L NJ	\$B5,<TAGO	RECALIBRATE	
001260	U6D0	D380	0C52		DC	Z'0'	400MS LIMIT	
001261	U6D2	0000			L NJ	\$B5,<TIMDE		
001262	U6D3	D380	0E9E		DC	400		
001263	U6D5	0190			B	>+\$C		
001264	U6D6	0F80		T	L NJ	\$B5,<TDER		
001265	U6D7	D380	0C85		DC	Z'0646'	RTZ TIMEOUT >400MS	
001266	U6D9	0646			L NJ	\$B5,<TAGI		
001267	U6DA	D380	0C50		DC	Z'D7'		
001268	U6DC	D700			LB	<TEMPA,=Z'20'	ON CYL ?	
001269	U6DD	8280	0FE7					
	U6DF	2000						
001270	U6E0	0500		T	BBT	>+\$A		

001271 06E1 D380 UC85 LNJ \$B5,<TDER
 001272 06E3 0648 DC Z*0648'
 001273 06E4 8280 UF67 SA LB <TEMPA,=Z*01' NOT ON CYL
 001274 06E6 0100 T BBT >+\$U
 001275 06E8 0380 UC85 LNJ \$B5,<TDER
 001276 06EA 0650 DC Z*0650'
 001277 06EB 9870 U335 SD LDR \$R1,=821
 001278 06ED 8280 UFC4 LB <IDEN,=Z*8' CYL 821
 001279 06F0 8000 T BBT >+\$E
 001280 06F1 0500 LNJ SR1,1
 001281 06F2 1041 STR SR1,<CYADMX
 001282 06F4 1C01 LDV SR1,=1
 001283 06F5 9F00 UFAD T0602B STR SR1,<CYAD
 001284 06F7 9F00 U000 T STR SR1,<+\$D
 001285 06F9 D380 OE91 LNJ SB5,<LV10SU
 001286 06FB 0E82 DC <LV10TI
 001287 06FC 8755 CL =\$R0
 001288 06FD 8756 CL =\$R0
 001289 06FF EF57 STR SR6,=\$R7
 001290 2C07 LDV SR2,=7
 001291 0700 DB80 070E LAB SB5,<T0602D
 001292 0702 DF80 UEBD STB SB5,<TIMDE5
 001293 0704 DB80 UEAS LAB SB5,<TIMDE1
 001294 0706 DF80 0E7F STB SB5,<IMB5
 001295 0708 D380 UDC6 LNJ SB5,<SKCYF
 001296 070A 0000 DC 0
 001297 070B D380 UE9E LNJ SB5,<TIMDE
 001298 070D 0007 DC 7
 001299 070E DF80 T T0602D B >+\$E
 001300 070F D380 UC85 LNJ SB5,<TDER
 001301 0711 0652 DC Z*0652'
 001302 0712 5D01 SE CMV \$R5,=1
 001303 0713 0F7F NUP >-\$1
 001304 0714 0300 T SA BG >+\$E
 001305 0715 D380 UC50 LNJ SB5,<TAGI
 001306 0717 D300 DC Z*D3'
 001307 0718 9800 UFAD LDR \$R1,<CYAD
 001308 071A 8280 UF67 LB <TEMPA,=Z*04' TEST SLOPE BIT
 001309 071C 0400
 001310 071D 0500 T BBT >+\$E
 001311 071E 1B80 BUDD SR1,>+\$C
 001312 071F 0F80 B >+\$U
 001313 0720 1BFF T \$B BUDD SR1,>+\$D
 001314 0721 D380 UC85 SC LNJ SB5,<TDER
 001315 0723 0678 DC Z*0678'
 001316 0724 D380 UC85 SD LNJ SB5,<TDER
 001317 0726 0680 DC Z*0680'
 001318 0727 D380 UC50 SE LNJ SB5,<TAGI
 001319 0729 D700 DC Z*D7'
 001320 072A 8280 UF67 LB <TEMPA,=Z*20' ON CYL ?
 001321 072C 2000
 001322 072D 0500 T BBT >+\$E
 001323 072E D380 UC85 LNJ SB5,<TDER
 001324 0730 0654 DC Z*0654'
 001325 0731 8280 UF67 SE LB <TEMPA,=Z*01' NOT ON CYL
 001326 0733 0100
 001327 0734 0580 T BBT >+\$F
 001328 0735 D380 UC50 LNJ SB5,<TAGI
 001329 0737 D200 DC Z*D2'
 001330 0738 D380 UD46 LNJ SB5,<BIS
 001331 073A 0001 DC X*1'
 001332 073B D380 UC85 LNJ SB5,<TDER
 001333 073D 0656 DC Z*0656'
 001334 073E 9800 UFAD LDR \$R1,<CYAD
 001335 0740 8280 UFC2 LB <FLAG,X*01' SECOND PASS ?
 001336 0742 0001 T BBT >+\$A
 001337 0743 0500 SA ADV \$R1,=-1
 001338 0744 8AD1 INC \$R1,=T0602B
 001339 0745 9900 UFAD BDEC \$R1,=T0602B
 001340 0747 0380 UF65 T0602C LNJ \$B5,*TDTE
 001341 0751 0602 DC Z*0602'
 *
 *
 *
 * TEST 0603 RANDOM SEEK TEST
 *
 *
 001347 0752 8700 UFC2 10603 CL <FLAG
 001348 0754 8700 UFC1 CL <ERFL
 001350 0756 8756 CL =\$R0
 001351 0757 8757 CL =\$R7
 001352 0758 8D00 UF68 SDI <TEMPC
 001353 075A 9870 0603 LDR \$R1,=Z*0603'
 001354 075C 9F00 UF68 STR \$R1,=TEST
 001355 075E D380 UC6D LNJ \$B5,*TDST
 001356 0760 D380 UC27 LNJ \$B5,*FRRC
 001357 0762 D380 UC52 LNJ \$B5,*TAGO
 001358 0764 D280 DC Z*D280'
 001359 0765 D380 UE38 LNJ SB5,*TIME
 001360 0767 015E DC 350
 001361 0768 D380 UC50 LNJ SB5,*TAGI
 001362 076A D700 DC Z*D7'
 001363 076B 8280 UF67 LB <TEMPA,=Z*20' ON CYL ?
 001364 076C 2000
 001365 076E 0500 T BBT >+\$A
 001366 076F D380 UC85 LNJ SB5,<TDER
 001367 0771 0660 DC Z*0660'
 001368 0772 8280 UF67 SA LB <TEMPA,=Z*01' ON CYL NOT SET
 001369 0774 0100 T BBT >+\$D
 001370 0775 0580 LNJ SB5,<TDER
 001371 0776 D380 UC85 DC Z*0662'
 001372 0778 0662 LAB \$B1,*SA10DV
 001373 0779 9880 1211 STB \$B1,*ZHISAZ+10*\$AF
 001374 077D A870 U8F0 LDR \$R2,=-10000
 001375 077F AF00 UFCE STR \$R2,=<LPCNT TOTAL SEEKS STORE (-) LOOP COUNTER

001375	0781	AF00 0FCF	T0603B STR	\$R2,<LPCUR	ALSO IN CURRENT COUNTER	
001376	0783	FBC0 0003	T0603A CALL	ZV\$FRK,TEMPA,D-1	GET RANDOM NUMBER	
	0785	D380 0000	X			
	0787	0F80				
	0788	0FE7				
	0789	0FAF				
001377	078A	9800 0FE7	LDR	\$R1,<TEMPA		
001378	078C	9570 03FF	AND	\$R1,=X'03FF'	CLEAR HI BITS	
001379	078E	9970 0336	CMR	\$R1,=822		
001380	0790	0280 0783	BGE	<T0603A	TOO BIG	
001381	0792	8280 0FC4	LB	<IDEN,=Z'8000'	822 CYL ?	
	0794	8000	T	BBT	YES	
001382	0795	0500		SUK	CUT IN HALF FOR 410 CYL	
001383	0796	1041	T	STR		
001384	0797	9F00 0000	\$D	\$R1,<+SC	SETUP LEV10 RUPT	
001385	0799	D380 0E91		LNJ	HERE	
001386	079B	0E80		DC	SEEK TO	
001387	079C	D380 0DC6		LNJ	THIS CYL ADDRESS	
001388	079E	0000	\$C	DC	TIME SEEK	
001389	079F	D380 0L9E		LNJ	100MS MAX.	
001390	07A1	0064		DC	OK	
001391	07A2	0F80	T	B	SET CONTINUE FLAG	
001392	07A3	8900 0FC1		LBT	<ERFL,=X'02'	
	07A5	0002		LNJ	\$B5,<TDER	
001393	07A6	D380 UC85		DC	Z'0666'	SEEK TIMEOUT
001394	07A8	0664		LNJ	\$B5,<TAGI	
001395	07A9	D380 UC50	\$D	DC	Z'D7'	
001396	07AB	D700		LB	<TEMPA,=Z'20'	ON CYL ?
001397	07AC	8280 0FE7				
	07AD	2000	T	BBT	>+\$L	
001398	07AE	0500		LNJ	\$B5,<TDER1	
001399	07B0	D380 UC81		DC	Z'0666'	NOT ON CYL
001400	07B2	0666		LB	<TEMPA,=Z'01'	DIAG. BIT ?
001401	07B3	8280 0FE7	\$E			
	07B5	0100	T	BBF	>+\$H	
001402	07B6	0580		LNJ	\$B5,<TAGI	TEST FAULTS
001403	07B7	D380 UC50		DC	Z'D2'	DONT CARE BIT
001404	07B9	D200		LNJ	\$B5,<SBIS	PRINT FAULT BITS
001405	07BA	D380 UD46		DC	Z'01'	CLEAR CONTINUE FLAG
001406	07BC	0001		LNJ	\$B5,<TDER1	
001407	07BD	D380 UC81		DC	Z'0668'	
001408	07BF	0668		LB	<ERFL,=X'02'	
001409	07C0	8800 0FC1	\$H			
	07C2	0002	T	BBF	>+\$F	NORMAL RETURN
001410	07C3	0580		LNJ	\$B5,<TDTE	END CONTINUE MODE
001411	07C4	D380 UCDD		DC	Z'0603'	
001412	07C6	0603		LNJ	<START	
001413	07C7	UF80 0100	\$F	DC	\$B5,<QUIT	HIT BRK TO STOP
001414	07C9	D380 UD33		DC	Z+\$G	NO BREAK
001415	07CB	0000		LNJ	<+\$A	GO HERE ON BRK
001416	07CC	0000		DC	SR2,<LPCUR	GET CURRENT COUNT
001417	07CD	A800 UFCF	\$G	BINC	SR2,<T0603B	DO NEXT SEEK
001418	07CF	2780 0781				GET STORED TIME LOOPS
001419	07D1	8C80 0FE8	\$A	LDI	<TEMPC	GET AVERAGE TIME
001420	07D3	D380 0EE8		LNJ	\$B5,<TMAVE	DO A LINEFEED
001421	07D5	D380 0F7B		LNJ	\$B5,<CRLF	PRINT AVE TIME
	07D7	FBC0 0003		CALL	ZV\$IH.ZV\$TD,AVETM	
	07D9	D380 0000	X			
	07DB	0F80				
	07DC	0FA7				
001423	07D0	FBC0 0003		CALL	ZV\$1,MSKTM	SEEK TIME
	07DF	D380 0000	X			
	07E1	UF80				
	07E2	10A5				
001424	07E3	D380 UCDD		LNJ	\$B5,<TDTE	EMD TEST
001425	07E5	0603		DC	Z'0603'	=====
001426	*					
001427	*					
001428	*					
001429	*					
001430	*					
001431	*					
001432	*					
001433	07E6	8700 0FC2	T0604	CL	<FLAG	
001434	07E8	8700 0FC1		CL	<ERFL	
001435	07EA	8756		CL	=\$R0	
001436	07EB	8757		CL	=\$R7	
001437	07EC	8D00 0FE8		SDI	<TEMPB	CLEAR TEMPB,TEMPC
001438	07EE	9870 0604		LDR	SR1,=T0604'	TEST NUMBER
001439	07F0	9F00 0FE8		STR	SR1,<TEST	
001440	07F2	D380 0E6D		LNJ	\$B5,<TDST	'START TEST'
001441	07F4	9B80 1211		LAB	SB1,<SA10DV	
001442	07F6	9F80 000A	X	STB	SB1,<CHISAZ+10*\$AF	GET CYL ADDRESS
001443	07F8	D380 UD58		LNJ	SB5,<GETCY	SETUP LEV10
001444	07FA	D380 0E91		DC	SB5,<LV10SU	FOR THIS PROCESS
001445	07FC	0E80		LNJ	<LV10TM	SEEK
001446	07FD	D380 UC6		DC	\$B5,<SKCYF	TO CYL 0
001447	07FF	0000		DC	0	TIME TILL DEV RUPT
001448	0800	D380 0E9E		LNJ	\$B5,<TIMDE	MAX TIME = 125 MS
001449	0802	007D		DC	125	TIME IS OK
001450	0803	0F80	T	B	>+\$C	XXXXXX
001451	0804	0F7F		NOP	\$-1	TIMEOUT RTN
001452	0805	D380 UC50		LNJ	\$B5,<TAGI	GET FAULT DATA
001453	0807	D300		DC	Z'D3'	SB1/IS DATA
001454	0808	D380 UD46		LNJ	\$B5,<SBIS	DONT CARE BITS
001455	080A	00F9		DC	X'F9'	SEEK TIMEOUT
001456	080B	D380 UC85		LNJ	\$B5,<TDER	XXXXXX
001457	080D	0674		DC	Z'0674'	
001458	080E	0F7F		SC	>-\$1	
001459	080F	D380 UC50		NOP	DC	
001460	0811	D600		LNJ	\$B5,<TAGI	
001461	0812	8280 0FE7		DC	Z'D6'	
	0814	2000		LB	<TEMPA,=Z'20'	ON CYL ?
001462	0815	0500	T	BBT	>+\$D	YES
001463	0816	D380 UC85		LNJ	\$B5,<TDER	ON CYL NOT SET
001464	0818	0672		DC	Z'0672'	GET SELECTED CYL
001465	0819	9800 UFAD		STR	SR1,<CYAD	
001466	081B	9F00 0000	T	LNJ	SR1,<+\$E	SET UP LV10
001467	081D	D380 0E91		DC	\$B5,<LV10SU	FOR THIS PROCESS
001468	081F	0E80				

001469	0820	8700	0F88		CL	<TEMPB	CLEAR TIME ACCUMULATORS	
001470	0822	8700	0FE9		CL	<TEMPC		
001471	0824	D380	0DC6		LNJ	\$B5,<SKCYF	SEEK	
001472	0826	0000		\$L	DC	0	SELECTED CYL	
001473	0827	D380	0E9E		LNJ	\$B5,<TIME	TIME SEEK	
001474	0829	007D			DC	125	MAX TIME	
001475	082A	0F80		T	B	>+\$1	OK	
001476	082B	0F7F			NUP	>\$-1	XXXXX	
001477	082C	D380	0C50		LNJ	\$B5,<TAGI	TIMEOUT TO HERE	
001478	082E	D300			DC	Z'D3'	GET FAULT DATA	
001479	082F	D380	0D46		LNJ	\$B5,<SBIS	SB/IS DATA	
001480	0831	00F9			DC	X'F9'	DONT CARE BITS	
001481	0832	D380	0C85		LNJ	\$B5,<TDER	SEEK TIMEOUT	
001482	0834	0674		\$F	DC	Z'0674'	XXXXX	
001483	0835	0F7F			NUP	>\$-1		
001484	0836	D380	0C50		LNJ	\$B5,<TAGI		
001485	0838	D600			DC	Z'D0'		
001486	0839	8280	0F87		LB	<TEMPA,=Z'20'	ON CYL	
001487	083C	0500		T	BBT	>+\$A	YES	
001488	083D	D380	0C85		LNJ	\$B5,<TDER	ON CYL NOT SET	
001489	083F	0676			LC	Z'0676'	INH PRINT ?	
001490	0840	8280	0FC5	\$A	LB	<IPFL,=X'1'		
001491	0842	0001			BBT	>+\$0	YES	
001492	0843	0500		T	CALL	ZV\$1.ZV\$QC,MSLP	LOOP ?	
001493	0844	FBCU	0003					
001494	0846	D380	0000	X				
001495	0848	0F80						
001496	0849	10B0						
001497	084A	D380	0D0B		LNJ	\$B5,<YSNO	Y OR N ?	
001498	084C	0857			DC	<T0604C	NO	
001499	084D	084E			DC	<+\$AF	YES	
001499	084E	8900	0FC5		LB	<IPFL,=X'1'	TURN ON INH PRINT	
001500	0850	0001						
001501	0851	D380	0D33	\$B	LNJ	\$B5,<QUIT	CHECK FOR BREAK	
001502	0853	07FA			DC	<T0604D	KEEP LOOPING	
001503	0854	0855			DC	<+\$AF	GET OUT	
001504	0855	8700	0FC5		CL	<IPFL	RESET INH PRINT	
001505	0856	D380	0CDD		T0604C	\$B5,<TDTE	END TEST	
001506	0857	0604			DC	Z'0604'		
001507	0858			*				
001508	0859	085A	8700	0FC2	T0700	CL		
001509	085C	8700	0FC1		CL	<FLAG		
001510	085E	8756			CL	<ERFL		
001511	085F	8757			CL	=\$R6		
001512	0860	8D00	0F88		SDI	=\$R7		
001513	0862	8D00	0FEC		SDI	<TEMPB		
001514	0864	9870	0700		LDK	<TMFWD	CLEAR TEMPB, IEMPC	
001515	0866	9F00	0FEB		STR	\$R1,=Z'0700'	CLR TMFWD, TMRLV MAX FWD/REV TIME	
001516	0868	D380	0C6D		LNJ	\$R1,<TEST	TEST NUMBER	
001517	0869	9B80	1211	X	LAB	\$B5,<TDST		
001518	086A	9B80	000A		STB	\$B1,<SA10DV	'START TEST'	
001519	086C	9F80	000A		STB	\$B1,<ZHISAZ+10*\$AF	ENB LEV 10	
001520	086E	D380	0C52		LNJ	\$B5,<TAGU		
001521	0870	D280			DC	Z'D280'	RTZ	
001522	0871	D380	0E38		LNJ	\$B5,<TIME	400 MS DELAY	
001523	0873	0190			DC	400		
001524	0874	D380	0C50		LNJ	\$B5,<TAGI		
001525	0876	D700			DC	Z'D7'		
001526	0877	8280	0F87		LB	<TEMPA,=Z'20'	ON CYL ?	
001527	0879	2000						
001528	087A	0500		T	BBT	>+\$A	YES	
001529	087B	D380	0C85		LNJ	\$B5,<TDER	ON CYL NOT ACTIVE	
001530	087D	0714			DC	Z'0714'	ENTER LOOP COUNT	
001531	087E	FBCU	0003		\$A	CALL	ZV\$1.ZV\$TC,MSLCT	
001532	0880	D380	0000	X				
001533	0882	0F80						
001534	0883	10B0						
001535	0884	FBCU	0003			CALL	ZV\$1D,LPCNT	INPUT LOOP COUNT
001536	0886	D380	0000	X				
001537	0888	0F80						
001538	0889	8200	0FCE					
001539	088C	A800	0FCE		NEG	<LPCNT	MAKE IT NEGATIVE	
001540	088E	AF00	0FCF		LDR	\$R2,<LPCNT	PUT IN CURRENT COUNTER	
001541	0890	D380	0C50		STR	\$R2,<LPCUR		
001542	0892	D700			T0700A	LNJ		
001543	0893	8280	0F87		DC	\$B5,<TAGI		
001544	0895	0100			LB	Z'D7'	DIAGNOSTIC ?	
001545	0896	05B0		T	BBF	>+\$C	NO	
001546	0897	D380	0C50		LNJ	\$B5,<TAGI		
001547	0899	D200			DC	Z'D2'		
001548	08A0	8280	0F87		LB	<TEMPA,=Z'FE'	ANY BITS ?	
001549	08A1	FE00						
001550	08A2	0500		T	BBT	>+\$B	YES	
001551	08A3	D380	0C85		LNJ	\$B5,<TDER	NO DIAG BITS SET	
001552	08A4	0716			DC	Z'0716'	SB/IS DATA	
001553	08A5	D380	0D46	\$B	LNJ	\$B5,<SBIS	CHECK ALL BITS EXCFPT PROTECT	
001554	08A6	0001			DC	X'01'		
001555	08A7	D380	0C85		LNJ	\$B5,<TDER	DISPLAY FAULT BITS	
001556	08A8	0718			DC	Z'0718'	822 CYLS ?	
001557	08A9	8280	0FC4	\$C	LB	<IDEN,=Z'80'		
001558	08AA	8000						
001559	08AB	0580		T	BBF	>+\$U		
001560	08AC	9870	0336		LDR	\$R1,=822		
001561	08AD	0F80		T	B	>+\$E		
001562	08AE	9870	019A		\$D	\$R1,=410		
001563	08AF	09F0	08B7		STR	\$R1,<T0700B	410 CYLS	
001564	08B0	D380	0E91		LNJ	\$B5,<LV10SU	PUT IN SEEK CMD	
001565	08B1	0E80			DC	<LV10TM	SET LV10 RUPR	
001566	08B2	D380	0DC6		LNJ	\$B5,<SKCYF	LV10 TIMER	
001567	08B3	0000			DC	0	SEEK TO	
001568	08B4	D380	0E9E		LNJ	\$B5,<TIME	THIS CYL	
001569	08B5	00B4			DC	100	TIME DEVICE EVENT	
001570	08B6	08B0		T	B	>+\$A	100 MS MAX TIME	
001571	08B7	D380	0C50		T0700C	LNJ	OK	

001563	U8BE	D200		DC	Z'D20'		
001564	U8BF	8280	UFE7	LB	<TEMPA,=Z'FE'	CHECK DIAG BITS	
001565	U8C1	FE00		BBT	>+\$F	SOME ARE SET	
001566	U8C2	0500		LNJ	\$B5,<TDER	NONE ARE SET	
001567	U8C3	D380	UC85	DC	Z'0722'		
001568	U8C5	0722		LNJ	\$B5,<SBIS	SBVIS DATA	
001569	U8C8	0001		DC	X'01'	TEST ALL BITS EXCEPT PROTECT	
001570	U8C9	D380	UC85	LNJ	\$B5,<TDER	DISPLAY ERROR BITS	
001571	U8CA	0724		DC	Z'0724'	MAX FWD ?	
001572	U8CC	D900	UFEC	CMR	\$R5,<TMFWD	NO	
001573	U8CE	0380		BLE	>+\$B	STORE MAX FWD TIME	
001574	U8CF	DF00	UFEC	STR	\$R5,<TMFWD	SET LEV 10 AGAIN	
001575	U8D1	D380	UE91	LNJ	\$B5,<LV10SU	SEEK TO	
001576	U8D3	0E80		DC	<LV10TM	CYL 0	
001577	U8D4	D380	ODC6	LNJ	\$B5,<SKCYF	TIME DEVICE EVENT	
001578	U8D6	0000		DC	0	100MS MAX	
001579	U8D7	D380	UE9E	LNJ	\$B5,<TIMDE	100	
001580	U8D9	0064		DC	>+\$C	OK	
001581	U8DA	0F80		B	<T0700C	REPORT ERROR	
001582	U8DB	0F80	08BC	CMR	\$R5,<TMREV	MAX REV ?	
001583	U8DD	D900	0FED	BLE	>+\$D	NO	
001584	U8DF	0380		STR	\$R5,<TMREV	STORE MAX REV TIME	
001585	U8E0	D380	0FED	LNJ	\$B5,<QUIT	HIT BREAK TO QUIT	
001586	U8E2	D380	OD33	DC	<+\$E	CONTINUE	
001587	U8E4	0000		DC	<+\$F	EXIT	
001588	U8E5	0000		LDR	\$R2,<LPCUR	GET CURRENT COUNTER	
001589	U8E6	A800	UFCF	DINC	\$R2,<T0700F	LOOP BACK AGAIN	
001590	U8E8	2780	U88E	LNJ	\$B5,<TMAVE	GET AVE TIME	
001591	U8EA	D380	UEBE	SOR	\$R6,1	DIVIDE BY 2	
001592	U8EC	6041		CAD	=\$R0	ROUND RESULT	
001593	U8ED	8E6		STR	\$R6,<AVETM	FOR 2 SEEKS/LOOP	
001594	U8EE	EFO0	0FA7	LNJ	\$B5,<CRLF	DO A LINE FEED	
001595	U8FO	D380	UF7B	CALL	ZV\$TH.ZV\$TD,TMFWD	PRINT FWD TIME	
001596	U8F2	FBC0	0003				
	U8F4	D380	0000	X			
	U8F6	0F80					
	U8F7	0FEC					
001597	U8F8	FBC0	0003		CALL	ZV\$T,MSMXF	MAX FWD TIME
	U8FA	D380	0000	X			
	U8FC	0F80					
	U8FD	10CB					
001598	U8FE	D380	UF7B	LNJ	\$B5,<CRLF		
001599	U900	FBC0	0003	CALL	ZV\$TH.ZV\$TD,TMREV	PRINT REV TIME	
	U902	D380	0000	X			
	U904	0F80					
	U905	0FED					
001600	U906	FBC0	0003		CALL	ZV\$T,MSMXR	MAX REV TIME
	U908	D380	0000	X			
	U90A	0F80					
	U90D	10D6					
001601	U90C	D380	UF7B	LNJ	\$B5,<CRFL		
001602	U90E	FBC0	0003	CALL	ZV\$TH.ZV\$TD,AVETM	PRINT AVE TIME	
	U910	D380	0000	X			
	U912	0F80					
	U913	0FA7					
001603	U914	FBC0	0003		CALL	ZV\$T,MSKTM	AVE SEEK TIME
	U916	D380	0000	X			
	U918	0F80					
	U919	10A5					
001604	U91A	8280	UF4	LB	<IDEN,=Z'CO'	CHECK DEVICE TYPE	
	U91C	C000		BBT	>+\$E		
001605	U91D	0500		CMR	\$R6,=36	IT'S 40 MBS	
001606	U91E	E970	0024	BL	<T0700D	<36 MS	
001607	U920	0200	094C	CMR	\$R6,=40		
001608	U922	E970	0028	BL	<T0700D	>40 MS	
001609	U924	0300	094C	CMR	<T0700E	WITHIN LIMITS	
001610	U926	0F80	094F	BL	<IDEN,=Z'80'		
001611	U928	8280	UF4	BBT	>+\$F		
	U92A	8000		CMR	\$R6,=33	IT'S 150 MBS	
001612	U92B	0500		BL	<T0700D	<33 MS	
001613	U92L	E970	0021	CMR	\$R6,=37		
001614	U92L	0200	094C	BL	<T0700D	>37MS	
001615	U930	E970	0025	CMR	\$R6,=37		
001616	U932	0300	094C	BL	<T0700E		
001617	U934	0F80	094F	CMR	\$R6,=50	IT'S 300 MBS	
001618	U936	8280	UF4	BL	<T0700D	<50MS	
	U938	4000		CMR	\$R6,=56	=<56MS	
001619	U939	0500		BL	<T0700E		
001620	U93A	E970	0032	CMR	\$R6,=50	IT'S 80 MBS	
001621	U93C	0200	094C	BL	<T0700D	<50MS	
001622	U93E	E970	0038	CMR	\$R6,=56	>56MS	
001623	U940	0300	094C	BL	<T0700E		
001624	U942	0F80	094F	CMR	\$R6,=50	IT'S 300 MBS	
001625	U944	E970	0032	BL	<T0700D	<50MS	
001626	U946	0200	094C	CMR	\$R6,=56	=<56MS	
001627	U948	E970	0038	BL	<T0700E		
001628	U94A	0380	094F	T0700U	LNJ	\$B5,<TDER	
001629	U94C	D380	UC85	DC	Z'0728'	AVE TIME FAILED	
001630	U94E	U728		T0700E	LNJ	\$B5,<TDTE	
001631	U94F	D380	UCDD	DC	Z'0700'	TEST END	
001632	U951	0700		*			
001633	*			*			
001634	*			*			
001635	*			*****			
001636	*			* TEST 0900 RPS TEST			
001637	*			*			
001638	*			*			
001639	U952	8700	UF2	T0900	CL	<FLAG	
001640	U954	8700	UFC1	CL	<ERFL		
001641	U956	9870	U900	LDR	\$R1,=2*0900*	TEST NO	
001642	U958	9F00	UFEB	STR	\$R1,<TEST		
001643	U95A	D380	UC6D	LNJ	\$B5,<TDST	START TEST	
001644	U95C	D380	UC27	LNJ	\$B5,<FRRC	FAULT RESET/READY CHECK	
001645	U95E	D380	UC52	LNJ	\$B5,<TAGO		
001646	U960	D580		DC	Z'D580'	LOAD TARGET REG	
001647	U961	D380	UC50	LNJ	\$B5,<TAGI		

001648 U963 D580 DC Z'D580'
 001649 U964 9800 UFE7 LDR \$R1,<TEMPA
 001650 U966 1048 SOK \$R1,8
 001651 U967 9670 0080 XOR \$R1,=X'80'
 001652 U969 1900 BEZ \$R1,>+\$A
 001653 U96A D380 UC85 LNJ \$B5,<TDER
 001654 U96C 0912 DC Z'0912'
 001655 U96D D380 UC52 \$A LNJ \$B5,<TAG0
 001656 U96F D5FF DC Z'D5FF'
 001657 U970 D380 UC50 LNJ \$B5,<TAG1
 001658 U972 D5FF DC Z'D5FF'
 001659 U973 9800 UFE7 LDK \$R1,<TEMPA
 001660 U975 1046 XOR \$R1,8
 001661 U976 9670 00FF BEZ \$R1,>+\$B
 001662 U978 1900 T LNJ \$B5,<TDER
 001663 U979 D380 UC85 DC Z'0924'
 001664 U97B 0924 CB80 0000 T \$B STB \$B4,<+\$C
 001666 U97E D380 UF23 LNJ \$B5,<FLBF
 001667 U980 0FFF DC <X'FFFF
 001668 U981 0001 DC 1
 001669 U982 0000 RESV \$AF,0
 001670 U983 0080 DC 128
 001671 U984 2C01 LDV \$R2,=1
 001672 U985 B800 11FD LDR \$R3,<OTTASK
 001673 U987 D870 D500 LDR \$R5,=Z'D5'
 001674 U989 FB70 FFFF LDR \$R7,=-1
 001675 U98B AC80 UFC7 LDB \$B2,<KKPT
 001676 U98D 9680 0961 LAB \$B1,<T09LV5
 001677 U98F 9F80 120E STB \$B1,<SA5P
 001678 U991 1C09 LDV \$R1,=9
 001679 U992 9F00 0000 X STK \$R1,<ZHRTCI
 001680 U994 9F00 0000 X STR \$R4,<ZHRTCC
 001681 U996 88D1 DEC \$R1
 001682 U997 4C05 LDV \$R4,=5
 001683 U998 CF00 0000 X STK \$R4,<ZHRTCL
 001684 U99A 0004 RTCN CMR \$R1,<ZHRTCC
 001685 U99B 9900 0000 X BL >-\$A
 001686 U99D 027E LDV \$R1,<IOPREAD
 001687 U99E 9800 11F6 LDV \$R4,=63
 001688 U9A0 4C3F * LAST SECT COUNT
 * MUST GO FAST HERE TO GET SECTORS ON FLY
 001690 U9A1 FF02 \$A STK \$R7,\$B2
 001691 U9A2 8182 \$B IULD \$B2,=\$R1,=\$R2
 001692 U9A3 0051
 001694 U9A5 07FL
 001695 U9A6 8055 \$C BIOP IO >-\$B
 U9A7 0053 =\$R5,=\$R3
 001696 U9A8 07FE
 001697 U9A9 F902 \$D BIOP >-\$C
 001698 U9AA 097F CMR \$R7,\$B2
 001699 U9AB E082 BE >-\$D
 001700 U9AC 69F5 LDH \$R6,\$B2
 001701 U9AD 0005 BNE \$R6,>-\$A
 001702 U9AE FF02 RTCF
 001703 U9AF UF80 U9CC STR \$R7,\$B2
 B <T0900B
 *-----
 * KTC RUPT BRINGS US HERE
 *-----
 T09LV5 RTCF NO SECT 0 IN 67MS
 LAB \$B1,<T0900A SETUP FOR
 STB \$B1,<SA15P ERROR REPORT
 LEV =Z'0000+15 AT LEV 15
 HLT B >-\$1
 *-----
 T0900A LB =\$R6,X'C0' ANY OTHER BITS ?
 U9BD 00C0
 001716 U9BC 0580 T BBF >+\$A
 001717 U9BD 70D0 DOK \$R7,16
 001718 U9BE 8900 UFC5 LB1 <IPFL,X'2'
 U9C0 0002
 001719 U9C1 5048 SOK \$R5,8
 001720 U9C2 D570 000F AND \$R5,=X'0F'
 001721 U9C4 UF00 UFE9 STK \$R5,<TEMPC
 001722 U9C6 D380 UC85 LNJ \$B5,<TDER
 001723 U9C8 0994 DC Z'0994'
 001724 U9C9 D380 UC85 \$A LNJ \$B5,<TDER
 001725 U9CB 0990 DC Z'0990'
 001726 U9CC 8182 T0900B EQU \$
 001727 U9CD 0051 \$A IULD \$B2,=\$R1,=\$R2
 U9CE 0052
 001728 U9CF 07F0
 001729 U9D0 8055 \$B BIOP >-\$A
 U9D1 0053 =\$R5,=\$R3
 001730 U9D2 07FE
 001731 U9D3 F902 \$C BIOP >-\$B
 001732 U9D4 097F CMR \$R7,\$B2
 001733 U9D5 C1F2 BE >-\$C
 001734 U9D6 0376 CMH \$R4,+\$B2
 BU >-\$A RECORD 63 ?
 001735
 001736
 001737
 001738
 001739
 001740 U9D7 1C01 SD LDV \$R1,=1 INITIAL SECTOR COUNT
 001741 U9D8 AC80 UFC7 LDB \$B2,<KKPT ADDRESS OF RETURNED DATA
 001742 U9DA A0F2 \$E LDH \$R2,+\$B2 GET STORED DATA
 001743 U9DD A951 CMR \$R2,=\$R1 NEW DATA ?
 001744 U9DC 027L BL >-\$E TRY SOME MORE
 001745 U9DD 0300 BG >+\$F MUST HAVE SKIPPED ONE
 001746 U9DE 8A01 INC =\$R1 NEXT SEQ DATA
 001747 U9DF 1D31 CMV \$R1,=63 LAST DATA ?
 001748 U9E0 UEA7A BL >-\$E GO AGAIN
 001749 U9E1 UFB0 09F0 B <T0900C ALL FINISHED
 001750 U9E3 9F57 STK \$R1,=\$R6 *SB* DATA
 001751 U9E4 AF57 STR \$R2,=\$R7 *IS* DATA
 001752 U9L5 8900 UFC5 LBT <IPFL,X'2' ENABLE *IS*/*SB* PRINTOUT

001753 U9E7 0002
 001754 U9E8 504b
 001754 U9E9 L570 000F
 001755 U9EB DF00 0FE9
 001756 U9ED D380 UC85
 001757 U9EF U992
 001758 U9F0 D380 UCDD
 001759 U9F2 0900

SUR \$R5,8
 AND \$R5,=X'0F'
 STR \$R5,<TEMPC
 LNJ \$B5,<TDER
 DC Z'092'
 T0900C LNJ \$B5,<TDTE
 DC Z'0900'

GET TAG
 GET TAG NO.
 WRONG SECTOR
 END TEST

*

 * TEST 1000
 *

 * WRITE PROTECT TEST
 *

 * TEST 1000
 *

1000 CL <FLAG
 CL <ERFL
 LDR \$R1,=Z'1000'
 STR \$R1,<TEST
 LNJ \$B5,<TDST
 CALL ZV\$T.ZV\$TC,MSPS1

STORE TEST NUMBER
 START TEST
 PROT SWTCH LIT ?

U9FD FBC0 0003
 U9FF D380 0000 X
 UAO1 UF80
 UAO2 10FF
 U01775 UAO3 D380 UD0B
 U01776 UAO5 0000
 U01777 UAO6 0000
 U01778 UAO7 FBC0 0003
 UAO9 D380 0000 X
 UAOB UF80
 UAOC 1117

LNJ \$B5,<YSNO
 DC <+\$A
 DC <+\$D
 CALL ZV\$T.ZV\$TC,MSPS2

Y OR N RESPONSE
 'N' RESPONSE
 'Y' RESPONSE
 'DEPRESS PROT SWTCH

U01779 UAOD FBC0 0003
 UAOE D380 0000 X
 UAOF 10EA
 U01780 UAI3 D380 UD0B
 U01781 UAI5 UAOU
 U01782 UAI6 UAI7
 U01783 UAI7 D380 UC50
 U01784 UAI9 D200
 U01785 UAI9 8280 UFE7
 UAI1 0100
 U01786 UAI1 0500 T
 U01787 UAI2 D380 UC85
 U01788 UAI2 1040
 U01789 UAI21 FBC0 0003
 UAI23 D380 0000 X
 UAI25 UF80
 UAI26 1117

\$F CALL ZV\$T.ZV\$TC,MSDONE

'Y' WHEN DONE

LNJ \$B5,<YSNO
 DC <+\$F
 DC <+\$AF
 LNJ \$B5,<TAGI
 DC Z'D'
 LB <TEMPA,=Z'01'

Y OR N REPLY
 'N' REPLY
 'Y' REPLY
 GET DIAG TAG
 WRITE PROT ?

U01790 UAI27 FBC0 0003
 UAI29 D380 0000 X
 UAI2B UF80
 UAI2C 10EA
 U01791 UAI2D D380 UD0B
 U01792 UAI2F UAI27
 U01793 UAI30 UAI31
 U01794 UAI31 D380 UC50
 U01795 UAI33 D200
 U01796 UAI34 8280 UFE7
 UAI36 0100
 U01797 UAI37 0580 T
 U01798 UAI38 D380 UC85
 U01799 UAI3A 1042
 U01800 UAI3D D380 UCDD
 U01801 UAI3D 1000

\$F CALL ZV\$T.ZV\$TC,MSDONE

'Y' WHEN DONE

LNJ \$B5,<YSNO
 DC <+\$F
 DC <+\$AF
 LNJ \$B5,<TAGI
 DC Z'D'
 LB <TEMPA,=Z'01'

Y OR N REPLY
 'N' REPLY
 'Y' REPLY
 GET DIAG TAG
 PROT BIT ON ?

BBF >+\$L
 LNJ \$B5,<TDER
 DC Z'1042'
 CALL ZV\$T.ZV\$TC,MSPS2

WRT PROT BIT RESET
 HIT PROT SW

U01802 UAI3E 8700 UFC2
 U01803 UAI40 8700 UFC1
 U01810 UAI42 9870 1001
 U01811 UAI44 9F00 0FE9
 U01812 UAI46 D380 UC6D
 U01813 UAI48 FBC0 0003
 UAI4A D380 0000 X
 UAI4C UF80
 UAI4D 111E

T1001 CL <FLAG
 CL <ERFL
 LDK \$R1,=Z'1001'
 STR \$R1,<TEST
 LNJ \$B5,<TDST
 CALL ZV\$T.ZV\$TC,MSWRT

STORE TEST NO
 START TEST
 'OK TO WRITE ?

U01814 UAI4E FBC0 0003
 UAI50 D380 0000 X
 UAI52 UF80
 UAI53 110L
 U01815 UAI54 D380 UD0B
 U01816 UAI56 0A58
 U01817 UAI57 0A5D
 U01818 UAI58 8900 UFC2
 UAI5A 0002
 U01819 UAI5B UF80 0BF2
 U01820 UAI5D 9B80 1211
 U01821 UAI5F 9F80 000A X
 U01822 UAI61 D380 UD58
 U01823 UAI63 D380 UDA4
 U01824 UAI65 D380 UE91
 U01825 UAI67 0E82
 U01826 UAI68 B800 0FAC
 U01827 UAI6A 9800 0FAD
 U01828 UAI6C 9F30 0FB9
 U01829 UAI6E 9F00 0000 T
 U01830 UAI70 9870 002E
 U01831 UAI72 8280 UFC4
 UAI74 8000
 UAI75 0580 T
 BBF >+\$A

CALL ZV\$T,MSPS1A

'REPLY Y OR N

LNJ \$B5,<YSNO
 DC <T1001A
 DC <T1001B
 T1001A LBT <FLAG,=X'02'

AND GET OUT
 SETUP LV10 ISA
 STB \$B1,<SA10DV
 STB \$B1,<ZHISAZ+10*\$AF

NO
 YES
 SET BYPASS FLAG

GET CYL ADDRESS
 GET SECTOR ADDRESS
 SETUP FOR LV10
 TIMING PROCESS
 GET DEV NO.
 GET CYL ADDRESS
 SETUP CYL ADDR FOR CW1
 STORE IN SEEK ROUTINE
 MAX 46 MS FOR 410 CYLS
 822 CYLS ?

KEEP 46MS FOR 410 CYL DEV

001833	UAT0	9A70	000A			ADD	\$R1,=10	MAX 56MS FOR 422 CYLS
001834	UAT0	9F00	0000	I	\$A	STR	\$R1,<+\$C	STORE IN TIMING PROCESS
001835	UAT0	D380	0FC6			LNJ	\$B5,<SKCYF	SEEK
001836	UAT0	0000			\$B	DC	0	CYL ADDRESS IN HERE
001837	UAT0	D380	0E9E			LNJ	\$B5,<TIMDE	TIME SEEK
001838	UAT0	0000		T		DC	0	MAX TIME IN HERE
001839	UAT0	0F80			\$C	B	>+\$D	TIME OK
001840	UAT0	D380	0C85			LNJ	\$B5,<TDER	TIMEOUT ERROR
001841	UAB3	1012			\$D	DC	Z'1012'	
001842	UAT0	D380	0C50			LNJ	\$B5,<TAG1	
001843	UAT0	0700				DC	Z'D7'	
001844	UAT0	8280	0FE7			LB	<TEMPA,=Z'200'	ON CYL ?
001845	UAT0	2000						
001846	UAT0	0500		T		BBT	>+\$E	
001847	UAT0	D380	0C85			LNJ	\$B5,<TDER	
001848	UAT0	1014				DC	Z'1014'	NOT ON CYL
001849	UAT0	1C04			\$E	LDV	\$R1,=4	FOR 5 TRACK
001850	UAT0	8280	0FC4			LB	<IDEN,=Z'4'	19 TRACKS ?
001851	UAT0	0A91	4000					
001852	UAT0	0580		T		BBF	>+\$F	
001853	UAT0	1E0E				ADV	\$R1,=14	NO
001854	UAT0	9F00	0FF1		\$F	STR	\$R1,<TRKMX	MAKE 19 TRACKS
001855	UAT0	8751				CL	=\$R1	STORE MAX TRK
001856	UAT0	9F00	0FF0		T1001C	STR	\$R1,<TRKAD	START AT TRK 0
001857	UAT0	8280	0FE7			CALL	ZV\$T.ZV\$TC,MSTKNO	NEW TRACK
001858	UAT0	0A99	FBC0 0003					PRINT TRACK
001859	UAT0	UAA1	0380 0000	X				
001860	UAT0	UAA3	0F80					
001861	UAT0	UAA4	0FF0					
001862	UAT0	UAA5	0800 0FAC			LDR	\$R3,<BITE	GET DEVICE NUMBER
001863	UAT0	UAA7	9A80 0000	T		SRM	\$R1,<+\$F,=X'1F'	PUT TRK ADDR IN COMMAND
001864	UAT0	UAA9	001F					
001865	UAT0	UAB0	1008			SOL	\$R1,8	SETUP TRACK ADDRESS
001866	UAT0	UAB1	9400 0FD2			OK	\$R1,<SECAD	AND SECTOR ADDR
001867	UAT0	UAB2	9F30 0FB0			STR	\$R1,<DXRN,\$R3	FOR CW2
001868	UAT0	UAB3	D380 0C52		\$F	LNJ	\$B5,<TAGO	SELCT TRACK AUDRESS
001869	UAT0	UAB4	D300			DC	Z'D300'	
001870	UAT0	UAB5	D380 0C50			LNJ	\$B5,<TAG1	GET DIAG REG
001871	UAT0	UAB6	D200			DC	Z'D2'	HEAD SELCT ?
001872	UAT0	UAB7	8280 0FE7			LB	<TEMPA,=Z'80'	
001873	UAT0	UAB8	8000 11F7					YES
001874	UAT0	UAC1	0000 0FE7					
001875	UAT0	UAC2	0560	T		BBF	>+\$A	
001876	UAT0	UAC3	D380 UC85			LNJ	\$B5,<TDER	
001877	UAT0	UAC4	1016			DC	Z'1016'	NO HEAD SELECT
001878	UAT0	UAC5	9800 11E8		\$A	LNJ	\$B5,<SUFB	SETUP FORMAT BUFFER
001879	UAT0	UAC6	8051		\$B	IULD	WBUF,<IOWRIT,<TEMPA	
001880	UAT0	UAC7	0000 11FD					
001881	UAT0	UAC8	07FB			BIOF	>-\$D	
001882	UAT0	UAC9	D380 UF18			LNJ	\$B5,<SUCA	SETUP CONFIG. WORDS
001883	UAT0	UAD0	9800 11E8			LDR	\$R1,<FMRW	FORMAT COMMAND
001884	UAT0	UAD1	8051		\$C	IO	=\$R1,<OTTASK	TASK
001885	UAT0	UAD2	0200					
001886	UAT0	UAD3	D380 UD46			BIOF	>-\$L	
001887	UAT0	UAD4	0001			LNJ	\$B5,<STALL	STALL
001888	UAT0	UAD5	0ADE			B	>+\$D	
001889	UAT0	UAD6	D380 UC85			LNJ	\$B5,<TDER	FORMAT HANGUP
001890	UAT0	UAD7	1050		\$D	DC	Z'1050'	
001891	UAT0	UAD8	UAD2			LNJ	\$B5,<TAG1	
001892	UAT0	UAD9	D700			DC	Z'D7'	
001893	UAT0	UAE0	8280 0FE7			LB	<TEMPA,=Z'01'	TEST DIAG BIT
001894	UAT0	UAE1	0100					
001895	UAT0	UAE2	0560	T		BBF	>+\$A	
001896	UAT0	UAE3	D380 UC50			LNJ	\$B5,<TAG1	
001897	UAT0	UAE4	1050			DC	Z'D2'	GET SB/IS DATA
001898	UAT0	UAE5	UAE4			LNJ	\$B5,<SBIS	DONT CARE BIT
001899	UAT0	UAE6	0E82			DC	X'01'	
001900	UAT0	UAE7	1C01			LNJ	\$B5,<TDER	PRINT DEVICE FAULTS
001901	UAT0	UAE8	8051		\$A	DC	Z'1020'	SETUP LV10 Rupt
001902	UAT0	UAE9	0000 11F8			LNJ	\$B5,<LV10SU	TIME DEVICE EVENT
001903	UAT0	UAF0	07FB		\$B	DC	(\$V10T1	
001904	UAT0	UAF1	D380 UF18			LDV	=\$R1,=1	OFFSET RANGE = 1
001905	UAT0	UAF2	9800 11E9			IO	=\$R1,<OTFRG	
001906	UAT0	UAF3	8051					
001907	UAT0	UAF4	0000 11FD			BIOF	>-\$A	
001908	UAT0	UAF5	07FB			LNJ	\$B5,<SUCA	SETUP CONFIG. WORDS
001909	UAT0	UAF6	D380 UC85			LDR	\$R1,<RWD	SEARCH AND READ
001910	UAT0	UAF7	1018		\$B	IO	=\$R1,<OTTASK	TASK
001911	UAT0	UAF8	B800 UFAC					
001912	UAT0	UAF9	D380 UF18			BIOF	>-\$L	
001913	UAT0	UAF10	9870 0009			LNJ	\$B5,<STALL	TIME DEVICE TILL Rupt
001914	UAT0	UAF11	8051			B	>+\$E	MAX 25MS
001915	UAT0	UAF12	0000 11FD			NOP	=\$-1	OK
001916	UAT0	UAF13	07FB			LNJ	\$B5,<TDER	XXXXXX
001917	UAT0	UAF14	D380 UF8			DC	Z'1018'	
001918	UAT0	UAF15	9870 0009			LNJ	\$R3,<BITE	SECTOR NOT FOUND IN 25MS
001919	UAT0	UAF16	8051			LDR	\$R3,<SUCA	GET DEV ADDR
001920	UAT0	UAF17	1052			LDR	=\$R1,=300	SETUP CONFIG. WDS
001921	UAT0	UAF18	9844 0009			IULD	RBUF,<IOREAD,=\$R1	DIAG FORMAT READ RANGE
001922	UAT0	UAF19	9970 0019					
001923	UAT0	UAF20	0051			BIOF	>-\$U	
001924	UAT0	UAF21	07FC			LDR	=\$R1,=Z'8A'	DIAG READ CMD
001925	UAT0	UAF22	9870 8A00			IO	=\$R1,<OTTASK	
001926	UAT0	UAF23	8051					
001927	UAT0	UAF24	0000 11FD			BIOF	>-\$G	
001928	UAT0	UAF25	07FB			LNJ	=\$B5,<STALL	STALL
001929	UAT0	UAF26	D380 UF8			B	>+\$D	
001930	UAT0	UAF27	9870 0009			LNJ	\$B5,<TDER	FORMAT READ HANGUP
001931	UAT0	UAF28	8051			DC	Z'1052'	GET SYNCH BYTE
001932	UAT0	UAF29	1052			LDR	=\$R1,RBUF,9	
001933	UAT0	UAF30	9844 0009			CMR	=\$R1,X'19'	

001923	0B1C	0900	T	BE	>+\$A	OK
001924	0B1D	D380 UC85		LNJ	\$B5,<TDER	
001925	0B1F	1032		DC	Z'1032'	NO SYNCH BYTE
001926	0B20	9804		LDR	\$R1,RBUF	GET CYL ADDR
001927	0B21	9900 0FAD		CMR	\$R1,<CYAD	
001928	0B23	0900	T	BE	>+\$B	
001929	0B24	D380 UC85		LNJ	\$B5,<TDER	
001930	0B26	1026		DC	Z'1026'	CYL ADDR ERROR
001931	0B27	90C4 0001		LDH	\$R1,RBUF,1	
001932	0B29	9900 0FF0		CMR	\$R1,<TRKAD	
001933	0B2C	0900	T	BE	>+\$C	
001934	0B2C	D380 UC85		LNJ	\$B5,<TDER	
001935	0B2E	1026		DC	Z'1026'	
001936	0B2F	9844 0001		LDR	\$R1,RBUF,1	TRK ADDR ERROR
001937	0B31	9570 00FF		AND	\$R1,=X'FF'	GET SECTOR ADDR
001938	0B33	9900 0FD2		CMR	\$R1,<SECAD	
001939	0B35	0900	T	BE	>+\$D	
001940	0B36	D380 UC85		LNJ	\$B5,<TDER	
001941	0B38	1028		DC	Z'1028'	SECT ADDR ERROR
001942	0B39	D380 UC50		LDR	\$B5,<TAG1	
001943	0B3B	D700		DC	Z'D7'	
001944	0B3C	8280 0FE7		LB	<TEMPA,=Z'01'	DIAG BIT ?
	0B3E	0100				
001945	0B3F	0580	T	BBF	>+\$E	
001946	0B40	D380 OC50		LNJ	\$B5,<TAG1	
001947	0B42	D200		DC	Z'D2'	DIAGS
001948	0B43	D380 0D46		LNJ	\$B5,<SBIS	SB/IS PRINT
001949	0B45	0001		DC	X'01'	DONT CARE BIT
001950	0B46	D380 UC85		LNJ	\$B5,<TDER	READ DIAG ERROR
001951	0B48	1036		DC	Z'1036'	GET BUFFER ADDR
001952	0B49	AC80 UFC6		LDB	\$B2,<JJPT	FILL BUFFER WITH STRING
001953	0B4B	AF80 0000	T	STB	\$B2,<+\$F	WORST CASE PATTERN
001954	0B4D	D380 UF23		LNJ	\$B5,<FLBF	STRING LENGTH
001955	0B4F	UFF6		DC	<WORSC	BUFF ADDRESS
001956	0B50	0005		DC	5	WRITE BUFF LENGTH
001957	0B51	0000		RESV	\$AF,0	GET READ BUFF ADDR
001958	0B52	0080		DC	128	
001959	0B53	AC80 UFC7		LDB	\$B2,<KKPT	
001960	0B55	AF80 0000	T	STB	\$B2,<+\$G	
001961	0B57	D380 UF23		LNJ	\$B5,<FLBF	
001962	0B59	0FD0		DC	<NULL	
001963	0B5A	0001		DC	1	
001964	0B5B	0000		RESV	\$AF,0	
001965	0B5C	0096		DC	150	
001966	0B5D	9870 0100		LDR	\$R1,=256	
001967	0B5F	8186		IULD	WBUF,<IOWRIT,=\$R1	WRITE RANGE
	0B60	0000 11F7				WRITE WORST CASE
001968	0B63	07FC		BIOF	>-\$A	
001969	0B64	D380 UF18		LNJ	\$B5,<SUCA	RESET CONFIGURATION WORDS
001970	0B66	9800 11E9		LDR	\$R1,<RWD	WRITE COMMAND
001971	0B68	8051		IU	=\$R1,<OTTASK	
	0B69	0000 11FD				
001972	0B6D	07FD		BIOF	>-\$B	
001973	0B6E	D380 UF18		LNJ	\$B5,<STALL	STALL
001974	0B6F	0F80	T	B	>+\$U	
001975	0B7E	D380 UC85		LNJ	\$B5,<TDER	WRITE DATA HANGUP
001976	0B71	1054		DC	Z'1054'	
001977	0B72	D380 UC50		LNJ	\$B5,<TAG1	
001978	0B74	D700		DC	Z'D7'	GET DIAG BIT
001979	0B75	8280 0FE7		LB	<TEMPA,=Z'01'	OK
	0B77	0100				
001980	0B78	0580	T	BBF	>+\$C	
001981	0B79	D380 UC50		LNJ	\$B5,<TAG1	
001982	0B7B	D200		DC	Z'D2'	GET SB/IS DATA
001983	0B7C	D380 UD46		LNJ	\$B5,<SBIS	DONT CARE BIT
001984	0B7E	0001		DC	X'01'	
001985	0B7F	D380 UC85		LNJ	\$B5,<TDER	PRINT DEV FAULTS
001986	0B81	1020		DC	Z'1020'	GET DEV ADDR
001987	0B82	B800 UFAC		LDR	\$K3,<BITE	SETUP CONFIG. WORDS
001988	0B84	D380 UF18		LNJ	\$B5,<SUCA	DIAG FORM READ RANGF
001989	0B86	9870 0100		LDR	\$R1,=256	
001990	0B88	8184		IULD	WBUF,<IOWRIT,=\$R1	
	0B89	0000 11F6				
001991	0B8C	0051		BIOF	>-\$U	READ DATA
001992	0B8D	A870 8980		LDR	\$R2,=Z'898'	
001993	0B8F	8052		IU	=\$R2,<OTTASK	
	0B90	0000 11FD				
001994	0B92	07FD		BIOF	>-\$E	STALL
001995	0B93	D380 UEL8		LNJ	\$B5,<STALL	
001996	0B95	UF80	T	B	>+\$U	
001997	0B96	D380 UC85		LNJ	\$B5,<TDER	
001998	0B98	1056		DC	Z'1056'	READ DATA HANGUP
001999	0B99	9870 0100		LDR	\$R1,=256	
002000	0B9D	9F54		STR	\$R1,=R4	GET RANGE IN BYTES
002001	0B9E	4041		SUR	\$R4,1	CHANGE TO WORDS
002002	0B9F	88D4		DEC	=\$R4	SET WORD COUNTER
002003	0C01	8752		CL	=\$R2	CLEAR BUFF INDEX
002004	0C04	ACD4		LDB	=\$B2,=RBUF	SETUP FOR B-PUP ADDR
002005	0CA0	E826		T1001D	LDR	'SB' DATA
002006	0CA1	F86E		LDR	\$R7,\$B2+\$R2	'IS' DATA
002007	0CA2	E957		CMR	\$R7,=\$R7	CHECK DATA
002008	0CA3	8900 0BE2		BE	<T1001E	OK
002009	0CA5	AF00 UFE9		STR	\$R2,<TEMPC	ERROR WORD LOCATION
002010	0CA7	8900 UFC1		LBT	<ERFL,=X'1'	SET ERROR FLAG
	0CA9	0001				
002011	0CAAA	FBC0 0003		CALL	ZV\$T,ZV\$TC,MSWORD	PRINT WORD
	0CAB	D380 0000	X			
	0CAC	0F80				
	0CAF	1190				
002012	0CB0	FBC0 0003		CALL	ZV\$TH,TEMPC	PRINT HEX WORD
	0CB2	D380 0000	X			
	0CB4	0F80				
	0CB5	0FE9				
002013	0CB6	8D00 UFE7		SUB	<TEMPA	
002014	0CB8	FBC0 0003	X	CALL	ZV\$T,MSIS	STORE IS/SB DATA
	0CBA	D380 0000				PRINT 'IS'
	0CBC	0F80				
	0CBF	1198				

002015	UBBE	FBC0 0003		CALL	ZV\$HZ,TEMPB,ASCBF	CONV 'IS' DATA ASCII
	UBC0	D380 0000	X			
	UBC2	0F80				
	UBC3	0FE8				
	UBC4	0FA8				
002016	UBC5	FBC0 0003		CALL	ZV\$T,ASCBF	PRINT HEX 'IS' DATA
	UBC7	D380 0000	X			
	UBC9	0F80				
	UBCA	0FA8				
002017	UBCB	FBC0 0003		CALL	ZV\$T,MSSB	PRINT 'SB'
	UBCD	D380 0000	X			
	UBCF	0F80				
	UBD0	11DA				
002018	UBD1	FBC0 0003		CALL	ZV\$HZ,TEMPA,ASCBF	CONV 'SB' DATA ASCII
	UBD3	D380 0000	X			
	UBD5	0F80				
	UBD6	0FE7				
	UBD7	0FA8				
002019	UBD8	FBC0 0003		CALL	ZV\$T,ASCBF	PRINT HEX 'SB' DATA
	UBDA	D380 0000	X			
	UBDC	0F80				
	UBDD	0FA8				
002020	UBDE	D380 0B33		LNJ	\$B5,<QUIT	HIT BREAK TO QUIT
002021	UBE0	0B2E		DC	<T1001E	CONTINUE
002022	UBE1	0000		DC	<\$F	EXIT
002023	UBE2	4700 0BA0	T1001E	BDEC	\$R4,<T1001D	GET NXT WORD
002024	UBE4	8280 UFC1		\$F	LB <ERFL,=X+1,	ANY ERRORS ?
	UBE5	0001	T			
002025	UBE7	05B0		BBF	>+\$A	
002026	UBE8	U380 UC85		LNJ	\$B5,<TDER	
002027	UBE9	1034		DC	Z!1034!	DATA ERROR
002028	UBED	9B00 0FF0		\$A	LDK	GET NEXT TRACK
002029	UBE0	8AD1			\$R1,<TRKAD	LAST TRACK ?
002030	UBEL	9900 0FF1			INC	GO AGAIN
002031	UBFU	0380 0A97				END TEST
002032	UBF2	U380 UC00	T1001F	BLE	\$B5,<TDTE	
002033	UBF4	1001		LNJ	Z!1001!	
002034	UBF5	FBC0 0003		CALL	ZV\$T.ZV\$TC,MSETD	END T&D TESTS.
	UBF7	D380 0000	X			
	UBF9	0F80				
	UBFA	10F6				
	UBFB	0F80 0100				
002035	*			B	<START	
002036	*					
002037	*					
002038	*					
002039	*					
002040	*					
002041	LL	NOP	>-\$1			
002042	UBFD	UF7F	NOP			
002043	UBFE	UF7F	NOP			
002044	UBFF	UF7F	NOP			
002045	UC00	UF7F	NOP			
002046	UC01	UF7F	NOP			
002047	UC02	UF7F	NOP			
002048	UC03	UF7F	NOP			
002049	UC04	UF7F	NOP			
002050	UC05	UF7F	NOP			
002051	UC06	UF7F	NOP			
002052	UC07	UF7F	NOP			
002053	UC08	UF7F	NOP			
002054	UC09	UF7F	NOP			
002055	UC0A	UF7F	NOP			
002056	UC0B	UF7F	NOP			
002057	UC0C	UF7F	NOP			
002058	UC0D	UF7F	NOP			
002059	UC0E	UF7F	NOP			
002060	UC0F	UF7F	NOP			
002061	UC10	UF7F	NOP			
002062	UC11	UF7F	NOP			
002063	UC12	UF7F	NOP			
002064	UC13	UF7F	NOP			
002065	UC14	UF7F	NOP			
002066	UC15	UF7F	NOP			
002067	UC16	UF7F	NOP			
002068	UC17	UF7F	NOP			
002069	UC18	UF7F	NOP			
002070	UC19	UF7F	NOP			
002071	UC1A	UF7F	NOP			
002072	UC1B	UF7F	NOP			
002073	UC1C	UF7F	NOP			
002074	UC1D	UF7F	NOP			
002075	UC1E	UF7F	NOP			
002076	UC1F	UF7F	NOP			
002077	UC20	UF7F	NOP			
002078	UC21	UF7F	NOP			
002079	*					
002080	*					
002081	*					
002082	UC22	0000	KESV	\$AF,0		
002083	UC23	0000	KESV	\$AF,0		
002084	UC24	0000	KESV	\$AF,0		
002085	UC25	0000	KESV	\$AF,0		
002086	UC26	0000	KESV	\$AF,0		
002087	*					
002088	*					

***** ROOM FOR CONSTANTS, ETC. *****

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002089 ****
002090 ****
002091 ****
002092 ****
002093 ****
002094 ****
002095 ****
002096 ****
002097 UC27 DF80 UC4F FRKC STB $B5,<FRRCB5
002098 UC29 D380 UC52 FRKCA LNJ $B5,<TAGO
002099 UC2B D270 UC2C UC30 UC50 LNJ Z:D70!
002100 UC2E UC31 1000 UC2F 8280 UFET FRKC LDC $B5,<TAG1
002101 UC32 0580 UC49 LNJ Z:D71!
002102 UC34 8280 UFET UC35 0100 UC2F 8280 UFET FRKC LU <TEMPA,=Z:10!
002103 UC37 0580 UC4C UC38 0380 UC50 LNJ $B5,<TAGI
002104 UC39 0380 UC50 UC3A 0100 UC2F 8280 UFET FRKC LU Z:D72!
002105 UC37 0580 UC4C UC39 0380 UC50 LNJ $B5,<TAGI
002106 UC3A 0100 UC3B 0200 UC3C 8280 UFET FRKC LU Z:D73!
002107 UC3D 0200 UC3E FLU0 UC3F 0500 UC4F FRKC LU <TEMPA,=Z:FE!
002108 UC3F 0500 UC4F FRKC LU <TEMPA,=Z:01!
002109 UC40 D380 UC85 1 BBT >+$A
002110 UC41 0008 UC42 0008 UC43 0380 UC46 SA LNJ $B5,<TDER
002111 UC44 0001 UC45 0001 UC46 0380 UC85 SA LNJ $B5,<SBIS
002112 UC47 0001 UC48 0010 UC49 0380 UC85 FRKC LNJ X:01!
002113 UC45 0001 UC46 0010 UC47 0001 UC48 0010 UC49 0380 UC85 FRKC LNJ $B5,<TDER
002114 UC46 0010 UC47 0001 UC48 0010 UC49 0380 UC85 FRKC LNJ Z:0010!
002115 UC48 0010 UC49 0096 UC4A 0380 UC85 FRKC LNJ $B5,<TDER
002116 UC49 0096 UC4B 0096 UC4C UC4E 8385 FRKCB LDB $B5,<FRRCB5
002117 UC4B 0096 UC4C UC4E 8385 FRKCB LDB $B5,<FRRCB5
002118 UC4C UC4F UC4E 8385 FRKCB JMP $B5
002119 UC4F 0000 FRKCB RESV $AF,0
002120 UC4F 0000 *
002121 UC4F 0000 *
002122 UC4F 0000 *
002123 UC4F 0000 *
002124 UC4F 0000 *
002125 UC4F 0000 *
002126 UC4F 0000 *
002127 UC50 8754 1 TAGI CL =$R4
002128 UC51 0F80 1 TAGO LDV $R4,X:40!
002129 UC52 4C40 SA STR $R4,<TASK
002130 UC53 CF00 UF04 SA IOR $R4,<IOREAD
002131 UC54 C400 11FD UC55 8180 UFET UC56 0004 UFAF
002132 UC55 8180 UFET UC57 8180 UFET UC58 0000 UF0F
002133 UC58 0000 UF0F UC59 0000 UF0F UC5A 0000 UF0F
002134 UC59 0000 UF0F UC5B C875 UC5C 07FB UC5D 8054
002135 UC5D 8054 UC5E 0000 11FD UC5F 0000 11FD
002136 UC5F 0000 11FD UC60 07FD UC61 07FD UC62 DF80 UC6C
002137 UC61 07FD UC63 8980 UF04 UC64 8980 UF04 UC65 0300
002138 UC62 DF80 UC6C UC66 0300 UC67 D380 UF12 UC68 0F80
002139 UC63 8980 UF04 UC64 8980 UF04 UC65 0300 UC66 0300
002140 UC66 0300 UC67 D380 UF12 UC68 0F80 UC69 0F80
002141 UC67 D380 UF12 UC68 0F80 UC69 0F80 UC70 1181
002142 UC68 0F80 UC69 0F80 UC71 FBC0 0003 UC72 0F80
002143 UC69 0F80 UC73 D380 0000 UC74 0F80 UC75 0F80
002144 UC70 1181 UC76 1181 UC77 FBC0 0003 UC78 0F80
002145 UC71 FBC0 0003 UC79 D380 0000 UC7A 0F80 UC7B 0F80
002146 UC72 0F80 UC7C 0FEB UC7D DC80 UC80 UC7E 8385
002147 UC73 D380 0000 UC7F 8385 TDST STB $B5,<TDSTB5
002148 UC74 0F80 UC7F 8385 TDST RESV $AF,0
002149 UC75 0F80 UC7G 0000 *
002150 UC76 1181 UC7H 0000 *
002151 UC77 FBC0 0003 UC7I 0000 *
002152 UC78 0F80 UC7J 0000 *
002153 UC79 D380 UC80 UC7K 0000 *
002154 UC7A 0F80 UC7L 0000 *
002155 UC7B 0F80 UC7M 0000 *
002156 UC7C 0F80 UC7N 0000 *
002157 UC7D DC80 UC80 UC7O 0000 *
002158 UC7E 8385 UC7P 0000 *
002159 UC7F 8385 TDSTB5 RESV $AF,0
002160 UC80 0000 *
002161 UC81 8280 UF01 TDTR1 LB <ERFL,X:02!
002162 UC82 0002 TDTR1 LB <ERFL,X:02!
002163 UC83 0500 TDTR1 LB <ERFL,X:02!
002164 UC84 0500 TDTR1 LB <ERFL,X:02!
002165 UC85 8F00 UF05 TDTR1 LB <ERFL,X:02!
002166 UC86 FFFF TDTR1 LB <ERFL,X:02!
002167 UC87 C800 UFEB TDTR1 LB <ERFL,X:02!
002168 UC88 C800 UFEB TDTR1 LB <ERFL,X:02!
002169 UC89 C570 FF00 TDTR1 LB <ERFL,X:02!
002170 UC8A C570 FF00 TDTR1 LB <ERFL,X:02!
002171 UC8B C475 TDTR1 LB <ERFL,X:02!
002172 UC8C DFB0 UCDC TDTR1 LB <ERFL,X:02!
002173 UC8D CF00 1157 TDTR1 LB <ERFL,X:02!
002174 UC8E 1157 TDTR1 LB <ERFL,X:02!
002175 UC8F 1157 TDTR1 LB <ERFL,X:02!
002176 UC90 8280 UF05 TDTR1 LB <ERFL,X:02!
002177 UC91 FBC0 0003 TDTR1 LB <ERFL,X:02!
002178 UC92 D380 0000 TDTR1 LB <ERFL,X:02!
002179 UC93 0F80 TDTR1 LB <ERFL,X:02!
002180 UC94 1157 TDTR1 LB <ERFL,X:02!
002181 UC95 1157 TDTR1 LB <ERFL,X:02!
002182 UC96 1138 TDTR1 LB <ERFL,X:02!
002183 UC97 FBC0 0003 TDTR1 LB <ERFL,X:02!
002184 UC98 D380 0000 TDTR1 LB <ERFL,X:02!
002185 UC99 0F80 TDTR1 LB <ERFL,X:02!
002186 UC9A 1157 TDTR1 LB <ERFL,X:02!
002187 UC9B 8280 UF05 TDTR1 LB <ERFL,X:02!
002188 UC9C 0002 TDTR1 LB <ERFL,X:02!
002189 UC9D 0002 TDTR1 LB <ERFL,X:02!
002190 UC9E 0002 TDTR1 LB <ERFL,X:02!
002191 UC9F 0002 TDTR1 LB <ERFL,X:02!
**** SUBROUTINES ****
**** FAULT RESET/READY CHECK ROUTINE ****
**** CHECK FOR ANY FAULTS ****
**** LAUNCH TO TAG COMMANDS ****
**** START TEST ****
**** TEST FOR CONTINUE MODE ****
**** SAVE REGISTERS ****
**** GET TEST NUMBER ****
**** STRIP SUBTEST ****
**** GET DICTIONARY ENTRY ****
**** SAVE CONTINUE ADDR ****
**** PUT IN MESSAGE ****
**** PRINT ERROR MESSAGE ****
**** WITH DICT ENTRY ****
**** IS ? SB DATA PRINT ? ****

```

002177	UCAU	0560		T	BDF	>+\$A	NO	
002178	UCAI	D380	UF33		LNJ	\$B5,<P1SBC	PRINT IS/SB DATA	
002179	UCA3	8900	UF01		LDT	<ERFL,=Z'0001'	SET ERROR FLAG	
002180	UCA5	0001			LDF	<IPFL,=X'21'	CLEAR IS/SB PRINT FLAG	
002181	UCA6	8800	UF05		LDT	<FLAG,=Z'10'	OVFLOW FLAG	
002182	UCA8	0002						
002183	UCA9	8800	UF02	T	BDF	>+\$C	OVERFLOW MSG	
002184	UCAD	1000			CALL	ZV\$1,MSTMUF		
002185	UCAC	0580		X				
002186	UCAD	FBC0	0003					
002187	UCAF	D380	0000					
002188	UCBL	UF80						
002189	UCB2	1187						
002190	UCB3	6280	UF01		\$C	LDB	<ERFL,=X'021'	
002191	UCB5	0002			BDF	>+\$B	CONTINUE	
002192	UCB6	6580		T	LDB	\$B5,<TDERB5	EXIT	
002193	UCD7	DC80	UCDC		JMP	\$B5	GET CONTINUE ADDR	
002194	UCD9	8385			CALL	ZV\$1,ZV\$TC,MSREGS	PRINT ALL REGISTERS	
002195	UCDA	FBC0	0003					
002196	UCDB	D380	0000	X				
002197	UCBE	UF80						
002198	UCBF	11A8						
002199	UCCL	6380	UF7B		TDER2	LNJ	\$B5,<CRLF	
002200	UCC2	9B80	UF05		LAB	\$B1,<SAVE-1	GET REGISTERS	
002201	UCC4	4FC0			LDV	\$R4,-\$4	LINE COUNTER	
002202	UCCS	CF00	0FE9		SC	\$R4,<TEMPC	SAVE IT	
002203	UCC7	5CFC			LDV	\$R5,-\$4	4 WORDS PER LINE	
002204	UCC8	FCF1			SE	LDB	\$B7+\$B1	GET DATA
002205	UCC9	D380	UEE2		CALL	LNJ	\$B5,<LNGASC	CONVERT TO ASCII
002206	UCCB	FBC0	0003	X		ZV\$1,LNGAD+\$AF-1	PRINT DATA	
002207	UCCD	D380	0000					
002208	UCCF	UF80						
002209	UCD0	UFCA						
002210	UCD1	57F7			BINC	\$R5,-\$E	GET NEXT DATA	
002211	UCD2	D380	UF7B		LNJ	\$B5,<CRLF	START NEXT LINE	
002212	UCD4	C800	0FE9		LDR	\$R4,<TEMPC	GET NEXT LINE	
002213	UCD6	47EF			BINC	\$R4,-\$C		
002214	UCD7	D380	UCDD		LNJ	\$B5,<TDTE	END THIS TEST	
002215	UCD9	0000			DC	X'0'	S/R WILL INSERT TEST NUMBER	
002216	UCDA	UF80	U100		D	<START	RETURN TO RESTART	
002217	UCDC	0000			*			
002218	UCDL	0000			TDERE5	RLSV	\$AF,0	
002219	UCDL	0000			*			
002220	UCDL	0000			*			
002221	UCDL	0000			*			
002222	UCDL	0000			*			
002223	UCDL	0000			*			
002224	UCDL	0000			*			
002225	UCDL	0000			*			
002226	UCDL	0000			*			
002227	UCDL	0000			*			
002228	UCDU	C875			TDT	LUR	\$R4,+\$B5	
002229	UCDE	UF80	000A		STB	\$B5,<TDTEB5		
002230	UCEO	C400	0FE9		UK	\$R4,<TEST		
002231	UCEZ	CF00	1162		STR	\$R4,<MSTE		
002232	UCE4	FBC0	0003	X	CALL	ZV\$1HZ,MSTE,LFCR	ADD TEST NUMBER	
002233	UCE6	D380	0000					
002234	UCE8	UF80						
002235	UCE9	1162						
002236	UCEA	UFCA						
002237	UCED	8800	UF02		LDF	<FLAG,=X'021'	TEST BYPASS AND RESET	
002238	UCEE	0006		T	BDF	>+\$C		
002239	UCEL	0580			CALL	ZV\$1,MSTBP	BYPASS MSG	
002240	UCEF	FBC0	0003					
002241	UCF1	D380	0000	X				
002242	UCF3	UF80						
002243	UCF4	1178						
002244	UCF5	UF80			T	SC	>+\$B	
002245	UCF6	8800	UF01		LDF	<ERFL,=Z'0001'	TEST FOR ERRORS AND RESET	
002246	UCF8	0001			BDF	>+\$A		
002247	UCF9	0500		T	CALL	ZV\$1,MSTEP	TEST PASSED MESSAGE	
002248	UCFA	FBC0	0003					
002249	UCFB	D380	0000	X				
002250	UCFC	UF80						
002251	UCFF	1170						
002252	UD00	UF55			T	SA	>+\$B	
002253	UD01	FBC0	0003		CALL	ZV\$1,MSTEF	TEST FAIL MESSAGE	
002254	UD03	D380	0000	X				
002255	UD05	UF80						
002256	UD06	1163						
002257	UD07	DC80	000A		\$D	LDB	\$B5,<TDTEB5	
002258	UD09	8385			JMP	\$B5		
002259	UD0A	0000						
002260	UD10	0000			*			
002261	UD11	UF80			TDT	E5		
002262	UD12	UF0C			ESV	\$AF,0		
002263	UD13	0FE9			*			
002264	UD14	C800	0FE9					
002265	UD16	4048			LUR	\$R4,<TEMPC		
002266	UD17	C970	0059		SUR	\$R4,\$8	SHIFT IT	
002267	UD19	0900		T	CMR	\$R4,=X'591		
002268	UD1A	C970	004E		BE	>+\$B	IT IS 'Y'?	
002269	UD1C	0900		T	CMR	\$R4,=X'4E1	IS IT 'N'?	
002270	UD1D	FBC0	0003		BE	>+\$C	IT IS	
002271	UD1F	D380	0000	X	CALL	ZV\$1,ZV\$QC,MSBLNK	? NEITHER	
002272	UD21	UF80						
002273	UD22	1042						
002274	UD23	0FEA			B	>-\$A	RETRY	

```

002264 UD24 DC80 UD32      $B   LDB   $B5,<YSNOB5          BUMP RETURN ADDRESS
002265 UD26 DBC5 0001      LAB   $B5,>$B5,$AF
002266 UD28 DF80 UD32      STB   $B5,<YSNOB5
002267 UD2A D380 0E38      SC    LNJ   $B5,<TIME
002268 UD2C 03E8          DC    1000
002269 UD2B D380 0F7B      LNJ   $B5,<CRLF
002270 UD2F DC80 UD32      LDB   $B5,<YSNOB5          WAIT 1 SEC FOR ANY CRLF
002271 UD31 8380          JMP   *$B5          THEN SEND ONE
002272
002273 UD32 0000          *YSNOB5 RESV $AF,0
002274
002275
002276
002277
002278
002279
002280
002281 UD33 DF80 UD45      QUIT  STB   $B5,<QUITB5          TEST FOR BRK
002282 UD35 FBFO 0001      CALL  ZV$BRK
002283 UD39 8980 0000      X
002284 UD3B 0900          T     CMZ   <ZV$BK
002285 UD3C DC80 UD45      BE    >+$A
002286 UD3E DBC5 0001      LDB   $B5,<QUITB5          BREAK=1
002287 UD40 DF80 UD45      LAB   $B5,$B5,$AF
002288 UD42 DC80 UD45      STB   $B5,<QUITB5          NO BRK
002289 UD44 8380          SA    LDB   $B5,<QUITB5          BUMP RETURN ADDRESS
002290
002291 UD45 0000          *A    JMP   *$B5
002292
002293
002294
002295
002296
002297
002298
002299
002300
002301
002302
002303 UD46 E875          SB1S  LDR   $R6,+$B5          GET DONT CARE MASK
002304 UD47 4048          SUR   $R4,8           SHIFT TAG LABLE
002305 UD48 C570 000F          AND   $R4,=X'F'
002306 UD49 CF00 0FE9          STR   $R4,<TEMPC
002307 UD4C C080 0FE7          LDH   $R4,<TEMPA
002308 UD4E CF00 0FE7          STK   $R4,<TEMPA          SHIFT RETURN DATA
002309 UD50 E500 0FE7          AND   $R6,<TEMPA          PUT IT BACK
002310 UD52 F800 0FE7          LDR   $R7,<TEMPA          SB DATA
002311 UD54 8900 UFC5          LBT   <IPFL,=X'2'          IS DATA
002312 UD56 0002          JMP   $B5           SET PRINT FLAG
002313 UD57 8385          RETURN
002314
002315
002316
002317
002318
002319 UD58 DF80 UD7D      GLTCY STB   $B5,<GTCYB5          ASK FOR CYL
002320 UD5A FBFO 0003          CALL  ZV$1.ZV$QC,MSCYNO
002321 UD5C D380 0000          X
002322 UD5E UF80          UD65 UFAD          SC    CALL  ZV$1D,CYAD          INPUT CYL
002323 UD66 9800 UFAD          LDR   $R1,<CYAD
002324 UD68 1800          UD69 9970 0336          BLZ   $R1,>+$A          CYL IS NEG
002325 UD6B 037L          UD6C 9970 019A          CMR   $R1,=B22          HI CYL
002326 UD6E 0380          UD6F 8280 UFC4          BG    >+$A
002327 UD71 8000          UD72 057C          CMR   $R1,=410          >B22
002328 UD75 D380 0000          BLE   >+$B          GOOD FOR ANY DEV
002329 UD77 UF80          UD78 1042          LB    <IDEN,=Z'8000*          B22 CYLS ?
002330 UD79 0003          UD7A DC80 UD7D          $A    BBT   >+$B          YES
002331 UD7C 8385          UD7D 0000          CALL  ZV$T.ZV$QC,MSBLNK          ILLEGAL CYL
002332
002333
002334
002335 UD7D 0000          $B    B   >-$C          RETRY
002336
002337
002338
002339
002340
002341
002342 UD7E DF80 0DA3      GETTRK STB   $B5,<GTTKB5          ASK FOR TRK
002343 UD80 FBFO 0003          CALL  ZV$1.ZV$QC,MSTKNO
002344 UD82 D380 0000          X
002345 UD84 UF80          UD85 1144          SC    CALL  ZV$1D,TRKAD          INPUT TRK
002346 UD86 9800 UFF0          LDR   $R1,<TRKAD
002347 UD88 1800          UD8E 9970 0012          BLZ   $R1,>+$A          TRK IS NEG
002348 UD91 037L          UD92 9970 0004          CMR   $R1,=18          HI TRK
002349 UD94 0380          UD95 8280 UFC4          BG    >+$A
002350 UD96 4000          UD97 4000          CMR   $R1,=4          GOOD FOR ANY DEV
002351 UD98 8280 UFC4          BLE   >+$B          19 TRKS ?
002352 UD99 4000

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002352	UD98	057C	T	\$A	B7T CALL	>+\$0 ZV\$T.ZV\$QC,MSBLNK	YES
002353	UD99	FBC0 0003	X				ILLEGAL TRK
002354	UD9B	D380 0000					
002355	UD9D	0F80					
002356	UD9E	1042					
002357	UD9F	0FE7					
002358	UDA0	DC80 0DA3		\$B	LDB JMP	>-\$C \$B5,<GTTKB5 \$B5	RETRY
002359	UDA2	8385		*			
002360	UDA3	0000		*			
002361	UDA4	GTTKB5 RESV		*			
002362	UDA5	0AF,0		*			
002363	UDA6	*****		*			
002364	UDA7	*****		*			
002365	UDA8	GETSC	STB	\$B5,<GTSCB5	ZV\$T.ZV\$QC,MSSCNO	ASK FOR SECT	
002366	UDA9	FBC0 0003	X	CALL	ZV\$T.ZV\$QC,MSBLNK		
002367	UDAA	D380 0000					
002368	UDAB	UDAE 0000	X				
002369	UDAC	UDB0 0F80					
002370	UDAD	UDB1 0FD2					
002371	UDB2	9800 0FD2	I		LDR	\$R1,<SECAD	
002372	UDB3	1800 0F02			BLZ	\$R1,>+\$A	
002373	UDB4	9970 003F			CMR	\$R1,>=63	
002374	UDB5	UDB7 0380	T		BLE	>+\$D	
002375	UDB6	FBC0 0003	X		CALL	ZV\$T.ZV\$QC,MSBLNK	SEC IS NEG
002376	UDB7	D380 0000					HI SECT
002377	UDB8	UDB9 0F80					ILLEGAL SECT
002378	UDB9	UDBD 1042					
002379	UDB0	0FEE					
002380	UDB1	DC60 0DC2		\$B	b	>-\$C	RETRY
002381	UDB2	8385			LDB	\$B5,<GTSCB5	
002382	UDB3	0000			JMP	\$B5	
002383	UDB4	GTSCB5 RESV		*			
002384	UDB5	0AF,0		*			
002385	UDB6	*****		*			
002386	UDB7	*****		*			
002387	UDB8	SKCYS LBT <FLAG,=X*8*		*		SEEK CYLINDER ADDRESS	
002388	UDB9	0000 0FC2					SET SLOW FLAG
002389	UDCA	UDC5 0008					
002390	UDCB	C872					
002391	UDCC	DF80 0DF4					
002392	UDCD	b800 UFAC					
002393	UDCE	CF30 0FB9					
002394	UDCF	D380 UF18					
002395	UDD1	8180 0FE7					
002396	UDD2	0000 11F7					
002397	UDD3	0000 0FAF					
002398	UDD4	0F7A					
002399	UDD5	8280 0FC2					
002400	UDD6	0008					
002401	UDD7	UDD9 0500	I		B7T	>+\$C	CHECK FOR SLOW
002402	UDD8	C870 0100			LDK	\$R4,>=Z\$0100*	
002403	UDD9	UDD9 0500		\$B	TD	=\$R4,<OTTASK	LOAD SEEK COMMAND
002404	UDD0	8054 0000					SEEK
002405	UDD1	11FD					
002406	UDD2	UDD9 0500					
002407	UDD3	0F80					
002408	UDD4	0DE1 UDDED	T		BI0F	>-\$D	
002409	UDD5	0DE3 00FF			b	>+\$U	
002410	UDD6	0DE4 4048			SRM	\$R4,<SKCYB,=X*FF*	STORE LO CYL ADDR
002411	UDD7	0DE5 0DEA			SUR	\$R4,8	GET HI ADDR
002412	UDD8	0DE7 00FF			SRM	\$R4,<SKCYA,=X*FF*	STORE HI CYL
002413	UDD9	0DE8 D380 UC52					
002414	UDD0	0DEA D400					
002415	UDD1	0DEB D380 UC52					
002416	UDD2	0DEC D600					
002417	UDD3	0DD0 8800 0FC2					
002418	UDD4	0DD1 0008					
002419	UDD5	UDF1 UC80 0DF4					
002420	UDD6	8385					
002421	UDD7	0DD2 0DD3					
002422	UDD8	UDF2 0F80 0E00					
002423	UDD9	C830 0FB5					
002424	UDD0	UDF4 01BA					
002425	UDD1	UDF5 D380 0F6F					
002426	UDD2	UDF6 DC80 0E00					
002427	UDD3	UDF7 8385					
002428	UDD4	0E00 0000					
002429	UDD5	CDCH STB \$B5,<CDCHB5				RETURN	
002430	UDD6	0E01 8F00 0FD5					
002431	UDD7	UDD3 FFFF					
002432	UDD8	0E04 9B80 0100					
002433	UDD9	0E06 9F80 1222					
002434	UDD0	0E08 FBC0 0003					
002435	UDD1	0E01 8F00 0FD5					
002436	UDD2	0E04 9B80 0100					
002437	UDD3	0E06 9F80 1222					
002438	UDD4	0E08 FBC0 0003					
002439	UDD5	TH15 SAVE <SAVE-1,=-1				SAVE ALL REGS	
002440	UDD6	LAB \$B1,<START					
002441	UDD7	STB \$B1,<ISA1P					
002442	UDD8	CALL ZV\$T.ZV\$TC,MSMRT				SAVE IN CURRENT TSAP	
002443	UDD9					MISSING RESOURCE TRAP	

U02439 UE0A D380 0000 X
 U02440 UE0C 0F80
 U02441 UE0D 10BF
 U02442 UE0E 0003 KTT RETURN FROM TRAP

 * HANDLE RTC RUPT AND CALIBRATE CLOCK (AT LEVEL 5)
 * RTCFC RTCF STOP THE CLOCK
 STR \$R4,=\$R6 STORE MAJOR TIME LOOPS
 STR \$R2,=\$R7 STORE MINOR TIME LOOPS
 SVI <TMLPA STORE LOOPS
 DOR \$R7,16
 DIV \$R7,=1000 TEST FOR ROUNDING
 DIV \$R6,=500
 BLEZ \$R6,>+\$A
 INC \$=SR7 ROUND
 NEG \$=SR7
 SA TIME FOR 1 MILLISECOND
 STR \$R7,<SYNCHC
 LAB \$B1,<RESUME
 STB \$B1,<SA15P
 LEV =Z'8000'+15 PREPARE FOR SUSPEND
 HLT SUSPEND TO LEVEL 15
 B >\$-1 DIDN'T SET UP FOR SFCOND INTERRUPT

 * CALIBRATE THE CPU FOR ONE SECOND
 *
 SYNCH CL =\$R4 LOOPS FOR 1 SEC
 RTCN RTCN START THE CLOCK
 X SYNCHA CMR \$R1,<ZHRTCC SYNCHRONIZE THE CLOCK TICKS
 BL <SYNCHA WAIT FOR FIRST TICK
 LDV \$R1,=-1 IN CASE RTC DUESN'T RUPT
 *
 SYNCHB LUV \$R2,=-20 -20
 BINC \$R2,<\$ COUNT LOOPS/SEC (/MS)
 BINC \$R4,<SYNCHB
 * IF DOING INITIAL CALIBRATION, SHOULD RUPT OUT OF LOOP TIMEC.
 *
 LDR \$R4,<SYNCHC NMBR OF LOOPS/MS
 BINC \$R1,<SYNCHB NMBR OF MSEC'S
 JMP \$B5 RETURN
 *
 SYNCHC RESV 1,0 NEG NMBR TO RE-INIT FOR 1 MSEC

 * TIMEOUT FOR N MILLISECONDS
 *
 TIME LDR \$R1,+\$B5 GET NMBR OF MSEC'S
 NEG \$=SR1
 LDR \$R4,<SYNCHC SET UP FOR FIRST MSEC
 B <SYNCHB TIMEOUT

 * RTC INTERRUPT ROUTINE
 *
 *
 T10MS LBT <FLAG,=X'4' 10 MILLISEC FLAG + OR - 8.3MS
 T1SEC LDR \$R4+\$B5 1 SEC ENTRY
 STB \$B5,<TIMOT5
 X \$A STR \$R4,<ZHRTCI INITIAL VALUE
 STR \$R4,<ZHRTCC CURRENT VALUE
 LDR \$B1,<TIMELOV5 GO HERE AFTER TIMEOUT
 LAB \$B1,<SA5P
 STB \$B1,<SA5DV
 MUL \$R5,=2 CONVERT TO TICKS
 U02495 OE47 CB55 TOTAL TICKS
 U02496 OE48 8280 UFC2 10 MILLISECS ?
 U02497 OE4A 0004
 U02498 OE4C C370 0064 T BBF >+\$A NO- ITS 1 SECOND
 U02499 OE4E 8800 UFC2 DIV \$R4,=100 CONVERT TO MS
 U02500 OE50 0004 LBF <FLAG,=X'4' FOR NEXT TIME
 U02501 OE51 CF00 0000 X \$A
 U02502 OE53 CF00 0000 X STR \$R4,<ZHRTCI
 U02503 OE55 9B80 0E62 STR \$R4,<ZHRTCC
 U02504 OE57 9F80 120E LAB \$B1,<TIMELOV5 CURRENT VALUE
 U02505 OE59 9B80 1208 STB \$B1,<SA5P GO HERE AFTER TIMEOUT
 U02506 OE5B 9F80 0005 X LDV \$B1,<SA5DV
 U02507 OE5D 2C05 STB \$B1,<ZHISAZ+5*\$AF
 U02508 OE5E AF00 0000 X RTCN \$R2,<ZHRTCL SET RUPT TO LVL 5
 U02509 OE60 0004 JMP \$B5 TURN CLOCK ON
 U02510 OE61 8385 JUMP RETURN TO PROCESS
 U02511
 U02512
 U02513 UE62 0005 TIMLVS RTCF TURN OFF CLOCK
 U02514 UE63 UC80 0E6C LDB \$B5,<TIMOT5
 U02515 UE65 C875 LDR \$R4,+\$B5 BUMP RETURN ADDRESS
 U02516 UE66 DF80 121A STB \$B5,<SA15P PREPARE FOR SUSPEND
 U02517 UE68 8E70 800F LEV =Z'8000'+15 SUSPEND TO LVL 15
 U02518 UE6A 0000 HLT
 U02519 UE6B 0FFF B >\$-1
 U02520
 U02521 UE6C 0000 TIMOT5 RESV \$AF,0
 *
 *
 * TIMER LOOP INTERRUPTED BY DEVICE EVENT
 * LOOPS ARE TOTLED AND STORED IN \$R6 AND \$R7
 * ENTRY DECLARED WITH LV10SU ROUTINE
 * LV10TM ENTRY ACCUMULATES MULTI EVENTS TEMPB/TEMPC
 * LV10T1 ENTRY RETURNS SINGLE EVENT TIME LOOP IN \$R5
 *
 *
 U02532 UE6D DF80 0E7F TIMER STB \$B5,<TIMEB5
 U02533 UE6F 8725 CL =\$R5 SET MINOR LOOP COUNT
 U02534 UE70 4CEC LDV \$R4,=-20 LOOP 20 TIMES
 U02535 UE71 4780 0E71 BINC \$R4,<\$ INCR MAJOR LOOP
 U02536 UE73 5780 0E70 BINC \$R5,<-\$A
 U02537 UE75 8900 UFC2 LBT <FLAG,=X'10' TIMER OVERFLOW
 U02538 UE77 0010
 U02539 UE78 D8FF 7FFF SB LDR • \$R5,=Z'7FFF
 U02540 UE7A 8040 0379 STOPIO,<DTCONT FORCE MAX TIME
 U02541 UE7C 0000 11FB
 U02542 UE7F 0000 B1OF >-\$B
 U02543 TIMB5 RESV \$AF,0
 *

002544 * DEVICE RUPT TAKES US HERE
 002545 *
 002546 UEB0 BC80 UFE8 LV10TM LDI <TEMPB
 002547 UEB2 FA55 LV10T1 ADD \$R7,\$R5
 002548 UEB3 8ED6 CAD =\$R6
 002549 UEB4 U680 BCF >+\$A
 002550 UEB5 0000 HLT
 002551 UEB6 8D80 UFE8 \$A SDI <TEMPB
 002552 UEB8 DC80 UE7F LDB \$B5,<TIME5
 002553 UEEA UF7F NOP >-\$1
 002554 UEB9 DF80 121A STB \$B5,<SA15P
 002555 UEBD 8E70 800F LEV =Z'8000'+15
 002556 UEBF 0000 HLT
 002557 UEB0 UFFF b >-\$1
 002558 *
 002559 *
 002560 *
 002561 * SET UP FOR LEV 10 RUPT
 002562 * LNJ \$B5,<LV10SU
 002563 * DC <PROCESS
 002564 *
 002565 *
 002566 UEB1 9CF5 LV10SU LDB \$B1,*+\$B5
 002567 UEB2 9F80 1214 STB \$B1,<SA10P
 002568 UEB4 8C51 AND =\$R1
 002569 UEB5 9570 03C0 OR =Z'03C0'
 002570 UEB7 9470 000A \$A IU =\$R1,<10
 002571 UEB9 8051 \$A IO =\$R1,<UTRUPT
 002572 UEB0 0000 11FC B10F >-\$A
 002573 UEB1 07FD JMP \$B5
 002574 *
 002575 *
 002576 *
 002577 * TIME DEVICE EVENT AT LEVEL 10 (MILLISECS)
 002578 * PREVIOUSLY LV10SU CALLED WITH
 002579 * LV10TM OR LV10T1 PARAMETERS
 002580 * \$R5 RETURNS TIME IN MS
 002581 * DEVICE RUPT INH ON EXIT
 002582 * ENTRY LNJ \$B5,<TIMEDE
 002583 * DC MSEC
 002584 * OK RETURN
 002585 * TIMEOUT KTN
 002586 *
 002587 *
 002588 UEB1 A875 TIMDE LDR \$R2,*+\$B5
 002589 UEBF DF80 UEBD STB \$B5,<TIMEDE5
 002590 UEA1 8756 CL =\$R6
 002591 UEA2 8757 CL =\$R7
 002592 UEA3 D380 UEBD TIMDE1 LNJ \$B5,<TIMER
 002593 UEA5 C800 UEB7 LDR \$R4,<SYNCHC
 002594 UEA7 8254 NEG =\$R4
 002595 UEA8 8750 CL =\$R6
 002596 UEA9 F855 LDR \$R7,<R5
 002597 UEA9 F354 DIV \$R7,<\$R4
 002598 UEB0 4041 SQR \$R4,1
 002599 UEB1 E354 DIV \$R6,<\$R4
 002600 UEB0 6A80 BLEZ \$R6,>+\$A
 002601 UEB1 8A07 INC =\$R7
 002602 UEBF FF55 STR \$R7,<=\$R5
 002603 UEB0 8751 \$B \$B1 CL =\$R1,<UTRUPT
 002604 UEB1 8051 \$D IU =\$R1,<UTRUPT
 002605 UEB2 0000 11FC B10F >-\$A
 002606 UEB4 07FD LDB \$B5,<TIMEDE5
 002607 UEB5 DC80 UEBD NOP >-\$1
 002608 UEB7 UF7F CMR \$R5,<=\$R2
 002609 UEB8 D952 T \$B \$B5,<=\$R2
 002610 UEB9 0380 BLEZ LAB \$B5,\$B5,\$AF
 002611 UEBA DBC5 0001 \$C JMP \$B5
 002612 UEB1 8385 *
 002613 UEBD 0000 TIMDE5 RESV \$AF,0
 002614 *
 002615 *
 002616 * COMPUTE AVE. TIME FOR TOTAL IN \$R6 AND \$R7
 002617 * AVE TIME IN \$R6 (MILLISECS)
 002618 *
 002619 *
 002620 UEB1 C800 UFCF T MAVE LDR \$R4,<LPCUR
 002621 UEC0 8A04 INC =\$R4
 002622 UEC1 C200 UFCE SUB \$R4,<LPCNT
 002623 UEC3 8C80 UFE8 LDI <TEMPB
 002624 UEC5 F354 DIV \$R4,<=\$R4
 002625 UEC6 4041 SQR \$R4,1
 002626 UEC7 E354 DIV \$R6,<=\$R4
 002627 UEC8 6A80 BLEZ \$R6,>+\$A
 002628 UEC9 8A07 INC =\$R7
 002629 UEC1 FF56 STR \$R7,<=\$R6
 002630 UECB C800 UEB7 LDR \$R4,<SYNCHC
 002631 UEC0 8254 NEG =\$R4
 002632 UEC1 E354 DIV \$R6,<=\$R4
 002633 UECF EF00 UFA7 STR \$R6,<AVETM
 002634 UED1 8385 JMP \$B5
 002635 *
 002636 *
 002637 *
 002638 *
 002639 *
 002640 UED2 DF80 UEE1 VDTR STB \$B5,<VDTRB5
 002641 UED4 9F00 UEDF STR \$R1,<VDTRA
 002642 UED6 FBC0 0003 CALL ZV\$1,VDTRA X
 002643 UEDC DC80 UEE1 LDB \$B5,<VDTRB5
 002644 UEDF 0000 JMP \$B5
 002645 UEDA 0F80 *
 002646 UEDF 0000 VDTRA RESV 1,0
 002647 UED0 2424 UC '\$\$'
 002648 UEE1 0000 VDTRB5 RESV \$AF,0
 002649 * CONVENT SAF OR LAF ADDRESS TO ASCII
 002650 *

```

002651 * HEX ADDRESS IN $B7
002652 * ASCII RESULT IN LNGAD
002653 *
002654 *
002655 UEE2 4C01 LNGASC LDV $R4,=$AF SET UP FOR EXIT
002656 UEE3 8254 NEG ==$R4
002657 UEE4 FF80 UFE7 STB $B7,<TEMPA STORE ADDR WORD/S
002658 UEE6 AB80 UFE7 LAB $B2,<TEMPA
002659 UEE8 BB80 UFCA LAB $B3,<LNGAD
002660 UEEA 8751 CL ==$R1
002661 UEEF 8752 CL ==$R2 WORD COUNTER
002662 UEEF F85E LDR $R7,$B2,+$R1 CHAR COUNTER
002663 UEEF 3CFC LDV $R3,=-4 GET ADDR WORD
002664 UEEE 8756 CL ==$R6 BYTE COUNTER
002665 UEEF 7084 DOL $R7,4 MORE AREA
002666 UEEF 6D09 CMV $R6,=9 STORE CHAR IN $R6
002667 UEF1 0380 BLE >+$C ALPHA OR NUMBER
002668 UEF2 6E07 ADV $R6,=7 NUMBER
002669 UEF3 6E30 ADV $R6,=X'30' ALPHA CONVERT
002670 UEF4 E7EF STH $R6,$B3,+$R2 ASCII CONVERT
002671 UEF5 37F9 BINC $R3,>-$B STORE ASCII
002672 UEF6 47F6 BINC $R4,>-$A GET NEXT CHAR
002673 UEF7 8385 JMP $B5 GET NEXT WORD
002674 *
002675 *
002676 ****
002677 * IO STALL ROUTINE
002678 *
002679 *
002680 UEF8 DF80 OF0C STALL STB $B5,<STALB5 CLEAR COUNTER
002681 UEEA 8755 CL ==$R5
002682 UEFB 8000 UFE5 $A IU <STAT1,<INSTW1
002683 UEFF 0000 1204 $B BIOT <STALLB EXIT
002684 UF01 57FA BINC $R5,>-$A WAIT HERE FOR BIOT
002685 UF02 8000 11F4 IU <STUPIO,<OTCONT ABORT
002686 UF04 0000 11FB BIOP >-$B
002687 UF06 07FC LDB $B5,<STALB5
002688 UF07 DC80 OF0C LAB $B5,$B5,$AF BUMP FOR ERROR RETURN
002689 UF09 DBC5 0001
002690 UF0D 8385 STALLB JMP $B5
002691 UFFC 0000 STALB5 RESV $AF,0
002692 *
002693 ****
002694 * GET TWO STATUS WORDS
002695 *
002696 UFFD 8000 0FE6 INSTAT IU <STAT2,<INSTW2 GET STATUS WORD 2
002697 UFFE 0000 1205 $B BIOP >INSTAT
002698 UFI1 07FC ISTAT1 IU <STAT1,<INSTW1 GET STATUS WORD 1
002699 UFI2 8000 0FE5
002700 UFI4 0000 1204
002701 UFI6 07FC
002702 UFI7 8385
002703 ****
002704 UFI8 8030 0FB9 SUCA IU <DXFN,$R3,<OTCONF SET UP CW1
002705 UFI9 0000 11F9 SUCB IU >SUCA
002706 UFI10 07FC <DXRN,$R3,<OTCON2 SET UP CW2
002707 UFI11 8030 UFB0 BIOP >SUCA
002708 UFI12 0000 11FA JMP $B5 RETURN
002709 *
002710 ****
002711 * FILL BUFFER WITH WORD STRING
002712 *
002713 * LNJ $B5,<FLBF
002714 * DC <SENDLK SENDER ADDRESS
002715 * DC DD SENDER WORD LENGTH
002716 * DC <RECEIVER RECEIVER ADDRESS
002717 * DC DD RECEIVER WORD LENGTH
002718 *
002719 UF23 ACF5 FLBF LDR $B2,+$B5 ADDR OF SENDER
002720 UF24 C875 LDR $R4,+$B5 LENGTH OF SENDER
002721 UF25 BC55 LDR $B3,+$B5 ADDR OF REC
002722 UF26 D875 LDR $R5,+$B5 LENGTH OF REC
002723 UF27 88D4 DEC ==$R4 SENDER COUNTER
002724 UF28 CF57 STR $R4,=$R7 SAVE INIT COUNT
002725 UF29 88D5 DEC ==$R5 REC COUNTER
002726 UF2A 8751 CL ==$R1 INITIALIZE INDEX REGS
002727 UF2B 8752 CL ==$R2
002728 UF2C E85E LDR $R6,$B2,+$R1 GET SENDER DATA
002729 UF2D EF6F STK $R6,$B3,+$R2 STORE IN RECEIVER
002730 UF2E 4700 BDEC $R4,>+$B MORE TO SEND ?
002731 UF2F 8751 CL ==$R1 RESET SENDER INDEX
002732 UF30 FF54 STR $R7,=$R4 INIT SENDER COUNT
002733 UF31 577B BDEC $R5,>-$A RECEIVER FILLED ?
002734 UF32 8385 JMP $B5
002735 *
002736 *
002737 ****
002738 * PRINT "IS" AND "SHOULD-BE" DATA
002739 *
002740 UF33 DF80 UF6E PISBC STB $B5,<PISBB5
002741 UF35 8D00 UFE7 SDI <TEMPA
002742 UF37 FBC0 0003 CALL ZV$T,MSTAG PRINT "TAG"
UF38 0000 X
UF39 UF80 X
UF3A UF80 X
UF3C 11D6
002743 UF3D FBC0 0003 CALL ZV$HZ,TEMPC,VRBL CONVERT TAG DATA TO ASCII
UF3E 0000 X
UF3F UF80 0000 X
UF40 UF80 X
UF41 UF80 X
UF42 UFE9 X
UF43 UFE2 X
002744 UF44 9800 OFF4 LDR $R1,<VRBL+2 PRINT HEX TAG
002745 UF45 D380 UED2 LNJ $B5,<VDTK
002746 UF46 D380 0000 CALL ZV$T,MSIS PRINT "IS"
UF47 FBC0 0003 X
UF48 FBC0 0003 X
UF49 D380 0000 X

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002747	UF4C UF80 UF4D 1198		CALL ZV\$HZ,TEMPB,VRBL	CONVERT "IS" DATA ASCII
002748	UF4E FBC0 0003 UF50 D380 0000	X	LDR LNJ \$R1,<VRBL+2 CALL ZV\$1,MSSB	PRINT HEX "IS" DATA PRINT "SB"
002749	UF52 UF80			
002750	UF53 0FE8 UF54 OFF2			
002751	UF55 9800 UFF4 UF57 D380 0ED2		LDR LNJ \$R1,<VRBL+2 CALL ZV\$1,MSSB	PRINT HEX "IS" DATA PRINT "SB"
002752	UF59 FBC0 0003 UF5B D380 0000	X		
002753	UF5C UF80			
002754	UF5E 11DA			
002755	UF5F FBC0 0003 UF61 D380 0000	X	CALL ZV\$HZ,TEMPA,VRBL	CONVERT SB DATA TO ASCII
002756	UF63 UF80			
002757	UF64 0FE7			
002758	UF65 UF72			
002759	UF66 9800 UFF4 UF68 D380 0ED2		LDK LNJ \$R1,<VRBL+2 *\$A NUP >\$Z LDB \$B5,<VDTR	PRINT HEX "SB" DATA
002760	UF69 8385		JMP \$B5	RETURN
002761	UF6A 0F02			
002762	UF6B DC80 UF6E			
002763	UF6C 8385			
002764	UF6D 9880 11F6			
002765	UF71 ABB0 120A			
002766	UF73 9800 UF84			
002767	UF75 9AF1			
002768	UF76 0180			
002769	UF77 9DD2			
002770	UF78 0200 UF75			
002771	UF79 8385			
002772	UF7A 0F75			
002773	UF7B UF80 UF86			
002774	UF7D FBC0 0003 UF7E D380 0000	X	CRLF STB \$B5,<CRLFBS CALL ZV\$T,ZV\$TC,NULL	(CRLF)
002775	UF7F UF80			
002776	UF80 6385		LDB \$B5,<CRLFBS JMP \$B5	RETURN
002777	UF81 UF80			
002778	UF82 0FD0			
002779	UF83 DC80 UF86			
002780	UF84 6385			
002781	UF85 0000			
002782	UF86 0000			
002783	UF87 FBFO 0001 UF88 D380 0000	X	PCH CALL ZV\$PCH	B <START RESTART
002784	UF89 UF80 0100			
002785	UF8A 0000			
002786	UF8B 0000			
002787	UF8C UF80 UF46			
002788	UF8D 9C80 UF6C			
002789	UF8E E830 UF89			
002790	UF8F F830 UF8D			
002791	UF90 FF00 FF00			
002792	UF91 9800 UFD3			
002793	UF92 A851			
002794	UF93 8AL2			
002795	UF94 2002			
002796	UF95 AF00 UF47			
002797	UF96 EF71			
002798	UF97 F71			
002799	UF98 8A07			
002800	UF99 1700 UF9E			
002801	UF9A DC80 UF46			
002802	UF9B 6385			
002803	UF9C 0000			
002804	UF9D 0000		SUFB B STB \$B5,<SUFB5 LDB \$B1,<JJPT LDR \$R6,<DXFN,\$R3 LDR \$R7,<DXRN,\$R3 AND \$R7,=Z'FF00' LDR \$R1,<SECTRK LDR \$R2,=\$R1 INC \$R2 SUL \$R2,2 STR \$R2,<TEMPA SUFBA STR \$R6,+\$B1 STR \$R7,+\$B1 INC \$R7 BDEC \$R1,<SUFB5 LDB \$B5,<SUFB5 JMP \$B5	STORE RETURN BUFFER ADDRESS LOAD CW1 LOAD CW2 SET SECTOR COUNT TO ZERO SET UP RANGE NUMBER OF ID'S 4 BYTES PER ID STORE RANGE SET UP ID'S INCREMENT SECTOR NUMBER BRANCH IF NOT COMPLETE RETURN
			SUFB5 KLSV \$AF,0	RETURN ADDRESS

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002805      ****
002806      *
002807      * CONSTANTS, MESSAGES, TABLES, BUFFERS, ETC.
002808      *
002809      ****
002810      * CONSTANTS, BUFFERS
002811      *
002812      UF A7 0000   AVLT M  RESV  1,0   AVERAGE TIME
002813      UF A8 0000   ASCBF  RESV  3,0   ASCII BUFFER
002814      UF AB 2420   DC      '$'    DRIVE NUMBER
002815      UF AC 0000   BITE   RESV  1,0   CURRENT CYL ADDRESS
002816      UF AD 0000   CYADMX RESV  1,0   MAX CYL ADDR
002817      UF AE 0000   D-1    DC      1
002818      UF AF 0001   DEVID  RESV  4,0   ID'S OF DEVICES FOUND
002819      UF B0 0000   DRIVE0 RESV  1,0   CURRENTLY SELECTED UNIT (FROM "DRIVE0" ETC.)
002820      UF B1 0000   DRIVE0 DC      Z:0U00:
002821      UF B5 0000   DRIVE1 DC      Z:0U080:
002822      UF B6 0080   DRIVE2 DC      Z:0100:
002823      UF B7 0100   DRIVE3 DC      Z:0180:
002824      UF B8 0180
002825      UF B9 0001   DXFN   RESV  4,1   HEX CYLINDER NUMBER FOR CURRENT DRIVE (CW1)
002826      UF B0 0000   DXRN   RESV  4,0   HEX SECTOR NUMBER FOR CURRENT DRIVE (CW2)
002827      UF C1 0000   ERFL   RESV  1,0   DIAGNOSTIC ERROR FLAG
002828      UF C2 0000   FLAG   RESV  1,0   GENERAL PURPOSE FLAG
002829      UF C3 0000   GOFLAG RESV  1,0   IF SET, BYPASS "START"
002830      UF C4 0000   IDEN   RESV  1,0   ID CODE
002831      UF C5 0000   IPFL   RESV  1,0   INHIBIT PRINT FLAG
002832      UF C6 0000   JJPT   RESV  $AF,0  WRITE BUFFER POINTER
002833      UF C7 0000   KKPT   RESV  $AF,0  READ BUFFER POINTER
002834      UF C8 805C   LFCR   DC      Z:805C: CRLF FOR ZVS
002835      UF C9 0400   LENGTH DC      1024 LENGTH (WORDS) OF WRITE/READ BUFFERS
002836      UF CA 0000   LNGAD  RESV  $AF*2,0 LONG ADDRESS ASCII
002837      UF CC 2020   2424   LNGAD  RESV  '$' END OF MESSAGE
002838      UF CE 0000   LPCTNT RESV  1,0   LOOPING COUNTER
002839      UF CF 0000   LPCUR  RESV  1,0   CURRENT LOOPING COUNTFR
002840      UF D0 0000   NULL   DC      0
002841      UF D1 003C   RTCHZ  DC      60
002842      UF D2 0000   SECAD  RESV  1,0   DEFAULT RTC FREQUENCY
002843      UF D3 003F   SECTRK RESV  1,63  CURRENT SECTOR ADDR
002844      UF D4 0000   TASK   RESV  1,0   NUMBER OF SECTORS PFR TRACK
002845      UF D5 0000   SAVE-1 RESV  16*$AF,0 CONTEXT SAVE AREA
002846      UF D5 0000   STAT1  RESV  1,0   FIRST STATUS WORD
002847      UF D6 0000   STAT2  RESV  1,0   SECOND STATUS WORD
002848      UF D7 0000   TEMPA  RESV  1,0
002849      UF D8 0000   TEMPB  RESV  1,0
002850      UF D9 0000   TLMPC  RESV  1,0
002851      UF EA 0000   TEMPD  RESV  1,0
002852      UF EB 0000   TEST   RESV  1,0
002853      UF EC 0000   TMFWD  RESV  1,0   TEST NUMBER
002854      UF ED 0000   TMREV  RESV  1,0   FWD SEEK TIME
002855      UF FF 0000   TMLPA  RESV  1,0   REV SEEK TIME
002856      UF FE 0000   TMLPB  RESV  1,0   MAJOR TIME LOOP
002857      UF FF 0000   TRKAD  RESV  1,0   MINOR TIME LOOP
002858      UF F1 0000   TRKMX  RESV  1,0   CURRENT TRK ADDR
002859      UF F2 0000   VRBL   RESV  3,0   MAX TRACK ADDR
002860      UF F5 2424   VRBLZ DC      '$'   VARIABLE CONVERSION BUFFER
002861      UF FG 6B6D   B6DB  16LB   WORSC  DC      Z:E06DB6DB16EB6DB6DB16* WORST CASE PATTERN
002862      UF FD FFFF
002863      UF FC 0000   X-FFFF DC      Z:FFFF* RETURNED STATUS FOR ZVS ROUTINES
002864
002865
002866
002867
002868
002871      UF FD 534D  4453  322C   ZVSTAT RESV  1,0
1000      2052  4556  2043   ****
2C20      5354  4F52
4147      4520  4D4F
4455      4C45  2054
2644      2C20  4645
4220      3138  2C31
3937      3824
434F      5059  5249
4748      5420  3139
3737      2042  5920
484F      4E45  5957
454C      4C20  494E
464F      524D  4154
494E      4E20  5359
5354      454D  532C
2049      4E43  2E00
002873
002874      102F  2020  2020   MSCYNO TEXT  '----- CYL NO. $'
1032      2020  4359  4C20
4E4F  ZE20  2024
002875      1038  2020  2020   MSLVNO TEXT  '     DRIVE NO. $'
103B      2020  4452  4956
4520  4E4F  2E20
2024
002876      1042  2020  2020   MSHLNK TEXT  '$! BLANKS'
002877      1045  524D  5741   MSFWRV TEXT  '$! FIRMWARE REV. $'
5245  2052  4556
1048      2E20  2024
002878      104D  5345  5256   MSIND  TEXT  '$OBSERVE THE START INDICATOR ON THE DEVICES$'
1050      4520  5448  4520
5354  4152  5420
494E  4449  4341
544F  5220  474E
2054  4845  2044
4556  4943  4524
4946  2049  4C4C
554D  494E  4154
4544  2C20  5052
4553  5320  5354
4152  5420  5357
4954  4348  2041
4E44
002880      1075  0D0A
002881      1076  2020  2020  2045   TEXT  Z:0D0A! CRLF
1079      4E54  4552  2028
5929  2041  5420
434F  4E53  4F4C
4520

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002882	1083	000A 2024	MSIND2 TEXT	Z"000A", "\$" CRLF "IF NOT ILLUMINATED,ENTER (N) AT CONSOLE "
002883	1085	4946 204E 4F54		
	1088	2049 4C4C 554D		
		494E 4154 4544		
		2C45 4554 4552		
		2028 4E29 2041		
		5420 434F 4E53		
		4F4C 4920		
002884	1099	000A 2024		
002885	109B	4948 5354 414C	MSILP TEXT	Z"000A", "\$" CRLF "INSTALL LOGIC PLUG\$"
	109E	4C20 444F 4749		
		4320 504C 5547		
		2400		
002886	10A5	2020 4U53 2041	MSKTM TEXT	" MS AVE. SEEK TIME \$"
	10A8	5645 2E20 5345		
		454B 2054 494D		
		4520 2400		
002887	10B0	454E 5445 5220	MSLCT TEXT	"ENTER LOOP COUNT \$"
	10B3	4C4F 4F50 2043		
		4F55 4E54 2020		
		2020 2020 2400		
002888	10B6	4C4F 4F50 2024	MSLP TEXT	"LOOP \$"
002889	10BF	4D49 5353 494E	MSMRT TEXT	"MISSING RESOURCE TRAP \$"
	10C2	4720 5245 534F		
		5552 4345 2054		
		5241 5020 2400		
002890	10CD	2020 4U53 204D	MSMXF TEXT	" MS MAX. FWD TIME \$"
	10CE	4158 2E20 4657		
		4420 2054 494D		
		4520 2400		
002891	10D0	2020 4U53 204D	MSMXR TEXT	" MS MAX. REV TIME \$"
	10D9	4158 2E20 5245		
		5620 2054 494D		
		4520 2400		
002892	10E1	2020 2020 2020	MSUVID TEXT	" DEVICE ID\$"
	10E4	2044 4556 4943		
002893	10EA	5245 504C 5920	MSDONE TEXT	"REPLY (Y) WHEN DONE \$"
	10ED	2859 2920 5748		
		454E 2044 4F4E		
		4520 2020 2024		
002894	10F6	454E 4420 5426	MSETD TEXT	"END T&D ROUTINE \$"
	10F9	4420 524F 5554		
002895	10FF	5052 4F54 4543	MSPS1 TEXT	"PROTECT SWITCH LIT ? \$\$"
	1102	5420 5357 4954		
		4348 204C 4954		
		2020 3F20 2024		
		2400		
002896	110C	5245 5350 4F4E	MSPSIA TEXT	"RESPOND (Y) OR (N) \$\$"
	110F	4420 2859 2920		
		4F52 2028 4E29		
		2024 2400		
002897	1117	4445 5052 4553	MSPS2 TEXT	"DEPRESS PROTECT SWITCH \$"
	111A	5320 5052 4F54		
		4543 5420 5357		
		4954 4348 2024		
002898	1123	2020 2020 2020	MSSCNO TEXT	" SEC NO. \$"
	1126	2020 5345 4320		
		4E4F 2E20 2020		
		2400		
002899	112D	4445 5052 4553	MSSTR TEXT	"DEPRESS START SWITCH \$"
	1130	5320 5354 4152		
		5420 5357 4954		
		4348 2024		
002900	1136	4552 524F 5220	MSTDERR TEXT	"ERROR DICTIONARY ENTRY\$"
	113B	4E41 5259 2045		
		4E54 5259 2400		
002901	1144	2020 2020 2020	MSTKNO TEXT	" TRK NO. \$"
	1147	2020 5452 4820		
		4E4F 2E20 2020		
		2400		
002902	114E	5749 5448 494E	MSTIME TEXT	"WITHIN 15 SECS. \$"
	1151	2031 3520 5345		
		4353 2E20 2400		
002903	1157	0000	MSDICT RESV	1,0
002904	1158	5245 4D4F 5645	MSRLP TEXT	"REMOVE LOGIC PLUG \$"
	115B	204C 4F47 4943		
		2050 4C55 4720		
		2400		
002905	1162	0000	MSTE RESV	1,0
002906	1163	2020 5445 5354	MSTEF TEXT	" TEST FAILED ***** \$"
	1166	2046 4149 4C45		
		4420 2A2A 2A2A		
		2A2A 2A2A 2A2A		
		2024		
002907	1170	2020 5445 5354	MSTEP TEXT	" TEST PASSED \$"
	1173	2050 4153 5345		
		4420 2400		
002908	1178	2020 5445 5354	MSTBP TEXT	" TEST BYPASSED \$"
	117D	2042 5950 4153		
		5345 4420 2400		
002909	1181	5354 4152 5420	MTEST TEXT	"START TEST\$"
	1184	5445 5354 2400		
002910	1187	2054 494D 4552	MSTMOF TEXT	" TIMER OVERFLOW \$"
	118A	204F 5645 5246		
		4C4F 5720 2400		
002911	1190	2020 574F 5244	MSWORD TEXT	" WORD\$"
	1193	2400	MSCHAN TEXT	" CHANNELS\$"
002912	1194	4348 414E 4E45		
	1197	4C24	MSIS TEXT	" IS \$"
002913	1198	2020 4953 2020		
	119B	2400	MSND TEXT	"NO DEVICE THIS CHANNEL \$"
002914	119C	4E4F 2044 4556		
	119F	4943 4520 5448	MSREGS TEXT	"REGISTER DUMP;\$\$"
		4953 2043 4841		
		4E4E 454C 2024		
002915	11AB	5245 4749 5354	MSRTC TEXT	"POWER FREQ, HZ\$"
	11AB	4552 2044 554D		
		503A 5424 5220		
002916	11B0	504F 5745 5220		
	11B3	4652 4551 2C20		

002917 11B8 485A 2400
 11B9 5245 4144 2020
 11B9 4255 4646 2041
 4444 5220 2848
 4558 293A 2020
 2400

002918 11C5 5245 5354 4152
 11C8 5424
 002919 11C9 5752 4954 4520
 11C9 4255 4646 2041
 4444 5220 2848
 4558 293A 2020
 2400

002920 11D6 2020 5441 4720
 11D9 2400
 002921 11D0 2C20 2053 4220
 2024
 002922 11DE 4F4B 2054 4F20
 11E1 5752 4954 4520
 3F20 2024

002923
 002924
 002925

002926 11E6 0000
 11E7 0100
 002928 11E8 8000
 002929 11E9 8100
 002930 11EA 8200
 002931 11EB 8300
 002932 11EC 8400
 002933 11ED C000
 002934 11EE C800
 002935 11EF D000
 002936 11F0 2000
 002937 11F1 1000
 002938 11F2 0800

 * LIST OF TASK WORDS (IO <OTTASK)
 * RECAL DC Z'0000' RECALIBRATE
 SEEK DC Z'0100' SEEK
 FMRW DC Z'8000' FORMAT READ OR WRITE
 RWD DC Z'8100' READ OR WRITE DATA
 DFMRW DC Z'8200' DIAGNOSTIC FORMAT READ OR WRITE
 DRWD DC Z'8300' DIAGNOSTIC READ OR WRITE DATA
 FRIDW DC Z'8400' FORMAT READ IV OR FORMAT WRITE
 WRPAR DC Z'C000' WRAPAROUND
 SZRL DC Z'8C00' SEIZE OR RELEASE
 TCIO DC Z'D000' TAG CODE IN OR OUT
 ASB DC Z'2U00' AUTOMATIC SEEK BIT
 SSB DC Z'1U00' SECTOR SIZE BIT
 ARB DC Z'0800' AUTOMATIC RPS BIT

 * LIST OF CONTROL WORDS (IO <STOPIO<OTCONT)
 * INZBDC DC Z'8000' INITIALIZE MSC, DO QLT ON 4 PORTS OF MSC
 STOPIO DC Z'4000' RESET BSY, RUPT IF FNB, STOP DMA (AFFECTS 1 P)

 * CHANNEL CONSTANTS
 * CHAN DC Z'1200' DEFAULT CHAN ADDRESS
 * IOREAD DC Z'1209' IOLD READ
 IOWRIT DC Z'1249' IOLD WRITE
 OTUFRG DC Z'120F' OUTPUT OFFSET RANGE
 * OTCNF DC Z'1211' OUTPUT CONFIGURATION WORD 1
 OTCN2 DC Z'1213' OUTPUT CONFIGURATION WORD 2
 OTCNT DC Z'1201' OUTPUT CONTROL WORD
 OTKURT DC Z'1203' OUTPUT INTERRUPT CONTROL
 OTTASK DC Z'1207' OUTPUT TASK WORD
 * INCONF DC Z'1210' INPUT CONFIGURATION WORD 1
 INCONF2 DC Z'1212' INPUT CONFIGURATION WORD 2
 INFWRV DC Z'123C' INPUT FIRMWARE REV
 INIDEN DC Z'1226' INPUT ID CODE
 INRANG DC Z'120C' INPUT RANGE
 INRPUT DC Z'1202' INPUT INTERRUPT CONTROL
 INSTW1 DC Z'1218' INPUT STATUS WORD 1
 INSTW2 DC Z'121A' INPUT STATUS WORD 2
 INMMA DC Z'120A' INPUT MEMORY MODULE ADDRESS
 INMBA DC Z'1208' INPUT MEMORY BYTE ADDRESS
 INUFRG DC Z'120E' INPUT OFFSET RANGE
 INTASK DC Z'1206' INPUT TASK WORD
 * CHANZ EQU \$ END OF CHANNEL WORDS
 *
 * LEVEL 5 INTERRUPT SAVE AREA (RTC LEVEL)
 * SA5TL RESV \$AF,0 TSA LINK
 SA5DV RESV 1,0 DEVICE
 SA5M1 DC 0 MASK 1
 SA5M2 DC 0 MASK 2
 SA5P RESV \$AF,0 P REG
 SA5S DC Z'4000' PRIV BIT
 *
 * LEVEL 10 INTERRUPT SAVE AREA (DEVICE INTERRUPT LVL. WHFN ENABLED)
 * SA10TL RESV \$AF,0 TSA LINK
 SA10DV RESV 1,0 DEVICE
 SA10M1 DC 0 MASK 1
 SA10M2 DC 0 MASK 2
 SA10P RESV \$AF,0 P REG
 SA10S DC Z'4000' PRIV BIT
 *
 * LEVEL 15 INTERRUPT SAVE AREA (NORMAL RUNNING LEVEL)
 * SA15TL RESV \$AF,0 TSA LINK
 SA15DV RESV 1,0 DEVICE
 SA15M1 DC 0 MASK 1
 SA15M2 DC 0 MASK 2
 SA15P RESV \$AF,0 P REG
 SA15S DC Z'4000' PRIV BIT
 *
 * TRAP SAVE AREA 1
 * TSALI RESV \$AF,0 TSA LINK
 TSALII RESV 1,0 REGISTER
 TSAIR3 RESV 1,0 R3
 TSALF RESV 1,0 F
 TSAIZ RESV 1,0 Z
 TSAIEA RESV \$AF,0 EA
 TSAIP RESV \$AF,0 P
 TSAIB3 RESV \$AF,0 B3
 *
 * TRAP SAVE AREA 2
 * TSAS2 RESV \$AF,0 TSA LINK

003016 1225 0000
 003017 1226 0000
 003018 1227 0000
 003019 1228 0000
 003020 1229 0000
 003021 122A 0000
 003022 122B 0000
 003023 122C 0100
 0000 EKR CULNT

TSA21 RESV 1,0
 TSA2B3 RESV 1,0
 TSA2F RESV 1,0
 TSA2Z RESV 1,0
 TSA2EA RESV \$AF,0
 TSA2P RESV \$AF,0
 TSA2B3 RESV \$AF,0
 *****END*****

I REGISTER
 R3
 F
 Z
 EA
 P
 B3

*****END*****
 *****SMD52.*START*****

TITLE SMD52, *REV C*, STORAGE MODULE T&D, FEB 18, 1978
 503 SA 500B 504B 520B 537 540B 573B 611B 621B 624B 627B
 630B 650B 655B 662B 728B 750B 762B 779B 785B 835B
 840B 866B 897B 912B 936B 978B 1004B 1050B 1096B 1124B
 1172B 1177B 1199B 1209B 1247B 1270B 1333B 1364B 1416 1487B
 1527B 1561B 1619B 1652B 1686B 1700B 1716B 1728B 1734B 1776
 1832B 1867B 1884B 1898B 1923B 1968B 2025B 2109B 2129B 2134B
 2177B 2204B 2238B 2263B 2284B 2323B 2325B 2346B 2348B 2369B
 2395B 2450B 2497B 2536B 2549B 2572B 2600B 2627B 2672B 2684B
 2733B

519 SA
 539 SA
 576 SA
 606 SA
 623 SA
 626 SA
 629 SA
 632 SA
 653 SA
 658 SA
 665 SA
 678 SA
 733 SA
 746 SA
 753 SA
 760 SA
 782 SA
 837 SA
 864 SA
 904 SA
 909 SA
 934 SA
 981 SA
 1007 SA
 1057 SA
 1099 SA
 1128 SA
 1171 SA
 1180 SA
 1202 SA
 1208 SA
 1250 SA
 1273 SA
 1304 SA
 1339 SA
 1367 SA
 1419 SA
 1490 SA
 1530 SA
 1572 SA
 1625 SA
 1655 SA
 1685 SA
 1692 SA
 1724 SA
 1727 SA
 1778 SA
 1834 SA
 1870 SA
 1891 SA
 1897 SA
 1926 SA
 1967 SA
 2028 SA
 2112 SA
 2131 SA
 2133 SA
 2179 SA
 2199 SA
 2241 SA
 2255 SA
 2288 SA
 2330 SA
 2353 SA
 2372 SA
 2394 SA
 2452 SA
 2500 SA
 2534 SA
 2551 SA
 2571 SA
 2602 SA
 2629 SA
 2662 SA
 2682 SA
 2728 SA
 2756 SA
 SA

	402	407C	413	417	429C	438C	524C	528C	562	581
583	640	698	1241C	1372C	1442C	1495	1499	1519C	1669	1782
	1793	1821C	1957	1964	2082	2083	2084	2085	2086	2121
	2145	2160	2190	2214	2223	2245	2265	2273	2286	2291
	2335	2358	2377	2413	2429	2505C	2521	2542	2610	2613
	2648	2655	2688	2691	2759	2778	2804	2832	2833	2836
	2845	2867	2869	2977	2981	2986	2990	2995	2999	3004
	3009	3010	3011	3015	3020	3021	3022			
	571B	609B	613B	760B	789B	795B	836B	847B	864B	879B
	886B	905B	946B	982B	1010B	1060B	1131B	1174B	1211B	1251B
	1309B	1368B	1382B	1491B	1542B	1573B	1662B	1694B	1730B	1777
	1829C	1872B	1902B	1928B	1972B	2137B	2185B	2236B	2240B	2259B
	2327B	2329B	2350B	2352B	2371B	2400B	2540B	2671B	2686B	2730B

612 SB
 615 SB
 644 SB

699792
704450
808445
890813
101347
111315
111312
111311
111310
111309
111308
111307
111306
111305
111304
111303
111302
111301
111300
111300

\$B1	426	427C	428	429C	435	436C	437	438C	464	466
	467	475	476C	477C	497	498C	511	512C	517	518C
	523	524C	525	526C	527	528C	1240	1241C	1371	1372C
1441	1442C	1518	1519C	1676	1677C	1708	1709C	1820	1821C	
2195	2199	2202C	2208	2212	2436	2437C	2454	2455C	2502	2502C
2503C	2504	2505C	2566	2567C	2763	2766	2767	2788	2797C	
\$B2	465	467	479	480C	1675	1692C	1693	1697	1699	1702C
	1727	1731	1733	1741	1742	1952	1953C	1959	1960C	2004
\$B3	2006	2196	2203C	2658	2662	2719	2728	2764	2767	
\$B4	2659	2670C	2721	2729C						
\$B5	398	399C	400	410	521	1665C				
	411B	415B	450B	536B	556B	560B	564B	567B	574B	579B
	585B	587B	004B	638B	642B	646B	651B	656B	663B	665B
	677B	680B	692B	693B	694B	699B	704B	718B	719B	723B
	725B	729B	731B	735B	737B	739B	746B	751B	753B	755B
	758B	763B	769B	773B	777B	780B	782B	787B	790B	792B
	797B	799B	803B	807B	809B	811B	815B	817B	819B	821B
	825B	829B	831B	833B	837B	848B	850B	854B	856B	860B
	862B	867B	871B	873B	875B	877B	880B	882B	890B	892B
	894B	900B	902B	906B	909B	913B	916B	918B	929B	934B
	937B	941B	944B	947B	960B	961B	965B	969B	971B	975B
	979B	983B	987B	991B	995B	997B	999B	1001B	1005B	1007B
	1011B	1013B	1016B	1018B	1025B	1027B	1029B	1033B	1037B	1039B
	1041B	1043B	1045B	1053B	1055B	1057B	1061B	1063B	1065B	1067B
	1071B	1078B	1080B	1084B	1091B	1093B	1097B	1099B	1116B	1117B
	1119B	1121B	1126B	1128B	1132B	1136B	1140B	1143B	1145B	1149B
	1153B	1157B	1159B	1163B	1165B	1169B	1175B	1178B	1182B	1184B
	1186B	1190B	1192B	1194B	1196B	1200B	1202B	1204B	1214B	1216B
	1220B	1222B	1224B	1239B	1242B	1244B	1248B	1252B	1254B	1256B
	1258B	1260B	1262B	1265B	1267B	1271B	1275B	1285B	1291B	1292B
	1293	1294C	1295B	1297B	1300B	1305B	1313B	1315B	1317B	1321B
	1325B	1327B	1329B	1340B	1355B	1356B	1357B	1359B	1361B	1365B
	1369B	1385B	1387B	1389B	1393B	1395B	1399B	1403B	1405B	1407B
	1411B	1414B	1420B	1421B	1424B	1440B	1443B	1446B	1447B	1448B
	1452B	1454B	1456B	1459B	1463B	1467B	1471B	1473B	1477B	1479B
	1481B	1484B	1488B	1493B	1497B	1501B	1517B	1520B	1522B	1524B
	1528B	1535B	1539B	1543B	1545B	1547B	1555B	1557B	1559B	1562B
	1566B	1568B	1570B	1575B	1577B	1579B	1586B	1591B	1595B	1598B
	1601B	1629B	1631B	1643B	1644B	1645B	1647B	1653B	1655B	1657B
	1663B	1666B	1722B	1724B	1756B	1758B	1773B	1775B	1780B	1783B
	1787B	1791B	1794B	1796B	1800B	1812B	1815B	1822B	1823B	1824B
	1835B	1837B	1840B	1842B	1846B	1862B	1864B	1866B	1870B	1873B
	1877B	1879B	1881B	1885B	1887B	1889B	1891B	1899B	1903B	1907B
	1910B	1917B	1919B	1924B	1929B	1934B	1940B	1942B	1946B	1948B
	1950B	1954B	1961B	1969B	1973B	1975B	1977B	1981B	1983B	1985B
	1988B	1995B	1997B	2020B	2026B	2032B	2097C	2098B	2100B	2106B
	2110B	2112B	2114B	2116B	2118	2119B	2135	2138C	2141B	2142B
	2143B	2153C	2154B	2157	2158B	2171	2172C	2178B	2186B	2187B
	2189B	2213B	2216B	2219B	2228B	2229C	2242B	2243B	2254C	2264
	2265	2269	2266C	2267B	2269B	2270	2271B	2281C	2285	2286
	2286	2287C	2288	2289	2303	2312B	2319C	2332B	2333B	2342C
	2355	2356B	2365C	2374	2375B	2389	2390C	2393B	2405B	2407B
	2410	2411B	2422C	2425B	2426	2427B	2476B	2482	2491	2492C
	2509B	2514	2515	2516C	2532C	2552	2554C	2566	2573B	2588
	2589C	2592B	2606	2610	2610	2611B	2634B	2640C	2643	2644B
	2673B	2680C	2687	2688	2688	2689B	2700B	2708B	2719	2720
	2721	2722	2734B	2740C	2745B	2749B	2753B	2756	2757B	2769B
\$B6	2773C	2775	2776B	2787C	2801	2802B				
\$B7	408	409	414	522						
\$C	410	414	2122	2657C						
	584B	761B	768B	806B	845B	865B	885B	915B	986B	1024B
	1036B	1070B	1139B	1181B	1264B	1310B	1384C	1450B	1538B	1581B
	1665C	1696B	1732B	1786B	1834C	1876B	1933B	1980B	2140B	2167B
	2162B	2218B	2234B	2261B	2331B	2354B	2373B	2397B	2609B	2667B

TITLE SMLS2, REV C, STORAGE MODULE T&D, FEB 18, 1978

PAGE 41

ZV\$HZ 2015B 2018B 2743B 2747B 2751B
 ZV\$1A 2255B 606B 1531B 2321B 2344B 2367B
 ZV\$1D 422B 615B
 ZV\$1H 462B 398
 ZV\$LK 387
 ZV\$PCH 2782B
 ZV\$GC 221B 461B 535B 605B 610B 612B 614B 1492B 2262B 2320B
 2330B 2343B 2353B 2366B 2372B
 ZV\$KD 393B
 ZV\$SV1 387
 ZV\$SV2 387
 ZV\$T 412B 413B 416B 417B 421B 461B 501B 507B 535B 557B
 559B 576B 578B 605B 610B 612B 614B 635B 637B 743B
 744B 745B 775B 776B 908B 943B 1423B 1492B 1530B 1597B
 1600B 1603B 1774B 1778B 1779B 1789B 1790B 1813B 1814B 1855B
 2011B 2014B 2016B 2017B 2019B 2034B 2155B 2174B 2183B 2188B
 2214B 2235B 2239B 2241B 2262B 2320B 2330B 2343B 2353B 2366B
 2372B 2438B 2642B 2742B 2746B 2750B 2774B
 ZV\$TC 412B 416B 501B 507B 557B 559B 576B 578B 635B 637B
 743B 744B 745B 775B 908B 943B 1530B 1774B 1778B 1779B
 1789B 1790B 1813B 1855B 2011B 2034B 2155B 2174B 2188B 2438B
 2803 ZV\$STAT 2255
 622 LABELS
 2669 REFERENCES
 3024 RECORDS
 0 U FLAGS
 1 M FLAGS
 313 N FLAGS
 CROSS RLF VERSION L - 24 SEPT, 1978
 RS LINKER VERSION 5.00 02/21/78 1203.5 EST TUE
 INK MAP FOR SMD52
 START 0100
 LOW 0000
 HIGH 1A61
 CURRENT 1A62
 *LUC DEFS
 ZHCOLMN 0000
 *SMD52 0000 REV C
 ZHPFR 0000
 ZH1SA 0002
 ZHNTSA 0010
 ZHRTC1 0014
 ZHRTCC 0015
 ZHRTCL 0016
 ZHWUTC 0017
 ZHMERC 001F
 ZH1AFD 0020
 ZHTH29 0063
 ZHTH28 0064
 ZHTH27 0065
 ZHTH26 0066
 ZHTH25 0067
 ZHTH24 0068
 ZHTH23 0069
 ZHTH22 006A
 ZHTH21 006B
 ZHTH20 006C
 ZHTH19 006D
 ZHTH18 006E
 ZHTH17 006F
 ZHMEMP 006F
 ZHTH16 0070
 ZHLERR 0070
 ZHTH15 0071
 ZHNRES 0071
 ZHTH14 0072
 ZHPMEM 0072
 ZHTH13 0073
 ZHP BP 0073
 ZHTH12 0074
 ZHTH11 0075
 ZHTH10 0076
 ZHTH9 0077
 ZHTH8 0078
 ZHTH7 0079
 ZHTH6 007A
 ZHGVFL 007A
 ZHTH5 007B
 ZHUP-N 007B
 ZHTH4 007C
 ZHTH3 007D
 ZHSC-N 007D
 ZHTH2 007E
 ZHRC 007E
 ZHTH1 007F
 ZHMCL 007F
 ZHISAZ 0080
 ZHIVBS 0080
 ZHTVBS 0080
 *ZV\$1 122C REV. 5.0
 ZV\$TC 1235
 ZV\$T 122C
 ZV\$WL 1249
 ZV\$U 123E
 *ZV\$1H 125D
 ZV\$1D 1262
 ZV\$1H 125D
 ZV\$1AU 1267
 ZV\$--2 127F
 ZV\$--3 1291
 *ZV\$TH 12F6
 ZV\$TH 12F6
 ZV\$TD 132D
 ZV\$TH2 131E
 *ZV\$FK 1346
 ZV\$FK 1346
 ZV\$FI 1368
 ZV\$FS 1388
 ZV\$FRA 1398

ZV\$FRX	1399
ZV\$FRK	1350
ZV\$FRD	139A
ZV\$FRH	1397
*ZV\$IA	139D REV. 6.0
ZV\$IA	139E
ZV\$AKU	1443
ZV\$ABP	1445
ZV\$--I	1408
ZV\$IAV	1471
*ZV\$DUK	1475
ZV\$DRK	1475
*ZV\$CLH	148F
ZV\$CH	148F
*ZV\$GP	1591
ZV\$GP	1591
ZV\$--4	15B1
*ZV\$IA	15B1
ZV\$IL	15C7
ZV\$IA	15B1
ZV\$IS	15C2
*ZV\$IU	15F6
ZV\$IU	15F6
*ZV\$MLW	1628 REV. 0
ZV\$MLW	1628
ZV\$ILK	1657
*ZV\$LK	166F REV. 5.0
ZV\$IA	169B
ZV\$LK	166F
ZV\$--U	1682
*ZV\$LU	16DF REV. 6.0
ZV\$LU	16DF
ZV\$LK	1709
ZV\$UK	1705
ZV\$--Z	1863
ZV\$UF	1784
ZV\$SV1	1853
ZV\$SV3	1873
ZV\$AF	16F0
ZV\$TY	16F2
ZV\$IU	16F1
ZV\$LFZ	16F9
ZV\$IK	16F5
ZV\$RAK	16F6
ZV\$SI	16FA
ZV\$DUU	16F3
ZV\$ULU	16FD
ZV\$KLU	16FE
ZV\$KCL	16FB
ZV\$NSK	1702
ZV\$SLK	1700
ZV\$LKS	1704
ZV\$IZ	1717
ZV\$IR	170C
ZV\$DAI	16EE
ZV\$IM	1753
ZV\$ILKU	1706
ZV\$ILKL	1707
ZV\$LRU	1708
ZV\$LKL	1709
ZV\$MBU	170A
ZV\$CF	16F8
ZV\$--S	170F
ZV\$MDU	16EF
ZV\$MCU	170B
H1DAU	170A
ZV\$RAW	16F7
ZV\$RD	1dAF
ZV\$CTL	16F4
ZV\$BL	1822
ZV\$SI	180F
ZV\$HDC	1800
ZV\$99	1A5D
ZV\$LSA	1712
ZV\$UH	1700
ZV\$LU	1791
ZV\$SH	1793
*UNLINK MODULE(S)	
ZV\$IU	
ZV\$HZ	
ZV\$TH	

O

C

C