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IDENTIFICATION

PRODUCT CODE: AC-E950B-MC  
PRODUCT NAME: CXKMCB0 KMC-11 MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

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MAIN DEC CHANGE NOTICE  
MAY BE REQUIRED FOR  
PROGRAM TO OPERATE

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1. ABSTRACT

KMC IS AN IOMOD THAT EXERCISES UP TO AND INCLUDING TWO CONSECUTIVELY ADDRESSED AND CONSECUTIVELY VECTORED KMC11 SYNCHRONOUS INTERFACES. IT USES NO LINE UNIT FOR RECEIVING AND TRANSMITTING DATA. DATA BUFFERS ARE TRANSMITTED AND RECEIVED FROM PDP11 MEMORY TO KMC11 & VICE VERSA. THE RECEIVER AND TRANSMITTER ISRS ARE PERFORMED AT LEVEL 0 (PIRQ). DATA CHECKING IS PERFORMED AT LEVEL 0 AND DONE OUTSIDE THE ISRS.

2. REQUIREMENTS

HARDWARE: AT LEAST 1 KMC11

STORAGE:: KMC REQUIRES:

1. DECIMAL WORDS: 2235
2. OCTAL WORDS: 04273
3. OCTAL BYTES: 10566

3. PASS DEFINITION

ONE PASS OF THE KMAA MODULE CONSISTS OF TRANSMITTING AND RECEIVING 1 BUFFERS OF 2-512 CHARACTERS 200 TIMES FOR EACH SELECTED DEVICE.

4. EXECUTION TIME

RUNNING ALONE ON AN 11/45 ONE PASS TAKES APPROXIMATELY ONE MINUTE. IF RUN AT XX BAUD AND XX BUFFER SIZE

5. CONFIGURATION PARAMETERS.

DEFAULT PARAMETERS:

ADDR: 1, VECTOR: 1, BR1: 5, BR2: 5, DVID1: 1, SR1:0

KMAA WILL RUN UP TO TWO CONSECUTIVELY ADDRESSED AND CONSECUTIVELY VECTORED KMC11'S. THERE ARE THREE PARAMETERS WHICH CAN BE CONTROLLED IN THIS MODULE.

1. NPR RATE:- THIS CONTROLS THE RATE OF NPR'S OCCURRING FROM KMC11'S. USING MODIFY COMMAND THIS CAN BE SET TO SPECIFIC VALUE. THE ADDRESS OF THIS PARAMETER IS 226 IN KMAA MODULE.

THIS PARAMETER CAN BE CHOSEN IN TWO DIFFERENT WAYS.

I. WHEN SR1<BIT15>:=1 THEN WHATEVER IN LOC 222[RTMULV] IS LOADED INTO NPRATE LOCATION[226].

II. WHEN SR1<BIT14>:=1 THEN NPRATE BECOMES EQUAL TO RTMULV MULTIPLIED BY SR1<6:11>.

DEFAULT:: 10000[OCTAL]

RANGE:: 12-7888 USEC/NPR.

2. NPR/BR RATE:- CONTROLS THE RATE OF NPR'S PER INTERRUPT.

LIKE NPR RATE THIS PARAMETER CAN ALSO BE CHOSEN IN TWO DIFFERENT WAYS. IN THIS CASE SZMULV[220] WILL BE USED IN PLACE OF RTMULV. AND SR1<0:5> CONTENTS WILL BE USED TO MULTIPLY.  
PARAMETERS :: RSIZE(202),XSIZE(204)

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DEFAULT:: 377(OCTAL)

RANGE:: 1-377(OCTAL)

CAN BE SET USING MOD COMMAND

3.DIRECTION OF NPR'S:- THE DIRECTION OF NPR'S

CAN BE CHOSEN SETTING PROPER BIT IN SR1.

EXPLANATION FOLLOWS.

6. DEVICE/OPTION SETUP

SR1(SWITCH REGISTER CONTENTS) OPTION.

BIT15:1 I.E SR1:10XXXX

NPRATE:= RTMULV \* SR1 <6:11>

BIT14:1 I.E SR1:04XXXX

NPR/BR:= SZMULV \* SR1 <0:5>

BIT 15 & 14:0

NPRATE:= RTMULV

BIT 15 & 14:1

NPR/BR:= SZMULV

BIT13:1 I.E SR1:X2XXXX

DEFAULT RATE.

BIT12:1 I.E SR1:X1XXXX

ILLEGAL.

BIT13 & 12:0

XMIT ONLY.

BIT13 & 12:1

RECEIVE ONLY

SR1 BITS 6:11

DEFAULT.

SR1 BITS 5:0

ILLEGAL.

NPR RATE MULTIPLIER.

NPR/BR RATE MULTIPLIER.

NOTE: SR1 CAN BE SET UP AT CONFIGURATION TIME OR  
AT RUN TIME WITH A MOD COMMAND.

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7. MODULE OPERATION

1. LOAD SOFTWARE POINTERS IN LINK TABLE. SET PARAMETERS.
2. LOAD VECTORS AND PRIORITIES IN TABLE
3. LOAD MICRO-CODE, VERIFY IT AND INITIATE IT.
4. ENABLE SELECTED DEVICES.
5. SCAN FOR ALL DEVICES TO FINISH
6. IF NOT DONE GO TO 4.  
IF HUNG REPORT SO AND DROP HUNG DEVICE.
7. CHECK DATA FOR ALL DEVICES SELECTED.
8. DECREMENT ITERATION COUNT
9. IF NOT = 0 GO TO 1
10. SIGNAL ENDPASS.

IISR: INPUT INTERRUPT SERVICE ROUTINE.

- I1. GET INTERRUPTING KMCSCR.
- I3. IF RECEIVE BA/CC WAS REQUESTED, LOAD REC BA/CC.
- I4. IF XMIT BA/CC WAS REQUESTED, LOAD XMIT BA/CC.
- I5. RTI

OISR: OUTPUT INTERRUPT SERVICE ROUTINE.

- O1. GET INTERRUPTING KMCSCR
- O2. IF ERROR, REPORT IT AND EXIT.
- O3. IF XMIT DONE OR REC DONE, SET APPROPRIATE BITS IN  
THE ENDPASS FLAG FOR THE DEVICE.
- O4. RTI

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9. NON-STANDARD PRINTOUTS

IF THE MODULE "HANGS" IN WHICH NOT ALL SELECTED DEVICES  
HAVE FINISHED, THEN A "HUNG" MESSAGE IS PRINTED OUT.  
CHECK THE ENDPASS FLAGS FOR EACH SELECTED DEVICE IN  
THE LINK TABLE TO DETERMINE WHICH DEVICE FAILED TO  
FINISH AND HOW FAR IT GOT.

FOR EXAMPLE:

THE TWO ENDPASS FLAGS ARE LOCATED IN THE LINK TABLE  
(INTLNK) AT THE FOLLOWING LOCATIONS.

XX11:  
XX21:

ONLY BITS 0 THRU 3 ARE USED AND ARE DEFINED AS FOLLOWS:

BIT1 = 1	RECEIVE BA/CC'S WERE LOADED.
BIT0 = 1	TRANSMIT BA/CC'S WERE LOADED.
BIT2 = 1	TRANSMIT DONE'S WERE RECEIVED.
BIT3 = 1	RECEIVE DONE'S WERE RECEIVED.

A CORRECT END PASS FLAG = 17. WHEN THE ENDPASS FLAGS  
= 17 FOR THE SELECTED DEVICES, THE DATA IS CHECKED. IF  
A "HUNG" MESSAGE IS TYPED IT IS BECAUSE ONE OR BOTH  
DEVICES DID NOT FINISH. TO FIND WHICH ONE, CHECK THE  
END PASS FLAGS, ANY THAT ARE NOT EQUAL TO 17 ARE THE  
HUNG DEVICES. CHECK WHICH BITS OF THE ENDPASS FLAG ARE  
CLEAR TO SEE WHAT IT WAS TRYING TO DO.

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178 000000*
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185 000000*
186 000000* 046513 041103 040
187 000005* 000000
188 000006* 000001
189 000010* 000001
190 000012* 240
191 000013* 240
192 000014* 000001
193 000016* 000000
194 000020* 000000
195 000022* 000000
196 000024* 000000
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198 000026* 140000
199 000036* 000072
200 000032* 000224
201 000034* 000000
202 000036* 000200
203 000040* 000000
204 000042* 000000
205 000044* 000000
206 000046* 000000
207 000050* 000000
208 000052* 000000
209 000054* 000000
210 000056* 000000
211 000056* 000000
212 000060* 000000
213 000062* 000000
214 000064* 000000
215 000066* 000000
216 000070* 000000
217 000072* 000000
218 000074* 000000
219 000076* 000000
220 001000* 000000
221 001020* 000000
222 001020* 000000
223 000040* 000000
224 001040* 000000
225 001060* 000000
226 001060* 000000
227 000040* 000000
228 001120* 000322
229 000114* 000000
230 000116* 000000
231 000120* 000000
232 000122* 000136
233 000224*

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IDMOD<KMCB > 1,1,5,5,0,200,136
MODULE 140000,KMCB,1,1,5,5,0,200,136
.TITLE KMCB DEC/X11 SYSTEM EXERCISER MODULE
; DDXCDM VERSION 6 23-MAY-78
LIST RN
*****
BEGIN:
MODNAM: .ASCII /KMCB / ;MODULE NAME
XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUF USAGE
ADDR: *0 ;1ST DEVICE ADDR
VECTOR: 1+0 ;1ST DEVICE VECTOR.
BR1: .BYTE PRTY5+0 ;1ST BR LEVEL.
BR2: .BYTE PRTY5+0 ;2ND BR LEVEL.
DVID1: 0+1 ;DEVICE INDICATOR 1.
SR1: OPEN ;SWITCH REGISTER 1
SR2: OPEN ;SWITCH REGISTER 2
SR3: OPEN ;SWITCH REGISTER 3
SR4: OPEN ;SWITCH REGISTER 4
*****
STAT: 140000 ;STATUS WORD
INVT: START ;MODULE START ADDR.
SPOINT: MODSP ;MODULE STACK POINTER.
PASCNT: 0 ;PASS COUNTER.
ICOUNT: 200 ;LOC TO COUNT ITERATIONS
SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
SOPPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
CONFIC: ;RESERVED FOR MONITOR USE
RES1: 0 ;RESERVED FOR MONITOR USE
RES2: 0 ;RESERVED FOR MONITOR USE
SVRO: OPEN ;LOC TO SAVE R0.
SVR1: OPEN ;LOC TO SAVE R1.
SVR2: OPEN ;LOC TO SAVE R2.
SVR3: OPEN ;LOC TO SAVE R3.
SVR4: OPEN ;LOC TO SAVE R4.
SVR5: OPEN ;LOC TO SAVE R5.
SVR6: OPEN ;LOC TO SAVE R6.
CSRA: OPEN ;ADDR OF CURRENT CSP.
SBADR: ;ADDR OF GOOD DATA, OR
ACSR: OPEN ;CONTENTS OF CSR.
WASADR: ;ADDR OF BAD DATA, OR
ASTAT: OPEN ;STATUS REG CONTENTS.
ERRTYP: ;TYPE OF ERROR
ASH: OPEN ;EXPECTED DATA.
AWAS: OPEN ;ACTUAL DATA.
RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
WDRTO: OPEN ;WORDS TO MEMORY PER ITERATION
WDRF: OPEN ;WORDS FROM MEMORY PER ITERATION
INTR: OPEN ;# OF INTERRUPTS PER ITERATION
IDNUM: 136 ;MODULE IDENTIFICATION NUMBER=136
MODSP:

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244 000224* 000000
245 000226* 000000
246 000230* 000000
247 000232* 000117
248 000234* 000000
249 000236* 000000
250 000240* 000377
251 000242* 000377
252 000244* 000000
253 000246* 000000
254 000250* 000000
255 000252* 000000
256 000254* 000000
257 000256* 000002
258 000260* 000100
259 000262* 000
260 000263* 000
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263 000264* 010000
264 000266* 000000
265 000270* 000000
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281 000272* 005067 177772
282 000276* 032767 177774
283 000304* 001004
284 000306* 016767 177502 177714
285 000314* 001002
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287 000316* 104410 000000
288 000322* 005067 177706
289 000326* 012700 005276

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*****
VARIABLES FOR KMC11
*****
DLV1: .WORD 0 ;DEVICE 1 DELAY COUNT.
DLV2: .WORD 0 ;DEVICE 2 DELAY COUNT.
SELECT: .WORD 0 ;TEMPORARY SELECTED DEVICE'S
FLAG: .WORD 17 ;END PASS FLAG.
FIRST: .WORD 0 ;FIRST PASS FLAG.
MASK: .WORD 0 ;TEMPORARY VARIABLE.
RSIZE: .WORD 377 ;RECEIVE BUFFER SIZE.
XSIZE: .WORD 377 ;TRANSMIT BUFFER SIZE.
VA: .WORD 0 ;VIRTUAL ADDRESS.
PA: .WORD 0 ;PHYSICAL ADDRESS.
EA: .WORD 0 ;EXTENDED ADDRESS.
SARO: .WORD 0 ;SAVE LOC FOR R0.
SARI: .WORD 0 ;SAVE LOC FOR R1.
SZMULV: .WORD 2 ;LOCATION USED TO CALCULATE NPR/BR RATE.
RTMULV: .WORD 100 ;LOCATION USED TO CALCULATE NRR RATE.
TERM: 0 ;TERMINATING VALUE.
RCOLV: .BYTE 0 ;XMTR ONLY FLAG=SPAD<16>
XCOLV: .BYTE 0 ;RCV ONLY FLAG=SPAD <15>
NPRTE: .WORD 10000 ;LOCATION FOR NPR RATE.
TEMP: .WORD 0 ;TEMPORARY VARIABLE.
FLAG: .WORD 0 ;USED TO LOAD MAIN MEMORY.
*****
BEGIN THE DEC./X11 MODULE FOR THE KMC11
;
START: CLR FLAG ;SET FOR FIRST PASS.
; BNE #<<3>,DVID1 ;DROP MODULE IF OTHER THEN
; BNE DROPT,SELECT ;FIRST 2 DEVICES ARE SELECTED
; MOV DVID1,SELECT ;SELECT=ACTIVE DEVICES.
; BNE RSTRT ;DROP MODULE IF NO ACTIVE DEVICES.
DROP: ENDS,BEGIN ;
RSTRT: CLR FIRST ;INITIALIZE THE FIRST TIME FLAG.
LOOP: MOV #RBUF11,R0 ;GET SET TO CLEAR BUFFERS.

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290 000332* 005020  
291 000334* 020027 007276*  
292 000340* 003774  
293 000342* 011780 177662  
294 000344* 011780  
295 000350* 005767 177714  
296 000354* 001173  
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308 000356* 105067 177700 CLR RB RCOLV ;INITIALIZE THE FLAGS.  
309 000362* 105067 177675 CLR RB XN11 ;INITIALIZE THE END PASS FLAG.  
310 000366* 005067 003642 CLR XX21 ;INITIALIZE THE END PASS FLAG.  
311 000372* 005067 003660 CLR XX21 ;INITIALIZE THE END PASS FLAG.  
312 000376* 005767 177414 TST SR1 ;IS IT MULTIPLY OPTION?  
313 000402* 190426 BIT #R1T14,SR1 ;IS THE RATE SET?  
314 000412* 001467 040000 177404 BEQ ;NO, TAKE DEFAULT.  
315 000414* 016767 177636 MOV SZMULV,RSIZE ;SET NPR/RR RATE.  
316 000420* 016767 177630 177626 MOV RTMULV,NPRTR ;SET NPR/RR RATE.PARAMETER.  
317 000436* 000455 DR ;SET UP THE REST.  
318 000440* 032767 040000 177350 2S: BIT #BIT14,SR1 ;IS SR1<14>=1?  
319 000446* 010611 MOV #1,R1 ;GET THE POINT THE ERROR & DROP THE MODULE.  
320 000450* 010611 MOV #1,R2 ;RETRIEVE BUFFER SIZE MULTIPLICAND.  
321 000460* 006202 ASR R2 ;SET UP NPR RATE MULTIPLICAND.  
322 000464* 006202 ASR R2 ;SET UP NPR RATE MULTIPLICAND.  
323 000466* 006202 ASR R2 ;SET UP NPR RATE MULTIPLICAND.  
324 000470* 006202 ASR R2 ;SET UP NPR RATE MULTIPLICAND.  
325 000474* 006202 ASR R2 ;SET UP NPR RATE MULTIPLICAND.  
326 000474* 006202 BIC #177700,R1 ;CLEAR THE EXTRA BITS.  
327 000500* 042702 MOV #177700,R2 ;CLEAR UNNECESSARY BITS.  
328 000504* 010103 MOV #R3 ;GET THE MULTIPLICAND...  
329 000506* 004567 JSR #5,MULTPLY ;MULTIPLY & RETURN THE RESULT IN TEMP.  
330 000516* 022767 000377 177542 CMP #37,TEMP ;CHECK IF WITHIN LIMIT...  
331 000524* 003406 BLE ;NO, TAKE THE DEFAULT...  
332 000526* 016767 177534 177504 MOV TEMP,RSIZE ;RSIZE=NPR/RR RATE.  
333 000542* 010203 MOV R2,R5 ;GET THE MULTIPLICAND...  
334 000544* 016704 177510 MOV RTMULV,R4 ;GET THE MULTIPLICAND...  
335 000550* 004767 JSR #10000,TEMP ;MULTIPLY & PUT RESULT IN TEMP.  
336 000562* 003403 BLE ;CHECK IF WITHIN LIMIT...  
337 000564* 016767 177476 177472 MOV TEMP,NPRTR ;NPRTR=NPR/TIME.  
338 000572* 032767 010000 177216 3S: BIT #BIT12,SR1 ;IS IT RECEIVE ONLY?
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346 000600* 001413 BEQ #5 ;NO, CHECK IF XMITR ONLY.  
347 000602* 032767 020000 177206 BIT #BIT13,SR1 ;IS XMITR ONLY ALSO SET?  
348 000610* 001404 BEQ #6 ;NO, SETUP FOR RECEIVE ONLY.  
349 000612* 104403 000000* 002016* 4S: MISCNS,BEGIN,SOFT1 ;ASCII MESSAGE CALL WITH COMMON HEADER  
350 000620* 000636 BR DROP ;DROP THE MODULE.  
351 000622* 105167 177434 6S: COMB RCOLV ;SET RECEIVE ONLY FLAG.  
352 000636* 000409 7S: COMB #7,OTHER ;SET UP OTHER VARIABLES.  
353 000640* 021789 020000 177160 5S: BIT #BIT13,SR1 ;IS XMITR ONLY SET?  
354 000636* 001402 BEQ #7 ;NO, DO BOTH.  
355 000640* 105167 177417 COMB XN11 ;SET XMITR ONLY FLAG.  
356 000644* 017011 177136 MOV ADDR,R1 ;GET THE FIRST DEVICE CSR.  
357 000650* 016703 177354 7S: MOV SELECT,R3 ;GET THE DEVICE SELECTED.  
358 000654* 005067 177406 8S: CLR TEMP ;CLEAR THE RETRY COUNT.  
359 000660* 006203 ASR #3 ;ANY DEVICE REMAINS.  
360 000662* 103404 BCS ;YES, GO AND LOAD MICRO-CODE INTO IT.  
361 000664* 001427 BEQ SETUP1 ;SETUP THE REST.  
362 000666* 062701 000010 10S: ADD #10,R1 ;UPDATE THE CSR.  
363 000672* 000770 BR #5 ;LOAD THE NEXT DEVICE.  
364 000674* 004767 002456 9S: JSR PC,WCRAM ;WRITE THE CRAM WITH MICRO-CODE.  
365 000700* 004767 002446 JSR PC,WEMRY ;AND LOAD LOWER HALF OF MAIN MEMORY WITH XMITR BUFFER.  
366 000704* 004767 002714 JSR PC,VERIFY ;VERIFY MICRO-CODE & XMITR DATA.  
367 000710* 005767 177322 TST MASK ;IS THERE ANY ERROR.  
368 000714* 001764 BEQ #10 ;NO, GO INITIATE IT.  
369 000716* 104403 000000* 002022* MISCNS,BEGIN,SOFT2 ;ASCII MESSAGE CALL WITH COMMON HEADER  
370 000724* 005267 177336 INC TEMP ;INCREMENT RETRY COUNT.  
371 000730* 022767 000003 177330 CMP #3,TEMP ;IS IT TRIED THREE TIMES?  
372 000736* 003556 BCT ;DECREMENT AGAIN!  
373 000740* 000167 177352 JMP DROP ;DROP THE MODULE.  
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384 000744* 016701 177036 SETUP1: MOV ADDR,R1 ;GET THE DEVICE CSR.  
385 000750* 016702 177034 MOV VECTOR,R2 ;GET THE VECTOR.  
386 000754* 012703 004222* MOV #INTLNK,R3 ;GET THE POINTER TO INTERRUPT LINKAGE.  
387 000760* 016767 177246 003246 MOV FLAG,XX11 ;SET THE END PASS FLAG FOR DEV#1.  
388 000766* 016767 177240 003262 MOV FLAG,XX21 ;SET THE END PASS FLAG FOR DEV#2.  
389 000774* 012767 007279* 006354 MOV #PRING,INQIN ;SET UP ALL QUEUES & ITS POINTERS.  
390 001002* 012767 007276* 006350 MOV #PRING,INQOUT ;SET UP ALL QUEUES & ITS POINTERS.  
391 001010* 012767 007316* 006346 MOV #PRROUT,OUTQOUT ;SET UP ALL QUEUES & ITS POINTERS.  
392 001016* 012767 007316* 006336 MOV #PRROUT,OUTQIN ;SET UP ALL QUEUES & ITS POINTERS.  
393 001024* 012767 007336* 006334 MOV #REGQ,REGQ1 ;SET UP ALL QUEUES & ITS POINTERS.  
394 001032* 012767 007336* 006330 MOV #REGQ,REGQ0 ;SET UP ALL QUEUES & ITS POINTERS.  
395 001040* 016700 177164 SELECT,RO ;RO= DEVICES SELECTED.  
396 001044* 006200 2S: ASR RO ;ANY DEVICE ACTIVE?  
397 001046* 103410 BCS #4 ;YES, GO SET IT UP.  
398 001050* 001446 SEQ SETUP2 ;ALL DONE?  
399 001052* 062701 000010 3S: ADD #10,R1 ;NO, UPDATE CSR.  
400 001056* 062702 000010 ADD #10,R2 ;UPDATE VECTOR  
401 001062* 062703 000022 ADD #10,R3 ;UPDATE LINK.
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402 001066 000766 BR 2S ;GO SET UP FOR NEXT DEVICE.
403 001070 010312 MOV R3,(R2) ;LOAD INTERRUPT VECTOR ADDRESS.
404 001100 010163 MOV R1,(R3) ;SET THE PRIORITY.
405 001100 010163 MOV R1,(R3) ;SET THE DEVICE CSR.
406 001104 010362 MOV R3,4(R2) ;LOAD XMITR.
407 001110 062762 MOV #4,4(R2) ;INTERRUPT VECTOR.
408 001116 187652 CLRB R8,(R2) ;SET THE PRIORITY.
410 001130 005063 CLR 16(R3) ;CLEAR END PASS FLAG FOR DEV#1.
411 001134 105767 TSTB XMOly ;IS XMITR ONLY FLAG SET?
412 001140 011763 BNE 3S ;NO CHECK FOR RECEIVE ONLY FLAG.
413 001140 011763 MOV #BIT1,16(R3) ;SET XMITR BITS IN ENDPASS FLAG.
414 001150 000740 BR 3S ;
415 001152 105767 TSTB RCOLY ;IS RECEIVE ONLY FLAG SET?
416 001156 011793 BEQ 3S ;NO DONT SET ANY BITS IN ENDPASS FLAG.
417 001156 011793 MOV #BIT0,12(R3) ;SET RECEIVE BITS IN ENDPASS FLAG.
418 001166 016701 ADDR,R1 ;R1=CSR ADDRESS.
419 001172 016700 SELECT,R0 ;R0=SELECT.
420 001176 016700 MOV R0 ;
421 001176 016700 BCS 3S ;ANY DEVICE ACTIVE?
422 001202 001454 BEQ SCAN ;YES GO & INITIATE THE DEVICE DEC/X MODULE.
423 001204 062701 ADD #10,R1 ;ALL DONE, GO AND SCAN.
424 001210 007772 BR 3S ;UPDATE CSR ADDRESS.
425 001210 007772 BR 3S ;CONTINUE.
426 001216 005061 BNE FIRST ;IS IT FIRST PASS???
427 001220 012711 MOV #BIT14,(R1) ;NO, THEN DON'T INITIALIZE DEVICE
428 001226 005061 CLR 4(R1) ;MASTER CLEAR FIRST TIME ONLY.
429 001226 005061 CLR 4(R1) ;INITIALIZE THE UNIBUS CSR'S.
430 001232 005061 CLR 4(R1) ;INITIALIZE THE UNIBUS CSR'S.
431 001236 005061 CLR 6(R1) ;INITIALIZE THE UNIBUS CSR'S.
432 001240 105711 MOV #15,(R1) ;INITIALIZE THE UNIBUS CSR'S.
433 001246 105711 TSTB R1 ;IS RD I SET?
434 001250 100415 BMI 12S ;YES, THEN START DECC...
435 001252 010067 MOV R0,SAR0 ;SAVE REGISTER R0...
436 001256 104407 MOV R1,SAR1 ;SAVE REGISTER R1...
437 001256 104407 BREAK$,BEGIN ;TEMPORARY RETURN TO MONITOR...
438 001266 104407 BREAK$,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
439 001272 016700 MOV SAR0,R0 ;RESTORE REGISTER R0...
440 001272 016700 MOV SAR1,R1 ;RESTORE REGISTER R1...
441 001306 000761 BR 9S ;WAIT FOR RD I TO SET...
442 001304 052761 BIS #20,2(R1) ;SET IED...
443 001312 105767 TSTB RCOLY ;IS RECEIVE ONLY SET?
444 001312 105767 BNE 6S ;NO BRANCH IF NO.
445 001320 052711 BIS #20,(R1) ;SET IET, RECEIVE PA/CC I
446 001324 000727 BR 2S ;CONTINUE.
447 001326 052711 BIS #24,(R1) ;SET IET, XMITR PA/CC I.
448 001332 000724 BR 2S ;CONTINUE.
;
;*****
;* THIS ROUTINE SCANS ALL DEVICES END PASS FLAGS
;* UNTIL ALL ACTIVE KNC11 DEVICES ARE FINISHED.
;* UPDATES PASS COUNT AND LOOPS TILL 200 PASSES.
;* ARE DONE CHECKS DATA AND PRINTS OUT DATA ERRORS.
;* REPORTS END OF PASS.
;*
```

```
458 ;*
459 ;*
460 ;*
461 001334 012767 000003 176674 SCAN: MOV #3,MASK ;SET BIT FOR ALL DEVICES.
462 001342 012767 000010 176654 MOV #10,DLV1 ;SET DELAY COUNT.
463 001350 005067 176652 CLR DLV2 ;CLEAR DELAY COUNT.
464 001354 026767 176652 002652 1S: CMP FLAG,XX11 ;IS DEVICE 1 ALL DONE?
465 001362 001003 BNE 2S ;NO, CHECK THE NEXT ONE.
466 001364 042767 000001 176644 BIC #BIT0,MASK ;CLEAR THE DEVICE BIT.
467 001372 026767 176634 002656 2S: CMP FLAG,XX21 ;IS DEVICE #2 ALL DONE?
468 001380 001003 BNE 3S ;NO, GO AND WAIT.
469 001402 042767 000002 176626 BIC #BIT1,MASK ;CLEAR THE DEVICE BIT.
470 001410 005767 176622 3S: TST MASK ;ARE ALL DEVICES DONE?
471 001414 001064 BNE 16S ;NO, GO AND WAIT.
472 001422 012701 MOV #INTLNK+10,R1 ;R1 POINTS TO DEVICE CSR.
473 001422 016700 MOV #SELECT,R0 ;R0 CONTAINS BITS FOR ACTIVE DEVICES.
474 001426 012703 005266 MOV #BUFTAB,R3 ;R3=POINTER TO RECEIVER BUFFER.
475 001432 006200 4S: ASR R0 ;IS ANY DEVICE ACTIVE?
476 001434 103417 BCS 8S ;YES, GO AND CHECK THE DATA.
477 001436 001404 BEQ 6S ;IS IT ALL DONE THEN BRANCH.
478 001440 062701 ADD #2,R1 ;UPDATE R1 TO NEXT DEVICE CSR.
479 001444 005723 TST (R3)+ ;UPDATE R3 TO NEXT BUFFER.
480 001446 000771 BR 4S ;CONTINUE.
481 001450 012767 177777 176556 6S: MOV #1,FIRST ;SET FIRST PASS FLAG.
482 001456 012767 177777 176604 MOV #1,FLAG ;SET FLAG FOR MICRO-CODE LOADED.
483 001464 104413 000000 7S: ENDT$,BEGIN ;SIGNAL END OF ITERATION.
484 ;*
485 001470 000167 176632 JMP LOOP ;LOOP THE MODULE.
;*****
;* CHECK THE DATA IN RECEIVE BUFFER...
;*
489 ;*
490 ;*
491 ;*
492 ;*
493 001474 105767 176563 8S: TSTB XMOly ;IS IT XMIT ONLY???
494 001500 001357 BNE 5S ;YES, THEN DONT CHECK THE DATA...
495 001502 011302 MOV (R3),R2 ;R2=POINTS TO RECEIVER BUFFER.
496 001504 012705 004266 9S: MOV #XBUF,R5 ;R5=POINTS TO GOOD DATA.
497 001506 012705 004266 9S: MOV #XBUF,R5 ;R5=POINTS TO GOOD DATA.
498 001512 016767 176522 10S: MOV #SIZE,TEMP ;SET THE BUFFER SIZE.
499 001520 121514 CMPB (R5),(R4) ;COMPARE DATA.
500 001522 001434 BEQ 11S ;GOOD, COMPARE NEXT CHAR.
501 001524 011167 176350 MOV (R1),CSRA ;LOAD CSRA.
502 001530 010567 176346 MOV R5,SBADR ;LOAD GOOD ADDRESS.
503 001534 010467 176344 MOV R4,WASADR ;LOAD BAD ADDRESS.
504 001540 111567 176342 MOVB (R5),ASB ;LOAD GOOD DATA.
505 001544 111467 176340 MOVB (R4),AWAS ;LOAD BAD DATA.
506 ;*****
507 001550 104404 000000 DATERS$,BEGIN ;DATA ERROR!!!
508 ;*
509 001554 122524 176504 11S: CMPB (R5)+,(R4)+ ;PCP BUFFER DATA POINTERS.
510 001556 005367 DEC TEMP ;ALL DONE?
511 001562 001356 BNE 10S ;NO, DO THE NEXT.
512 001564 000725 BR 16S ;GO, AND CHECK REMAINING DEVICE.
513 001566 ;*
```



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514 001566* 104407 000000*
515 001572* 104407 000000*
516
517 001576* 005367 176424
518 001602* 001402
519 001604* 000167 177544
520 001610* 005367 176410
521 001614* 001402
522 001622* 016700 177532
523 001622* 016700 176410
524 001626* 040067 176376
525 001632* 006000
526 001634* 103004
527 001636* 004367 000024
528 001642* 004232*
529 001644* 000001
530 001646* 006000
531 001650* 103004
532 001652* 004367 000010
533 001656* 004254*
534 001660* 000002
535 001662* 000167 176440
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```

BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
;THEN CONTINUE AT NEXT INSTRUCTION.
;DECREMENT DELAY COUNT FOR #2.
DEC DLY2 ;WAIT FOR DEVICE TO COMPLETE.
BEC #6 ;DECREMENT DFLAY COUNT FOR #1.
JMP 15
DEC DLY1
BEC #6 ;WAIT FOR DEVICE TO COMPLETE.
JMP MASK,RO ;RO=HUNG DEVICE BITS.
BIC RO,SELECT ;DROP ANY HUNG DEVICE.
RDR RO ;WAS DEV#1 HUNG?
BCC 17\$ ;BRANCH IF NO.
JSR R3,XERR ;TYPE ERROR MESSAGE & DROP.
CSRC1 ;POINTER TO DEV#1 CSR.
RDR RO ;DEVICE NUMBER FOR TYPEOUT.
BCC 18\$ ;WAS DEV#2 HUNG?
JSC R3,XERR ;BRANCH IF NOT.
CSR22 ;TYPE ERROR MESSAGE THEN DROP.
;POINTER TO DEV#2 CSR.
;DEVICE NUMBER FOR TYPEOUT.
;RESTART MODULE.
17\$: JSC R3,XERR
18\$: JMP LOOP

\*\*\*\*\*
THIS SUBROUTINE DROPS THE HUNG DEVICE.
CLEARS OUT DEVICE SELECT BITS.
PRINTS THE EXTENDED ERROR MESSAGE.
\*\*\*\*\*

XERR: MOV (R3),R2 ;GET POINTER TO CSR.
MOV (R3),DEV ;GET DEVICE #.
RIS #0,DEV ;MAKE IT ASCII.
MSGNS,BEGIN,DRDP1 ;ASCII MESSAGE CALL WITH COMMON HEADER
MOV (R2),R1 ;GET CSR ADDRESS.
MOV R1,CSRA ;SAVE CSR.
MOV (R1),ACSR ;SAVE CONTENTS OF SEL0.
MOV 2(R1),ASTAT ;SAVE CONTENTS OF SEL2.
MOV 4(R1),DLV1 ;SAVE CONTENTS OF SEL1.
MOV 6(R1),DLV2 ;SAVE CONTENTS OF SEL3.
MOV 2(R2),ESAV1 ;SAVE END PASS FLAG.
MOV (R2),ESAV2 ;SAVE RECEIVE BUFFER OFFSET.
MOV 10(R2),ESAV3 ;SAVE DEVY/SMTR COUNTERS.
DEV #1,RTYP ;NO INTERRUPT
\*\*\*\*\*
RDERS,REGIN,ETABLE ;DUMP KMC CSR'S AND STATUS FLAGS
\*\*\*\*\*
CLR (R1) ;SHUT OFF HUNG KMC11.
RTS R3
DEV: ;WORD 0
DROP1: XDROP1
DEV
XDROP2
-1

```

570 002016* 002110*
571 002020* 177777*
572 002022* 002171*
573 002024* 177777*
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575
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578 002026* 020045 046513 030503
579 002034* 020061 042504 044526
580 002042* 042503 021440 000040
581
582 002050* 051511 044040 047125
583 002056* 020107 047101 020104
584 002064* 040510 020123 042502
585 002072* 040510 042040 047522
586 002100* 050100 042105 027056
587 002106* 000045
588
589 002110* 020045 051105 047522
590 002116* 020122 047111 051440
591 002124* 052105 044524 043516
592 002132* 052440 020120 053523
593 002140* 052111 04103 051040
594 002146* 043505 051511 042524
595 002154* 020122 020045 042522
596 002162* 052123 051101 020124
597 002170* 000
598
599 002171* 045 042440 051122
600 002176* 051117 044440 020116
601 002204* 047514 042101 047111
602 002212* 020107 044515 051103
603 002220* 026517 047503 042504
604 002226* 053440 046111 020114
605 002234* 042522 051124 027131
606 002242* 027056 000045
607
608
609
610
611 002246* 000000
612 002250* 000000
613 002252* 000000
614 002254* 000000
615
616
617
618 002256* 000224*
619 002260* 000226*
620 002262* 002246*
621 002264* 002250*
622 002266* 002252*
623 002270* 177777*
624 002272* 002246*
625 002274* 002250*

```

SOFT1: SOFT11
SOFT2: SOFT21
-1
-1

\*\*\*
ERROR MESSAGES.....
\*\*\*

XDROP1: .ASCIZ /% KMC11 DEVICE # /
XDROP2: .ASCIZ /IS HUNG AND HAS BEEN DROPPED..%/

SOFT11: .ASCIZ /% ERROR IN SETTING UP SWITCH REGISTER & RESTART /

SOFT21: .ASCIZ /% ERROR IN LOADING MICRO-CODE WILL RETRY...%/

.EVEN
\*\*\*
EXTENDED ERROR PRINTOUT LOCATIONS....
\*\*\*
ESAV1: .WORD 0
ESAV2: .WORD 0
ESAV3: .WORD 0
ESAV4: .WORD 0
\*\*\*
TABLE OF ADDRESSES FOR EXTERNAL ERROR PRINT OUTS....
\*\*\*
ETABLE: DLV1
DLV2
ESAV1
ESAV2
ESAV3
-1
FTABLE: ESAV1
ESAV2

626 002276\* 002252\*  
627 002300\* 002254\*  
628 002302\* 177777

ESAVJ  
ESAV4  
-1

\*\*\*\*\*  
: INPUT INTERRUPT SERVICE ROUTINE  
: THIS ROUTINE SERVES THE IN INTERRUPT  
: FROM KMC11 BY LOADING REQUESTED XMTR  
: OR RECEIVE BA/CC I  
:\*\*\*\*\*

630  
631  
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639  
640 002304\* 010577 005046  
641 002310\* 062767 000002 005040  
642 002316\* 022767 007316\* 005032  
643 002324\* 003003  
644 002326\* 012767 007276\* 005022  
645 002334\* 012605  
646  
647 002336\* 000004 000000\* 002344\*  
648  
649 002344\* 017705 005010  
650 002350\* 062767 000002 005002  
651 002356\* 022767 007316\* 004774  
652 002400\* 003003  
653 002366\* 012767 007276\* 004764  
654 002374\* 016501 000004  
655 002400\* 032711 000007  
656 002404\* 011410 000004  
657 002406\* 032711 000004  
658 002417\* 001033  
659 002414\* 104403 000000\* 002554\*  
660 002422\* 104400 000000  
661 002426\* 016504 000010  
662 002432\* 011467 175606  
663 002436\* 004767 001510  
664 002442\* 016761 175600  
665 002450\* 016761 175574 000004  
666 002456\* 056761 175556 000006  
667 002464\* 052765 000002 000006  
668  
669 002472\* 142711 000220  
670  
671 002476\* 104400 000000\*  
672 002502\* 012767 004266\* 175534  
673 002510\* 004767 001436  
674 002514\* 016761 175526 000004  
675 002520\* 016761 175526 000006  
676 002530\* 056761 175526 000006  
677 002536\* 052765 000001 000006  
678  
679 002544\* 142711 000200  
680 002550\* 104400 000000\*  
681 002554\* 002560

INISR: MOV R5, @INQIN ;SAVE LINK POINTER IN QUEUE.  
ADD #2, INQIN ;UPDATE THE QUEUE POINTER.  
CMP #PIRINQ+20, INQIN ;END OF QUEUE?  
RGT IS ;NO, CONTINUE  
MOV #PIRINQ, INQIN ;RESET QUEUE POINTER.  
1\$: MOV (SP)+, R5 ;RESTORE R5, I.E. POP THE STACK.  
PIRQS, BEGIN, 2\$ ;QUEUE UP TO CONTINUE AT 2\$ AND RTI  
2\$: MOV @INQOUT, R5 ;RESTORE THE LINK POINTER FROM QUEUE.  
ADD #2, INQOUT ;UPDATE THE QUEUE POINTER.  
CMP #PIRINQ+20, INQOUT ;IS IT END OF QUEUE?  
RGT IS ;NO, CONTINUE  
MOV #PIRINQ, INQOUT ;RESET THE QUEUE POINTER.  
3\$: MOV 4(R5), R1 ;LOAD CSR ADDRESS.  
MOV R1, R1 ;LOAD CSR ADDRESS.  
BIT #7, (R1) ;RECEIVE BA/CC I?  
BEQ #0, R1 ;BRANCH IF YES.  
BIT #4, (R1) ;XMTR BA/CC I?  
BNE #0, R1 ;BRANCH IF YES.  
MSGNS, BEGIN, ILINT ;ASCII MESSAGE CALL WITH COMMON HEADER  
RECV: MOV #10, R4 ;GET RECEIVE BUFFER POINTER.  
MOV (R4), VA ;VA=RECEIVE BUFFER VIRTUAL ADDRESS..  
JSR PC, @BITS ;GET PHYSICAL ADDRESS.  
MOV #4, R1 ;SET 4=PHYSICAL ADDRESS.  
MOV #6, R1 ;SEL=EXTENDED BITS OF ADDRESS.  
RIS #SIZE, 6(R1) ;LOAD RECEIVE CHARACTER COUNT.  
BIS #BIT1, 6(R5) ;SET BIT1 IN END PASS FLAG FOR  
RIS #BIT7, 6(R5) ;CLEAR RDI TO REQUEST SERVICE FROM  
RIS #BIT7, 6(R5) ;KMC11. CLEAR IEI.  
EXIT\$; BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.  
XMTR: MOV #XBUF, VA ;LOAD XBUFFER VIRTUAL ADDRESS.  
JSR PC, @BITS ;GET EXTENDED PHYSICAL ADDRESS.  
MOV #4, (R1) ;LOAD PHYSICAL XMTR BUFFER.  
MOV #6, (R1) ;LOAD EXTENDED ADDRESS BITS XMTR BUFFER.  
RIS #SIZE, 6(R1) ;LOAD XMTR CHARACTER COUNT.  
RIS #BIT0, 6(R5) ;SET BIT0 IN END PASS FLAG FOR  
RIS #BIT0, 6(R5) ;XMTR BA/CC I LOADED.  
1\$: BIC #BIT7, (R1) ;CLEAR RDI TO INDICATE START OF OPERATION  
EXIT\$; BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.  
ILINT: MES2

682 002556\* 177777  
683 002560\* 044440  
684 002566\* 044440 052116 051105  
685 002574\* 052522 052120 053440  
686 002602\* 052111 020110 047516  
687 002610\* 010440 050505 042526  
688 002614\* 052123 054440 052105  
689 002624\* 052440 020520 020441  
690 002632\* 000045

MES2: -1  
;ASCII /\*INPUT INTERRUPT WITH NO REQUEST SET UP!!!\*/

.EVEN

\*\*\*\*\*  
: OUTPUT INTERRUPT SERVICE ROUTINE  
: THIS ROUTINE SERVES THE OUT INTERRUPT  
: FROM KMC11 BY 1 REPORTING ERROR  
: 2 ACCEPTING RECEIVE OR XMTR DONE  
:\*\*\*\*\*

701  
702  
703 002634\* 010577 004522  
704 002640\* 062767 000002 004514  
705 002646\* 022767 007336\* 004506  
706 002654\* 003003  
707 002656\* 012767 007316\* 004476  
708 002664\* 012605  
709  
710 002666\* 000004 000000\* 002674\*  
711  
712 002674\* 017705 004464  
713 002700\* 062767 000002 004456  
714 002706\* 022767 007336\* 004450  
715 002714\* 003003  
716 002716\* 012767 007316\* 004440  
717 002724\* 011501  
718 002726\* 032761 000002 000002  
719 002734\* 001450  
720 002736\* 032761 000010 000002  
721 002744\* 001403  
722 002746\* 104403 000000\* 003162\*  
723 002754\* 032761 000040 000002  
724 002762\* 001403  
725 002764\* 104403 000000\* 003162\*  
726 002772\* 032761 000040 000002  
727 003002\* 104403 000000\* 003152\*  
728 003002\* 104403 000000\* 003152\*  
729 003010\* 010167 175064  
730 003014\* 016167 000004 175064  
731 003022\* 016167 000006 175054  
732 003030\* 012767 000001 175050  
733  
734 003036\* 104405 000000\* 000000  
735  
736  
737 003044\* 142761 000352 000002

OUTISR: MOV R5, @OUTQIN ;SAVE LINK POINTER IN QUEUE.  
ADD #2, OUTQIN ;UPDATE THE QUEUE POINTER.  
CMP #PIROUTQ+20, OUTQIN ;IS IT END OF QUEUE?  
RGT IS ;NO, CONTINUE  
MOV #PIROUTQ, OUTQIN ;RESET THE QUEUE POINTER.  
1\$: MOV (SP)+, R5 ;POP THE STACK POINTER  
PIRQS, BEGIN, 2\$ ;QUEUE UP TO CONTINUE AT 2\$ AND RTI  
2\$: MOV @OUTQOUT, R5 ;RESTORE THE LINK POINTER FROM QUEUE.  
ADD #2, OUTQOUT ;UPDATE THE QUEUE POINTER.  
CMP #PIROUTQ+20, OUTQOUT ;IS IT END OF QUEUE?  
RGT IS ;NO, CONTINUE  
MOV #PIROUTQ, OUTQOUT ;RESET THE QUEUE POINTER.  
3\$: MOV (R5), R1 ;LOAD CSR ADDRESS.  
MOV R1, R1 ;LOAD CSR ADDRESS.  
BIT #1, 2(R1) ;IS CONTROL/D ERROR REPORT?  
BEQ #0, R1 ;NO, THEN CHECK FOR RCY OR XMTR.  
BIT #3, 2(R1) ;IS IT SOFT ERROR.  
BEQ #0, R1 ;NO, THEN CHECK REMAINING...  
MSGNS, BEGIN, SFT1 ;ASCII MESSAGE CALL WITH COMMON HEADER  
9\$: BIT #16, 2(R1) ;IS IT DATA ERROR.  
BEQ #0, R1 ;NO, CHECK THE REMAINING...  
MSGNS, BEGIN, DTER1 ;ASCII MESSAGE CALL WITH COMMON HEADER  
12\$: BIT #15, 2(R1) ;IS NON EX MEMORY ERROR.  
BEQ #0, R1 ;NO THEN MUST BE DATA ERROR.  
MSGNS, BEGIN, NXMMRY ;ASCII MESSAGE CALL WITH COMMON HEADER  
5\$: MOV R1, CSRA ;LOAD DEVICE CSR ADDRESS.  
MOV 4(R1), ACSR ;LOAD CONTENTS OF DEVICE CSR.  
MOV (R1), ASTAT ;LOAD ERROR BITS.  
MOV #1, @ERRTP ;DATA ERROR  
HDRS; BEGIN, NULL ;A CNTL/O RECEIVED  
BIC #352, 2(R1) ;ASTAT= ERROR BITS.  
;CLEAR RDO, NON EX MEM., CNTL/O.

```
738 003052* 104400 000000*          EXITS,BEGIN          ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
739
740 003056* 032761 000004 000002 4$: BIT #BIT2,2(R1)          ;IS IT XMTR DONE?
741 003064* 001411          BEQ 65                    ;NO,RECEIVE/SERVE IT.
742
743 003066* 052765 000004 000002     BIS #BIT2,2(R5)          ;SET XMTR DOWN BIT IN END PASS FLAG.
744 003074* 105767 175163          TSTB #MOL,4             ;IS IT XMTR ONLY???
745 003100* 001006          BNE 75                    ;CONTINUE.
746 003102* 142711 000004          BICB #4,(R1)           ;CONTINUE.
747 003106* 000403          BR 75                   ;CONTINUE.
748 003110* 052765 000010 000002 6$: BIS #BIT3,2(R5)          ;SET RECEIVE DONE BIT IN ENDPASS FLAG.
749 003116* 026765 175110          CMP FLAG,2(R5)        ;ALL DONE?
750 003124* 001005          BNE 85                 ;NO,CONTINUE.
751 003136* 142761 000020 000002     BICB #BIT4,2(R1)       ;CLEAR IO.
752 003144* 142711 000020          BICB #BIT4,(R1)       ;CLEAR THE 'IEI.
753 003140* 142761 000205 000002 8$: BICB #BIT7|BIT2|BIT0,2(R1) ;CLEAR R00,XMTR OR RCV DONE, BA/CC 0...
754 003146* 104400 000000*          EXITS,BEGIN          ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
755 003152* 001166          NXMMRV: MES1 -1
756 003154* 177177          SFT1: MES4 -1
757 003156* 003303          SFT1: MES4 -1
758 003160* 177777          DTER1: MES5 -1
759 003162* 077744          DTER1: MES5 -1
760 003164* 177744          MES1: .ASCII /% NON EXISTENT MEMORY ADDRESS ERROR %/
761 003166* 020045 047516 020116
762 003174* 054105 051511 042524
763 003206* 051119 046440 046504
764 003210* 051119 020131 042101
765 003216* 051104 051505 020123
766 003224* 051105 047522 020122
767
768 003234* 026040 041517 052503 .ASCIZ / OCCURED WHILE DOING NPR'S..... %/
769 003242* 042522 020104 044127
770 003250* 046111 020105 047504
771 003256* 047111 020107 050116
772 003264* 023522 027123 027056
773 003272* 027056 027056 027056
774 003300* 022440 000000
775 003304* 051140 043117 MES4: .ASCIZ /% SOFT ERROR %/
776 003310* 020124 051105 047522
777 003316* 020122 000045
778 003322* 020045 040504 MES5: .ASCIZ /% DATA ERROR ON TRANSMIT %/
779 003326* 051122 051117
780 003336* 047440 020116 051124
781 003344* 047101 046523 052111
782 003352* 022440 000000
783
784 .EVEN
785
786 *****
787 *****
788 *****
789 *****
790 *****
791 *****
792 *****
793 003356* 005000 WCRAM: CLR R0 ;R0=POINTS TO CRAM ADDRESS.
```

```
794 003360* 012702 007372*          MOV #KNAAMC,R2        ;R2 POINTS TO MICRO-CODE.
795 003364* 005011          CLR (R1)              ;CLEAR SEL0.
796 003366* 010061 000004          MOV R0,4(R1)          ;LOAD THE CRAM ADDRESS...
797 003372* 012261 000006          MOV (R2)+,6(R1)       ;LOAD WORD TO BE WRITTEN...
798 003376* 012711 002000          MOV #BIT10,(R1)       ;SET ROM0.
799 003402* 005011          MOV #BIT13|BIT10,(R1) ;WRITE IT!
800 003406* 005200          INC R0                ;INCREMENT CRAM ADDRESS.
801 003410* 022700 002000          CMP #2000,R0          ;OVER FLOW?
802 003414* 012702          BLT 804               ;YES,REPORT AND RETURN.
803 003416* 022719 000000          CMP #0,(R2)          ;IS IT END OF MICRO-CODE?
804 003422* 001360          BNE 805               ;NO,CONTINUE LOADING.
805 003424* 012705 000011          MOV #1,R5            ;R5=THE SCRATCH PAD ADDRESS
806 003430* 012702 000262          MOV #COLY,R2         ;SET THE PARAMETER POINTER.
807 003434* 005011          CLR (R1)             ;CLEAR SEL0.
808 003436* 042767 000077 000014     BIC #77,4$           ;CLEAR THE ADDRESS IN INSTRUCTION..
809 003444* 050567 000010          BIS #5,4$           ;SET SCRATCH PAD ADDRESS
810 003450* 112621 000004          MOVB (R2)+,4(R1)     ;LOAD SEL4 WITH DATA.
811 003454* 004367 000446          JSR R3,ROMCLK        ;CLOCK THE INSTRUCTION IN
812 003460* 123100          ; THIS LOCATION.
813 003462* 005205          INC R5                ;INCREMENT SCRATCH PAD ADDRESS.
814 003464* 022705          CMP #15,R5           ;IS IT DONE?
815 003470* 003361          BGT 4$               ;BRANCH IF NOT DONE.
816 003472* 005011          CLR (R1)             ;CLEAR SEL0.
817 003474* 000207          RTS                  ;RETURN
818
819 003476* 104403 000000* 003506 2$: MSGNS,BEGIN,CRMOWF ;ASCII MESSAGE CALL WITH COMMON HEADER
820 003504* 000772          BR 5$                ;RETURN.
821
822 CRMOWF: MES3 -1
823 003510* 177777          MES3: .ASCIZ /% MICRO-CODE OVER FLOWS CRAM %/
824 003512* 020045 044515 051103
825 003520* 026517 047503 042504
826 003526* 047440 050106 020122
827 003534* 046106 053517 020123
828 003542* 051103 046501 022440
829 003550* 000000
830 003552* 003552*
831 .EVEN
832 *****
833 *****
834 *****
835 *****
836 *****
837 *****
838 *****
839 *****
840 WMEMRV: JSR R3,ROMCLK ;CLOCK INSTRUCTION.
841 003552* 004367 000350          010000 ;LOAD MAR(0)?
842 003556* 010000          JSR R3,ROMCLK        ;CLOCK INSTRUCTION.
843 003560* 004367 000342          004002 ;SET POINTER IN KMCL1 MEMORY.
844 003564* 004002          MOV #XBUF,R0         ;SET POINTER TO DATA.
845 003566* 012700 004266*          #100,R2            ;SET THE COUNT.
846 003572* 012702 001000          CLR 4(R1)           ;
847 003576* 005061 000004          CLR 4(R1)           ;
848 003602* 112061 000004          1$: MOVB (R0)+,4(R1)   ;BSEL4<--GOOD DATA.
849 003606* 004367 000314          JSR R3,ROMCLK
```



```

962 004216 001374
963 004220 000205
964
965
966
967
968
969
970
971 004222 000000
972 004223 004567 176056
973 004226 004567 176402
974 004232 000000
975 004234 000000
976 004236 005272
977 004239 000000
978 004242 000000
979
980 004244 004567 176034
981 004250 004567 176360
982 004254 000000
983 004256 000000
984 004258 005272
985 004262 000000
986 004264 000000
987
988
989
990
991
992
993
994 004266 000400 001402 002404
995 004274 003406 004410 012
996 004301 0013 006414 007416
997 004306 010420 011422 012424
998 004314 013426 014430 015432
999 004322 016434 017436 040
1000 004324 005272 021442 022444
1001 004334 023446 024450 025452
1002 004342 026454 027456 030460
1003 004350 031462 032464 033466
1004 004352 001041 036070
1005 004362 037075 040077 041101
1006 004370 042103 043105 044107
1007 004376 045111 046113 047115
1008 004384 050117 051119 052123
1009 004412 053125 054127
1010 004416 055131 056133 057135
1011 004424 056055 041101 042103
1012 004432 043105 044107 045111
1013 004440 046113 047115 050117
1014 004446 051121
1015 004450 054123 053125 054127
1016 004456 055131 030533 077535
1017 004464 100600

```

```

;NOT DONE THEN CONTINUE,
;RETURN...
;*****
;LINK TABLE TO INTERRUPT SERVICE ROUTINE.
;*****
INLNK:
JSR R5,INISR ;INPUT INTERRUPT SERVICE ROUTINE.
JSR R5,OUTISR ;OUTPUT INTERRUPT SERVICE ROUTINE.
CSR1: .WORD 0 ;CSR ADDRESS FOR DEV #1.
XX11: .WORD 0 ;END PASS FLAG FOR DEV #1.
RBUF1 .RBUF1 ;RECEIVE BUFFER POINTER FOR DEV #1.
XX12: .WORD 0 ;REC/XMITR COUNTERS.
.WORD 0 ;ERROR COUNTS FOR DEV #1.
JSR R5,INISR ;INPUT INTERRUPT SERVICE ROUTINE.
JSR R5,OUTISR ;OUTPUT INTERRUPT SERVICE ROUTINE.
CSR22: .WORD 0 ;CSR ADDRESS FOR DEV #2.
XX21: .WORD 0 ;END PASS FLAG FOR DEV #2.
RBUF2 .RBUF2 ;RECEIVE BUFFER POINTER FOR DEV #2.
XX22: .WORD 0 ;REC/XMITR COUNTERS.
.WORD 0 ;ERROR COUNTS FOR DEV #2.
;*****
;BUFFERS & QUEUES.
;*****
RBUF: .ASCII <000><001><002><003><004><005><006><007><010><011><012>
.ASCII <013><014><015><016><017><020><021><022><023><024><025>
.ASCII <026><027><030><031><032><033><034><035><036><037><040>
.ASCII ~!#"#$%&'()*+,-./0123456789;<
.ASCII /=?@ABCDEFGHIJKLMNPQRSTUVWXYZ/
.ASCII /YZ[\]^_`ABCDEFHIJKLMNPQ/R/
.ASCII /SXUVWXYZ[1]/<177><200><201><202>

```

```

1018 004467 203 102604 103606 .ASCII <203><204><205><206><207><210><211><212><213><214><215>
1019 004474 104610 105612 106614 .ASCII <216><217><220><221><222><223><224><225><226><227><230>
1020 004502 107616 108620 111624 .ASCII <231><232><233><234><235><236><237><240><241><242><243>
1021 004510 112624 113626 250 .ASCII <244><245><246><247><250><251><252><253><254><255><256>
1022 004515 231 115632 116634 .ASCII <257><260><261><262><263><264><265><266><267><270><271>
1023 004522 117636 120640 121642 .ASCII <272><273><274><275><276><277><300><301><302><303><304>
1024 004530 122644 123646 124650 .ASCII <305><306><307><310><311><312><313><314><315><316><317>
1025 004536 125654 126656 256 .ASCII <320><321><322><323><324><325><326><327><330><331><332>
1026 004543 257 130660 131662 .ASCII <333><334><335><336><337><340><341><342><343><344><345>
1027 004550 132664 133666 134670 .ASCII <346><347><350><351><352><353><354><355><356><357><360>
1028 004556 134670 135672 136674 .ASCII <361><362><363><364><365><366><367><370><371><372><373>
1029 004564 140700 141702 304 .ASCII <374><375><376><377><377>
1030 004571 305 143706 144710 ;
1031 004576 145712 146714 147716 ;
1032 004604 150720 151722 152724 .EVEN
1033 004612 153726 154730
1034 004617 333 156734
1035 004624 160740 161742 162744
1036 004632 163746 164750 165752 .ASCII <361><362><363><364><365><366><367><370><371><372><373>
1037 004640 166754 167756
1038 004645 361 171762 172764
1039 004652 173766 174770 175772 .ASCII <374><375><376><377><377>
1040 004660 176774 177776
1041 004665 000400 .BLKB 400
1042
1043 005266 .EVEN
1044
1045 005266 005272 BUFTAB: RBUF1 ;BUFFER POINTER FOR DEV #1.
1046 005270 005274 RBUF2 ;BUFFER POINTER FOR DEV #2.
1047
1048
1049
1050
1051 005272 TABLE OF RECEIVE BUFFERS.
1052 005272 005276 RBUF1: RBUF11 ;RECEIVE BUFFERS FOR DEV #1.
1053
1054 005274 RBUF2: RBUF21 ;RECEIVE BUFFERS FOR DEV #2.
1055 005274 006276 RBUF21
1056
1057
1058
1059 005276 001000 RBUF11: .BLKB 1000 ;RECEIVE BUFFER 11.
1060
1061
1062
1063 006276 001000 RBUF21: .BLKB 1000 ;RECEIVE BUFFER 21.
1064 007276 BASE1:
1065
1066
1067
1068
1069
1070 007276 000010
1071 007316 000010
1072 007336 000010
1073 007356 000000

```

```

;*****
;QUEUES AND ITS POINTERS.
;*****
PRTINQ: .BLKW 10 ;INPUT INTERRUPT QUEUE.
PRTOUTQ: .BLKW 10 ;OUTPUT INTERRUPT QUEUE.
REGQ: .BLKW 10
INQIN: .WORD 0 ;INPUT QUEUE POINTER.

```

1074 007360 000000  
1075 007362 000000  
1076 007364 000000  
1077 007366 000000  
1078 007370 000000  
1079 007372

INQOUT: .WORD 0 ;INPUT QUEUE POINTER.  
OUTQIN: .WORD 0 ;OUTPUT QUEUE POINTER.  
OUTQOUT: .WORD 0 ;OUTPUT QUEUE POINTER.  
REGQI: .WORD 0 ;REGISTER QUEUES.  
REGQO: .WORD 0 ;REGISTER QUEUES.  
MICRCD:

1080  
1081  
1082  
1083  
1084  
1085  
1086  
1087  
1088  
1089  
1090  
1091  
1092  
1093  
1094  
1095  
1096  
1097  
1098  
1099  
1100  
1101  
1102  
1103  
1104

```
*****
;
; DEC/X11 MICRO-CODE FOR KMC11 IT WORKS AS FOLLOWS:
; 1) INTERRUPT 11 PROGRAM WHEN
;   A. IET & RDI SET - INTERRUPT AT LOC. XX0 FOR REQUEST OF
;   RECEIVE OR XMIT BA/CC I.
;   B. IEO & RDO SET - INTERRUPT AT LOC. XX4 FOR REPORTING
;   ERRORS (BY CNTL/O) OR REPORTING THE JOB DONE
; 2) WHEN RDI CLEARED BY THE 11 PROGRAM IT GOES AND
;   SERVES THE REQUEST I.E. EITHER RECEIVE OR XMIT.
;   A RECEIVE OPERATION IS TO RECEIVE A BUFFER FROM
;   KMC11 TO PDP11.
;   B. TRANSMIT OPERATION IS TO TRANSMIT A BUFFER FROM
;   PDP11 TO KMC11.
; 3) IN NORMAL CASE (BOTH RECEIVE & XMIT) XMIT HAS TO BE DONE
;   FIRST THEN RECEIVE.
; 4) IT CONTROLS NFR RATE BY INTERNAL SOFTWARE CLOCK & NFR/BR
;   RATE IS CONTROLLED BY CHARACTER COUNT.
; 5) IN CASE OF XMIT OPERATION DATA CHECKING IS DONE
;   INSIDE KMC11.
;
;*****
```

1105 007372  
1106 007374  
1107 007376  
1108 007400  
1109 007402  
1110 007404  
1111 007406  
1112 007410  
1113 007412  
1114 007414  
1115 007416  
1116 007420  
1117 007422  
1118 007424  
1119 007426  
1120 007430  
1121 007432  
1122 007434  
1123 007436  
1124 007440  
1125 007442  
1126 007444  
1127 007446  
1128 007450  
1129 007452

```
KMAAMC: MOVE # 0, BREG ;CLEAR B REGISTER
; MAR <0:7> := 0.
; MAR <8:0> := 0.
MOVE # 0, MLCR
MOVE BREG, SPAD <0> ;CLEAR SPAD <0>.
MOVE BREG, SPAD <1> ;CLEAR SPAD <1>.
MOVE BREG, SPAD <2> ;CLEAR SPAD <2>.
MOVE BREG, SPAD <3> ;CLEAR SPAD <3>.
MOVE BREG, SPAD <4> ;CLEAR SPAD <4>.
MOVE BREG, SPAD <5> ;CLEAR SPAD <5>.
MOVE BREG, SPAD <6> ;CLEAR SPAD <6>.
MOVE BREG, SPAD <7> ;CLEAR SPAD <7>.
MOVE BREG, SPAD <10> ;CLEAR SPAD <10>.
MOVE BREG, SPAD <17> ;CLEAR SPAD <17>.
MOVE BREG, OUT1 <0> ;CLEAR BSEL0.
MOVE BREG, OUT1 <1> ;CLEAR BSEL1.
MOVE BREG, OUT1 <3> ;CLEAR BSEL3.
MOVE BREG, OUT1 <4> ;CLEAR BSEL4.
MOVE BREG, OUT1 <5> ;CLEAR BSEL5.
MOVE BREG, OUT1 <6> ;CLEAR BSEL6.
MOVE BREG, OUT1 <7> ;CLEAR BSEL7.
MOVE BREG, OUT1 <10> ;CLEAR BSEL10.
MOVE BREG, OUT1 <11> ;CLEAR BSEL11.
MOVE BREG, OUT0 <1> ;CLEAR BSEL1.
MOVE BREG, OUT0 <2> ;CLEAR BSEL2.
```

1130 007454  
1131 007456  
1132 007460  
1133 007462  
1134 007464  
1135 007466  
1136 007470  
1137 007472  
1138 007474  
1139 007476  
1140 007500  
1141 007502  
1142 007504  
1143 007506  
1144 007510  
1145 007512  
1146 007514  
1147 007516  
1148 007520  
1149 007522  
1150 007524  
1151 007526  
1152 007530  
1153 007530  
1154 007534  
1155 007536  
1156 007536  
1157 007540  
1158 007542  
1159 007544  
1160 007546  
1161 007550  
1162 007552  
1163 007554  
1164 007556  
1165 007560  
1166  
1167  
1168  
1169  
1170  
1171  
1172  
1173 007562  
1174 007564  
1175 007566  
1176 007570  
1177 007572  
1178 007574  
1179 007576  
1180 007600  
1181 007602  
1182 007604  
1183 007610  
1184 007612  
1185 007616

```
MOVE BREG, OUT0 <3> ;
MOVE BREG, OUT0 <4> ;
MOVE BREG, OUT0 <5> ;
MOVE BREG, OUT0 <6> ;
MOVE BREG, OUT0 <7> ;
MOVE BREG, OUT0 <10> ;
;
1$: MOVE # 0, MEM, MARINC ;CLEAR MEMORY LOCATION & INC MAR
; SPAD <0> ;INCREMENT THE COUNT
; SADC SPAD <1> ;ADD CARRY IN TO MSB.
; SBR <1>, BREG ;IS IT DONE...
; STRTS ;IF DONE AND START.
; SBR ;ELSE, CONTINUE
; # 200, BREG ;SET RD 1 BIT IN B REGISTER.
; INP1 <CSR0>, SPAD <0> ;SAVE SEL0 IN SPAD <0>.
; OR BREG, SPAD <0>, OUT1 <CSR0> ;SET RD 1 IN SEL0.
; MCDLP: MOVE INP1 <CSR2>, BREG ;IS RD0 SET??
; BBT7 MCDLP ;WAIT TILL IT CLEARS...
; BBT7 INP1 <CSR0>, BREG ;LOAD BRG WITH SEL0.
; SRR ;IF RD 1 SET, GO CHECK IET.
; INTRPT ;IF CLEARED, PROCESS BUFFERS.
; BBT4 INTRPT ;IF IGT IS ALSO SET THEN GO INTERRUPT
; SBR ;ELSE WAIT.
;
INTRPT: # 0, BREG ;SET UP TO CLEAR CSRS.
; BREG, OUT1 <CSR4> ;CLEAR BSEL4.
; BREG, OUT1 <CSR5> ;CLEAR BSEL5.
; BREG, OUT1 <CSR6> ;CLEAR BSEL6.
; BREG, OUT1 <CSR7> ;CLEAR BSEL7.
; # 200, BREG ;SET BR REG. WITH VECTOR=XX0.
; INP1 <CSR11>, SPAD <0> ;GET MICRO WISC. REGISTER.
; OR SPAD <0>, BREG, OUT1 <CSR11> ;SET BR REG. I.E. INTERRUPT AT XX0.
; 1$: MOVE INP1 <CSR11>, BREG ;IS BR REG BIT CLEARED.
; BBT7 ;NO, WAIT OR SPIN ON IT.
; 2$: MOVE INP1 <CSR0>, BREG ;IS IT SERVED??
; BBT7 ;NO THEN WAIT...
; SBR MCDLP ;RETURN TO LOOP.
;
;*****
; PROCESSES THE BUFFER REQUEST.
;*****
;
PRCSBF: MOVE INP1 <CSR6>, SPAD <15> ;LOAD SPAD <15> WITH C.C LOW BYTE.
; # 7, BREG ;LOAD MASKING BITS IN B REGISTER
; INP1 <CSR7>, SPAD <16> ;GET HIGH BYTE OF CHARACTER COUNT.
; AND BREG, SPAD <16>, SPAD <16> ;LOAD HIGH BYTE OF C.C. IN SPAD <16>.
; 2$: MOVE # 7, BREG ;LOAD MASK IN B REG.
; BBT7 ;GET BSEL0.
; AND BREG, SPAD <0>, BR, SP ;MASK OUT AND LOAD IN BREG & SPAD.
; MOVE MEM, SPAD <4>
; # 4, MEM ;LOAD EXPECTED IN MEM.
; SIFEQ MEM, SPAD <0> ;IF IET BA/CC I LOADED!! SERVE IT!
; MOVE # 0, MEM ;LOAD EXPECTED IN MEM.
; SIFEQ MEM, SPAD <0> ;RECVE ;RECVD BA/CC I LOADED!! SERVE IT!
; MOVE SPAD <4>, MEM ;RESTORE MEMGRY LOCATION
```

```
1186 007620 *      MOVE # 1,BREG ;SET THE TYPE OF ERROR.
1187 007622 *      MOVE BREG,SPAD <0> ;
1188 007624 *      CALL SFTERR ;NONE! REPORT ERROR!!
1189 007630 *      SBR STRTS ;RETURN TO LOOP.
;
;*****
;
;SERVE THE RECEIVE REQUEST.
;1. DATA TRANSMIT FROM KMCB11 TO PDP11. THROUGH NPR'S.
;2. CONTROL NPR/RR & NPR RATE.
;3. REPORT DONE OR ERROR A. NON EX MEM.
; B. SOFT ERROR.
;*****
;
1200 007632 *      RECVE: MOVE SPAD <4>,MEM ;RESTORE MEMORY LOCATION.
1201 007634 *      MOVE SPAD <10>,BREG ;IS XMIT FLAG SET?
1202 007636 *      BZ IS ;YES, THEN SERVE THE REQUEST.
1203 007640 *      MOVE SPAD <11>,BREG ;IS RECEIVE ONLY FLAG SET?
1204 007642 *      BZ IS ;YES, THEN PROCEED TO SERVE THE REQUEST.
1205 007644 *      MOVE # 1,BREG ;SET THE TYPE OF ERROR...
1206 007646 *      MOVE BREG,SPAD <0> ;
1207 007650 *      CALL SFTERR ;NO, REPORT ERROR!! RCVE. BEFORE XMIT.
1208 007654 *      SBR STRTS ;WAIT FOR NEXT REQUEST.
1209 007656 *      INPI <CSR4>,OUT0 <6> ;LOAD OUTBA <0:7>
1210 007660 *      MOVE INPI <CSR4>,SPAD <6> ;LOAD PARALLEL COUNT..
1211 007662 *      MOVE INPI <CSR5>,OUT0 <7> ;LOAD OUTBA <8:15>.
1212 007664 *      MOVE INPI <CSR5>,SPAD <7> ;SET PARALLEL COUNT.
1213 007666 *      MOVE INPI <CSR7>,SPAD <0> ;GET OUTBA EXTENDED BITS.
1214 007670 *      MOVE # 300,BREG ;GET THE MASK...
1215 007672 *      AND BREG,SPAD <0>,BR SP ;
1216 007674 *      SHFBRT ;SET IT IN RIGHT PLACE.
1217 007676 *      SHFBRT ;SET IT IN RIGHT PLACE.
1218 007678 *      SHFBRT ;SET IT IN RIGHT PLACE.
1219 007700 *      SHFBRT ;SET IT IN RIGHT PLACE.
1220 007702 *      MOVE MEM,SPAD <1> ;SAVE MEMORY LOCATION.
1221 007704 *      MOVE # 14,MEM ;GET MASK
1222 007710 *      MOVE MEM,SPAD <0> ;IN SPAD <0>.
1223 007712 *      AND SPAD <0>,BREG,OUT1 <11> ;SET OUT BA <16:17>
1224 007714 *      MOVE SPAD <0>,MEM ;RESTORE MEMORY LOCATION.
1225 007716 *      MOVE # 0,MLR ;SET THE BUFFER POINTER.
1226 007720 *      MOVE SPAD <11>,BREG ;IS RCOLY FLAG SET?
1227 007722 *      BZ IS ;YES, THEN SET LOWER BUFFER ADDRESS.
1228 007724 *      MOVE # 0,NPR ;CLEAR THE PAGE REGISTER.
1229 007726 *      SBR RCVLP ;START THE TRANSMISSION.
1230 007730 *      MOVE # 2,NPR ;SET POINTER TO GOOD DATA BUFFER.
1231 007732 *      MOVE MEM,OUT0 <3>,MARLNC ;LOAD LOW BYTE OF DATA.
1232 007734 *      MOVE MEM,OUT0 <3>,MARLNC ;LOAD HIGH BYTE OF DATA.
1233 007736 *      MOVE # 021,BREG ;SET
1234 007740 *      MOVE INPI <10>,SPAD <0> ;WORD XFR, NPR OUT
1235 007742 *      OR BREG,SPAD <0>,OUT1 <10> ;IS NPR DONE? & NPR RQ..
1236 007744 *      INPI <10>,BREG ;
1237 007746 *      SBR 25 ;NO, CHECK FOR NON EX MEM.
1238 007750 *      SBR 35 ;YES, PREPARE FOR NEXT.
1239 007752 *      MOVE INPI <11>,BREG ;IS NON EX MEM. SET?
1240 007754 *      OR 45 ;YES,REPORT FATAL ERROR.
```

```
1242 007756 *      SBR 15 ;NO, WAIT FOR NPR TO CLEAR.
1243 007760 *      MOVE # 2,BREG ;SET THE TYPE OF ERROR...
1244 007762 *      MOVE BREG,SPAD <0> ;
1245 007764 *      CALL NEXMEM ;REPORT NON EX MEM ERROR.
1246 007770 *      SBR STRTS ;WAIT FOR NEXT REQUEST
1247 007772 *      SDEC SPAD <15> ;DECREMENT THE COUNT.
1248 007774 *      BZ B7 ;BRANCH IF IT WAS 0
1249 007776 *      ZINC SPAD <6> ;UPDATE RECEIVE BUFFER
1250 010000 *      ZINC SPAD <6> ;
1251 010002 *      SADC SPAD <7> ;
1252 010004 *      DC 75 ;UPDATE EXTENDED BITS IF CARRY SET.
1253 010006 *      MOVE SPAD <6>,BREG ;SET THE ADDRESS.
1254 010010 *      MOVE BREG,OUT0 <6> ;
1255 010012 *      MOVE SPAD <7>,BREG ;SET THE OUTBA..
1256 010014 *      MOVE BREG,OUT0 <7> ;
1257 010016 *      CALL NPRATE ;WAIT TO MAINTAIN NPR RATE.
1258 010022 *      SBR RCVLP ;DO THE NEXT XFR.
1259 010024 *      SDEC SPAD <7> ;SET BACK TO 377.
1260 010026 *      MOVE # 4,BREG ;LOAD LSH OF EXTENDED ADDRESS.
1261 010030 *      MOVE INPI <11>,SPAD <0> ;GET EXTENDED ADDRESS BITS.
1262 010032 *      SADD BREG,SPAD <0>,OUT1 <11> ;INCREMENT EXTENDED BITS.
1263 010034 *      SBR 65 ;WAIT TO MAINTAIN RATE THEN PROCEED.
1264 010036 *      SDEC SPAD <16> ;DECREMENT THE MSP'S.
1265 010040 *      BZ RCVDNE ;BRANCH IF DONE
1266 010042 *      SBR 65 ;ELSE, DO THE NEXT
1267 010044 *      RCVDNE: MOVE # 201,BREG ;SET RD 0 & RCV DONE BITS IN BREG.
1268 010046 *      MOVE INPI <CSR2>,SPAD <0> ;GET RSEL2
1269 010050 *      OR BREG,SPAD <0>,OUT1 <CSR2> ;SET THE BITS IN RSEL2.
1270 010052 *      MOVE INPI <CSR2>,BREG ;DUMMY XFR TO CHECK IEQ?
1271 010054 *      BBA 15 ;BRANCH IF IEQ SET
1272 010056 *      SBR STRTS ;WAIT FOR NEXT REQUEST.
1273 010060 *      MOVE # 300,BREG ;SET BR REG. VCR:EX4
1274 010062 *      MOVE INPI <CSR11>,SPAD <0> ;GET MICRO-PROCESSOR MISC. REG.
1275 010064 *      OR BREG,SPAD <0>,OUT1 <CSR11> ;SET THE BITS IN MISC. REG.
```

```

1276 010066*
1277 010070*
1278 010072*
1279 010074*
1280 010076*
1281 010100*
1282 010102*
1283 010104*
1284
1285
1286
1287
1288
1289
1290
1291
1292 010106*
1293 010110*
1294 010112*
1295 010114*
1296 010116*
1297 010120*
1298 010122*
1299 010124*
1300 010126*
1301 010130*
1302 010132*
1303 010134*
1304 010136*
1305 010140*

2$: MOVE INP1 <CSR11>,BREG ;IS BR REQ CLEARED?
BB7 2$ ;NO SPIN ON IT
MOVE SPAD <1>,BREG ;SET TO CLEAR VCTR:=XX4...
MOVE INP1 <CSR11>,SPAD <0> ;GET UPMS REGISTER...
AND BREG,SPAD <0>,OUT1 <CSR11> ;CLEAR VCTR:=XX4...
MOVE # 0,BREG ;GET SET TO CLEAR XMIT FLAG.
MOVE BREG,SPAD <10> ;CLEAR XMIT FLAG.
SBR STR5 ;WAIT FOR NEXT REQUEST.
;*****
;** TRANSMIT THE BUFFER FORM PDP11 TO KMC11
;** DOES ALL THE FUNCTIONS AS RECEIVE OPERATION AND
;** ALSO CHECKS THE DATA.
;*****
XMIT: MOVE SPAD <4>,MEM ;RESTORE MEMORY LOCATION.
MOVE INP1 <CSR4>,OUT0 <4> ;GET LOW BYTE OF BUFFER ADDRESS.
MOVE INP1 <CSR4>,SPAD <6> ;SET PARALLEL ADDRESS IN SCRATCH PAD.
MOVE INP1 <CSR5>,OUT0 <5> ;GET HIGH BYTE OF BUFFER ADDRESS. PAD.
MOVE INP1 <CSR5>,SPAD <7> ;SET PARALLEL ADDRESS IN SCRATCH PAD.
MOVE INP1 <CSR7>,SPAD <0> ;GET EXTENDED BITS IN BREG.
MOVE # 10,BREG ;GET THE MASK...
AND BREG,SPAD <0>,BR.SP ;*****
SHFBRT ;POSITION THE BITS.
SHFBRT ;POSITION THE BITS.
SHFBRT ;POSITION THE BITS.
SHFBRT ;POSITION THE BITS.
MOVE MEM,SPAD <1> ;SAVE MEMORY LOC.
MOVE # 14,MEM ;

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1306 010142*
1307 010144*
1308 010146*
1309 010150*
1310 010152*
1311 010154*
1312 010156*
1313 010160*
1314 010162*
1315 010164*
1316 010166*
1317 010170*
1318 010172*
1319 010174*
1320 010176*
1321 010200*
1322 010202*
1323 010206*
1324 010210*
1325 010212*
1326 010214*
1327 010216*
1328 010220*
1329 010222*
1330 010224*
1331 010226*
1332 010230*
1333 010232*
1334 010234*
1335 010236*
1336 010240*
1337 010244*
1338 010246*
1339 010250*
1340 010252*
1341 010254*
1342 010256*
1343 010260*
1344 010262*
1345 010264*
1346 010266*
1347 010270*
1348 010272*
1349 010274*
1350 010276*
1351 010300*
1352 010302*
1353 010304*
1354 010306*
1355 010310*
1356 010312*
1357 010314*
1358 010316*
1359 010320*
1360 010322*
1361 010324*

MOVE MEM,SPAD <0> ;GET THE MASKING BITS.
AND BREG,SPAD <0>,OUT1 <40> ;LOAD EXTENDED ADDRESS BITS OF BUFFER
MOVE SPAD <1>,MEM ;RESTORE MEMRY.
MOVE # 0,MLR ;CLEAR MEMORY LOCATION REGISTER
MOVE # 0,MPR ;CLEAR MEMORY PAGE REGISTER.
XMTLP: INP1 <CSR10>,SPAD <0> ;GET NPR CONTROL REGISTER.
MOVE # 01,BREG ;SET NPR SQ. BIT IN BREG.
OR BREG,SPAD <0>,OUT1 <CSR10> ;SET NPR RQ BIT.
1$: MOVE INP1 <CSR10>,BREG ;IS NPR DONE?
BBO 2$ ;NO CHECK FOR NON EX. MEM
SBR ;YES, PREPARE FOR NEXT MEM
2$: MOVE INP1 <CSR11>,BREG ;IS NON EX. MEM. SET?
BBO 4$ ;REPORT FATAL ERROR IF IT IS.
SBR ;NO. WAIT FOR NPR TO CLEAR.
4$: MOVE # 2,BREG ;SET THE TYPE OF ERROR...
MOVE BREG,SPAD <0> ;
CALL NEXMEM ;REPORT NON EX. MEM. ERROR.
SBR STR5 ;WAIT FOR NEXT REQUEST.
MOVE INP0 <0>,MEM MARINC ;LOAD THE DATA IN TO MEMORY.
MOVE INP0 <1>,MEM MARINC ;LOAD THE DATA IN TO MEMORY.
SDEC SPAD <15> ;DECREMENT THE COUNT
BZ 6$ ;BRANCH IF IT HAS 0
SINC SPAD <6> ;UPDATE THE XMIT
SINC SPAD <6> ;" " " " " " " "
SADC SPAD <7> ;UPDATE BUFFER POINTER.
8$: MOVE SPAD <6>,BREG ;SET OUTBA ADDRESS IF CARRY SET.
MOVE BREG,OUT0 <4> ;SET OUTBA ADDRESS.
MOVE SPAD <7>,BREG ;SET OUTBA ADDRESS.
MOVE BREG,OUT0 <5> ;
CALL NPRA ;WAIT TO MAINTAIN THE NPR RATE.
SBR ;DO THE NEXT XPR.
7$: SPAD <7> ;SET IT BACK TO 377.
MOVE # 4,BREG ;LOAD LSB OF EXTENDED ADDRESS.
INP1 <CSR10>,SPAD <4> ;LOAD BREG WITH EXTENDED BITS.
SADD BREG,SPAD <4>,OUT1 <CSR10> ;INCREMENT THE EXTENDED BITS.
SBR ;WAIT TO MAINTAIN RATE THEN PROCEED.
5$: SPAD <16> ;DECREMENT THE MSB'S
SDEC ;BRANCH IF DONE.
BZ 6$ ;ELSE DO THE NEXT.
XMTDNE: MOVE INP1 <CSR6>,SPAD <15> ;GET LOW BYTE OF CHAR. COUNT.
MOVE # 77,BREG ;LOAD THE MASK
INP1 <CSR7>,SPAD <16> ;GET HIGH BYTE OF CHAR. COUNT.
AND BREG,SPAD <16>,SPAD <16> ;LOAD HIGH BYTE OF CHAR. COUNT.
MOVE # 0,BREG ;
MOVE BREG,SPAD <1> ;SET POINTERS TO DATA
MOVE BREG,SPAD <2> ;BUFFERS.
MOVE BREG,SPAD <3> ;SET POINTER TO GOOD.
CKDPL: MOVE BREG,SPAD <4> ;
MOVE SPAD <1>,MLR ;
MOVE SPAD <2>,MPR ;
MOVE MEM,SPAD <5> ;LOAD GOOD DATA.
MOVE SPAD <3>,MLR ;
MOVE SPAD <4>,MPR ;
SIFEQ SPAD <5>,MEM 1$ ;COMPARE DATA. GO TO 1$ IF GOOD

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1362 010330*      MOVE # 20,BREG          ;SET THE TYPE OF ERROR...
1363 010332*      MOVE BREG,SPAD <0>          ;
1364 010334*      CALL DATERR          ;REPORT DATA ERROR.
1365 010340*      SDEC SPAD <15>          ;DECREMENT COUNT
1366 010342*      RZ 2$          ;BRANCH IF LOW BYTE CIRCLED
1367 010344*      SINC SPAD <3>          ;UPDATE GOOD DATA
1368 010346*      SADC SPAD <4>          ;BUFFER POINTER
1369 010350*      SINC SPAD <15>          ;UPDATE DATA BUFFER POINTER
1370 010352*      SADC SPAD <2>          ;AND LOAD IT IN MAR.
1371 010354*      MOVE SPAD <1>,MLR      ;SET THE MAR...
1372 010356*      MOVE SPAD <2>,MPR      ;SET THE MAR...
  
```

```

1373 010360*      SBR CKDTLP          ;CHECK THE NEXT CHAR.
1374 010362*      SDEC SPAD <16>          ;DECREMENT MSP COUNT.
1375 010364*      RZ 3$          ;BRANCH IF DONE.
1376 010366*      SBR 9$          ;PREPARE FOR THE NEXT.
1377 010370*      MOVE # 205,BREG          ;SET RD 0, XMIT DONE BA/CC 0 /N BREG.
1378 010372*      MOVE INPI <CSR2>,SPAD <0> ;GET BSEL2.
1379 010374*      OR BREG,SPAD <0>,OUT1 <CSR2> ;SET BITS IN BSEL?.
1380 010376*      MOVE INPI <CSR2>,BREG      ;IS ISG SET?
1381 010400*      BB4 4$          ;YES, GO INTERRUPT
1382 010402*      SBR STRTS          ;WAIT FOR NEXT INSTRUCTION. REQUEST.
1383 010404*      MOVE # 300,BREG          ;
1384 010406*      MOVE INPI <CSR11>,SPAD <0> ;
1385 010410*      OR BREG,SPAD <0>,OUT1 <CSR11> ;SET BR RQ,VCTR=XX4
1386 010412*      MOVE INPI <CSR11>,BREG      ;IS BR RQ CLEARED
1387 010414*      BB7 5$          ;NO SPIN DR IT.
1388 010416*      MOVE # 277,BREG          ;SET TO CLEAR VCTR:=XX4.
1389 010420*      MOVE INPI <CSR11>,SPAD <0> ;GET UPMS REGISTER.
1390 010422*      AND BREG,SPAD <0>,OUT1 <CSR11> ;CLEAR VCTR:=XX4.
1391 010424*      SDEC SPAD <10>          ;SET XMIT FLAG
1392 010426*      SBR STRTS          ;GO TO IDLE STATE.
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1408 010430*      NPRATE: MOVE BREG,SPAD <17>          ;SAVE THE RETURN ADDRESS.
1409 010432*      MOVE SPAD <13>,BREG          ;GET NPR RATE COUNT...
1410 010434*      MOVE BREG,SPAD <0>          ;
1411 010436*      MOVE SPAD <14>,BREG          ;
1412 010440*      MOVE BREG,SPAD <17>          ;COUNT MOVE BREG,SPAD <17>
1413 010442*      SDEC SPAD <0>          ;
1414 010444*      RZ 2$          ;DECREMENT THE COUNT
1415 010446*      SBR 1$          ;BRANCH IF LOW BYTE IS COUNTED
1416 010450*      SDEC SPAD <1>          ;CONTINUE
1417 010452*      RZ 3$          ;RETURN IF DONE.
1418 010454*      SBR 1$          ;CONTINUE.
1419 010456*      SBR SPAD <17> PAGE0      ;RETURN.
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*****
;
; SUBROUTINES 1. NPR RATE CONTROL 2. ERROR REPORTING.
;
*****
1. NPRATE. THIS ROUTINE DELAYS THE NPR TO
MAINTAIN THE RATE SET BY THE OPERATOR.
;
; THE SMALLEST UNIT FOR RATE IS 2 MICRO-SECOND WHICH
; IS SIMULATED IN SOFTWARE USING MICRO-PROCESSOR
; MOVE INSTRUCTION. DELAY COUNT IS SET AT THE TIME OF
; LOADING THE MICRO-CODE.
;
NPRATE: MOVE BREG,SPAD <17>          ;SAVE THE RETURN ADDRESS.
MOVE SPAD <13>,BREG          ;GET NPR RATE COUNT...
MOVE BREG,SPAD <0>          ;
MOVE SPAD <14>,BREG          ;
MOVE BREG,SPAD <17>          ;COUNT MOVE BREG,SPAD <17>
1S: SDEC SPAD <0>          ;
RZ 2$          ;DECREMENT THE COUNT
SBR 1$          ;BRANCH IF LOW BYTE IS COUNTED
2S: SDEC SPAD <1>          ;CONTINUE
RZ 3$          ;RETURN IF DONE.
SBR 1$          ;CONTINUE.
3S: SBR SPAD <17> PAGE0      ;RETURN.
*****
;
; THIS ROUTINE REPORTS ERROR TO PDP11 MONITOR PROGRAM...
; I. SOFT ERROR:- (SPAD <4> BIT0:=1.)
; II. NON EXISTENT MEMORY:- (SPAD <4> BIT1:=1.)
; III. DATA ERROR:- (SPAD <4> BIT4:=1.)
;
  
```





PRTV2 =	000100	235#																		
PRTV3 =	000140	235#																		
PRTV4 =	000200	191#	235#																	
PRTV5 =	000300	235#																		
PRTV6 =	000300	235#																		
PRTV7 =	000340	235#																		
PSM =	177779	235#																		
PUSH =	005746	235#																		
PUSH2 =	024646	235#																		
RABD =	000700	235#																		
RACT =	001100	235#																		
RANDS =	104417	235#																		
RANNUM =	000054R	209#																		
RBCC =	000001	1#																		
RBR =	000200	1#																		
RBUF1 =	005272R	976#	1045#	1051#																
RBUF11 =	005276R	289#	1052#	1059#																
RBUF7 =	005274R	984#	1046#	1054#																
RBUF21 =	006278R	1055#	1068#																	
RCOLV =	000262R	260#	308#	352*	415	443	806	887												
RCVDNE =	010044R	1266#	1267#																	
RCVLP =	007432R	652#	1259																	
RCV =	007436R	652#	1259																	
RECVE =	007637R	1185#	1201#																	
REGO =	007336R	393#	394#	1072#																
REGO1 =	007366R	394#	1078#																	
REOM =	000002	1#																		
RESTRT =	000322R	228#	285	288#																
RES2 =	000056R	212#																		
ROMCLK =	004126R	811#	841	843	849	892	894	910	912	914	916	928#								
RONPR =	000001	1#																		
RRDY =	000240R	250#	316*	337*	498	666														
RSTRT =	000112R	228#																		
RTMULV =	000260R	258#	318	340																
SARO =	000252R	253#	435*	439																
SARO =	000252R	253#	435*	439																
SADAR =	000102R	221#	502*																	
SCAN =	001334R	422#	461#																	
SELECT =	000230R	296#	297	298	293	358	395	419	473	524*										
SETUP1 =	000216R	398#	418#																	
SETUP2 =	001166R	1189#	1209#	1432#																
SPTERR =	010460R	1432#	1432#																	
SPTERR1 =	010472R	1432#	1432#																	
SPT =	000001	1#	757#																	
SOPCNT =	000042R	204#																		
SOPERS =	104406	235#																		
SOPAS =	000046R	206#																		
SOP1 =	000116R	370#	570#																	
SOP11 =	002110R	570#	589#																	
SOP12 =	002022R	370#	572#																	
SOP121 =	000371R	577#	599#																	
SPOINT =	000032R	200#																		

SPSZ =	000040	1#	233																	
SR1 =	000016R	193#	312	314	320	322	323	345	347	354										
SR2 =	000017R	193#																		
SR3 =	000022R	195#																		
SR4 =	000024R	196#																		
START =	000272R	199#	281#																	
STARTS =	000258R	198#																		
SVRO =	000062R	213#	1142#	1190	1210	1247	1273	1284	1324	1383	1393									
SVR1 =	000064R	214#																		
SVR2 =	000066R	216#																		
SVR3 =	000070R	217#																		
SVR4 =	000072R	217#																		
SVR5 =	000074R	218#																		
SVR6 =	000076R	219#																		
SW1 =	000019	1#																		
SW2 =	000016	1#																		
SYSCNT =	000052R	208#																		
SZMULV =	000256R	264#	316	317	333															
TERM =	000000	259#	335	337	338	342	344	359*	371*	372	498*	510*	959*	960*						
TRPFD =	000022	235#																		
VA =	000244R	189#	662*	672*	944															
VECTOR =	000010R	189#	385																	
VECA =	000100	1#																		
VERIFY =	003624R	367#	866#																	
WASADR =	000104R	367#	793#																	
WCRAM =	003356R	465#																		
WDFR =	000116R	230#																		
WDTO =	000114R	229#																		
WHEWRY =	003552R	366#	840#																	
XABD =	000200	1#																		
XACT =	000100	1#																		
XBUF =	004266R	497#	672	845	903	994#														
XDROP1 =	000266R	565#	578#																	
XDROP2 =	002050R	567#	582#																	
XEOM =	000002	1#																		
XERR =	001666R	167#	532	545#																
XFLAG =	000005R	1#																		
XMIT =	010106R	1183#	1292#																	
XMITR =	002502R	658#	672#																	
XMDLY =	000263R	261#	309#	356*	411	493	744													
XMTDNE =	010266R	1345#	1346#																	
XMTLP =	010154R	1311#	1338#																	
XRDV =	000020	1#																		
XSPU =	000040	1#																		
XSIZE =	000242R	251#	317*	338*	676	905														
XZOM =	000001	1#																		
XX11 =	004234R	310#	387*	464	975#															
XX12 =	004240R	318#	388*	467	983#															
XX22 =	004262R	318#																		
SSINIT =	007372R	1105#	1141	1142	1147	1149	1150	1151	1152	1163	1165	1166	1183	1185						
		1189#	1190	1204	1206	1208	1210	1229	1231	1236	1240	1242	1243	1246						
		1317	1319	1320	1323	1324	1328	1332	1337	1338	1343	1345	1346	1362						





DIAGNOSTIC ENGINEERING

2500

**digital**

DECO  DEPO  SUBMISSION

FOR RELEASE ENG. USE  
 NEW  CHANGE  DELETE

PRODUCT IDENTIFICATION

MD	LIBRARY	PRODUCT NUMBER	REV	PATCH	ECO TALLY	PRODUCT DATE			STATUS	DISTRIBUTION	1ST COPY - RIGHT YEAR	LAST COPY - RIGHT YEAR
	ZZ	CXKMC	B	1	Q/	DD	MMM	YY	OBSOLETE	X G	R	1976 1979

TITLE CXKMCB1 KMC-11 MODULE  
 AUTHOR D. BUTENHOF MAINTAINING GROUP DEC/X Supt GRP MAINTAINER D. BUTENHOF SUBMITTING ENGINEER D. BUTENHOF

PRODUCT COMPONENTS

CK	DESCRIPTION	PRODUCT NO.	REV	CK	DESCRIPTION	PRODUCT NO.	REV
	DOCUMENT				INDEX		
	LISTING				SOURCE MEDIA		
	OBJECT MEDIA				TEST MEDIA		
X	DECO	AF-E950B-M1					

PRODUCTS OBSOLETE (other than previous version)

LIBRARY	PRODUCT NUMBER	REV	LIBRARY	PRODUCT NUMBER	REV	LIBRARY	PRODUCT NUMBER	REV
MD			MD			MD		

PRODUCT CHARACTERISTICS

PROCESSORS PRODUCT OPERATES WITH (Enter all applicable 2-digit codes representing the Processor the product operates with. See separate instructions.)

OPERATIONAL CODES (Enter all applicable 2-digit codes that describe the product. See separate instructions.)

ACT/APT/XXDP	EXT	ACT SEQ NUMBER	ACT/XXDP COMPATIBLE?	APT COMPATIBLE?	1ST PASS RUN TIME	SUBSEQUENT PASS RUN TIME
INFORMATION FIELD			<input checked="" type="checkbox"/> Y <input type="checkbox"/> N	<input checked="" type="checkbox"/> Y <input type="checkbox"/> N	SECONDS	SECONDS

DECO/DEPO INFORMATION

PROBLEM REPORTS CLOSED: \_\_\_\_\_

DEVICE AFFECTED DEC/X11 MULTIMEDIA AFFECTED?  YES  NO

KIT NUMBERS	ZJ130-RB					
	ZJ129-RZ, FR					

PROBLEM:  
 ERRORS WHEN BUFFERS OVERLAP 32K BOUNDARY, DUE TO MICROCODE BUGS.

SOLUTION:  
 PATCH THE FOLLOWING MODULE LOCATIONS

DEPO PATCH AREA

CHANGE LOC	FROM	TO	CHANGE LOC	FROM	TO
10024	63167	63207			
10246	63167	63207			
10254	61014	61010			

SUBMITTING ENGINEER <i>D. Butenhof</i>	MANUFACTURING ENGINEER <i>E. Casella</i>	SUPPORT ENGINEER	CHARGE DECO/DEPO TO DISCRETE PROJECT NUMBER <b>99805460</b>
DATE: 19 APR 79	DATE: 25-APR-79	DATE:	
MAINTAINER <i>D. Butenhof</i>	FIELD SERVICE <i>W. Craft</i>	WAIVERING MANAGER	COORDINATION NO. <b>MC 3087</b>
DATE: 25 Apr. 79	DATE:	DATE:	