

.REM \_

IDENTIFICATION

PRODUCT CODE: AC-E739B-MC  
PRODUCT NAME: CXDRFB0 DRV11B MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

THE INFORMATION IN THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE AND SHOULD NOT BE CONSTRUED AS A COMMITMENT BY DIGITAL EQUIPMENT CORPORATION. DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR ANY ERRORS THAT MAY APPEAR IN THIS MANUAL.

THE SOFTWARE DESCRIBED IN THIS DOCUMENT IS FURNISHED TO THE PURCHASER UNDER A LICENSE FOR USE ON A SINGLE COMPUTER SYSTEM AND CAN BE COPIED (WITH INCLUSION OF DIGITALS COPYRIGHT NOTICE) ONLY FOR USE IN SUCH SYSTEM, EXCEPT AS MAY OTHERWISE BE PROVIDED IN WRITING BY DIGITAL.

DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY OF ITS SOFTWARE ON EQUIPMENT THAT IS NOT SUPPLIED BY DIGITAL.

COPYRIGHT (C) 1976,1978 DIGITAL EQUIPMENT CORPORATION

1. ABSTRACT

"DRF" IS AN "IOMODX" THAT EXERCISES ONLY ONE DRV11B. IF THE SYSTEM CONTAINS MORE THAN ONE DRV11B, ANOTHER COPY OF THIS MODULE MUST BE LOADED. A READ/WRITE REGISTER TEST IS PERFORMED TO ENSURE SOME OPERATING CONFIDENCE IN THE BASIC HARDWARE INTERFACE. THE DRV11B IS THEN ENABLED TO PERFORM DMA TRANSFERS TO AND FROM MEMORY USING DIFFERENT MODES AND DATA PATTERNS.

2. REQUIREMENTS

HARDWARE: DRV11B INTERPROCESSOR INTERFACE  
STORAGE:: DRF REQUIRES:

1. DECIMAL WORDS: 1489
2. OCTAL WORDS: 02721
3. OCTAL BYTES: 5642

3. PASS DEFINITION

ONE PASS OF DRF MODULE CONSISTS OF 3072 PROGRAM INTERRUPTS AND 204,800 NON-PROCESSOR REQUESTS.

4. EXECUTION TIME

DRF RUNNING ALONE ON LSI-11 TAKES APPROXIMATELY ONE MINUTE.

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:

DEVADR: 172410, VECTOR: 124, BR1: 4, DEVCNT: 1, SR1: 0

REQUIRED PARAMETERS:

NONE, THIS MODULE ONLY SUPPORTS ONE DRV11B.

6. DEVICE/OPTION SETUP

THE MAINTENCE "WRAP-AROUND" BERG CABLE MUST BE INSTALLED.

7. MODULE OPERATION

THE MODULE WILL BEGIN BY TESTING THE ABILITY OF THE BUS READ/WRITE  
REGISTERS TO FUNCTION PROPERLY. THE REGISTERS VERIFIED ARE:

WORD COUNT <16 BITS WIDE>  
BUFFER ADDRESS <15 BITS WIDE>  
DATA BUFFER <16 BITS WIDE>  
STATUS REGISTER <6 BITS TESTED>

8. OPERATION OPTIONS

NONE

9. NON STANDARD PRINTOUTS

ALL PRINTOUTS HAVE STANDARD MEANINGS AS REPRESENTED IN  
DEC/X11 DOCUMENTATION.

10. ENVIRONMENT

- #1 LSI-11 WITH 16K OF MEMORY  
RX11 FLOPPY DISK CONTROLLER WITH 2 DRIVE  
DRV11B INTERPROCESSOR INTERFACE WITH WRAPAROUND CABLE
- #2 LSI-11 WITH 24K OF MEMORY (8K CORE + 16K MOS)  
RX11 FLOPPY DISK CONTROLLER WITH 2 DRIVES  
DRV11B INTERPROCESSOR BUFFER  
DRV11B INTERPROCESSOR BUFFER  
DRV11B INTERPROCESSOR BUFFER

```
134 000000- IOMODX <DRFB>,172410,124,4,0,0,1000,121,BUFFER,512.  
135 000000- MODULE 150000,DRFB,172410,124,4,0,0,1000,121,BUFFER,512.,  
136 .TITLE DRFB DEC/X11 SYSTEM EXERCISER MODULE  
137 .VERSION 6 23-MAY-78  
138 ;  
139 ;*****  
140 000000- BEGIN: ;  
141 000000- 051104 041106 040 MODNAM: -ASCII /DRFB / ;MODULE NAME  
142 000000- 000 000 XFLA: -BYTE OPEN ;USED TO KEEP TRACK OF WBUF USAGE  
143 000000- 172410 ADDR: ;1ST DEVICE ADDR.  
144 000010- 000124 VECTOR: 124+0 ;1ST DEVICE VECTOR.  
145 000013- 000 BR1: -BYTE PRTV4+0 ;1ST BR LEVEL.  
146 000014- 000 BR2: -BYTE PRTV0+0 ;2ND BR LEVEL.  
147 000014- 000001 DVID1: 0+1 ;DEVICE INDICATOR 1.  
148 000016- 000000 SR1: OPEN ;SWITCH REGISTER 1  
149 000020- 000000 SR2: OPEN ;SWITCH REGISTER 2  
150 000020- 000000 SR3: OPEN ;SWITCH REGISTER 3  
151 000024- 000000 SR4: OPEN ;SWITCH REGISTER 4  
152 ;*****  
153 000026- 150000 STAT: 150000 ;STATUS WORD  
154 000030- 000264- INIT: START ;MODULE START ADDR.  
155 000032- 000250- SPOINT: MODDSP ;MODULE STACK POINTER.  
156 000034- 000000 PASCNT: 0 ;PASS COUNTER  
157 000030- 001000 ICOMP: 1000 ;# OF ITERATIONS PER PASS=1000  
158 000030- 000000 ICOUNT: 0 ;LOC TO COUNT ITERATIONS  
159 000042- 000000 SPCFCT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS  
160 000044- 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS  
161 000046- 000000 SPCPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS  
162 000048- 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS  
163 000052- 000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED  
164 000054- 000000 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED  
165 000056- 000000 CONF1G: 0 ;RESERVED FOR MONITOR USE  
166 000058- 000000 RES1: 0 ;RESERVED FOR MONITOR USE  
167 000060- 000000 RES2: 0 ;RESERVED FOR MONITOR USE  
168 000062- 000000 SVR0: OPEN ;LOC TO SAVE R0.  
169 000064- 000000 SVR1: OPEN ;LOC TO SAVE R1.  
170 000066- 000000 SVR2: OPEN ;LOC TO SAVE R2.  
171 000070- 000000 SVR3: OPEN ;LOC TO SAVE R3.  
172 000072- 000000 SVR4: OPEN ;LOC TO SAVE R4.  
173 000074- 000000 SVR5: OPEN ;LOC TO SAVE R5.  
174 000076- 000000 SVR6: OPEN ;LOC TO SAVE R6.  
175 000100- 000000 CSRA: OPEN ;ADDR OF CURRENT CSR.  
176 000102- 000000 SBADR: OPEN ;ADDR OF GOOD DATA, OR  
177 000104- 000000 ACSR: OPEN ;CONTENTS OF CSR.  
178 000104- 000000 WASADR: OPEN ;ADDR OF BAD DATA, OR  
179 000104- 000000 ASTAT: OPEN ;STATUS REG CONTENTS.  
180 000106- 000000 ERR1YP: OPEN ;TYPE OF ERROR  
181 000108- 000000 ASB: OPEN ;EXPECTED DATA.  
182 000110- 000000 AMAS: OPEN ;ACTUAL DATA.  
183 000112- 000264- RSTR1: RESTR1 ;RESTART ADDRESS AFTER END OF PASS  
184 000114- 000000 WD10: OPEN ;WORDS TO MEMORY PER ITERATION  
185 000116- 000000 WDPR: OPEN ;WORDS FROM MEMORY PER ITERATION  
186 000120- 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION  
187 000122- 000121 IDNUM: 121 ;MODULE IDENTIFICATION NUMBER=121  
188 000124- 003640- RBUFVA: BUFFER ;READ BUFFER VIRTUAL ADDRESS  
189 000126- 000000 RBUFP: OPEN ;READ BUFFER PHYSICAL ADDRESS
```

```
190 000130- 000000 RBUFEA: OPEN ;READ BUFFER EA BITS  
191 000132- 001000 RBUFSZ: 512 ;SIZE OF THE READ BUFFER  
192 000134- 000000 WBUFEA: OPEN ;WRITE BUFFER PHYSICAL ADDRESS  
193 000136- 000000 WBUFEA: OPEN ;WRITE BUFFER EA BITS  
194 000140- 000000 WBUFRQ: OPEN ;WRITE BUFFER SIZE REQUESTED  
195 000142- 000000 WBUFSZ: OPEN ;WRITE BUFFER SIZE AVAILABLE  
196 000144- 000000 CDECT: OPEN ;CDATA/DATCK ERROR COUNT  
197 000144- 000000 CDWDCT: OPEN ;CDATA/DATCK WORD COUNT  
198 000146- 000000 FREE: OPEN ;RESERVED FOR FUTURE USE  
199 .REPT SPSIZ ;MODULE STACK STARTS HERE.  
200 .NLIST 0  
201 .WORD 0  
202 .LIST  
203 .ENDR  
204 000250-  
205 ;*****
```

```
206 ;DRV11B BUS REGISTER ADDRESS POINTERS
207
208 000250- 172410 DRVWCR: 172410 ;WORD COUNT
209 000252- 172412 DRVBAR: 172412 ;BUFFER ADDRESS
210 000254- 172414 DRVCSR: 172414 ;COMMAND/STATUS
211 000256- 172416 DRVDBR: 172416 ;DATA BUFFER
212
213 ;DRV11B VECTOR ADDRESS POINTERS
214
215 000260- 000124 DRVCT0: 124 ;READY, NEX & INCOMPLETE DATIO VECTOR
216 000262- 000126 DRVCT2: 126 ;NEW PSW ON INTR
217
218 ;COMMON PROGRAM LOCATION(S)
219
220 177564 TPS=177564
221
222 000264- 012767 000232 177622 RESTR: MOV #232,WDTO ;232 WORDS TO MEM/ITERATION
223 000267- 012767 000235 177616 START: MOV #232,WDFR ;232 WORDS FROM MEM/ITERATION
224 000270- 012767 000238 177612 MOV #6,INTR ;6 INTERRUPTS/ITERATION
225 000300- 012767 000006- 177612 MOV #DRVWCR,RO ;SET UP REG ADRS POINTER
226 000306- 012700 000250- MOV ADDR,R1 ;GET BASE ADRS
227 000312- 012790 177470 SETUP2: MOV #R1(R0)+ ;LOAD EH
228 000320- 062701 000002- ADD #2,R1 ;
229 000322- 022700 000260- CMP #DRVDBR+2,RO ;ALL DONE?
230 000324- 001370 BNE SETUP2 ;BR IF NOT
231 000330- 001370 MOV #DRVCT0,RO ;SET UP DRV11B VECTOR ADRS POINTER
232 000336- 016701 177446 SETUP3: MOV VECTOR,R1 ;GET BASE VECTOR ADRS
233 000342- 010120 MOV #R1(R0)+ ;
234 000344- 062700 000002- ADD #2,R1 ;POINT TO NEXT
235 000350- 001370 000264- CMP #DRVCT2+2,RO ;ALL DONE?
236 000352- 001372 BNE SETUP3 ;BR IF NOT
237 000354- 016777 177700 177674 MOV DRVCT2,DRVCT0
238 000356- 005077 177672 CLR DRVCT2
239 000370- 104415 000000- 000124- GETPA$,BEGIN, RBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA
240
241
242
```

```
243 ;TEST THAT THE WORD COUNT REG IS WRITE/READABLE (FLOAT 0 COM PTRN)
244
245 000376- 016767 177646 177474 TSTWC: MOV DRVWCR,CSRA ;SET UP WC REG ADRS
246 000404- 005000 CLR RO ;RO SAYS SHIFT PTRN WHEN 0
247 000406- 012767 177776 177470 MOV #2,ASTAT ;FLOAT 0 RIGHT TO LEFT
248 000411- 016777 177464 177626 1S: MOV ASTAT,DRVWCR ;LDR WC
249 000420- 017767 177622 177452 MOV #DRVWCR,ACSR ;READ IT BACK
250 000430- 026767 177450 177444 CMP ASTAT,ACSR ;COMPARE RESULTS
251 000438- 001406 BEQ #25,ERRTYP ;BR IF SO
252 000440- 012767 000025 177440 MOV #25,ERRTYP ;BIT STUCK
253 *****
254 ;*****
255
256 000446- 104405 000000- 000000 2S: COM ASTAT ;COMPLEMENT ZERO
257 000454- 005167 177424 COM RO ;RO SAYS SHIFT LEFT WHEN = 0
258 000460- 005100 BNE #1,ASTAT ;TRY THE COMPLEMENT IF RO NOT 0
259 000464- 001354 177414 ASL ASTAT ;COMPLEMENT WAS DONE - NOW SHIFT ZERO LEFT
260 000470- 005267 177410 INC ASTAT ;KEEP LSB SET
261 000474- 103747 BCS #1,ASTAT ;AGAIN TILL ALL PATRNS DONE
262
263 ;TEST THAT THE BUFFER ADDRESS REG IS WRITE/READABLE (FLOAT 0 COM PTRN)
264
265 000476- 016767 177550 177374 TSTBA: MOV DRVBAR,CSRA ;SET UP BA REG ADRS
266 000504- 005000 CLR RO ;RO SAYS SHIFT PTRN WHEN 0
267 000506- 012767 177774 177370 MOV #4,ASTAT ;FLOAT 0 RIGHT TO LEFT
268 000511- 016777 177364 177530 1S: MOV ASTAT,DRVBAR ;LDR BA
269 000520- 017767 177524 177352 MOV #DRVBAR,ACSR ;READ IT BACK
270 000530- 026767 000001 177344 BIC #BIT0,ACSR ;DON'T WANT BIT0
271 000538- 026767 177342 177336 CMP ASTAT,ACSR ;COMPARE RESULTS
272 000544- 001406 BEQ #25,ERRTYP ;BR IF SO
273 000546- 012767 000025 177332 MOV #25,ERRTYP ;BIT STUCK
274 *****
275 ;*****
276 000554- 104405 000000- 000000 2S: HDRERS,BEGIN,NULL ;BUS ADRS WRITE/READ FAILURE
277 *****
278 000562- 005167 177316 COM ASTAT ;COMPLEMENT ZERO
279 000570- 042767 000001 177310 BIC #BIT0,ASTAT ;BIT 0 NOT INVOLVED
280 000574- 005100 COM RO ;RO SAYS SHIFT LEFT WHEN = 0
281 000576- 001346 BNE #1,ASTAT ;TRY THE COMPLEMENT IF RO NOT 0
282 000600- 006367 177300 ASL ASTAT ;COMPLEMENT WAS DONE - NOW SHIFT ZERO LEFT
283 000604- 193704 177274 STDB #1,ASTAT ;NEXT TEST OF BIT 15 DONE
284 000606- 062767 000002 177270 ADD #2,ASTAT ;KEEP ADDR LSB SET
285 000614- 000737 BR #1,ASTAT ;AGAIN TILL ALL PATRNS DONE
```

```
286 ;TEST THAT THE DATA BUFFER REG IS WRITE/READABLE (FLOAT 0 COM PTRN)
287
288 000616 016767 177434 177254 TSTDB: MOV DRVDBR,CSRA ;SET UP DB REG ADRS
289 000624 005000 CLR RO ;RO SAVS SHIFT PTRN WHEN 0
290 000628 012767 177776 177250 MOV #2,ASTAT ;FLOAT 0 RIGHT TO LEFT
291 000634 016777 177444 177414 1$: MOV ASTAT,DRVDBR ;LD DB
292 000642 017767 177410 177332 MOV DRVDBR,ACSR ;READ IT BACK
293 000650 026767 177230 177224 CMP ASTAT,ACSR ;COMPARE RESULTS
294 000656 001406 BEQ IF SO ;IF SO
295 000660 012767 000025 177220 MOV #25,ERRTYP ;BIT STUCK
296 *****
297 000666 104405 000000 000000 HRDERS,BEGIN,NULL ;DATA BUFFER WRITE/READ FAILURE (LOOP BACK)
298 *****
299 000674 005167 177204 2$: COM ASTAT ;COMPLEMENT ZERO
300 000700 005100 RO ;RO SAVS SHIFT LEFT WHEN = 0
301 000702 001354 BNE IS ;TRY THE COMPLEMENT IF RO NOT 0
302 000704 006367 177174 ASL ASTAT ;COMPLEMENT WAS DONE - NOW SHIFT ZERO LEFT
303 000710 005267 177170 INC ASTAT ;KEEP LSB SET
304 000714 103747 BCS IS ;AGAIN TILL ALL PATRNS DONE
305
306 ;TEST THAT THE DATA BUFFER REG IS BYTE ADDRESSABLE
307
308 000716 016700 177334 TSTDBB: MOV DRVDBR,RO ;GET DB REG ADRS
309 000722 010067 177152 MOV RO,CSRA ;SET UP DB REG ADRS
310 000726 005100 CLR (RO) ;ZERO DATA BUFFER REG
311 000730 012767 177177 177146 MOV #177177,ASTAT ;LD EXPECTED
312 000736 012767 077776 177136 MOV #77776,ACSR ;SEND DATA FROM "BDDAT"
313 000744 156760 B1SB ACSR,(RO) ;LOAD HI BYTE DB
314 000748 156760 B1SB ACSR,(RO) ;LOAD LO BYTE DB
315 000756 011067 177120 MOV (RO),ACSR ;READ IT BACK
316 000762 026767 177116 177112 CMP ASTAT,ACSR ;COMPARE RESULTS
317 000770 001406 BEQ IF SO ;NEXT TEST IF SO
318 000772 012767 000025 177106 MOV #25,ERRTYP ;BIT STUCK
319 *****
320 001000 104405 000000 000000 HRDERS,BEGIN,NULL ;DATA ERROR ON BYTE ADDRESSING THE DATA BUFFER REG
321 *****
322
```

```
323 ;TEST THAT THE 3 "FNCT" BITS CONTROL THE 3 "STAT" BITS (COUNT PTRN)
324
325 001006 016767 177242 177064 TSTFN3: MOV DRVCSR,CSRA ;SET UP CSR ADRS
326 001014 012767 007000 177062 MOV #7000,ASTAT ;LD EXPECTED
327 001022 012700 000016 MOV #16,RO ;RO CONTAINS "FNCT" BITS WRITTEN
328 001026 010077 177222 1$: MOV RO,DRVCSR ;WRITE INTO "FNCT" BITS
329 001032 017767 177422 177042 MOV DRVCSR,ACSR ;READ BACK THRU "STAT" BITS
330 001040 042767 170777 177034 BIC #170777,ACSR ;MASK TO "STAT" BITS ONLY
331 001046 026767 177032 177026 CMP ASTAT,ACSR ;COMPARE RESULTS
332 001054 001406 BEQ IF SO ;IF SO
333 001056 012767 000025 177022 MOV #25,ERRTYP ;BIT STUCK
334 *****
335 001064 104405 000000 000000 HRDERS,BEGIN,NULL ;"FNCT" BITS FAILED TO SET "STAT" BITS (LOOP BACK)
336 *****
337 001072 162767 001000 177004 2$: SUB #1000,ASTAT ;CHANGE TO NEXT EXPECTED
338 001100 162700 000002 SUB #2,RO ;DECREASE COUNT PATTERN
339 001104 100350 BPL IS ;DO AGAIN UNTIL 0 TESTED
340
341 ;TEST THAT GO CLRS READY & FNCT 2 WILL SET IT
342
343 001106 016767 177142 176764 TSTFAC: MOV DRVCSR,CSRA ;SET UP CSR ADRS
344 001114 005067 176764 CLR ASTAT ;EXPECT 0
345 001120 012777 000001 177126 MOV #1,DRVCSR ;SET GO WHICH SHOULD CLR READY
346 001126 017767 177122 176746 MOV DRVCSR,ACSR ;READ THE CSR
347 001134 026767 176744 176740 CMP ASTAT,ACSR ;COMPARE RESULTS
348 001142 001406 BEQ IF SO ;IF SO
349 001144 012767 000025 176734 MOV #25,ERRTYP ;BIT STUCK
350 *****
351 001152 104405 000000 000000 HRDERS,BEGIN,NULL ;THE GO BIT FAILED TO CLR READY
352 *****
353 001160 052777 000004 177066 1$: B1S #4,DRVCSR ;FNCT 2 SHOULD SET READY
354 001166 012767 002204 176710 MOV #2204,ASTAT ;LD EXPECTED
355 001174 017767 177054 176700 MOV DRVCSR,ACSR ;GET CSR
356 001202 016767 176676 176672 CMP ASTAT,ACSR ;COMPARE RESULTS
357 001210 001406 BEQ IF SO ;NEXT TEST IF SET
358 001212 012767 000025 176666 MOV #25,ERRTYP ;BIT STUCK
359 *****
360 001220 104405 000000 000000 HRDERS,BEGIN,NULL ;FNCT 2 (VIA ATTN) FAILED TO SET READY
361 *****
362
```

```

363 ;TEST THAT READY CONTROLS "BAR" BIT00
364
365 001226 012777 000004 177020 TSTBAR: MOV #4, @DRVCSR ;SET READY
366 001234 016767 177012 176636 MOV @RVBAR, CSRA ;SET UP BAR ADHS
367 001242 012767 000001 176634 MOV #1, A$TAT ;EXPECT LSB OF BAR
368 001250 005077 176776 CLR @RVBAR ;CLR BAR
369 001258 017767 176772 176620 MOV @RVBAR, ACSR ;READ BAR
370 001266 026767 176616 176612 CMP A$TAT, ACSR ;COMPARE RESULTS
371 001274 001406 ;BR IF SO
372 001272 012767 000025 176606 BEQ #25, ERRTYP ;BIT STUCK
373 ;*****
374 001300 104405 000000 000000 HRDERS, BEGIN, NULL ;A00 FAILED TO READ A ONE(SB TIED TO RDY)
375 ;*****
376 001306 012777 000001 176740 1$: MOV #1, @DRVCSR ;SET GO(CLRS BAR BIT00)
377 001314 017767 176732 176560 MOV @RVBAR, ACSR ;READ BAR
378 001322 006410 ;BR IF ZERO
379 001324 005067 176554 CLR A$TAT ;EXPECTED ZERO
380 001330 012767 000025 176550 MOV #25, ERRTYP ;BIT STUCK
381 ;*****
382 001336 104405 000000 000000 HRDERS, BEGIN, NULL ;WHEN RDY CLRED-A00 FAILED TO READ A ZERO
383 ;*****
384 001344 012777 000004 176702 2$: MOV #4, @DRVCSR ;INSURE RDY SET BEFORE ADVANCING
385 ;*****
386 ;TEST THAT "CYCLE" WILL CLOCK THE DBR (IN)
387
388 001352 016767 176700 176520 TSTCLK: MOV @RVDBR, CSRA ;SET UP DBR ADHS
389 001360 012767 176692 176516 MOV #12525, A$TAT ;LD EXPECTED
390 001368 017777 176692 176692 MOV A$TAT, @RVDBR ;LD DBR WITH #125252
391 001374 012777 000400 176652 MOV #400, @DRVCSR ;SET CYCLE - SHOULD CLK DBR (IN)
392 001402 012777 052525 176646 MOV #52525, @RVDBR ;CHANGE DBR (OUT) DATA - SHOULD NOT AFFECT (IN)
393 001410 017767 176642 176454 MOV @RVDBR, ACSR ;READ DBR
394 001418 026767 176442 176456 CMP A$TAT, ACSR ;COMPARE RESULTS
395 001424 001406 ;BR IF SO
396 001426 012767 000025 176452 BEQ #25, ERRTYP ;BIT STUCK
397 ;*****
398 001434 104405 000000 000000 HRDERS, BEGIN, NULL ;CYCLE DID NOT LATCH DBR (IN) DATA
399 ;*****
400 001442 042777 000400 176604 1$: BIC #400, @DRVCSR ;REMOVE CYCLE
401 001450 017767 052525 176426 MOV #52525, A$TAT ;NOW EXPECT #52525
402 001458 017767 000400 176426 MOV @RVDBR, ACSR ;READ DBR
403 001464 026767 176414 176410 CMP A$TAT, ACSR ;COMPARE RESULTS
404 001472 001406 ;NEXT TEST IF SO
405 001474 012767 000025 176404 BEQ #25, ERRTYP ;BIT STUCK
406 ;*****
407 001502 104405 000000 000000 HRDERS, BEGIN, NULL ;DBR FAILED TO READ WHEN CYCLE CLRED (NORMAL)
408 ;*****
409

```

```

410 ;TEST SINGLE "DATI" NPR TRANSFERS (FLOATING 0 COMPLEMENT PATRN)
411
412 001510 004567 001410 TSTDTI: JSR R5, SETVEC ;GO SET UP INTERRUPT RETURN
413 001514 001564 ; ;RETURN TO 2$ ON INTR
414 001516 017700 177776 CLR #2, R0 ;FLOAT ZERO RIGHT TO LEFT
415 001522 005001 ; ;CARRIES DATA SHIFTING
416 001524 012777 177777 176516 1$: MOV #1, @RVNCR ;DO ONE XFER
417 001532 016777 176370 176512 MOV @RVNCR, @RVNCR ;GET DATA WORD FROM "DBUF"
418 001540 010957 007074 ; ;SET UP MEM DATA
419 001544 010957 008104 176502 MOV #1, @RVNCR ;SET IF 6 GO
420 001552 052777 000400 176474 BIS #400, @DRVCSR ;SET CYCLE
421 001560 104400 000000 ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
422 001564 ;*****
423 2$: ;*****
424 001564 000004 000000 001572 ;IRQS, BEGIN, 5$ ; QUEUE UP TO CONTINUE AT 5$ AND RTI
425 ;*****
426 001572 004567 001362 5$: JSR R5, CKSTAT ;GO CHECK STATUS
427 001576 000780 ; ;CSR STATUS EXPECTED
428 001600 000001 ; ;OF XFRS
429 001602 012767 000032 176276 MOV #32, ERRTYP ;NPR ERROR
430 ;*****
431 001610 104405 000000 000000 HRDERS, BEGIN, NULL ;RETURN HERE IF STATUS ER - EXPECTED CYCLE, READY & IE
432 ;*****
433 001616 000451 ; ;GO RESTORE VECTOR
434 001620 012767 000032 176260 MOV #45, ERRTYP ;NPR ERROR
435 ;*****
436 001626 104405 000000 000000 HRDERS, BEGIN, NULL ;RETURN HERE IF WC ER - EXPECTED 0
437 ;*****
438 001634 000442 ; ;GO RESTORE VECTOR
439 001636 012767 000032 176242 MOV #45, ERRTYP ;NPR ERROR
440 ;*****
441 001644 104405 000000 000000 HRDERS, BEGIN, NULL ;RETURN HERE IF BAR ER - SHOULD = DBUF+2
442 ;*****
443 001652 000433 ; ;GO RESTORE VECTOR
444 001654 012767 003640 176220 MOV @BUFFER, SBADR ;RETURN HERE IF OK - SET UP XFER ADHS
445 001662 012767 003640 176214 MOV @BUFFER, WASADR ;RETURN HERE IF OK - SET UP XFER ADHS
446 001670 010067 176212 ; ;LD EXPECTED
447 001674 017767 176556 176206 MOV @RVDBR, AWAS ;READ DATA XFERED
448 001702 026767 176176 176172 CMP A$TAT, ACSR ;COMPARE RESULTS
449 001710 001406 ;BR IF SO
450 001712 016767 176340 176160 BEQ #35, ERRTYP ;BIT STUCK
451 ;*****
452 001720 104404 000000 000000 HRDERS, BEGIN, NULL ;SET UP DBR ADHS
453 ;*****
454 001724 000406 ; ;DATA ERROR!!!
455 001726 005100 ;*****
456 001730 005101 3$: BR #45, ERRTYP ;GO RESTORE VECTOR
457 001732 001274 ; ;RETURN HERE ON GOOD DATA - NOW COM PATRN
458 001734 005300 ; ;KEEP TRACK OF COMPLEMENT
459 001736 005300 ; ;DC COMPLEMENT OF THIS FLOATING ZERO IF 0
460 001740 103671 ; ;WAS DONE - NOW SHIFT ZERO LEFT
461 001742 004767 001172 4$: INR R0 ;KEEP LSB SET
462 ; ;MAIN ZERO BIT IN CARRY
463 ; ;GO RESTORE VECTOR

```

```
463 ;TEST SINGLE "DATO" NPR TRANSFERS (FLOATING 0 COMPLEMENT PATRN)
464
465 001746 004567 001152 TSTDTO: JSR R5,SETVEC ;GO SET UP INTERRUPT RETURN
466 001751 012766 177776 ;RETURN TO 75 ON INTR
467 001760 005901 ;FLOAT ZERO RIGHT NO LEFT
468 001762 012777 177777 176260 1S: CLR R1 ;R1 CONTROLS DATA SHIFTING
469 001770 016777 176264 176254 MOV #1,DRVMCR ;DO ONE XFER
470 001778 010777 176264 176254 MOV R0,DRVBAR ;WRITE DATA WORD TO "DBUF"
471 002002 012777 000103 176244 MOV R0,DRVCSR ;SET UP DATA IN DBR
472 002010 052777 000400 176236 BIS #103,DRVCSR ;SET IE & GO & FNCT1 (C1 CONTROL)
473 002022 104400 000000 EXITS,BEGIN ;SET CYCLE
474 ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
475
476 002022 000004 000000 002030 2S: PIRQS,BEGIN,5S ;-----
477 ; QUEUE UP TO CONTINUE AT 5S AND RTI
478
479 002030 004567 001124 5S: JSR R5,CKSTAT ;GO CHECK STATUS
480 002034 001702 ;CSR STATUS EXPECTED
481 002036 000001 ;# OF XFERS
482 002040 012767 000032 176040 MOV #32,ERRTYP ;NPR ERROR
483 ;*****
484 002046 104405 000000 000000 HDRERS,BEGIN,NULL ;RETURN HERE IF STATUS ER - EXPECTED STAT C
485 ;*****
486 ;CYCLE READY IE & FNCT 1
487 MOV #32,ERRTYP ;NPR ERROR
488 ;*****
489 002054 000451 000032 176022 HDRERS,BEGIN,NULL ;RETURN HERE IF WC ER - EXPECTED 0
490 002056 012767 000000 000000 ;*****
491 ;GO RESTORE VECTOR
492 BR 4S ;NPR ERROR
493 002064 104405 000000 000000 HDRERS,BEGIN,NULL ;RETURN HERE IF WC ER - EXPECTED 0
494 ;*****
495 002072 000442 000032 176004 BR 4S ;GO RESTORE VECTOR
496 002074 012767 000032 176004 MOV #32,ERRTYP ;NPR ERROR
497 ;*****
498 002102 104405 000000 000000 HDRERS,BEGIN,NULL ;RETURN HERE IF BAR ER - SHOULD = DBUF+2
499 ;*****
500 ;GO RESTORE VECTOR
501 BR 4S ;NPR ERROR
502 002110 000433 003640 175762 MOV #BUFFER,SBADR ;RETURN HERE IF OK - SET UP XFER ADRS
503 002116 012767 003640 175756 MOV #BUFFER,WASADR ;LD EXPECTED
504 002126 010067 175754 MOV R0,ASB ;GET DATA XFERED
505 002134 016767 001502 175750 MOV BUFFER,AWAS ;COMPARE RESULTS
506 002140 026767 175740 175734 CMP #STAT,ACSR ;BR IF SO
507 002146 001405 BEQ 2S ;SET UP DBR ADRS
508 002150 016767 176102 175722 MOV DRVDBR,CSRA ;*****
509 ;DATA ERROR!!!
510 002156 104404 000000 DATERS,BEGIN ;*****
511 ;GO RESTORE VECTOR
512 BR 4S ;RETURN HERE ON GOOD DATA - NOW COM PATRN
513 002164 005100 3S: COM R0 ;KEEP TRACK OF COMPLEMENT
514 002166 005101 COM R1 ;GO COMPLEMENT OF THIS FLOATING ZERO IF 0
515 002170 005104 BIC R0 ;COMPLEMENT WAS DONE - NOW SHIFT ZERO LEFT
516 002174 006300 ASL R0 ;KEEP LSB SET
517 002176 005200 INC R0 ;AGAIN TILL ZERO BIT IN CARRY
518 002200 004767 000734 4S: JSR PC,RSTVEC ;GO RESTORE VECTOR
```

```
517 ;TEST FOR 15 "DATI" NPR TRANSFERS (BURST MODE)
518
519 002204 004567 000714 I200: JSR R5,SETVEC ;GO SET UP INTERRUPT RETURN
520 002210 002255 001122 1S: ;RETURN TO 1S ON INTR
521 002216 004767 001122 ;GO LOAD BUFFER WITH COMPLEMENTING PATRN
522 002224 016777 175676 176024 MOV #15,DRVMCR ;LOAD BC REG - WILL DO 15 XFERS
523 002232 012777 175676 176020 MOV R0,DRVBAR ;SET UP CURRENT ADRS
524 002234 012777 000101 176014 MOV R0,DRVCSR ;SET IE & GO
525 002240 052777 000400 176006 BIS #101,DRVCSR ;SET CYCLE
526 002252 104400 000000 EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
527
528 002252 000004 000000 002260 1S: PIRQS,BEGIN,3S ;-----
529 ; QUEUE UP TO CONTINUE AT 3S AND RTI
530
531 002260 004567 000674 3S: JSR R5,CKSTAT ;GO CHECK STATUS
532 002264 000700 ;CSR STATUS EXPECTED
533 002266 000017 ;# OF XFERS
534 002270 012767 000032 175610 MOV #32,ERRTYP ;NPR ERROR
535 ;*****
536 002276 104405 000000 000000 HDRERS,BEGIN,NULL ;RETURN HERE IF STATUS ER - EXPECTED CYCLE, READY & IE
537 ;*****
538 ;GO RESTORE VECTOR
539 BR 2S ;NPR ERROR
540 002304 000440 000032 175572 MOV #32,ERRTYP ;GO RESTORE VECTOR
541 002306 012767 000032 175572 ;*****
542 HDRERS,BEGIN,NULL ;RETURN HERE IF WC ER - EXPECTED 0
543 ;*****
544 002314 104405 000000 000000 BR 2S ;GO RESTORE VECTOR
545 002322 000431 000032 175554 MOV #32,ERRTYP ;NPR ERROR
546 002324 012767 000032 175554 ;*****
547 HDRERS,BEGIN,NULL ;RETURN HERE IF BAR ER - SHOULD = DBUF+6
548 ;*****
549 ;GO RESTORE VECTOR
550 BR 2S ;OK - SET UP LAST XFER ADRS WHERE #177577 SHOULD BE
551 002340 000422 003676 175532 MOV #BUFFER+36,SBADR ;LD EXPECTED
552 002344 012767 003676 175530 MOV #177577,ASB ;DBR SHOULD HAVE LAST DATUM
553 002350 017667 175574 175524 MOV R0,DRVDBR,AWAS ;COMPARE RESULTS
554 002356 017667 175514 175510 CMP #STAT,ACSR ;BR IF SO
555 002364 026767 175514 175510 BEQ 2S ;SET UP DBR ADRS
556 002372 001405 MOV DRVDBR,CSRA ;*****
557 002374 016767 175656 175476 ;DATA ERROR!!!
558 002402 104404 000000 DATERS,BEGIN ;*****
559 002406 004767 000526 2S: JSR PC,RSTVEC ;GO RESTORE VECTOR
```

```
599
598
597
596
595
594
593
592
591
590
589
588
587
586
585
584
583
582
581
580
579
578
577
576
575
574
573
572
571
570
569
568
567
566
565
564
563
562
561
560
559
558
557
556
555
554
553
552
551
550
549
548
547
546
545
544
543
542
541
540
539
538
537
536
535
534
533
532
531
530
529
528
527
526
525
524
523
522
521
520
519
518
517
516
515
514
513
512
511
510
509
508
507
506
505
504
503
502
501
500
499
498
497
496
495
494
493
492
491
490
489
488
487
486
485
484
483
482
481
480
479
478
477
476
475
474
473
472
471
470
469
468
467
466
465
464
463
462
461
460
459
458
457
456
455
454
453
452
451
450
449
448
447
446
445
444
443
442
441
440
439
438
437
436
435
434
433
432
431
430
429
428
427
426
425
424
423
422
421
420
419
418
417
416
415
414
413
412
411
410
409
408
407
406
405
404
403
402
401
400
399
398
397
396
395
394
393
392
391
390
389
388
387
386
385
384
383
382
381
380
379
378
377
376
375
374
373
372
371
370
369
368
367
366
365
364
363
362
361
360
359
358
357
356
355
354
353
352
351
350
349
348
347
346
345
344
343
342
341
340
339
338
337
336
335
334
333
332
331
330
329
328
327
326
325
324
323
322
321
320
319
318
317
316
315
314
313
312
311
310
309
308
307
306
305
304
303
302
301
300
299
298
297
296
295
294
293
292
291
290
289
288
287
286
285
284
283
282
281
280
279
278
277
276
275
274
273
272
271
270
269
268
267
266
265
264
263
262
261
260
259
258
257
256
255
254
253
252
251
250
249
248
247
246
245
244
243
242
241
240
239
238
237
236
235
234
233
232
231
230
229
228
227
226
225
224
223
222
221
220
219
218
217
216
215
214
213
212
211
210
209
208
207
206
205
204
203
202
201
200
199
198
197
196
195
194
193
192
191
190
189
188
187
186
185
184
183
182
181
180
179
178
177
176
175
174
173
172
171
170
169
168
167
166
165
164
163
162
161
160
159
158
157
156
155
154
153
152
151
150
149
148
147
146
145
144
143
142
141
140
139
138
137
136
135
134
133
132
131
130
129
128
127
126
125
124
123
122
121
120
119
118
117
116
115
114
113
112
111
110
109
108
107
106
105
104
103
102
101
100
99
98
97
96
95
94
93
92
91
90
89
88
87
86
85
84
83
82
81
80
79
78
77
76
75
74
73
72
71
70
69
68
67
66
65
64
63
62
61
60
59
58
57
56
55
54
53
52
51
50
49
48
47
46
45
44
43
42
41
40
39
38
37
36
35
34
33
32
31
30
29
28
27
26
25
24
23
22
21
20
19
18
17
16
15
14
13
12
11
10
9
8
7
6
5
4
3
2
1
0

```

002412 004567 000506  
002416 002462  
002420 012777 177761 175622  
002426 016777 175474 175616  
002434 013777 177377 175614  
002442 013777 000103 175614  
002450 052777 000400 175576  
002456 104400 000000  
002462 000004 000000 002470  
002470 004567 000464  
002474 001762  
002476 000017  
002500 012767 000032 175400  
002506 104405 000000 000000  
002514 000422  
002516 012767 000032 175362  
002524 104405 000000 000000  
002532 000413  
002534 012767 000032 175344  
002542 104405 000000 000000  
002550 000404  
002552 004767 000634  
002556 104404 000000  
002562 004767 000352

200: JSR R5,SETVEC ;GO SET UP INTERRUPT RETURN  
;RETURN TO IS ON INTR  
MOV #-15,@DRVWCR ;WORD WC REG - WILL DO 15 XFER'S  
MOV RBUFPA,@DRVBAR ;SET UP CURRENT ADDR  
MOV #17377,@RDVDBR ;THIS WILL BE WRITTEN TO MEM  
MOV #103,@RVCSR ;SET IE FNCT 1 & GO  
BIS #400,@RVCSR ;SET CYCLE  
EXIT\$,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.  
;-----  
;PIRQS,BEGIN,3\$ ;QUEUE UP TO CONTINUE AT 3\$ AND RTI  
;-----  
3\$: JSR R5,CKSTAT ;GO CHECK STATUS  
;EXPECTED STATUS  
;# OF XFERS  
MOV #32,ERRTYP ;NPR ERROR  
;\*\*\*\*\*  
;\*\*\*\*\* ;RETURN HERE IF STATUS ER - EXPECTED STAT C  
;\*\*\*\*\*  
;\*\*\*\*\* ;CYCLE, READY, IE & FNCT 1  
BR 2\$ ;GO RESTORE VECTOR  
MOV #32,ERRTYP ;NPR ERROR  
;\*\*\*\*\*  
;\*\*\*\*\* ;RETURN HERE IF WC ER - EXPECTED 0  
;\*\*\*\*\*  
BR 2\$ ;GO RESTORE VECTOR  
MOV #32,ERRTYP ;NPR ERROR  
;\*\*\*\*\*  
;\*\*\*\*\* ;RETURN HERE IF BAR ER - SHOULD = DBUF+6  
;\*\*\*\*\*  
BR 2\$ ;GO RESTORE VECTOR  
JSR PC,CKDAT ;RETURN HERE IF OK - NOW GO CHECK DATA  
;\*\*\*\*\*  
;\*\*\*\*\* ;DATA ERROR!!!  
;\*\*\*\*\*  
DATERS,BEGIN ;DATA ERROR!!!  
;\*\*\*\*\*  
2\$: JSR PC,RSTVEC ;RETURN HERE IF DATA CHECK OK - GO RESTORE VECTOR

```
598
597
596
595
594
593
592
591
590
589
588
587
586
585
584
583
582
581
580
579
578
577
576
575
574
573
572
571
570
569
568
567
566
565
564
563
562
561
560
559
558
557
556
555
554
553
552
551
550
549
548
547
546
545
544
543
542
541
540
539
538
537
536
535
534
533
532
531
530
529
528
527
526
525
524
523
522
521
520
519
518
517
516
515
514
513
512
511
510
509
508
507
506
505
504
503
502
501
500
499
498
497
496
495
494
493
492
491
490
489
488
487
486
485
484
483
482
481
480
479
478
477
476
475
474
473
472
471
470
469
468
467
466
465
464
463
462
461
460
459
458
457
456
455
454
453
452
451
450
449
448
447
446
445
444
443
442
441
440
439
438
437
436
435
434
433
432
431
430
429
428
427
426
425
424
423
422
421
420
419
418
417
416
415
414
413
412
411
410
409
408
407
406
405
404
403
402
401
400
399
398
397
396
395
394
393
392
391
390
389
388
387
386
385
384
383
382
381
380
379
378
377
376
375
374
373
372
371
370
369
368
367
366
365
364
363
362
361
360
359
358
357
356
355
354
353
352
351
350
349
348
347
346
345
344
343
342
341
340
339
338
337
336
335
334
333
332
331
330
329
328
327
326
325
324
323
322
321
320
319
318
317
316
315
314
313
312
311
310
309
308
307
306
305
304
303
302
301
300
299
298
297
296
295
294
293
292
291
290
289
288
287
286
285
284
283
282
281
280
279
278
277
276
275
274
273
272
271
270
269
268
267
266
265
264
263
262
261
260
259
258
257
256
255
254
253
252
251
250
249
248
247
246
245
244
243
242
241
240
239
238
237
236
235
234
233
232
231
230
229
228
227
226
225
224
223
222
221
220
219
218
217
216
215
214
213
212
211
210
209
208
207
206
205
204
203
202
201
200
199
198
197
196
195
194
193
192
191
190
189
188
187
186
185
184
183
182
181
180
179
178
177
176
175
174
173
172
171
170
169
168
167
166
165
164
163
162
161
160
159
158
157
156
155
154
153
152
151
150
149
148
147
146
145
144
143
142
141
140
139
138
137
136
135
134
133
132
131
130
129
128
127
126
125
124
123
122
121
120
119
118
117
116
115
114
113
112
111
110
109
108
107
106
105
104
103
102
101
100
99
98
97
96
95
94
93
92
91
90
89
88
87
86
85
84
83
82
81
80
79
78
77
76
75
74
73
72
71
70
69
68
67
66
65
64
63
62
61
60
59
58
57
56
55
54
53
52
51
50
49
48
47
46
45
44
43
42
41
40
39
38
37
36
35
34
33
32
31
30
29
28
27
26
25
24
23
22
21
20
19
18
17
16
15
14
13
12
11
10
9
8
7
6
5
4
3
2
1
0

```

002566 004567 000332  
002574 002462  
002580 004767 000666  
002586 016777 175322 175444  
002594 012777 177470 175434  
002602 012777 010101 175434  
002610 052777 000400 175424  
002618 012767 000000 175252  
002626 104400 000000  
002642 000004 000000 002650  
002650 004567 000304  
002654 010700  
002658 000310  
002660 012767 000032 175220  
002666 104405 000000 000000  
002674 000424  
002676 012767 000032 175202  
002704 104405 000000 000000  
002712 000415  
002714 012767 000032 175164  
002722 104405 000000 000000  
002730 000406  
002732 004567 000600  
002736 000310  
002740 104404 000000  
002744 000240  
002746 004767 000166

2\$: JSR R5,SETVEC ;GO SET UP INTR RETURN  
;RETURN TO 2\$ ON INTR  
MOV PC,LDBUF1 ;GO SET UP DBUF (SPECIAL COM PATTERN)  
MOV RBUFPA,@DRVBAR ;SET UP CURRENT ADDR  
MOV #-200,@DRVWCR ;SET UP FOR 200 XFER'S  
MOV #101,@RVCSR ;SET WAIT, IE & GO  
BIS #400,@RVCSR ;SET CYCLE, IE & GO  
MOV #0,@MAS ;SET UP A COUNTER  
EXIT\$,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.  
;-----  
;PIRQS,BEGIN,4\$ ;QUEUE UP TO CONTINUE AT 4\$ AND RTI  
;-----  
4\$: JSR R5,CKSTAT ;GO CHECK STATUS  
;EXPECTED STATUS  
;# OF XFERS  
MOV #32,ERRTYP ;NPR ERROR  
;\*\*\*\*\*  
;\*\*\*\*\* ;RETURN HERE IF STATUS ER - EXPECTED MAINT CYCLE, READY  
;\*\*\*\*\*  
BR 3\$ ;GO RESTORE VECTOR  
MOV #32,ERRTYP ;NPR ERROR  
;\*\*\*\*\*  
;\*\*\*\*\* ;RETURN HERE IF WC ER - SHOULD BE 0  
;\*\*\*\*\*  
BR 3\$ ;GO RESTORE VECTOR  
MOV #32,ERRTYP ;NPR ERROR  
;\*\*\*\*\*  
;\*\*\*\*\* ;RETURN HERE IF BAR ER - SHOULD = BUFFER+40  
;\*\*\*\*\*  
BR 3\$ ;GO RESTORE VECTOR  
JSR R5,CKDAT1 ;RETURN HERE IF OK - NOW GO CHECK DATA  
;\*\*\*\*\*  
;\*\*\*\*\* ;# OF XFER'S TO CHECK  
;\*\*\*\*\*  
DATERS,BEGIN ;DATA ERROR!!!  
;\*\*\*\*\*  
3\$: MOV PC,RSTVEC ;RESTORE VECTOR NEXT  
;GO RESTORE VECTOR

```
639 ;TEST THE ADDRESS (I/O) ABILITY TO THE TTY PRINTER CSR
640 TSTTTY: JSR R5,SETVEC ;GO SET UP INTR RETURN
641 1S ;RETURN TO IS ON INTR
642 MOV #1,@DRVWCR ;SET UP WC - 1 XFER
643 MOV #1,@DRVBAR ;SET UP BUFFER ADRS - FROM PRINTER CSR
644 CLR @DRVBR ;ZERO THE DRR
645 MOV #161,@DRVCSR ;SET IE, XAD17 & XAD16, & GO
646 BIS #400,@DRVCSR ;SET CYCLE
647 EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
648
649 1S:
650 -----
651 ;IRQS,BEGIN,5S ; QUEUE UP TO CONTINUE AT 5S AND RTI
652 -----
653 5S: JSR R5,CKSTAT ;GO CK STATUS
654 760 ;CSR EXPECTED STATUS
655 1 ;# OF XFER'S
656 MOV #32,ERRTYP ;NPR ERROR
657 ;*****
658 HRDRS,BEGIN,NULL ;RETURN HERE IF STATUS ER - EXPECTED CYCLE
659 ;*****
660 BR 2S ;XAD17 & XAD16, RDV & IE
661 MOV #32,ERRTYP ;NPR ERROR
662 ;*****
663 HRDRS,BEGIN,NULL ;RETURN HERE IF WC ER - EXPECTED 0
664 ;*****
665 NOP ;MUST LEAVE THESE NOP'S IN
666 NOP
667 2S: JSR PC,RSTVEC ;GO RESTORE VECTOR
668
669 MOV #4,@DRVCSR ;CLEAR DEVICE
670 CLR @DRVCSR
671 ;END OF PASS
672
673 ENDITS,BEGIN ;SIGNAL END OF ITERATION.
674 JMP TSTWC ;MONITOR SHALL TEST END OF PASS
675 ;TEST THE DEVICE AGAIN
676
677
```

```
678 ;THIS ROUTINE SETS THE PRIORITY LEVEL FOR NO INTERRUPT -
679 ;SETS UP THE DRV11B INTERRUPT TO RETURN ON INTERRUPT
680 ;TO THE ADDRESS INDICATED ((R5)) BY THE CALL +2
681 003124 012577 175130 175124 SETVEC: MOV (R5),@DRVCT0 ;SET UP INTR RETURN ADRS
682 003130 012777 000200 ;REP PRIORITY LEVEL AT TOP ON INTR
683 003136 000205 ;EXIT
684
685 ;THIS ROUTINE CLEARS THE DRV11B CSR - RESTORES THE DRV11R
686 ;INTERRUPT VECTOR TO A HALT - RAISES PRIORITY LEVEL
687 003140 005077 175110 175106 RSTVEC: CLR @DRVCSR ;CLR STATUS & CONTROL
688 003144 016777 175112 ;POINT VECTOR TO HALT
689 003152 005077 175104 CLR @DRVCT2 ;SET UP HALT
690 003156 000207 ;EXIT
691
692 ;THIS ROUTINE CHECKS ON ALL DATA TRANSFERS FOR: CORRECT STATUS,
693 ;CORRECT WORD COUNT & CORRECT BUFFER ADDRESS - THE EXPECTED DATA IS
694 ;SUPPLIED IN THE CALL +2 & +4 - THE RETURN IS TO +54 IF NO ERRCFS
695 ;DETECTED - IF AN ERROR IS DETECTED THE RETURN IS TO THE APPROPRIATE ERROR EMT
696 003160 017767 175070 174714 CKSTAT: MOV @DRVCSR,ACSR ;READ THE STATUS
697 003166 042777 000100 175060 BIC #100,@DRVCSR ;DISABLE THE IE BIT
698 003174 012567 174704 174674 MOV (R5),ASTAT ;SET UP EXPECTED STATUS
699 003200 026767 174700 174674 CMP ASTAT,ACSR ;COMPARE
700 003206 001406 ;BR IF SO
701 003210 016767 175040 174662 MOV @DRVCSR,CSRA ;SET UP CSR ADRS
702 003216 062705 000002 174662 ADD #2,R5 ;POINT TO THE CSR ER
703 003222 000205 ;EXIT HERE ON STATUS ERROR
704 003224 017767 175020 174650 1S: MOV @DRVWCR,ACSR ;GET WC
705 003234 001410 ;BR IF ZERO
706 003242 016767 175010 174636 MOV @DRVWCR,CSRA ;SET UP WCR ADPS
707 003248 005067 174636 CLR ASTAT ;EXPECTED 0
708 003246 062705 000020 174636 ADD #20,R5 ;POINT TO THE WCR ER
709 003254 000205 ;EXIT HERE ON WCR ER
710 003254 011567 174624 2S: MOV (R5),ASTAT ;GET XFER #
711 003260 006367 174620 174612 ASL ASTAT ;CONVERT TO WORD
712 003264 062767 003640 174612 ADD @BUFFER,ASTAT ;POINT TO LAST XFER +2
713 003274 012767 174754 174602 MOV @DRVBAR,ACSR ;GET BAR ADRS
714 003300 042767 000001 174574 BIC #BIT0,ACSR ;DON'T WANT BIT00
715 003306 026767 174572 174566 CMP ASTAT,ACSR ;COMPARE
716 003314 001406 ;BR IF SO
717 003316 016767 174730 174554 MOV @DRVBAR,CSRA ;SET UP BAR ADRS
718 003324 062705 000036 174554 ADD #36,R5 ;POINT TO BAR ER
719 003330 000205 ;EXIT HERE ON BAR ER
720 003332 062705 000054 3S: ADD #54,R5 ;ALL OK - POINT TO GOOD EXIT
721 003336 000205 ;EXIT HERE IF NO ERRORS
```

```
722 }THIS ROUTINE LOADS "DBUF" WITH A FLOATING ZERO/ONE PATTERN
723 }FOR 14 LOCATIONS - THE LAST LOCATION IS LOADED WITH THE
724 }70767 WHICH SHOULD BE THE DATA WORD AVAILABLE IN THE DBR
725 }AT THE COMPLETION OF A 15 WORD TRANSFER
726 LDBUF: MOV #DBUF,R3 }GET BUFFER ADRS
727 15: MOV #77776,R4 }SET UP FLOATING ZERO PATRN
728 25: COM R4 }LOAD IT (FLOATING 0)
729 COM R4 }WAKE INTO FLOATING 1
730 BNE #BUFFER+36,R3 }BR END OF BUFFER?
731 }BR IF NOT
732 35: MOV #70707,(R3) }LOAD LAST DATVAR (SPECIAL)
733 RTS }GET OUT
734 45: MOV #R3 }LOAD IT (FLOATING 1)
735 COM R4 }AT END OF BUFFER?
736 BNE #BUFFER+36,R3 }BR IF SO
737 ASL R4 }BACK TO FLOATING ZERO
738 INC R4 }SHIFT LEFT
739 INC R4 }KEEP LSB SET
740 BCC }GO RESET FLOATING PATRN
741 BR }GO LOAD NEXT PATRN
742
743 }THIS ROUTINE CHECKS 15 LOCATIONS IN "DBUF" FOR GOOD TRANSFERRED
744 }DATA (#177377) OR "DATO" TRANSFERS - IF AN ERROR IS DETECTED
745 }HERE RETURN IS TO CALL +2 - IF NO ERROR THE RETURN IS TO CALL +4
746 CKDAT: MOV #DBUF,R1 }GET BUFFER ADRS
747 15: MOV #177377,(R1)+ }DATA OK?
748 BNE }BR IF SO
749 MOV #DBR,CSRA }SET UP DBR ADRS
750 MOV #R1,ASB }GET ACTUAL DATA XFERED
751 MOV #R1,SBADR }GET MEMORY ADRS
752 RTS
753 25: MOV #R1,ASB }RETURN TO ERROR
754 BNE #BUFFER+36,R1 }AT END OF "DBUF"?
755 BNE }IF MORE
756 ADD #4,(SP) }ADJUST STACK FOR GOOD RETURN
757 RTS }GET OUT
758
759
```

```
760 }THIS ROUTINE LOADS "BUFFER" WITH A UNIQUE FLOATING ZERO/ONE PATTERN
761 }DATA (#17776,0,1,0,17775,0,2,0,17774,0,3,0,17773,0,4,0,17772,0,10,0 ETC.)
762 }IT IS USED WITH MAINT BIT SET (DATA/DATO SEQUENCE) - 200 LOCS
763 }ARE LOADED WITH THIS PATTERN
764 LDBUF1: MOV #BUFFER,R3 }GET BUFFER ADRS
765 15: MOV #R3,R5 }SAVE IN R5
766 ADD #60,R5 }POINT TO END OF BUFFER
767 MOV #17776,R4 }SET UP FLOATING ZERO PATRN
768 25: COM R4 }LOAD IT (FLOATING 0)
769 COM R4 }ZERO NEXT
770 COM R4 }SET UP FLOATING 1
771 MOV #R3,R4 }LOAD IT
772 CLR R3 }ZERO NEXT
773 CME R3 }DO LOCS DONE?
774 BNE }BR IF NOT
775 PC }GET OUT
776 35: COM R4 }BACK TO FLOATING ZERO
777 ASL R4 }SHIFT LEFT
778 INC R4 }KEEP LSB SET
779 BCC }GO RESET FLOATING PATRN
780 BR }GO LOAD NEXT PATRN
781
782 }THIS ROUTINE CHECK 200 LOCATIONS IN "BUFFER" FOR GOOD TRANSFERRED
783 }DATA (17776,1,1,17775,1,1,17774,2,2,17773,17773,ETC.)
784 }ON MAINT MODE TRANSFERS - THE NUMBER OF CHECKS REQUIRED IS INDICATED
785 }BY THE CALL +2 - IF AN ERROR IS DETECTED THE RETURN IS TO CALL +4 -
786 }IF NO ERROR THE RETURN IS TO CALL +10
787 CKDAT1: MOV #R5,R0 }GET # OF CHECKS
788 MOV #BUFFER,R2 }GET BUFFER ADRS
789 15: MOV #17776,R1 }SET UP FLOATING ZERO PATRN
790 25: CMP R1,(R2)+ }R3 SAYS WHEN TO SHIFT PATRN
791 BNE }DATA OK?
792 BNE }BR IF NOT
793 CMP R1,(R2)+ }DATA WRITTEN OK?
794 BNE }BR IF NOT
795 SUB #2,R0 }ACCOUNT FOR TWO ADRS'S
796 BGT #R5 }BR IF MORE
797 ADD #R5 }ADJUST FOR GOOD RETURN
798 35: MOV #R2,ANAS }SET BAD DATA
799 MOV #R2,SBADR }GET MEM ADRS
800 MOV #R2,ASB }LD EXPECTED DATA
801 RTS }SET UP DBR ADRS
802 45: MOV #DBR,CSRA }RETURN TO ERROR
803 COM R1 }NOW EXPECT COMPLEMENT
804 COM R3 }TIME TO SHIFT?
805 BNE }BR IF NOT
806 ASL R1 }SHIFT LEFT
807 INC R1 }KEEP LSB SET
808 BCC }GO RESET FLOATING PATRN
809 BR }DO NEXT
810
811 BUFFER: NOP
812 .BLKW 512.
813 .END
```



|                   |      |      |      |      |      |      |
|-------------------|------|------|------|------|------|------|
| PUSH = 005746     | 206# |      |      |      |      |      |
| PUSH2 = 074646    | 206# |      |      |      |      |      |
| RANDS = 104417    | 164# |      |      |      |      |      |
| RANNUM = 000054R  | 164# |      |      |      |      |      |
| RBUFEA = 000136R  | 193# | 417  | 470  | 523  | 564  | 603  |
| RBUFEA = 000136R  | 193# |      |      |      |      |      |
| RBUFSZ = 000132R  | 191# |      |      |      |      |      |
| RBUFVA = 000124R  | 188# |      |      |      |      |      |
| RRESTRT = 000264R | 183# | 241# |      |      |      |      |
| RRESJ = 000056R   | 169# | 223# |      |      |      |      |
| RRESJ = 000060R   | 169# |      |      |      |      |      |
| RSTRRT = 000112R  | 163# |      |      |      |      |      |
| RSTVEC = 003140R  | 461# | 515  | 558  | 596  | 637  | 669  |
| SADDR = 000140R   | 461# | 444* | 498* | 549* | 751* | 999* |
| SETUP2 = 000316R  | 329# |      |      |      |      |      |
| SETUP3 = 000342R  | 232# |      |      |      |      |      |
| SETVEC = 003124R  | 417# |      |      |      |      |      |
| SOPFEN = 000042R  | 159# | 465  | 519  | 561  | 600  | 641  |
| SOPFEN = 044404   | 106# |      |      |      |      |      |
| SOPFAS = 000046R  | 161# |      |      |      |      |      |
| SPOINT = 000032R  | 155# |      |      |      |      |      |
| SPSIZ = 000016R   | 148# | 199  |      |      |      |      |
| SR1 = 000020R     | 149# |      |      |      |      |      |
| SR2 = 000022R     | 150# |      |      |      |      |      |
| SR3 = 000022R     | 150# |      |      |      |      |      |
| SR4 = 000026R     | 154# | 224# |      |      |      |      |
| START = 000026R   | 153# |      |      |      |      |      |
| STAT = 000026R    | 153# |      |      |      |      |      |
| SVRO = 000062R    | 168# |      |      |      |      |      |
| SVR1 = 000064R    | 169# |      |      |      |      |      |
| SVR2 = 000070R    | 171# |      |      |      |      |      |
| SVR3 = 000070R    | 171# |      |      |      |      |      |
| SVR4 = 000072R    | 172# |      |      |      |      |      |
| SVR5 = 000074R    | 174# |      |      |      |      |      |
| SVR6 = 000074R    | 174# |      |      |      |      |      |
| SYSCNT = 000052R  | 163# | 644  |      |      |      |      |
| TPS = 177554      | 220# |      |      |      |      |      |
| TRDFD = 000446R   | 265# |      |      |      |      |      |
| TRDA = 000446R    | 265# |      |      |      |      |      |
| TRBAR = 001226R   | 357# | 365# |      |      |      |      |
| TRCLK = 001352R   | 388# |      |      |      |      |      |
| TRDB = 000516R    | 287# | 288# |      |      |      |      |
| TRDB = 000516R    | 287# |      |      |      |      |      |
| TRDTI = 001510R   | 404# | 412# |      |      |      |      |
| TRDTI = 001746R   | 465# |      |      |      |      |      |
| TRIFAC = 001106R  | 349# |      |      |      |      |      |
| TRINS = 001086R   | 317# | 325# |      |      |      |      |
| TRITV = 002752R   | 641# |      |      |      |      |      |
| TRWC = 000376R    | 245# | 677  |      |      |      |      |
| VECTDR = 000010R  | 148# | 445* | 499* |      |      |      |
| WASADR = 000164R  | 178# |      |      |      |      |      |
| WBUFEA = 000136R  | 193# |      |      |      |      |      |
| WBUFEA = 000136R  | 193# |      |      |      |      |      |
| WBUFPA = 000134R  | 192# |      |      |      |      |      |
| WBUFRG = 000140R  | 194# |      |      |      |      |      |
| WBUFSZ = 000140R  | 194# |      |      |      |      |      |
| WDFR = 000116R    | 185# | 225* |      |      |      |      |

|                 |      |      |
|-----------------|------|------|
| WDTO = 000114R  | 184# | 224* |
| XFLAG = 000005R | 142# |      |
| = 005642R       | 811# |      |

. ABS. 000000 000  
005642 001

ERRORS DETECTED: 0  
DEFAULT GLOBALS GENERATED: 0

XDRFBO, XDRFBO/SQL/CRF:SYM-DDXCON, XDRFBO  
RUN-TIME: 2.2.4 SECONDS  
RUN-TIME RATIO: 15/5=2.6  
CORE USED: 7K (13 PAGES)