

IDENTIFICATION

PRODUCT CODE: AC-E044B-MC  
PRODUCT NAME: CZRLCBO RLC1 DRIVE TEST PART 1  
DATE CREATED: 11-OCT-78  
MAINTAINER: DIAGNOSTIC ENGINEERING  
AUTHOR: D. DEKNIS

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## 1.0 GENERAL INFORMATION

### 1.1 PROGRAM ABSTRACT

#### 1.1.1 STRUCTURE OF PROGRAM

THIS DIAGNOSTIC OCCUPIES 14.5K WORDS OF MEMORY AND IS COMPATIBLE WITH BOTH XXDP AND ACT. IT CAN BE RUN STANDALONE UNDER XXDP, AND CAN BE CHAINED UNDER XXDP, ACT AND APT IN ACT MODE (SEE "CREATE CORE IMAGE" COMMAND BELOW FOR DETAILS OF CHAINING PROCEDURE). IT IS A SINGLE PROGRAM FROM THE STANDPOINT OF THE DIAGNOSTIC USER, BUT WE HAVE INCORPORATED INTO IT A CONTROL MODULE WHICH WILL LATER BE RELEASED INDEPENDENTLY AS A DIAGNOSTIC SUPERVISOR.

WHEN THIS DIAGNOSTIC IS STARTED AT ADDRESS 200, CONTROL GOES FIRST TO THE SUPERVISOR PORTION, WHICH WILL ASK CERTAIN "HARD COPE" QUESTIONS ABOUT THE ENVIRONMENT. THEN IT WILL ENTER COMMAND MODE, INDICATED BY A PROMPT CHARACTER (DS B>). AT COMMAND MODE THE OPERATOR MAY ENTER ANY OF SEVERAL COMMANDS AS DESCRIBED BELOW.

THE SUPERVISOR CODING FOLLOWS IMMEDIATELY THE DIAGNOSTIC TEST CODING, BUT THE SUPERVISOR LISTING HAS BEEN SUPPRESSED FOR GENERAL DISTRIBUTION. A LIMITED DISTRIBUTION HAS BEEN MADE TO FIELD SERVICE OF THE SUPERVISOR ASSEMBLY LISTING, AND IT MAY BE CONSULTED IN EVENT OF A SOFTWARE PROBLEM.

#### 1.1.2 DIAGNOSTIC INFORMATION

THIS PROGRAM TESTS AND EXERCISES RLO1 DISK DRIVES RL11/RLV11 CONTROLLERS (4 DRIVES PER CONTROLLER). THE ENTIRE PROGRAM IS RUN ON THE FIRST DRIVE BEFORE STARTING ON THE SECOND. THE PROGRAM STARTS BY TESTING THE SIMPLEST FUNCTIONS FIRST USING THE LOGIC TESTED IN EARLIER TESTS TO TEST MORE COMPLEX FUNCTIONS.

THIS PROGRAM TESTS THE RLO1 INTERFACE AND BASIC DRIVE LOGIC. GET STATUS WITH RESET, GET STATUS, SEEK, AND READ HEADER ARE THE ONLY COMMANDS EXECUTED IN THE PROGRAM. ONLY SEEKS WITH 0 DIFFERENCE ARE USED SO NO HEAD MOVEMENT IS REQUIRED.

A SIGNIFICANT PORTION OF THE PROGRAM REQUIRES MANUAL INTERVENTION. THESE TESTS TEST THE COVER OPEN AND WRITE LOCK STATUS. THE DRIVE MUST BE LOADED AND UNLOADED TO TEST ALL THE CONDITIONS OF HEADS OUT, BRUSH HOME, AND DRIVE STATES. THE PROGRAM CAN BE RUN IN AUTOMATIC MODE IN WHICH CASE ALL TESTS REQUIRING MANUAL INTERVENTION ARE BYPASSED.

## 1.2 SYSTEM REQUIREMENTS

### 1.2.1 HARDWARE REQUIREMENTS

PDP-11/LSI-11 PROCESSOR WITH 16K OR MORE OF MEMORY  
 CONSOLE DEVICE (LA30, LA36, VT50, ETC.)  
 RL11/RLV11 CONTROLLER(S)

1 - 8 RL01 DRIVES  
 1 - 8 RL01K CARTRIDGES WITH BAD SECTOR FILE  
 KW11P, KW11L (OPTIONAL)  
 LINEPRINTER(OPTIONAL)

1.2.2 SOFTWARE REQUIREMENTS  
 -----

CZRLCBO RL01 DRIVE TEST PART 1  
 (FORMERLY MD-11-DZRLC-A)

1.3 RELATED DOCUMENTS AND STANDARDS

RL01 USERS MANUAL (EK-RL01-UG-PRE)  
 XXDP USERS MANUAL

1.4 DIAGNOSTIC HIERARCHY PREREQUISITES

THE RL01 SUBSYSTEM SHOULD HAVE SUCCESSFULLY RUN THE FOLLOWING PROGRAMS:

CVRLAAQ	RLV11 RL01 DISKLESS TEST (RLV11)
CZRLABO	RL11/RLV11 CONTROLLER TEST PART 1
CZRLBBO	RL11/RLV11 CONTROLLER TEST PART 2

1.5 ASSUMPTIONS

THE HARDWARE OTHER THAN THE RL01 SUBSYSTEM IS ASSUMED TO WORK PROPERLY. FALSE ERRORS MAY BE REPORTED IF THE PROCESSOR, ETC., DO NOT FUNCTION PROPERLY.

2.0 OPERATING INSTRUCTIONS

2.1 HOW TO RUN THIS DIAGNOSTIC

2.1.1 THE SIX STEPS OF EXECUTION

THIS DIAGNOSTIC SHOULD BE LOADED AND STARTED USING NORMAL XXDP PROCEDURES. THE START COMMAND SHOULD NOT SPECIFY AN ADDRESS, BECAUSE THE DIAGNOSTIC HAS THE PROPER TRANSFER ADDRESS CODED INTO IT.

WHEN THIS DIAGNOSTIC IS STARTED, THE FOLLOWING STEPS WILL OCCUR:

\*\*\*\*\*  
 \* STEP 1 \*  
 \*\*\*\*\*

A SHORT SERIES OF "HARDCORE QUESTIONS" WILL BE ASKED:

QUESTION	MEANING	-----	-----
----------	---------	-------	-------



L-CLK (L) N ?	IS THERE AN L-CLOCK?
P-CLK (L) N ?	" " " P-CLOCK?
50HZ (L) N ?	IS THE POWER 50 CYCLES (AS IN EUROPE)?
LSI (L) N ?	IS MACHINE AN LSI?
LPT (L) N ?	IS THERE A LINE PRINTER?
MEM (K) (D) 16 ?	HOW MANY K OF MEMORY ARE THERE?

THE DEFAULTS (SHOWN AFTER EACH QUESTION) CAN BE SELECTED BY HITTING CARRIAGE RETURN. IT IS POSSIBLE THAT NOT ALL OF THE QUESTIONS WILL BE ASKED: FOR EXAMPLE, IF YOU SAY "YES" TO THE L-CLOCK QUESTION, THE P-CLOCK QUESTION WILL NOT BE ASKED.

IF NEITHER P OR L CLOCK ARE ANSWERED YES THE OPERATOR WILL BE ASKED TO TYPE TWO CHARACTERS 4 SECONDS APART.

\*\*\*\*\*  
\* STEP 2 \*  
\*\*\*\*\*

WHEN YOU HAVE ANSWERED ALL THE HARDCORE QUESTIONS, THE DIAGNOSTIC WILL ISSUE THE PROMPT "DS-B>". FROM THIS POINT UNTIL THE TIME WHEN YOU RESTART XXDP, YOU WILL BE TALKING TO THE DIAGNOSTIC, NOT XXDP. WE WILL REFER TO THE PRESENCE OF THIS PROMPT AS BEING IN DIAGNOSTIC COMMAND MODE, AS OPPOSED TO XXDP COMMAND MODE.

AT THIS POINT YOU WILL ENTER A "START" COMMAND. THIS IS NOT THE SAME AS THE XXDP "START" COMMAND, WHICH YOU ALREADY ISSUED IN RESPONSE TO THE XXDP DOT PROMPT. THIS "START" COMMAND CAN TAKE A NUMBER OF SWITCHES AND FLAGS (ALL OPTIONAL) AND THE DETAILS OF THESE ARE SET FORTH IN "2.3 DETAILS OF COMMANDS AND SYNTAX". HOWEVER, IN ORDER TO USE THE PROGRAM, ALL YOU NEED TO SAY IS SOMETHING LIKE THIS:

STA/PASS:1/FLAGS:HOE

THINGS TO NOTE HERE:

1. ONLY THE FIRST THREE CHARACTERS OF THIS OR ANY COMMAND AT THE "DS-B>" LEVEL NEED TO BE TYPED.
2. THE "PASS" SWITCH SPECIFIES HOW MANY PASSES YOU DESIRE. A PASS CONSISTS OF RUNNING THE FULL DIAGNOSTIC AGAINST ALL UNITS BEING TESTED (THIS WILL BE EXPLAINED SHORTLY). ONE PASS IS SPECIFIED IN THE ABOVE EXAMPLE.
3. THE "FLAGS" SWITCH MAY SPECIFY ANY OF A NUMBER OF FLAGS, BUT THE MAIN USEFUL ONES ARE:

LOE	LOOP ONE ERROR
HOE	HALT ON ERROR
IER	INHIBIT ERROR PRINTOUT

THE HOE FLAG IS SPECIFIED IN THE ABOVE EXAMPLE (WE'LL SEE WHY SHORTLY).

\*\*\*\*\*  
\* STEP 3 \*  
\*\*\*\*\*

WHEN YOU HAVE TYPED IN A "START" COMMAND, THE DIAGNOSTIC WILL COME BACK WITH THE QUESTION "# UNITS?" TO WHICH YOU SHOULD RESPOND BY TYPING IN THE NUMBER OF DEVICES YOU WISH TO TEST.

A WORD OF WARNING HERE: THE NUMBER OF UNITS DEPENDS ON THE TARGET DEVICE OF THE DIAGNOSTIC. FOR EXAMPLE, IF THE DIAGNOSTIC IS DIRECTED AT A DISK DRIVE, THEN THE NUMBER OF UNITS WOULD BE THE NUMBER OF DRIVES TO BE TESTED. WHEREAS IF THE DIAGNOSTIC WAS DIRECTED AT THE DISK CONTROLLER, THEN THE NUMBER OF UNITS WOULD BE THE NUMBER OF CONTROLLERS. THE TARGET DEVICE OF A DIAGNOSTIC CAN ALWAYS BE DETERMINED BY INSPECTING THE "HEADER" STATEMENT NEAR THE BEGINNING OF THE SOURCE CODE. ONE OF THE OPERANDS OF THIS "HEADER" STATEMENT SHOULD BE THE DEVICE TYPE OF THE DIAGNOSTIC.

\*\*\*\*\*  
 \* STEP 4 \*  
 \*\*\*\*\*

WHEN YOU HAVE TYPED IN THE NUMBER OF UNITS TO BE TESTED, THE DIAGNOSTIC WILL ASK YOU THE "HARDWARE QUESTIONS". THE ANSWERS TO THESE QUESTIONS ARE USED TO BUILD TABLES IN COPE, CALLED "HARDWARE P-TABLES". ONE HARDWARE P-TABLE WILL BE BUILT FOR EACH UNIT TO BE TESTED.

THERE ARE SEVERAL HARDWARE QUESTIONS AND THE ENTIRE SERIES WILL BE POSED N TIMES, WHERE N IS THE NUMBER OF UNITS.

THIS REPRESENTS A NEW PHILOSOPHY IN DIAGNOSTIC ENGINEERING. DIAGNOSTICS IN THE FUTURE WILL NOT BE WRITTEN TO AUTOSIZE OR ASSUME STANDARD ADDRESSES; INSTEAD, THEY WILL ASK THE OPERATOR FOR ALL THE INFORMATION THEY NEED TO TEST THE DEVICE.

\*\*\*\*\*  
 \* STEP 5 \*  
 \*\*\*\*\*

AFTER YOU HAVE ANSWERED ALL THE HARDWARE QUESTIONS (SEC 2.5) FOR ALL THE UNITS, YOU WILL BE ASKED "CHANGE SW?" IF YOU WANT TO BE ASKED THE SOFTWARE QUESTIONS THAT DETERMINE THE BEHAVIOR OF THIS PROGRAM, TYPE "Y". IF YOU WANT TO TAKE ALL THE DEFAULTS TO THESE QUESTIONS, TYPE "N". IF YOU TYPE "Y" YOU WILL BE ASKED THE SOFTWARE QUESTIONS (SEC 2.6), AND THE ANSWERS WILL BE PUT INTO THE SOFTWARE P-TABLE IN THE PROGRAM. THE SERIES OF QUESTIONS WILL BE ASKED JUST ONCE, REGARDLESS OF THE NUMBER OF UNITS TO BE TESTED.

\*\*\*\*\*  
 \* STEP 6 \*  
 \*\*\*\*\*

AFTER YOU HAVE ANSWERED THE SOFTWARE QUESTIONS, THE DIAGNOSTIC WILL BEGIN TO EXECUTE THE HARDWARE TEST CODE. THERE ARE SEVERAL THINGS THAT CAN HAPPEN NEXT, DEPENDING ON WHETHER A HARDWARE ERROR IS ENCOUNTERED AND ALSO ON WHAT SWITCH VALUES YOU SELECTED ON THE START COMMAND. CONSIDER THE POSSIBILITIES:

1. IF NO ERROR IS ENCOUNTERED, THEN THE DIAGNOSTIC WILL SIMPLY EXECUTE THE DESIRED NUMBER OF PASSES AND RETURN TO COMMAND MODE (PROMPT DS-B>).

2. IF AN ERROR IS ENCOUNTERED, THEN ONE OF THREE THINGS HAPPENS, DEPENDING ON THE SETTINGS OF THE HOE AND LOE FLAGS.

HOE SET: THE ERROR WILL BE REPORTED ON THE CONSOLE AND THE DIAGNOSTIC WILL RETURN TO COMMAND MODE.  
 LOE SET: THE DIAGNOSTIC WILL LOOP ENDLESSLY ON THE BLOCK OF CODE THAT DETECTED THE ERROR.  
 NEITHER HOE NOR LOE SET: THE ERROR WILL BE REPORTED ON THE CONSOLE AND NORMAL EXECUTION WILL RESUME AS IF NO ERROR HAD OCCURED.

### 2.1.2 SAMPLE RUN-THROUGH

LET'S SEE HOW ALL THIS WORKS IN A REAL SITUATION. RECALL THAT WE ENTERED THE COMMAND "STA/PASS:1/FLAGS:HOE". THIS WOULD BE A VERY TYPICAL WAY TO RUN THE DIAGNOSTIC. IF NO ERRORS ARE ENCOUNTERED, THE SINGLE REQUESTED PASS WILL BE EXECUTED AND THE PROMPT WILL BE REISSUED.

IF AN ERROR IS ENCOUNTERED, THE ERROR WILL BE REPORTED AND THE PROMPT WILL BE REISSUED (BECAUSE THE HOE FLAG IS SET). AT THIS POINT THERE ARE FOUR DIFFERENT WAYS YOU CAN GET THE PROGRAM GOING AGAIN:

1. ISSUE ANOTHER "START" COMMAND (THUS GOING THRU ALL OF STEPS 2, 3, 4, 5, AND 6 AGAIN)
2. ISSUE A "RESTART" COMMAND (SAME AS START COMMAND EXCEPT THAT THE HARDWARE QUESTIONS ARE NOT ASKED)
3. ISSUE A "CONTINUE" COMMAND (EXECUTION WILL RESUME AT THE BEGINNING OF THE PARTICULAR HARDWARE TEST (MOST DIAGNOSTICS CONSIST OF A NUMBER OF THESE) THAT IT WAS IN WHEN THE ERROR HALT OCCURED. NO QUESTIONS ASKED.
4. ISSUE A "PROCEED" COMMAND: EXECUTION WILL RESUME AT THE INSTRUCTION FOLLOWING THE ERROR REPORT (THIS IS A SPECIAL COMMAND AND CAN BE ISSUED ONLY AT A HALT ON ERROR).

THE MOST TYPICAL THING TO DO HERE IS TO ISSUE THE PROCEED, BUT WITH DIFFERENT FLAG SETTINGS. PROBABLY YOU WOULD WANT TO SAY

PRO/FLAGS:IER:LOE:HOE=0

THIS WILL DO THE FOLLOWING:

1. TURN ON THE IER (INHIBIT ERROR PRINTOUT) FLAG
2. TURN ON THE LOE FLAG
3. TURN OFF THE HOE FLAG
4. RESUME EXECUTION AT INSTRUCTION AFTER ERROR REPORT

THE DIAGNOSTIC WILL NOW LOOP ON THE BLOCK OF CODE THAT DETECTED AND REPORTED THE ERROR, BUT NO ERROR PRINTOUT WILL OCCUR. THUS YOU CAN STUDY THE ERROR OR SCOPE IT OR WHATEVER.

WHEN YOU'VE SEEN ENOUGH, YOU MAY HIT CONTROL/C. THIS WILL TAKE YOU OUT OF THE LOOP AND PUT YOU BACK INTO COMMAND MODE. YOU NOW HAVE THREE CHOICES:

1. START
2. RESTART
3. CONTINUE

LET'S SAY YOU'VE REPAIRED THE DEFECT FOUND ABOVE AND WANT TO FINISH RUNNING THE DIAGNOSTIC. YOU WOULD TYPE

CON/FLAGS:HOE:IER=0:LOE=0

THIS WILL RESTORE THE FLAGS TO THEIR ORIGINAL VALUES AND RESUME EXECUTION AT THE BEGINNING OF THE HARDWARE TEST YOU WERE IN. IF THE ERROR DOES NOT RECUR, THE EXECUTION WILL FLOW RIGHT ON THRU TO THE NEXT ERROR OR TO END OF PASS.

IF AT END OF PASS YOU WANT TO RUN THE DIAGNOSTIC AGAIN, YOU HAVE TWO CHOICES:

1. START
2. RESTART

YOU WOULD CHOOSE ONE, DEPENDING ON WHETHER YOU WANTED TO ANSWER THE HARDWARE QUESTIONS AGAIN.



## 2.2 HOW TO CREATE A CHAINABLE FILE

THE DIAGNOSTIC AS RECEIVED FROM RELEASE ENGINEERING CANNOT BE RUN IN CHAIN MODE. THAT IS WHY IT BEARS THE EXTENSION "BIN" INSTEAD OF "BIC". THERE IS A WAY, HOWEVER, TO CREATE A CHAINABLE PROGRAM FROM WHAT YOU'VE GOT.

IT CONSISTS OF RUNNING THE PROGRAM WITH THE SPECIAL COMMAND "CCI" ISSUED WHERE YOU WOULD NORMALLY ISSUE A START COMMAND (TO THE PROMPT DS-B>). THIS COMMAND CAUSES THE DIAGNOSTIC TO GO THRU ALL THE QUESTIONS AND ANSWERS AND THEN TO HALT, JUST WHERE IT WOULD ORDINARILY BEGIN EXECUTION OF THE HARDWARE TEST CODE. AT THIS POINT YOU CAN DUMP THE PROGRAM AS IT SITS IN CORE TO THE LOAD MEDIUM, WITH THE NEW EXTENSION "BIC".

HERE IS A SAMPLE DIALOGUE TO ACCOMPLISH THIS:

```
.R UPD2
RESTART: XXXXXX
*CLR
*LOAD DIAG.BIN
XFER:200 CORE:0,60602
*START 200
L-CLK (L) N ?
-----
-----
```

```
DS-B>CCI
# UNITS (D) ? 4
-----
-----
```

```
CHANGE SW (L) ? N
PTAB END: 60632
```

```
*****
*AT THIS POINT THE MACHINE HALTS AND*
*YOU MUST RESTART AT ADDRESS XXXXXX*
*****
```

```
*HICORE 60632
CORE: 0,60632
*DUMP DK0: DIAG.BIC
```

THE RESULT OF DOING THIS IS THAT YOU CAN NOW BUILD AN XXDP CHAIN FILE CONTAINING THE XXDP COMMAND

```
.R DIAG.BIC
```

AND THE DIAGNOSTIC WILL EXECUTE WITHOUT MANUAL INTERVENTION, USING THE ANSWERS THAT YOU GAVE IT WHEN YOU DID THE CCI COMMAND.

## 2.3 DETAILS OF COMMANDS AND SYNTAX

### 2.3.1 TABLE OF COMMAND VALIDITY

THERE ARE FOUR WAYS OF ENTERING DIAGNOSTIC COMMAND MODE, AND DIFFERENT SUBSETS OF THE DIAG COMMAND SET ARE AVAILABLE WITH EACH:

HOW ENTERED	LEGAL COMMANDS
1. OPERATOR ENTERED "RUN DIAG"	START PRINT DISPLAY FLAGS ZFLAGS
2. DIAGNOSTIC HAS FINISHED ALL ITS REQUESTED PASSED	START RESTART PRINT DISPLAY FLAGS ZFLAGS
3. OPERATOR INTERRUPTED THE DIAGNOSTIC WITH CTRL/C	START RESTART CONTINUE PRINT DISPLAY FLAGS ZFLAGS
4. AN ERROR WAS ENCOUNTERED WITH THE HOE FLAG SET SET	START RESTART CONTINUE PROCEED PRINT DISPLAY FLAGS ZFLAGS

### 2.3.2 COMMAND SYNTAX

```
*****
STA(RT)/TESTS:TEST-LIST/PASS:PASS-CNT/FLAGS:FLAG-LIST/EOP:EOP-INCR
*****
```

THE DIAGNOSTIC IN CORE IS EXECUTED IN ACCORDANCE WITH THE SWITCHES SPECIFIED. THE MESSAGE "# UNITS?" IS PRINTED. THE START COMMAND MAY BE ISSUED WHEN DIAGNOSTIC COMMAND MODE HAS BEEN ENTERED VIA ONE OF THE FOLLOWING: A) OPERATOR TYPED "RUN DIAGNOSTIC" B) DIAGNOSTIC FINISHED EXECUTING C) ERROR WAS ENCOUNTERED WITH HOE FLAG SET D) OPERATOR ENTERED CONTROL/C.



AFTER THE OPERATOR RESPONDS TO "# UNITS?", THE HARDWARE DIALOGUE IS INITIATED. WHEN IT IS COMPLETED, THE QUESTIONS "CHANGE SW?" IS ISSUED, AND THE ANSWERS, IF GIVEN, BECOME THE NEW DEFAULTS. THEREFORE IT IS NECESSARY TO RELOAD THE PROGRAM IN ORDER TO RETURN TO THE LOAD DEFAULTS.

THE SWITCH ARGUMENTS ARE AS FOLLOWS:

"TEST-LIST" IS A SEQUENCE OF DECIMAL NUMBERS (1:2 ETC.) OR RANGES OF DECIMAL NUMBERS (1-5:8-10 ETC.) THAT SPECIFY THE TESTS TO BE EXECUTED. THE NUMBERS ARE SEPARATED BY COLONS. THE NUMBERS RANGE FROM 1 TO THE LARGEST TEST NUMBER IN THE DIAGNOSTIC. THEY MAY BE SPECIFIED IN ANY ORDER. TESTS WILL BE EXECUTED IN NUMERICAL ORDER REGARDLESS OF THE ORDER OF SPECIFICATION. THE DEFAULT IS TO EXECUTE ALL TESTS.

"PASS-CNT" IS A DECIMAL NUMBER INDICATING THE DESIRED NUMBER OF PASSES. A PASS IS DEFINED AS THE EXECUTION OF THE FULL DIAGNOSTIC (ALL SELECTED TESTS) AGAINST ALL UNITS SUBMITTED. THE DEFAULT IS NON-ENDING EXECUTION. "FLAG-LIST" IS A SEQUENCE OF ELEMENTS OF THE FORM <FLAG>, <FLAG=1>, OR <FLAG=0>, SEPARATED BY COLONS, WHERE <FLAG> HAS ONE OF THE FOLLOWING VALUES:

HOE	HALT ON ERROR, CAUSING COMMAND MODE TO BE ENTERED WHEN AN ERROR IS ENCOUNTERED
LOE	LOOP ON ERROR, CAUSING THE DIAGNOSTIC TO LOOP CONTINUOUSLY WITHIN THE SMALLEST DEFINED BLOCK OF CODING (SEGMENT, SURTEST, OR TEST) CONTAINING THE ERROR
IER	INHIBIT ERROR REPORTING
IBE	INHIBIT BASIC ERROR REPORTS
IXE	INHIBIT EXTENDED ERROR REPORTS
PRI	DIRECT ALL MESSAGES TO A LINE PRINTER
PNT	PRINT NUMBER OF TES BEING EXECUTED
BOE	BELL ON ERROR
UAM	RUN IN UNATTENDED MODE, BYPASSING MANUAL INTERVENTION TESTS
ISR	INHIBIT STATISTICAL REPORTS
IDU	INHIBIT DROPPING OF UNITS BY DIAGNOSTIC

THE FLAGS NAMED OR EQUATED TO 1 ARE SET, THOSE EQUATED TO 0 ARE CLEARED. A FLAG NOT SPECIFIED IS CLEARED. IF THE FLAGS SWITCH IS NOT GIVEN ALL FLAGS ARE CLEARED.

"EOP-INCR" IS A DECIMAL NUMBER INDICATING HOW OFTEN (IN TERMS OF PASSES) IT IS DESIRED THAT THE END OF PASS MESSAGE BE PRINTED. THE DEFAULT IS AT THE END OF EVERY PASS.

\*\*\*\*\*  
 RES(TART)/TEST:TEST-LIST/PASS:PASS-CNT/FLAGS:FLAG-LIST/EOP:EOP-INCR/UNITS:UNIT-LIST  
 \*\*\*\*\*

THE DIAGNOSTIC IN CORE IS EXECUTED IN ACCORDANCE WITH THE SWITCHES SPECIFIED. HOWEVER, NEW P-TABLES ARE NOT BUILT. INSTEAD, THE ONES IN CORE ARE USED. THE QUESTION "CHANGE SW?" IS ASKED, AND THE ANSWERS IF GIVEN BECOME THE NEW DEFAULTS. THE COMMAND MAY BE ISSUED WHEN COMMAND MODE HAS BEEN ENTERED VIA A) DIAGNOSTIC IS FINISHED B) HALT ON ERROR C) CONTROL/C.

THE SWITCH ARGUMENTS ARE AS IN THE START COMMAND EXCEPT:

1. "UNIT-LIST" IS A SEQUENCE OF LOGICAL UNIT NUMBERS RANGING FROM 1 THRU N (N = NUMBER OF UNITS BEING TESTED) SPECIFYING WHICH UNITS ARE TO BE TESTED. THE LOGICAL UNIT NUMBER DESIGNATES THE POSITION OF THE P-TABLE IN CORE, ACCORDING TO THE ORDER IN WHICH THEY WERE BUILT. THE UNITS SPECIFIED MUST NOT HAVE BEEN DROPPED BY THE OPERATOR DROP COMMAND. THE UNIT-LIST DEFAULTS TO "ALL THAT HAVE NOT BEEN DROPPED BY OPERATOR COMMAND". THE EFFECT OF THE UNIT-LIST LASTS UNTIL THE NEXT START (WHERE IT IS AUTOMATICALLY RESET TO "ALL") OR THE NEXT RESTART.
2. ALL UNSPECIFIED FLAG SETTINGS ARE UNCHANGED.

\*\*\*\*\*  
 CON(TINUE)/PASS:<PASS-CNT/FLAGS:<FLAG-LIST>  
 \*\*\*\*\*

COMMAND MODE MUST HAVE BEEN ENTERED DUE TO A HALT ON ERROR OR A CONTROL/C. THE EFFECT OF THE COMMAND IS TO GO TO THE BEGINNING OF THE TEST THAT WAS BEING EXECUTED WHEN THE HALT OR CONTROL/C TOOK PLACE. SOFTWARE DIALOGUE MAY OPTIONALLY BE REEXECUTED. HARDWARE PARAMETERS MAY NOT BE CHANGED.

THE SWITCH ARGUMENTS ARE AS IN THE START COMMAND EXCEPT:

1. DEFAULT FOR PASS-CNT IS THE UNSATISFIED PASS-CNT FROM THE PREVIOUS START OR RESTART
2. UNSPECIFIED FLAG SETTINGS ARE UNCHANGED

\*\*\*\*\*  
 PRO(CEED)/FLAGS:<FLAG-LIST>  
 \*\*\*\*\*

COMMAND MODE MUST HAVE BEEN ENTERED VIA A HALT ON ERROR. THE EFFECT OF THE COMMAND IS TO BEGIN EXECUTION AT THE LOCATION FOLLOWING THE ERROR CALL. NEITHER HARDWARE NOR SOFTWARE PARAMETERS MAY BE ALTERED.

THE SWITCH ARGUMENTS ARE THE SAME AS THE START COMMAND EXCEPT:

1. UNSPECIFIED FLAG SETTINGS ARE UNCHANGED

\*\*\*\*\*  
 CCI/TEST:TEST-LIST/PASS:PASS-CNT/FLAGS:FLAG-LIST/EOP:EOP-INCR  
 \*\*\*\*\*

THE DIAGNOSTIC EXECUTES THRU ALL OPERATOR DIALOGUE AND HALTS AT THE HARDWARE TEST CODE. NOW THE OPERATOR CAN DUMP THE CORE IMAGE TO THE MEDIUM WITH A BIC EXTENSION.

THE BIC FILE MUST BE HANDLED DIFFERENTLY DEPENDING ON WHETHER IT IS RUN MANUALLY OR IN CHAIN MODE. IF RUN MANUALLY IT CAN BE INVOKED EITHER WITH A "START" (IN WHICH CASE IT WILL BEHAVE LIKE THE BIN FILE: THE PRE-GENERATED ANSWERS TO OPERATOR QUESTIONS WILL BE IGNORED) OR WITH A "RESTART" (IN WHICH CASE THE PRE-GENERATED OPERATOR ANSWERS WILL BE USED).

IF RUN IN CHAIN MODE, AUTOMATIC EXECUTION WILL COMMENCE IMMEDIATELY FROM THE XXDP COMMAND ".R DIAG". THE COMMAND PROMPT "DS-B>" WILL NOT BE ISSUED.

ANY SWITCHES SPECIFIED ON THE CCI COMMAND WILL CARRY OVER WHEN THE BIC FILE IS RUN IN CHAIN MODE (EXCEPT THAT UAM IS ALWAYS SET THERE) BUT WILL NOT CARRY OVER WHEN IT IS RUN MANUALLY.

TO DO A CCI ON A FULL SIZED DIAGNOSTIC (14.5K WORDS), A MACHINE SIZE LARGER THAN 16K IS REQUIRED. THE EXACT SIZE NEEDED DEPENDS ON WHICH UTILITY IS USED TO EXECUTE THE DIAGNOSTIC AT CCI TIME.

\*\*\*\*\*  
 DRO(P)/UNITS:UNIT-LIST  
 \*\*\*\*\*

THE UNITS SPECIFIED ARE DROPPED FROM TESTING UNTIL THEY ARE ADDED BACK OR UNTIL A START COMMAND IS GIVEN. A DROP CANNOT BE FOLLOWED BY A PROCEED.

THERE IS ALSO A "DROP" MACRO INTERNAL TO THE DIAGNOSTIC, WHICH GIVES THE FACILITY OF AUTO-DROPPING. THE DURATION OF A PROGRAM DROP, HOWEVER, IS ONLY UNTIL THE NEXT START OR RESTART.

\*\*\*\*\*  
 ADD/UNITS:UNIT-LIST  
 \*\*\*\*\*

THE UNITS SPECIFIED ARE ADDED BACK (THEY MUST HAVE BEEN PREVIOUSLY DROPPED BY THE DROP COMMAND) TO THE TEST SEQUENCE. AN ADD CANNOT BE FOLLOWED BY A PROCEED.

\*\*\*\*\*  
 PRI(NT)  
 \*\*\*\*\*

ALL STATISTICS TABLES ACCUMULATED BY THE DIAGNOSTIC ARE PRINTED. THE ISR (INHIBIT STATISTICAL REPORTING) FLAG IS CLEARED.

\*\*\*\*\*  
 DIS(PLAY)/UNITS:<UNIT-LIST>  
 \*\*\*\*\*

THE HARDWARE P-TABLES FOR ALL UNITS UNDER TEST ARE PRINTED OUT IN THE FORMAT IN WHICH THEY WERE ENTERED. ANY UNITS THAT WERE DROPPED BY THE OPERATOR "DROP" COMMAND ARE SO DESIGNATED.

\*\*\*\*\*  
 FLA(GS)  
 \*\*\*\*\*

THE CURRENT SETTINGS OF ALL FLAGS ARE PRINTED.

\*\*\*\*\*  
 ZFL(AGS)  
 \*\*\*\*\*

ALL FLAGS ARE CLEARED.

#### 2.4 EXTENDED P-TABLE DIALOGUE

THE FULL CAPABILITY OF THE HARDWARE DIALOGUE IS REVEALED BY THE FOLLOWING DISCUSSION OF WHAT HAPPENS INTERNALLY.

AS SOON AS THE QUESTION "# UNITS?" IS ANSWERED (WITH THE NUMBER N, SAY) SPACE IN CORE IS ALLOCATED FOR N P-TABLES. ALL OF THE P-TABLES ARE OF THE SAME FORMAT, AND THERE IS A ONE-TO-ONE CORRESPONDENCE BETWEEN THE HARDWARE PARAMETER QUESTIONS AND THE SLOTS IN THE P-TABLE FORMAT.

ON THE FIRST TRIP THRU THE QUESTIONS, ALL OF THE SLOTS IN ALL OF THE P-TABLES ARE FILLED. IF THE OPERATOR TYPES IN LESS THAN N EXPLICIT VALUES IN RESPONSE TO A PARTICULAR QUESTION, THESE VALUES ARE PLACED IN THE P-TABLES (ONE VALUE GOING INTO THE PROPER SLOT OF EACH P-TABLE BEGINNING WITH THE FIRST P-TABLE) UNTIL THE STRING OF VALUES IS EXHAUSTED. THE LAST VALUE THAT SLOT IN THE REMAINING P-TABLES.

ON SUBSEQUENT TRIPS THRU THE QUESTIONS, THE SAME PROCESS IS CARRIED OUT, EXCEPT THAT THE EARLIEST P-TABLE NOT TO HAVE RECEIVED AN EXPLICIT VALUE IN ANY OF ITS SLOTS NOW ASSUMES THE ROLE THAT TABLE NUMBER ONE PLAYED IN THE FIRST TRIP.

THE SERIES OF QUESTIONS IS REISSUED UNTIL AT LEAST ONE QUESTION HAS RECEIVED N EXPLICIT VALUES FROM THE OPERATOR.

IN GIVING A STRING OF VALUES, COMMAS WITHOUT INTERVENING VALUES MAY BE USED TO INDICATE A REPETITION OF THE LAST NAMED VALUE.

A STRING OF VALUES MAY BE GIVEN AS A RANGE (6-10 FOR EXAMPLE). IF THE VALUES REPRESENT PURE NUMERICAL DATA, THIS SAMPLE RANGE TRANSLATES TO THE STRING 6,7,8,9,10 (AN INCREMENT OF 1). IF THE VALUES ARE ADDRESSES, THE SAMPLE RANGE TRANSLATES TO THE STRING 6,8,10 (AN INCREMENT OF 2).

NOW LET US SEE HOW WE COULD USE THESE CAPABILITIES TO CONSTRUCT A SET OF P-TABLES. ASSUME THAT WE HAVE 64 UNITS, AND THAT THERE ARE THREE HARDWARE PARAMETERS FOR EACH (THREE SLOTS IN THE P-TABLE, THREE HARDWARE QUESTIONS IN THE DIALOGUE). LET THE DESIRED VALUE FOR THE FIRST PARAMETER BE THE NUMBER 75 FOR ALL 64 TABLES. LET THE DESIRED VALUE FOR THE SECOND PARAMETER BE EQUAL TO THE UNIT NUMBER (1,2,3,...,64) EXCEPT FOR UNIT 50, WHICH SHOULD RECEIVE THE VALUE 49. LET THE DESIRED VALUE FOR THE THIRD PARAMETER BE THE NUMBER 76 FOR THE FIRST 20 UNITS AND THE NUMBER 77 FOR THE LAST 44 UNITS.

THE FOLLOWING DIALOGUE WOULD ACCOMPLISH THIS GOAL:

# UNITS (D) ? 64

UNIT 1

<QUESTION 1> ? 75  
<QUESTION 2> ? 1-20  
<QUESTION 3> ? 76

UNIT 21

<QUESTION 1> ?  
<QUESTION 2> ? 21-49,,51-64  
<QUESTION 3> ? 77

THE FIRST TIME THE SERIES IS ASKED, SLOT ONE RECEIVES A 75 IN ALL 64 TABLES. SLOT TWO RECEIVES THE VALUES 1, 2, 3, ..., 20 IN TABLES 1 THRU 20 AND A CONSTANT 20 IN TABLES 21 THRU 64. SLOT THREE RECEIVES A CONSTANT 76 IN ALL 64 TABLES.

THE SECOND TIME THRU THE SERIES, TABLES 21 THRU THE END ARE GOING TO BE AFFECTED (NOTE THAT THIS PIECE OF INFORMATION IS PRINTED OUT FOR THE OPERATOR IN THE FORM "UNIT XX" AT THE BEGINNING OF EACH SERIES). QUESTION 1 IS RESPONDED TO BY A <CR>, SO SLOT ONE STAYS A CONSTANT 75 IN TABLES 21 THRU 64, SINCE NO NEW EXPLICIT VALUES ARE TYPED IN. SLOT TWO GETS THE VALUES 21, 22, 23, ..., 49 IN TABLES 21 THRU 49, AND GETS A 49 IN SLOT 50, AND GETS THE VALUES 51, 52, 53, ..., 64 IN TABLES 51 THRU 64. SLOT THREE GETS THE VALUE 77 IN TABLES 21 THRU 64.

THE DIALOGUE IS TERMINATED WHEN THE SOFTWARE RECOGNIZES THAT 64 EXPLICIT VALUES HAVE BEEN GIVEN FOR AT LEAST ON QUESTION (NAMELY QUESTION 2).

## 2.5 HARDWARE PARAMETERS

THE FOLLOWING QUESTIONS WILL BE ASKED ON A START COMMAND. THE VALUE LOCATED TO THE LEFT OF THE QUESTION MARK IS THE DEFAULT VALUE THAT WILL BE TAKEN ON A CARRIAGE RETURN RESPONSE.

RL11 (L) Y?

ANSWER YES(Y) IF YOU HAVE AN RL11 CONTROLLER, NO(N) IF YOU HAVE AN RLV11 CONTROLLER.

BUS ADDRESS (O) 174400?

ANSWER WITH THE BUS ADDRESS OF THE CONTROLLER.

VECTOR (O) 330?

ANSWER WITH THE INTERRUPT VECTOR OF THE CONTROLLER.

BR LEVEL (O) 5?

ANSWER WITH THE INTERRUPT PRIORITY OF THE CONTROLLER.

DRIVE (O) 0?

ANSWER WITH THE DRIVE(S) CONNECTED TO THE CONTROLLER.

## 2.6 SOFTWARE PARAMETERS

THE FOLLOWING QUESTIONS ARE ASKED IF REQUESTED ON A START, RESTART, OR CONTINUE. THEY ALLOW FLEXIBILITY IN THE WAY THE PROGRAM BEHAVES. THE SOFTWARE PARAMETERS GIVE THE PROGRAM FLEXIBILITY IN THE WAY IT RUNS. THE PARAMETERS CAN BE MODIFIED ON A START, RESTART, OR CONTINUE BY ANSWERING (Y)ES TO THE FOLLOWING QUESTION:

## CHANGE S.W. ?

A YES ANSWER WILL ASK THE FOLLOWING SOFTWARE PARAMETER QUESTIONS, WITH THE PRESENT DEFAULT VALUE PRINTED TO THE LEFT OF THE QUESTION MARK. (THE LAST ANSWER GIVEN IS THE DEFAULT) THE DEFAULT IS TAKEN ON A <CR>. CONTROL Z (^Z) WILL DEFAULT ALL REMAINING QUESTIONS AND START THE TEST.

## EXECUTE DRIVE SELECT TESTS (N)?

IF "YES" TESTS 5 AND 6 ARE EXECUTED IN THE FIRST PASS OF THE PROGRAM. THESE TESTS REQUIRE MANUAL INTERVENTION TO CHANGE ADDRESS PLUGS AND REQUIRE A FULL COMPLEMENT OF ADDRESS PLUGS (0 - 3).

## EXECUTE HEAD ALIGNMENT SUPPORT (N)?

IF "YES", TEST 11 IS EXECUTED IN THE FIRST PASS.

## EXECUTE MANUAL INTERVENTION TESTS (N)?

IF "YES", TESTS 1, 2, 3, AND 4 ARE EXECUTED TO TEST BASIC INTTERFACE OPERATIONS, HEAD LOADING, HEAD UNLOADING, AND ALL STATE CHANGES.

## SPECIFY ERROR LIMIT (DECIMAL) (20)?

THIS PARAMETER SPECIFIES THE MAXIMUM NUMBER OF ERRORS ALLOWED. THIS LIMIT IS ON A PER DRIVE BASIS IN A SINGLE PASS. IF THE ERROR LIMIT IS EXCEEDED, THE DRIVE IS DROPPED FROM FURTHUR TESTING.

## DROP DRIVE IF NO RESPONSE (N)?

IF THIS PARAMETER IS SPECIFIED AS YES, THE PROGRAM WILL CHECK EACH DRIVE BEFORE TESTING STARTS TO DETERMINE IF IT IS READY OR IF IT WILL RESPOND TO A GET STATUS. IF IT IS NOT READY AND WILL NOT RESPOND TO A GET STATUS, THE DRIVE IS DROPPED AND A MESSAGE IS PRINTED.

## 3.0 ERROR INFORMATION

ALL ERRORS ARE PRINTED VIA CONSOLE DEVICE. THE ERROR INCLUDES ERROR NUMBER, TYPE AND PPROGRAM LOCATION. ERRORS INCLUDE REGISTERS BEFORE AND AT ERROR WITH RELEVANT DATA.

## 3.1 ERROR REPORTING

MOST ERROR REPORTS HAVE THE FOLLOWING FORMAT.

```

(1)  PROG NAME   ERR NUM   TEST NUM   SUPRTEST NUM   ERR PC
(2)  ROUTINE TRACE SEQ (IN SEQ CALLED)
      (ADDRESS)
      (ADDRESS)
      .
      (ADDRESS)

```

```

(3) TEST DESCRIPTION
(4) OPERATION:
(5) RESULT:
(6) ADDRESS OF UNIT UNDEP TEST
(7)          RLCS      RLDA      RLBA      RLMP      CVL      HD
(8) OP INIT
(9) OP DONE
(10) DRIVE STATUS
(11) WORD NUM IS (XXXXXX) SB (VVVVVV)
(12) TOTAL COMPARE ERRS: (ZZZ) OF (128)

```

THE ONLY EXCEPTION TO THE ABOVE FORMAT IS PURE DATA COMPARE ERRORS (NOT DETECTED BY READ ERROR). THEN THE FORMAT DOES NOT INCLUDE LINES 5 THROUGH 10.

LINE 1 IS THE ERROR HEADER AND IS PROVIDED BY THE SUPERVISOR. THE PROGRAM IS IDENTIFIED BY NAME WITH THE NUMBER OF TEST AND SUBTEST PRESENTLY BEING EXECUTED.

THE SUBTEST NUMBER IS UNIQUE IN THIS PROGRAM IN THAT IT DOES NOT REFER TO A PHYSICAL SUBTEST WITHIN A GIVEN TEST. RATHER IT REFLECTS THE NUMBER OF TIMES A SUBTEST HAS BEEN EXECUTED WITHIN A TEST. CONSEQUENTLY, ON A TEST THAT TESTS AN INCREMENTAL TYPE OF OPERATION (SUCH AS INCREMENTAL SEEK'S, READ ALL HEADERS FROM BOTH SURFACES, ETC.) THE SUBTEST WILL BE DESCRIPTIVE OF WHERE IN THE TEST THE ERROR OCCURRED.

THE ERROR P.C. IS THE PHYSICAL MEMORY LOCATION WHERE THE ERROR REPORT WAS INITIATED. SINCE MANY FUNCTIONS ARE SUBROUTINED, AND ERRORS ARE REPORTED FROM SUBROUTINES, THE ERROR P.C. IS NOT SUFFICIENT TO IDENTIFY THE LOCATION OF THE ERROR CALL AND THE ROUTINE TRACE SEQUENCE IS PROVIDED.

LINE 2 IS THE ROUTINE TRACE SEQUENCE. IF THE ERROR CALL IS INITIATED FROM WITHIN THE TEST (AS OPPOSED TO WITHIN A ROUTINE), THIS PORTION OF THE REPORT IS OMITTED. IF THE CALL IS INITIATED FROM A ROUTINE (WHICH MAY BE CALLED BY ANOTHER ROUTINE, WHICH MAY BE CALLED BY ANOTHER ROUTINE, ETC. SEVERAL LEVELS DEEP) THE ROUTINE TRACE SEQUENCE PROVIDES A TRAIL TO THE ACTUAL LOCATION WITHIN THE TEST THAT CALLED THE FIRST ROUTINE. THE FIRST ENTRY LISTED IS THE LOCATION WHERE THE FIRST ROUTINE WAS CALLED.

LINE 3 IS THE TEST DESCRIPTION AND IS ROUGHLY IDENTICAL TO THE NAME OF THE TEST BEING PERFORMED.

LINE 4 IDENTIFIES THE ACTUAL HARDWARE FUNCTION THAT IS BEING PERFORMED. ADDITIONAL INFORMATION ON THIS LINE IS DESCRIPTIVE OF SPECIFIC USE OF THE FUNCTION. FOR EXAMPLE, THE OPERATION LINE WILL READ "READ HEADERS FOR 40 HEADERS" WHEN ALL HEADERS ARE BEING READ FROM A TRACK.

LINE 5 IDENTIFIES THE ERROR THAT HAS BEEN DETECTED. THE CONTENT OF LINE 5 IDENTIFIES WHAT WAS BEING TESTED (SUCH AS DRIVE READY, CONTROLLER ERROR, DRIVE STATE, ETC.), WHAT IT IS AND WHAT IT SHOULD BE. LINE 5 MAY BE REPEATED IF MORE THAN ONE TESTED ITEM IS FOUND IN



## ERROR.

IN ADDITION LINE 5 WILL REPORT ANY HARDWARE DETECTED ERRORS SUCH AS OPERATION INCOMPLETE, HEADER CRC, ETC. IN THIS CASE THE FIRST LINE PRINTED AS RESULT WILL BE DETERMINED BY THE THREE ERROR BITS OPI, HNF/DLT, AND HCRC/DCRC. THE LINE WILL BE DETERMINED AS IN THE FOLLOWING TRUTH TABLE:

HNF/DLT	DCRC/HCRC	OPI	MESSAGE
1	1	1	HDR NOT FND/HDR CRC/OPI ERROR
0	1	1	HDR CRC ERROR
1	0	1	HDR NOT FND ERROR
0	1	0	DATA CRC ERROR
1	0	0	DATA LATE ERROR

LINE 6 IDENTIFIES THE PHYSICAL ADDRESS OF THE UNIT UNDER TEST. THIS ADDRESS IS BY UNIBUS ADDRESS OF THE CONTROLLER AND DRIVE NUMBER.

LINE 7 NAMES THE CONTROLLER REGISTERS (AND CYLINDER AND HEAD WHERE THESE ARE APPLICABLE IN THE REPORT) TO BE REPORTED.

LINE 8 PROVIDES THE CONTENTS OF CONTROLLER REGISTERS WHEN THE OPERATION WAS INITIATED.

LINE 9 PROVIDES THE CONTENTS OF THE CONTROLLER REGISTERS WHEN THE ERROR BEING REPORTED WAS DETECTED. FREQUENTLY THE REGISTER CONTENTS OF OP INIT AND OP DONE WILL BE DIFFERENT. OP INIT MAY INDICATE A SEEK WAS BEING PERFORMED BUT OP DONE MAY INDICATE THE ERROR WAS DETECTED BY A READ HEADER. THE REASON IS THAT A SEEK WAS EXECUTED AND DID NOT PROPERLY POSITION HEADS AND WHEN THE READ HEADER WAS DONE THE HEADS WERE ON THE WRONG CYLINDER.

LINE 10 IS THE DRIVE STATUS. THIS LINE IS ONLY REPORTED IF THE RLMP REGISTER DOES NOT CONTAIN THE ACTUAL DRIVE STATUS.

LINE 11 AND LINE 12 ARE REPORTED IF THE ERROR WAS DETECTED AS A COMPARE OPERATION, EITHER DATA OR HEADERS. IN ADDITION, GOOD AND BAD DATA IS REPORTED FOR ALL READ ERRORS.

### 3.1.1 SPECIFIC OPERATION MESSAGES

-----

THE OPERATION MESSAGE (LINE 4) IS GENERATED IN A DYNAMIC MANNER BASED ON THE SUBSYSTEM FUNCTION BEING EXECUTED AT THE TIME OF THE ERROR AND THE STATE OF THE FLAGS IN THE LOCATION TAGGED "OPFLAGS". THE POSSIBLE OPERATION MESSAGES ARE GIVEN BELOW.

SEEK - FROM (CYL NUM) DIFF (CYL DIFF) SGN (0 OR 1) HD (0 OR 1) WHERE THE VALUES ARE GIVEN IN OCTAL. THIS MESSAGE IS THE RESULT OF A SEEK OPERATION THAT WAS VERIFIED BY A READ HEADER AND THE HEAD POSITION AFTER A SEEK IS IN ERROR. (THE ACTUAL HEAD POSITION IN THIS ERROR SITUATION IS GIVEN IN THE RESULT LINE, LINE 5.)

READ DATA - IS A READ DATA OPERATION WHERE SOME FORM OF ERROR WAS DETECTED

IN THE ACTUAL READ OPERATION. THIS ERROR COULD BE HARDWARE DETECTED SUCH AS DATA CRC, HEADER CRC, HEADER NOT FOUND, ETC., OR A SOFTWARE DETECTED ERROR SUCH AS DRIVE READY RESET AFTER A READ DATA COMPLETED.

READ DATA WITH DATA COMPARE - IS AN ERROR THAT WAS DETECTED AS BAD DATA IN THE BUFFER AFTER A READ DATA OPERATION. WHEN THIS OPERATION IS REPORTED IT INDICATES THE ACTUAL READ DATA OPERATION COMPLETED WITH NO DETECTED ERRORS BUT THE DATA WAS WRONG.

READ HEADER - READ HEADER FOR 40 HEADERS - READ HEADER FOR 40 HEADERS WITH HEADER COMPARE - HAVE THE SAME GENERAL MEANING AS THE READ DATA AND READ DATA WITH DATA COMPARE. MESSAGES HAVING THE OPERATION OF READ HEADER OR READ HEADER FOR 40 HEADERS ARE THE RESULT OF ERRORS DETECTED IN THE ACTUAL OPERATION WHILE THE READ HEADER FOR 40 HEADERS WITH HEADER COMPARE INDICATES NO ERROR IN THE ACTUAL OPERATION BUT THE HEADER DATA ITSELF WAS IN ERROR.

WRITE DATA - RESET - GET STATUS - GET STATUS WITH RESET - ARE ALL BASIC OPERATIONS. AS BEFORE, THE ERROR DETECTION CAN BE EITHER HARDWARE OR SOFTWARE. THE RESULT LINE (LINE 5) WILL DEFINE THE REASON FOR THE REPORT.

LD DRV - UNLD DRV - ARE OPERATION MESSAGES THAT WILL APPEAR IN THE REPORT WHEN THE DRIVE LOAD AND UNLOAD SEQUENCE IS BEING TESTED.

ANOTHER GROUP OF OPERATION QUALIFIERS WILL BE REPORTED FOR OPERATIONS THAT FAIL IN SPECIFIC TESTS. THESE TESTS ARE THE WRITE/READ TEST PART 2, OVERWRITE TEST, AND THE ADJACENT CYLINDER INTERFERENCE TEST.

OPERATION -----	QUALIFIER -----
READ DATA WITH DATA COMPARE	FOL 0 TO CC SEEK
READ DATA	FOL 255 TO CC SEEK
WRITE DATA	FOL WRITE (NO SEEK)
READ HEADER	ADJ. CYL WRITTEN AFTER FWD SK
	ADJ. CYL WRITTEN AFTER REV SK
	SK FWD, WRT-SK REV, OVERWRT
	SK REV, WRT-SK FWD, OVERWRT

THE ABOVE OPERATIONS CAN BE REPORTED WITH ANY OF THE

QUALIFIERS. THE QUALIFIERS IN THESE TESTS ARE AN ATTEMPT TO MAKE THE REPORT MORE MEANINGFUL BY PROVIDING INFORMATION ABOUT THE SEQUENCE OF OPERATIONS BEING DONE.

THE QUALIFIERS "FOL 0 TO CC SEEK" AND "FOL 255 TO CC SEEK" INDICATE THAT THE SEQUENCE OF OPERATIONS INCLUDED A SEEK OF A GIVEN DIRECTION TO THE CYLINDER WHERE THE TEST IS BEING PERFORMED.

THE "FOL WRITE (NO SEEK)" QUALIFIER MEANS THAT THE OPERATION WAS DONE AFTER A WRITE WITH NO HEAD MOVEMENT BETWEEN THE WRITE AND READ.

THE QUALIFIER "ADJ CYL WRITTEN AFTER FWD SK" AND "ADJ CYL WRITTEN AFTER REV SK" WILL BE REPORTED ONLY IN THE ADJACENT CYLINDER INTERFERENCE TEST. THESE QUALIFIERS ARE USED WHEN THE ERROR OCCURS ON THE CYLINDER UNDER TEST AND DEFINE THE DIRECTION THE HEADS WERE MOVED WHEN THE ADJACENT CYLINDER WAS WRITTEN.

THE QUALIFIERS "SK FWD, WRT-SK REV, OVERWRT" AND "SK REV, WRT-SK FWD, OVERWRT" WILL BE REPORTED ONLY IN THE OVERWRITE TEST. THESE QUALIFIERS DEFINE THE DIRECTION OF HEAD MOTION BEFORE THE INITIAL WRITE AND THE OVERWRITE.

THE QUALIFIER "ON BAD SEC FILES" WILL BE REPORTED WITH THE WRITE DATA COMMAND IF THE PROGRAM ABORTS THAT COMMAND BECAUSE THE WRITE WOULD BE ON THE BAD SECTOR FILES.

### 3.1.2 SPECIFIC RESULT MESSAGES

THE RESULT MESSAGE (LINE 5) IS GENERATED DYNAMICALLY BASED ON THE EXPECTED RESULT OF THE OPERATION BEING TESTED. SINCE OPERATIONS ARE MONITORED DURING EXECUTION THE RESULT MESSAGE MAY REPORT AN ERROR DETECTED DURING THE OPERATION AS WELL AS THE ERRORS SEEN AT THE END OF THE OPERATION. ONLY THE FIRST ERROR SEEN IS REPORTED IN ALL CASES.

THE GENERAL FORMAT FOR THE RESULT LINE IS

RESULT:(VAR 1) IS (VAR 2) SB (VAR 3) (OPTIONAL QUALIFIER)

WHERE VARIABLE 1 CAN BE ONE OF THE FOLLOWING:

CONT ERR	(CONTROLLER ERROR)
DRV ERR	(DRIVE ERROR)
NON-EXSTNT MEM	(NON-EXISTANT MEMORY)
HDR CRC	(HEADER CRC ERROR)
DATA CRC	
HDR NOT FND	(HEADER NOT FOUND)
DATA LATE	
HDR NOT FND/HDR CRC/OPI	(ALL 3 BITS SET)
DRV RDY	(DRIVE READY)
SELECTED HEAD	
VOL CHK	(VOLUME CHECK)
COVER OPEN	
BRUSH HME	(BRUCH HOME)
WRT LCK	(WRITE LOCK)
HDS OUT	(HEADER OUT)
DRV SEL ERR	(DRIVE SELECT ERROR)
DRV STATE	(DRIVE STATE)

SPIN TIMEOUT	(SPINDLE TIMEOUT SPD ERROR)
WRT GAT ERR	(WRITE GATE ERROR)
SEEK TIMEOUT	(SKTO ERROR)
CUR HEAD ERR	(CURRENT IN HEAD ERROR)
WRT DAT ERR	(WRITE DATA ERROR)
OP INCOMPLETE	(OPI ERROR)
HDR/DAT ERR	(HEADER CRC OR DATA CRC ERROR BIT 11 OF CS REGISTER)
HDR NOT FND/DAT LATE	(HEADER NOT FOUND OR DATA LATE ERROR BIT 12 OF CS REGISTER)
CVL	(CYLINDER WHEN REPORTING A SEEK ERROR)

VARIABLE 2 WILL BE A VALUE THAT DEFINES WHAT THE RESULT ACTUALLY IS. THIS CAN BE A 1 OR 0 TO INDICATE A SET OF RESULT CONDITIONS, A NUMBER 0 TO 7 TO INDICATE THE DRIVE STATE, OR A NUMBER 0 TO 377 (OCTAL) TO IDENTIFY A CYLINDER NUMBER.

VARIABLE 3 DEFINES THAT THE VALUE GIVEN IS VARIABLE 2 SHOULD BE.

THE OPTIONAL QUALIFIER IS PROVIDED WHEN IT IS USEFUL TO KNOW WHEN THE ERROR WAS DETECTED IN THE OPERATION BEING PERFORMED. THIS QUALIFIER IS USED TO REPORT RESULTS SUCH AS:

BRUSH HVE	IS	1	SB	0	IN	STATE	2
HEADS OUT	IS	0	SB	1	IN	STATE	3
DRV RDY	IS	0	SB	1	IN	DATA	XFER
SELECTED HEAD	IS	1	SB	0	IN	CYCLE	UP
DRV RDY	IS	0	SB	1	IN	STATE	5
DRV RDY	IS	1	SB	0	IN	SEEK	W/O MOTION
DRV RDY	IS	0	SB	1	IN	10MS	
DRV RDY	IS	0	SB	1	IN	500MS	
DRV RDY	IS	0	SB	1	IN	5SECONDS	

THESE RESULTS, WHEN SEEN WITH THE OPERATION MESSAGE, WILL BE SELF EXPLANATORY.

OTHER RESULT MESSAGES THAT CAN BE PART OF AN ERROR REPORT ARE:

"INTERRUPT TO LATE" WHICH INDICATES THAT THE OPERATION BEING PERFORMED DID NOT

COMPLETE IN THE EXPECTED AMOUNT OF TIME. THIS RESULT CAN BE CAUSED BY THE DRIVE LOSING READY BEFORE STARTING A READ HEADER AND THEREFORE NOT COMPLETING THE READ HEADER IN 1MS.

"FAIL TO RELOAD HDS AFTER ERR CLEAR" IS REPORTED WHEN AN ERROR CAUSES HEADS TO UNLOAD AND AFTER THE ERROR IS CLEARED THE HEADS DO NOT RELOAD.

"UNKN DRV STATE-NO RDY, NO ERR, HDS OUT" IS REPORTED WHEN THE PROGRAM CANNOT DETERMINE THE DRIVE STATE OR STATUS.

"WRITE ABORTED" IS REPORTED WHEN THE PROGRAM ABORTS A WRITE TO PROTECT THE BAD SECTOR FILES.

"COULD NOT RETRIEVE DRIVE STATUS" IS REPORTED IF THE GET STATUS COMMAND DOES NOT COMPLETE SUCCESSFULLY WHEN THE STATUS IS REQUIRED TO REPORT AN ERROR.

"OPI SET-NO DRIVE RESPONSE" IS REPORTED AS THE RESULT WHEN THE GET STATUS COMMAND IS TIMED OUT (OPI SETS) WHEN THAT COMMAND IS BEING USED IN THE EARLY TESTS TO CHECK THE DRIVE INTERFACE.

"NO INTERRUPT ON CMND COMPLETE" IS REPORTED WHEN THE COMMAND SUCCESSFULLY COMPLETES BUT THE CONTROLLER HAS NOT GENERATED AN INTERRUPT.

"ERR DID NOT CLEAR" IS REPORTED WHEN THE RESET COMMAND DOES NOT CLEAR THE CONTROLLER ERRORS. THIS IS A CONTROLLER RELATED PROBLEM BUT IS REPORTED IF SEEN IN THE DRIVE TEST PROGRAMS.

"DRV ERR IS NOT CLEARED" IS REPORTED WHEN THE GET STATUS W/RESET COMMAND DOES NOT CLEAR ALL DRIVE ERRORS.

"UNEXPECTED ERR" IS REPORTED WHEN THE CONTROLLER SENSES AN ERROR BUT NO ERROR BITS ARE SET.

"BAD SEC FILE FMT ERR" IS REPORTED IF THE CONTENTS OF THE FILES DO NO CORRESPOND TO THE EXPECTED FORMAT. (REFER TO DEC STANDARD 144 FOR FORMAT SPECIFICS.)

### 3.1.3 OTHER MESSAGES

OTHER INFORMATION IS REPORTED UNDER VARIOUS CIRCUMSTANCES. ARE:

"BAD SEC FILES NOT STRD. ALL SEC ASSUMED GOOD." THIS MESSAGE IS PRINTED WHEN A PARTICULAR TEST REQUIRES THE BAD SECTOR FILES BUT THEY HAVE NOT BEEN STORED. THIS SITUATION WILL OCCUR IF THIS TEST IS STARTED OUT OF THE NORMAL PROGRAM SEQUENCE OR IF THE BAD SECTOR FILES COULD NOT BE READ.

"ERROR LIMIT EXCEEDED-UNIT DROPPED" IS REPORTED (WITH THE UNIT NUMBER) WHEN MORE THAN THE SPECIFIED NUMBER OF ERRORS (DEFAULT 20) HAVE OCCURED IN ANY SINGLE PASS.

### 3.2 ERROR HALTS

ERROR HALTS ARE SUPPORTED PER DESCRIBED IN THE PREVIOUS SECTION WITH /FLAG:HOE. THERE ARE NO OTHER HALTS.

## 4.0 PERFORMANCE AND PROGRESS REPORTS

### 4.1 PERFORMANCE REPORTS

THIS PROGRAM WILL NOT GIVE ANY PERFORMANCE REPORTS.

#### 4.2 PROGRESS REPORTS

THIS PROGRAM WILL NOT GIVE ANY PROGRESS REPORTS.

#### 5.0 DEVICE INFORMATION TABLES

THE RL11/RLV11 CONTROLLER HAS THE FOLLOWING FOUR(4) REGISTERS FOR CONTROL OF THE SUBSYSTEM.

##### RLCS - CONTROL AND STATUS REGISTER (XXXXX0)

---

BIT 15 - COMPOSITE ERROR  
 BIT 14 - DRIVE ERROR  
 BIT 13 - NON EXISTANT MEMORY ERROR  
 BIT 12 - HEADER NOT FOUND (WITH BIT 10 SET)  
           - DATA LATE (WITH BIT 10 CLEAR)  
 BIT 11 - HEADER CRC (WITH BIT 10 SET)  
           - DATA CRC (WITH BIT 10 CLEAR)  
 BIT 10 - OPERATION INCOMPLETE  
 BIT 9/8 - DRIVE SELECT (0-3)  
 BIT 7 - CONTROLLER READY  
 BIT 6 - INTERRUPT ENABLE  
 BIT 5 - EXTENDED BUS ADDRESS (BIT 17)  
 BIT 4 - EXTENDED BUS ADDRESS (BIT 16)  
 BIT 3-1 - FUNCTION CODE  
           0 - NOP (PDP-11) MAINT (LSI-11)  
           1 - WRITE CHECK  
           2 - GET DRIVE STATUS  
           3 - SEEK  
           4 - READ HEADER  
           5 - WRITE DATA  
           6 - READ DATA  
           7 - READ WITHOUT HEADER COMPARE

BIT 0 - DRIVE READY

##### RLBA - BUS ADDRESS REGISTER (XXXXX2)

---

BITS 15-1 BUS ADDRESS OF DATA TRANSFER  
 BIT 0 SHOULD BE 0

##### RLDA - DISK ADDRESS REGISTER (XXXXX4)

---

FOR READ/WRITE FUNCTIONS

---

BIT 15 - MUST BE ZERO(0)

BIT 14-7 - CYLINDER ADDRESS FOR TRANSFER  
 BIT 6 - SURFACE FOR TRANSFER  
 BIT 5-0 - SECTOR FOR TRANSFER (0-47)

FOR SEEK FUNCTION  
 -----

BIT 15 - MUST BE ZERO(0)  
 BIT 14-7 - DIFFERENCE TO NEW CYLINDER  
 BIT 6-5 - MUST BE ZERO(0)  
 BIT 4 - SURFACE  
 BIT 3 - MUST BE ZERO  
 BIT 2 - SEEK DIRECTION( 1 - IN / 0 - OUT )  
 BIT 1 - MUST BE ZERO  
 BIT 0 - MUST BE ONE(1)

FOR GET STATUS FUNCTION  
 -----

BIT 15-4 - IGNORED SHOULD BE ZERO  
 BIT 3 - DRIVE RESET  
 BIT 2 - MUST BE ZERO  
 BIT 1 - MUST BE ONE  
 BIT 0 - MUST BE ONE

RLMP - MULTIPURPOSE REGISTER  
 -----

FOR READ/WRITE FUNCTION  
 -----

BIT 15 - 0 - WORD COUNT(TWO'S COMPLIMENT)

FOR READ HEADER FUNCTION  
 -----

BIT 15-0 - DISK HEADER OF SECTOR (FIRST READ)  
           - ZERO WORD (SECOND READ)  
           - HEADER CRC (THIRD READ)

FOR GET STATUS FUNCTION  
 -----

HAS DRIVE STATUS

BIT 15 - WRITE DATA ERROR  
 BIT 14 - CURRENT HEAD ERROR(CHE)  
 BIT 13 - WRITE LOCK STATUS(WL)  
 BIT 12 - SEEK TIME OUT(SKTO)  
 BIT 11 - SPIN ERROR(SPE)  
 BIT 10 - WRITE GATE ERROR(WGE)  
 BIT 9 - VOLUME CHECK(VC)  
 BIT 8 - DRIVE SELECT ERROR(DSE)  
 BIT 7 - RESERVED(0)  
 BIT 6 - SURFACE

BIT 5 - COVER OPEN  
 BIT 4 - HEADS HOME  
 BIT 3 - BRUSHES HOME  
 BIT 2-0 - STATE BITS  
     0 - LOAD STATE  
     1 - SPIN UP  
     2 - BRUSH CYCLE  
     3 - LOAD HEADS  
     4 - SEEK - TRACK COUNTING  
     5 - SEEK - LINEAR MODE  
     6 - UNLOAD HEADS  
     7 - SPIN DOWN

## 6.0 TEST SUMMARIES

### TEST 1 BASIC INTERFACE TEST (PART 1)

LOAD IN DRIVE NUMBER. DO GET STATUS WITH RESET. IF OPI SETS:  
 DRIVE INTERFACE IS DEAD  
 DRIVE COMMAND SHIFT REGISTER NOT LOADING/SHIFTING  
 MARKER DETECTION FAILED  
 DRIVE IS NOT SELECTING OR AC LOW IS SET  
 SYSTEM OR STATUS CLOCKS NOT OPERATIONAL  
 GET STATUS DETECTION FAILED.

IF INTERRUPT WITH NO OPI, CHECK STATUS RECEIVED. COVER OPEN  
 AND BRUSH HOME SHOULD BE SET. IF NOT:  
 BAD STATUS DATA LINE  
 BAD COVER SWITCH OR LOGIC  
 DRIVE COMMAND SHIFT REGISTER  
 BAD BRUSH HOME SWITCH OR LOGIC

CHECK WRITE LOCK STATUS BIT SET. IF NOT:  
 BAD SWITCH OR WRITE LOCK LOGIC

DRIVE COMMAND SHIFT REGISTER

CHECK STATE FOR 0. IF NOT:  
 BAD STATE ROM  
 DRIVE COMMAND SHIFT REGISTER

CHECK VOLUME CHECK RESET. IF NOT:  
 BAD RESET DETECTION  
 BAD VOLUME CHECK LOGIC  
 DRIVE COMMAND SHIFT REGISTER

CHECK DRIVE ERROR RESET. IF NOT:  
 BAD DRIVE ERROR INTERFACE  
 SOME OTHER ERROR STUCK ON. REPORT WHICH ERROR.



NOTE: THIS TEST IS EXECUTED ONLY IF PROGRAM OPERATION MODE 2 IS SELECTED, MANUAL INTERVENTION TESTING IS REQUESTED, AND IS RUN IN FIRST PASS ONLY.

TEST 2 BASIC INTERFACE TEST (PART 2)

REQUEST OPERATOR TO CLOSE COVER AND RESET WRITE LOCK.

DO GET STATUS LOOP CHECKING IF COVER OPEN OR WRITE LOCK RESETS. WAIT 15 SECONDS FOR BOTH TO CHANGE. IF NO CHANGE, ASK OPERATOR TO TYPE CR IF PROCEDURE WAS FOLLOWED.

IF ONE CHANGED BUT NOT THE OTHER, REPORT WHICH FAILURE:

WRITE LOCK SWITCH OR LOGIC  
(OR) COVER OPEN SWITCH OR LOGIC  
DRIVE COMMAND SHIFT REGISTER

IF NEITHER CHANGED, REPORT BOTH FAILURES.

NOTE: THIS TEST IS EXECUTED ONLY IF PROGRAM OPERATION MODE 2 IS SELECTED, MANUAL INTERVENTION TESTING IS REQUESTED, AND IS RUN IN FIRST PASS ONLY.

TEST 3 HEAD LOADING TEST

REQUEST OPERATOR TO PRESS LOAD SWITCH.

DO GET STATUS LOOP CHECKING FOR STATE TO GO TO 1. WAIT 30 SECONDS FOR CHANGE. IF NO CHANGE, ASK OPERATOR TO CONFIRM ACTION BY TYPING CR.

IF LOAD WAS PRESSED:

BAD STATE ROM

BAD LOAD SWITCH OR LOGIC

CHECK THAT STATE 1 REMAINS FOR LESS THAN 30 SECONDS. IF NOT:

SPINDLE NOT TURNING OR TOO SLOW (AC SERVO)  
SECTOR PULSE DETECTION OR LOGIC BAD  
BAD CLOCK SHIFT REGISTER IN SPEED CONTROL  
BAD DISK ON SPEED LOGIC  
BAD STATE ROM

AND CHECK IF SPINUP TIMEOUT ERROR SET. IF NOT:

BAD STATE ROM  
BAD TIMEOUT DETECTION LOGIC

CHECK THAT STATE GOES TO 2. IF NOT:

BAD STATE ROM

CHECK THAT BRUSH HOME IS RESET 5 SECONDS OR LESS AFTER STATE IS 2. IF NOT:

BAD BRUSH HOME SWITCH OR LOGIC  
BAD BRUSH MOTOR (AC SERVO)

WAIT 30 SECONDS FOR BRUSH HOME TO SET. IF NOT:

BAD AC SERVO  
BAD SWITCH OR LATCH

CHECK THAT STATE HAS CHANGED TO 3. IF NOT:

BAD STATE ROM

AFTER STATE IS 3, CHECK HEADS OUT IS SET. IF NOT:

BAD SWITCH  
BAD SEEK CONTROL ROM  
BAD VELOCITY ROM  
BAD DC SERVO

CHECK VOLUME CHECK IS SET. IF NOT:

BAD VOLUME CHECK LOGIC

CHECK IF DRIVE ERROR IS SET. IF NOT:

BAD DRIVE ERROR LOGIC OR INTERFACE

WAIT 300 MS FOR STATE TO CHANGE TO 4. IF IT DOESN'T CHANGE:

STATE ROM BAD  
SEEK ROM  
VEL ROM  
GUARD BAND DETECTION

WAIT 15 MS FOR STATE TO CHANGE TO 5.

8 MS AFTER STATE GOES TO 5, DRIVE READY SHOULD SET. IF NOT:

INTEGRATOR OR NULL DETECTION FAILURE  
READY ONE SHOT BAD  
ENABLE TIMEOUT R NOT SETTING OR COUNT LOGIC BAD

NOTE: THIS TEST IS EXECUTED ONLY IF PROGRAM OPERATION MODE 2 IS SELECTED, MANUAL INTERVENTION TESTING IS REQUESTED, AND IS RUN IN FIRST PASS ONLY.

## TEST 4 HEAD UNLOADING TEST

CHECK DRIVE IS READY. IF NOT REPORT AND ASK OPERATOR TO MAKE DRIVE READY.

REQUEST OPERATOR TO UNLOAD DRIVE.

LOOP ON GET STATUS WAITING FOR STATE TO CHANGE TO 6. IF NO CHANGE:

BAD STATE ROM  
BAD SWITCH

WAIT 300 MS FOR STATE TO CHANGE TO 7. IF NO CHANGE:

BAD STATE ROM

AFTER STATE IS 7, WAIT 30 SEC FOR STATE TO CHANGE TO STATE 0.  
IF NO CHANGE:

NO BRAKING  
BAD AC SERVO

REQUEST OPERATOR TO LOAD DRIVE. WAIT UNTIL DRIVE BECOMES READY.

NOTE: THIS TEST IS EXECUTED ONLY IF PROGRAM OPERATION MODE 2 IS SELECTED, MANUAL INTERVENTION TESTING IS REQUESTED, AND IS RUN IN FIRST PASS ONLY.

## TEST 5 DRIVE SELECT TEST

INSTRUCT THE OPERATOR TO REMOVE DRIVE ADDRESS PLUGS FROM ALL DRIVES EXCEPT THE DRIVE UNDER TEST. ASK THAT CARRIAGE RETURN

BE TYPED WHEN DONE.

DO GET STATUS TO ADDRESS OF DRIVE UNDER TEST. CHECK THAT NO ERRORS ARE REPORTED. DO GET STATUS TO ALL OTHER ADDRESSES AND CHECK THAT OPI SETS FOR ALL OTHER ADDRESSES.

DO GET STATUS TO ADDRESS OF NEXT SEQUENTIAL ADDRESS. CHECK THAT NO ERRORS ARE REPORTED. DO GET STATUS TO ALL OTHER ADDRESSES AND CHECK THAT OPI SETS.

REPEAT FOR ALL DRIVE ADDRESSES (0,1,2,3 - 0 IS SEQUENTIAL AFTER 3).

NOTE: THIS TEST IS EXECUTED ONLY IF PROGRAM OPERATION MODE 2 IS SELECTED, DRIVE SELECT TESTING IS REQUESTED, AND IS RUN IN FIRST PASS ONLY.

## TEST 6 DRIVE SELECT ERROR TEST

REQUEST OPERATOR INSERT IDENTICAL ADDRESS PLUGS IN TWO DRIVES (MUST BE IDENTICAL TO NUMBER SPECIFIED EARLIER). REQUEST OPERATOR TYPE CARRIAGE RETURN WHEN READY.

PROCEDURE WILL BE TO GET STATUS AND CHECK FOR DRIVE SELECT ERROR. THEN RESET THAT DRIVE AND VERIFY THAT DRIVE SELECT ERROR IS NOT REPORTED AGAIN. WAIT 1 SECOND, THEN CHANGE DRIVE SELECT TO A DIFFERENT NUMBER AND BACK AGAIN. DRIVE SELECT ERROR SHOULD SET AGAIN.

OPERATOR SHOULD SEE THE FAULT LIGHT ON ON BOTH DRIVES. IF INDICATOR IS NOT SEEN ON A DRIVE:

DRIVE SELECT ERROR DETECTION IS BAD IN THAT DRIVE.

NOTE: THIS TEST IS EXECUTED ONLY IF PROGRAM OPERATION MODE 2 IS SELECTED, DRIVE SELECT TESTING IS REQUESTED, AND IS RUN IN FIRST PASS ONLY.

## 4.2 STANDARD TESTS

IF THE PROGRAM OPERATION MODE 1 IS SELECTED, THIS WILL BE THE FIRST TEST EXECUTED. THE DRIVE(S) TO BE TESTED MUST BE POWERED UP, HEADS LOADED, AND WRITE LOCK RESET.

## TEST 7 INITIAL STATE TEST

DO GET STATUS, WAIT FOR INTERRUPT.

IF OPI OCCURS:

DRIVE INTERFACE IS DEAD

DRIVE COMMAND SHIFT REGISTER NOT LOADING/SHIFTING  
DRIVE IS NOT SELECTING OR AC LOW IS SET  
SYSTEM OR STATUS CLOCKS NOT OPERATIONAL  
GET STATUS DETECTION FAILED.

IF INTERRUPT OCCURS WITHOUT OPI, CHECK DRIVE READY. READY SET INDICATES HEADS ARE LOADED AND ARE TRACKING (POSITION WORKING).

IF MANUAL INTERVENTION TESTS WERE RUN, CHECK THAT HEAD 0 IS SELECTED. IF NOT:

DRIVE CYCLE UP DID NOT SELECT HEAD 0

IF DRIVE READY IS SET, CHECK STATUS MESSAGE RECEIVED. HEADS OUT AND BRUSH HOME MUST BE SET. IF NOT:

DRIVE COMMAND SHIFT REGISTER NOT LOADING/SHIFTING  
 HEADS OUT OR BRUSH HOME SWITCH OR ASSOCIATED  
 CIRCUITRY BAD  
 STATUS DATA BAD

IF MANUAL INTERVENTION TESTS WERE RUN AND THIS IS THE FIRST  
 PASS CHECK THAT VOLUME CHECK AND DRIVE ERROR ARE SET.

CHECK ALL ERROR BITS ARE 0.

CHECK STATE IS 5. IF NOT:

DRIVE COMMAND SHIFT REGISTER BAD

TEST 8 INITIAL RESET STATE TEST

DO GET STATUS HEAD SELECT = 0, WAIT FOR INTERRUPT.

DO GET STATUS WITH RESET, WAIT FOR INTERRUPT. BOTH DRIVE  
 ERROR AND VOLUME CHECK SHOULD NOW BE RESET. IF NOT:

RESET DETECTION, RESET ERROR, OR VOLUME CHECK FLOP BAD  
 DRIVE COMMAND SHIFT REGISTER BAD

HEAD SELECTED BIT SHOULD STILL BE ZERO. IF NOT:

DRIVE COMMAND SHIFT REGISTER BAD  
 HEAD SELECT SHIFT REGISTER NOT LOADING

TEST 9 DRIVE READY TEST

DO SEEK WITH 0 DIFFERENCE, SIGN 0, HEAD 0. WAIT FOR  
 INTERRUPT. GET STATUS. CHECK STATE IS 5. IF NOT:

DIFFERENCE COUNTER PICKING UP BITS  
 COUNTER CIRCUITRY IS NOT INDICATING 0 DIFFERENCE

CHECK DRIVE READY IS RESET. IF NOT:

ENABLE TIMEOUT OR READY LATCH/ONE SHOT BAD

WAIT APPROX 8 MS FOR READY TO SET. IF IT TAKES LONGER OR  
 DOESN'T SET AT ALL:

HEADS MAY HAVE MOVED (INTEGRATOR OR NULL DETECTION)  
 READY ONE SHOT FAILED

CHECK DRIVE ERROR DID NOT SET. IF IT SET, DO GET STATUS AND  
 REPORT WHICH ERROR.

VERIFY HEAD SELECT IS ZERO.

## TEST 10 SEEK SIGN SWITCH TEST

DO SEEK WITH DIFFERENCE 0, SIGN 1, HEAD 0. WAIT FOR INTERRUPT. GET STATUS AND CHECK STATE IS 5. IF NOT:

COUNT ROM  
DIFFERENCE COUNTER PICKING UP BITS  
COUNTER CIRCUITRY IS NOT INDICATING 0 DIFFERENCE

VERIFY DRIVE IS NOT READY

WAIT APPROX 8 MS FOR READY TO SET. IF IT TAKES LONGER OR DOESN'T SET AT ALL:

HEADS ARE MOVING (INTEGRATOR OR NULL DETECTION)  
READY ONE SHOT FAILED  
COUNT ROM

VERIFY DRIVE ERROR DID NOT SET

VERIFY HEAD SELECT IS ZERO.

DO SEEK WITH 0 DIFFERENCE, OPPOSITE SIGN, HEAD 0. REPEAT ABOVE TESTS.

## TEST 11 HEAD ALIGNMENT SUPPORT ROUTINE

THIS TEST IS EXECUTED WHEN THE PROGRAM IS STARTED AT ADDRESS 204, HEAD ALIGNMENT SUPPORT IS REQUESTED, AND IN THE FIRST PASS ONLY. IT IS BYPASSED IF THE PROGRAM IS STARTED AT ANY OTHER ADDRESS AND IN THE SECOND AND SUBSEQUENT PASSES.

THIS TEST SELECTS THE DRIVE UNDER TEST AND LOOPS ON A GET

STATUS WITH RESET. THE WRITE LOCK BIT IS MONITORED AND WHEN WRITE LOCK IS RESET HEAD 0 IS SELECTED AND WHEN WRITE LOCK IS SET HEAD 1 IS SELECTED. THIS WILL PERMIT THE HEADS TO BE ALIGNED IN KEEPING WITH THE PRESENT HEAD ALIGNMENT PROCEDURE WITHOUT RETURNING TO THE CONSOLE.

TYPING A CARRIAGE RETURN ON THE CONSOLE WILL TERMINATE THIS TEST ON THE DRIVE UNDER TEST. BEFORE TERMINATING, THE TEST WILL CHECK THAT WRITE LOCK IS RESET. IF NOT, THE OPERATOR WILL BE REQUESTED TO RESET WRITE LOCK.

## TEST 12 HEAD SWITCHING TEST

DO SEEK WITH 0 DIFFERENCE, SIGN 0, HEAD 1. WAIT FOR INTERRUPT. GET STATUS AND CHECK STATE IS 5. IF NOT:

DIFFERENCE COUNTER IS PICKING UP BITS  
ASSOCIATED CIRCUITRY IS BAD

VERIFY DRIVE READY RESET. IF NOT:

ENABLE TIMEOUT OR READY LATCH/ONE SHOT BAD

WAIT APPROX 8 MS FOR READY TO SET. IF IT TAKES LONGER OR DOESN'T SET AT ALL:

HEADS ARE MOVING (INTEGRATOR OR NULL DETECTION)  
 READY ONE SHOT FAILED  
 DRIVE CANNOT TRACK WITH THIS HEAD

VERIFY DRIVE ERROR DID NOT SET.

DO GET STATUS, CHECK HEAD SELECT IS CORRECT. IF NOT:

HEAD SELECT REGISTER BAD  
 DRIVE COMMAND SHIFT REGISTER BAD

DO SEEK WITH 0 DIFFERENCE, SIGN 0, HEAD 0. REPEAT ABOVE TESTS.

TEST 13 READ HEADER TEST (PART 1)

DO SEEK WITH DIFFERENCE 0, HEAD 0, SIGN 0. WAIT FOR INTERRUPT AND WAIT FOR DRIVE READY.

DO READ HEADER, WAIT FOR INTERRUPT.

CHECK IF HEADER CRC ERROR SET. IF SET:

READ/WRITE BOARD BAD  
 READ DATA LINE BAD

CHECK IF BIT 6 OF WORD 1 IS SAME AS HEAD SELECT BIT IN STATUS. IF NOT:

HEADS ARE SWITCHED (CABLE)  
 HEAD SELECT LOGIC

IF MANUAL INTERVENTION TESTS WERE RUN AND HEAD ALIGNMENT TESTS WERE NOT RUN, CHECK THAT HEADER WORD 0 INDICATES HEADS ARE POSITIONED OVER CYLINDER 0. STORE HEADER WORD 1.

REPEAT TESTS USING HEAD 1.

CHECK THAT CYLINDER PORTION OF STORED HEADER WORD 1 IS THE SAME AS HEADER WORD 1 OF THIS HEADER. IF NOT:

HEADS ARE MISALIGNED

TEST 14 READ HEADER TEST (PART 2)

DO SEEK WITH DIFFERENCE 0, SIGN 0, HEAD 0. WAIT FOR INTERRUPT. WAIT FOR READY.

DO 40 CONSECUTIVE READ HEADER, STORE 3 HEADER WORDS AFTER EACH READ.

CHECK ALL HEADERS FOR SEQUENCE AND CONTENT (WORD 2 ALL ZERO, BIT 15 WORD 1 AND 3 IS 0, HS BIT WORD 1 IS 0). IF NOT:

BAD READ/WRITE BOARD  
BAD PACK

DO SEEK WITH DIFFERENCE 0, SIGN 0, HEAD 1. REPEAT ABOVE TEST FOR HEAD 1. %



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21 002110 FNDMOD DEVREG
22 002110 .WORD 0
23 002110 000000 .BLKW
24 002114 000001 DEVTYP <RLO1>
25 002114 046122 030460 000 .ASCII <RLCI>
26 002122 .EVEN
27
28 ;COPYRIGHT (C) 1977, 1978
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36 ;AT ALL TIMES REMAIN IN DEC.
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38 ;THE INFORMATION IN THIS DOCUMENT IS SUBJECT TO CHANGE
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40 ;BY DIGITAL EQUIPMENT CORPORATION.
41
42 ;DEC ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY
43 ;OF ITS SOFTWARE ON EQUIPMENT WHICH IS NOT SUPPLIED BY DEC.
44
45 002122 BGNMOD GLBEQAT
46
47 002122 EQUALS OFFSETS FOR HARDWARE P-TABLE
48 CSR =0 ;BUS ADDRESS
49 VECT =2 ;VECTOR ADDRESS
50 PRTOR =2 ;PRIORITY
51 DRSR =2 ;DRIVE SELECT BIT
52 CNT =10 ;CONTROLLER TYPE
53
54 ; OFFSET FOR SOFTWARE P-TABLE
55 MISWI =0 ;SOFTWARE PARAMETERS SWITCHES
56 LCLIM =2 ;CYLINDER LOWER LIMIT
57 HILIM =4 ;CYLINDER HIGH LIMIT
58 HDALIM =4 ;SELECTED HEAD FOR RUNNING TESTS
59 ERLIM =10 ;ERROR LIMIT
60 DCLIM =12 ;DATA COMPARE ERROR LIMIT
61
62 ; BIT ASSIGNMENT FOR SOFTWARE P-TABLE SWITCHES
63 ALLCYL =BIT00 ;USE ALL CYLINDERS
64 ALLSEC =BIT01 ;USE ALL SECTORS
65 DRSELT =BIT02 ;EXECUTE DRIVE SELECT TEST
66 HDALIM =BIT03 ;EXECUTE HEAD ALIGNMENT TEST
67 AUTOSZ =BIT04 ;AUTO SIZE FOR DRIVE-DROP IF NO RESPONSE
68 HEADLV =BIT12 ;HEAD LIMIT SPECIFIED FLAG
69 HCLVL =BIT13 ;HL LIMIT SPECIFIED FLAG
70 LCLVL =BIT14 ;LL LIMIT SPECIFIED FLAG
71 WITEST =BIT15 ;EXECUTE MANUAL INTERVENTION TESTS
72
73 ; SUBSYSTEM FUNCTIONS
74 CKDATA =102 ;WRITE CHECK
    
```

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73 000104 GTSTAT =104 ;GET STATUS
74 000106 SEEK =106 ;SEEK
75 000110 RDHEAD =110 ;READ HEADER
76 000112 WDATA =112 ;WRITE DATA
77 000114 RDATA =114 ;READ DATA
78 000116 RDNOHR =116 ;READ DATA IGNORE HEADERS
79 000100 NOOP =100 ;NO OPERATION
80
81 ; OPERATION FLAGS
82 COMPOP =7777 ;COMPOSITE OPERATION FLAGS
83 HRCOMP =BIT01 ;HEADER COMPARE OPERATION
84 DATACMP =BIT02 ;DATA COMPARE OPERATION
85 CYLUP =BIT03 ;CYCLE UP OPERATION
86 UNLOAD =BIT04 ;UNLOAD OPERATION
87 INOUTS =BIT05 ;IN-OUT SEEK OPERATION
88 OUTINS =BIT06 ;OUT-IN SEEK OPERATION
89 FOLWRT =BIT07 ;FOLLOWING WRITE OPERATION
90 REVSKS =BIT08 ;REV SEEK SEQ (ADJ INTERFERENCE)
91 FWDSKS =BIT09 ;FWD SEEK SEQ (ADJ INTERFERENCE)
92 REVSKO =BIT10 ;REV SEEK SEQ (OVERWRITE)
93 FWDSKO =BIT11 ;FWD SEEK SEQ (OVERWRITE)
94 BADADD =BIT12 ;BAD DISK ADDRESS
95 SEEKOP =BIT13 ;SEEK OPERATION
96 RELOAD =BIT14 ;RELOAD WAIT
97 HDR40 =BIT15 ;40 HEADER OPERATION
98 MQUALS =OUTINS|INOUTS|FOLWRT|REVSKS|FWDSKS|REVSKO|FWDSKO ;MESSAGE QUALIFIER BITS
99
100
101 ; ERROR FLAGS FROM SUBROUTINES
102 TOSLOW =BIT00 ;OPERATION TOOK TOO LONG
103 NINTRPT =BIT01 ;NO INTERRUPT FROM OPERATION
104 CONHNG =BIT02 ;CONTROLLER HUNG
105 NOCLR =BIT03 ;BAD CONTROLLER CLEAR
106
107 ; CONTROL AND STATUS REGISTER
108 RLCSR =0 ;CONTROL AND STATUS REGISTER
109 RLBA =2 ;BUS ADDRESS REGISTER
110 RLDA =4 ;DISK ADDRESS REGISTER
111 RLWP =6 ;MULTI-PURPOSE REGISTER
112
113 ; REGISTER PIT DEFINITIONS - CONTROL STATUS REGISTER
114 RLCSR =0 ;CONTROL AND STATUS REGISTER
115 ANVERR =10000 ;ANY ERROR BIT
116 DRVERR =4000 ;DRIVE ERROR BIT
117 NYVERR =20000 ;NON-EXISTANT MEMORY ERROR
118 DLTERR =10000 ;DATA LATE ERROR
119 HNFERR =10000 ;HEADER NOT FOUND ERROR
120 DCKERR =4000 ;DATA CHECK ERROR
121 HCRCEPR =4000 ;HEADER CHECK ERROR
122 OPTERR =2000 ;OPERATION INCOMPLETE ERROR
123 DSKSK =1400 ;DRIVE SELECT MASK
124 CDVMSK =2000 ;CONTROLLER READY MASK
125 INTEBL =100 ;INTERRUPT ENABLE MASK
126 RAMSK =60 ;BUS ADDRESS UPPER MASK
127 DRDYMSK =1 ;DRIVE READY MASK
    
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129 000077 ; REGISTER PIT DEFINITIONS - DISK ADDRESS FOR DATA XFER
130 000100 ; SPMASK =77 ;SECTOR ADDRESS MASK
131 077600 ; HSMASK =100 ;HEAD SELECT MASK
132 ; CAMASK =77600 ;CYLINDER ADDRESS MASK
133
134 000001 ; REGISTER RIT DEFINITIONS - DISK ADDRESS FOR SEEK
135 000004 ; MRSET0 =1 ;MUST BE SET, BIT 0
136 000020 ; DIRBIT =4 ;DIRECTION BIT
137 077600 ; H0SEL =77 ;HEAD SELECT RIT
138 ; DIRMASK =77600 ;CYLINDER DIFFERENCE MASK
139
140 000003 ; REGISTER RIT DEFINITIONS - DISK ADDRESS FOR GET STATUS
141 000010 ; GETSTAT =3 ;GET STATUS SETUP
142 ; DRSET =10 ;DRIVE RESET MASK
143
144 017777 ; REGISTER RIT DEFINITIONS - MP FOR DATA XFER
145 160000 ; WCMASK =17777 ;WORD COUNT MASK
146 ; WCRNG =160000 ;WORD COUNT RANGE MASK
147
148 077600 ; REGISTER RIT DEFINITIONS - MP FOR READ HEADER
149 000077 ; HDCVL =77600 ;CYLINDER MASK
150 000100 ; HDSEC =77 ;SECTOR MASK
151 000100 ; HDHSEL =100 ;HEAD SELECT MASK
152
153 000007 ; REGISTER RIT DEFINITIONS - MP FOR GET STATUS
154 000010 ; STAMSK =10 ;STATE MASK
155 000020 ; RRSTAT =10 ;BRUSH HOME STATUS
156 000040 ; HOSTAT =20 ;HEADS OUT STATUS
157 000080 ; COSTAT =40 ;COVER OPEN STATUS
158 000100 ; HSSAT =100 ;HEAD SELECT STATUS
159 000400 ; DSESTAT =400 ;DRIVE SELECT ERROR STATUS
160 001000 ; VCSTAT =1000 ;VOLUME CHECK STATUS
161 002000 ; WGESTAT =2000 ;WRITE GATE ERROR STATUS
162 004000 ; SPSTAT =4000 ;SPIN ERROR STATUS
163 010000 ; STOSTAT =10000 ;SEEK TIMEOUT ERROR STATUS
164 020000 ; WLSTAT =20000 ;WRITE LOCK STATUS
165 040000 ; HCESTAT =40000 ;HEAD CURRENT ERROR STATUS
166 100000 ; WDESTAT =100000 ;WRITE DATA ERROR STATUS
167
168 002122 ENDMOD
169 002122 BGNMOD
170
171 ;
172 002122 ; TABLE OF OPERATION MESSAGES
173 ; WORD 0 ; FILLER
174 ; WORD MWRCHK ; MESSAGE FOR WRITE CHECK
175 ; WORD MWRSTA ; GET STATUS
176 ; WORD MSEEK ; SEEK
177 ; WORD MREADH ; READ HEADER
178 ; WORD MWRITE ; WRITE DATA
179 ; WORD MWRDTP ; READ DATA
180 ; WORD MWRSET ; WITH RESET
181 ; WORD MWRATCP ; WITH DATA COMPARE
182 ; WORD MWDFCP ; WITH HEADER COMPARE
183 ; WORD MWADAD ; LOAD HEADS
184 ; WORD MWLOAD ; UNLOAD HEADS
185 ; WORD MWINOUT ; IN-OUT SEQ
    
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185 002154 ; WORD MOUTIN ; OUT-IN SEQ
186 002156 ; WORD MPFLWPT ; FOLLOWING WRITE
187 002159 ; WORD MREVSK ; REV SEEK
188 002162 ; WORD MFWDSK ; FWD SEEK
189 002164 ; WORD MRESKD ; REV SEEK
190 002166 ; WORD MFWSKD ; FWD SEEK
191 002170 ; WORD MWADAD ; BAD DISK ADD FOR WRITE
192 002172 ; WORD MW4HDR ; 40 HEADER OPERATION
193
194 ;
195 002174 ; TABLE OF RESULT NAME MESSAGE ADDRESSES
196 002178 ; WORD MCERR ; CONTROLLER ERROR
197 002200 ; WORD MWERRP ; DRIVE ERROR
198 002202 ; WORD MWNEPR ; NON-EXISTANT MEMORY ERROR
199 002204 ; WORD MWFLERR ; HEADER NOT FOUND-DATA LATE
200 002206 ; WORD MWDEPR ; HEADER OR DATA ERROR
201 002210 ; WORD MWDRST ; OPERATION INCOMPLETE
202 002212 ; WORD MNDRST ; NO DRIVE STATUS AVAILARLE
203 002214 ; WORD C ;
204 002216 ; WORD MWDEPR ; WRITE DATA ERROR
205 002218 ; WORD MHCEPR ; HEAD CURRENT ERROR
206 002220 ; WORD C ;
207 002222 ; WORD MWSTERR ; SEEK TIMEOUT ERROR
208 002224 ; WORD MWSPERR ; SPINDLE ERROR
209 002226 ; WORD MWGERR ; WRITE GATE ERROR
210 002230 ; WORD C ;
211 002232 ; WORD MWSEPR ; DRIVE SELECT ERROR
212
213 ;
214 002234 ; PATTERN TABLE
215 002236 ; WORD PAT1 ;
216 002238 ; WORD PAT2 ;
217 002240 ; WORD PAT3 ;
218 002242 ; WORD PAT4 ;
219 002244 ; WORD PAT5 ;
220 002246 ; WORD PAT6 ;
221 002248 ; WORD PAT7 ;
222 002250 ; WORD PAT8 ;
223 002252 ; WORD PAT9 ;
224 002254 ; WORD PAT10 ;
225 002256 ; WORD PAT11 ;
226
227 ;
228 002260 ; SUBROUTINE CALLING STACK ;STACK IS 12 WORDS LONG
229 002262 ; WORD 0 ;
230 002264 ; WORD 0 ;
231 002266 ; WORD 0 ;
232 002268 ; WORD 0 ;
233 002270 ; WORD 0 ;
234 002272 ; WORD 0 ;
235 002274 ; WORD 0 ;
236 002276 ; WORD 0 ;
237 002278 ; WORD 0 ;
238 002280 ; WORD 0 ;
239 002282 ; WORD 0 ;
240 002284 ; WORD 0 ;
241 002286 ; WORD 0 ;
242 002288 ; WORD 0 ;
243 002290 ; WORD 0 ;
244 002292 ; WORD 0 ;
245
246 002304 ; T25TBL: WORD 2 ;TABLE OF DIFFERENCES TO BE USED
247 002306 ; WORD 6 ;IN TEST 25
248 002310 ; WORD 9 ;
249 002312 ; WORD 12 ;
    
```

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241 002314 000021 .WORD 17.
242 002316 000026 .WORD 22.
243 002320 000033 .WORD 27.
244 002324 000040 .WORD 34.
245 002328 000047 .WORD 41.
246 002332 000054 .WORD 48.
247 002336 000061 .WORD 55.
248 .
249 .
250 .
251 002332 000010 ; TABLE TO BE USED IN TEST 33 AND 34 TO BUILD AND STORE THE
; CYLINDERS TO BE USED IN THE TEST.
CVLTRL: .BLKW 10
252 002352 2 .BYTE 2 ;TABLE OF DEFAULT CYLINDERS
253 002353 7 .BYTE 7
254 002354 14 .BYTE 14
255 002355 21 .BYTE 21
256 002356 28 .BYTE 28
257 002357 35 .BYTE 35
258 002358 42 .BYTE 42
259 002360 49 .BYTE 49
260 002361 56 .BYTE 56
261 002362 63 .BYTE 63
262 002363 70 .BYTE 70
263 002364 77 .BYTE 77
264 002365 84 .BYTE 84
265 002366 91 .BYTE 91
266 002367 98 .BYTE 98
267 002370 105 .BYTE 105
268 002373 112 .BYTE 112
269 002376 119 .BYTE 119
270 002379 126 .BYTE 126
271 002382 133 .BYTE 133
272 002385 140 .BYTE 140
273 002388 147 .BYTE 147
274 002391 154 .BYTE 154
275 002400 161 .BYTE 161
276 002403 168 .BYTE 168
277 002406 175 .BYTE 175
278 002409 182 .BYTE 182
279 002412 189 .BYTE 189
280 002415 196 .BYTE 196
281 002418 203 .BYTE 203
282 002421 210 .BYTE 210
283 002424 217 .BYTE 217
284 002427 224 .BYTE 224
285 002430 231 .BYTE 231
286 002433 238 .BYTE 238
287 002436 245 .BYTE 245
288 002439 252 .BYTE 252
289 002442 259 .BYTE 259
290 002445 266 .BYTE 266
291 002448 273 .BYTE 273
292 002451 280 .BYTE 280
293 002454 287 .BYTE 287
294 002457 294 .BYTE 294
295 002460 301 .BYTE 301
296 002424 000000 SSINDY: .WORD 0 ;SUBROUTINE STACK INDEX POINTER
    
```

```

297 .
298 .
299 002426 000000 ;
300 002430 000000 OPFLAG: .WORD 0 ;OPERATION FLAGS
301 002432 000000 DONE: .WORD 0 ;OPERATION COMPLETE FLAG
302 002434 000000 HADONE: .WORD 0 ;HEAD ALIGNMENT DONE FLAG
303 002436 000000 ERHEAD: .WORD 0 ;ADDRESS OF ERROR HEADER
304 002440 000000 MORECE: .WORD 0 ;MORE THAN 1 COMPARE ERROR
305 002444 000000 ERRSWI: .WORD 0 ;ERROR RETURN SWITCH
306 002448 000000 RSTFLAG: .WORD 0 ;BAD SECTOR FLAGS
307 002446 000000 WRLSW: .WORD 0 ;WRITE SWITCH
308 .
309 .
310 002450 000000 RLRS: .WORD 0 ;RL11 BASE ADDRESS
311 002452 000000 RLVEC: .WORD 0 ;RL11 VECTOR ADDRESS
312 002454 000000 PLORV: .WORD 0 ;DRIVE NUMBER UNDER TEST
313 .
314 002456 000000 L_CS: .WORD 0 ;CONTROLLER REGISTER STORAGE
315 002460 000000 L_BA: .WORD 0 ;BEFORE OPERATION
316 002462 000000 L_DA: .WORD 0
317 002464 000000 L_MP: .WORD 0
318 002466 000000 L_CS: .WORD 0 ;CONTROLLER REGISTER STORAGE
319 002470 000000 L_RA: .WORD 0 ;AFTER OPERATION
320 002472 000000 L_DA: .WORD 0
321 002474 000000 L_MP: .WORD 0
322 002476 000000 HDWRD1: .WORD 0 ;HEADER WORD STORAGE
323 002500 000000 HDWRD2: .WORD 0
324 002500 000000 HDWRD3: .WORD 0
325 .
326 002562 000000 T_STAT: .WORD 0 ;DRIVE STATE STORAGE
327 002504 000000 RESPARM: .WORD 0 ;PAPAM BLOCK FOR REASON REPORT
328 002506 000000 .WORD 0
329 002508 000000 .WORD 0
330 002510 000000 .WORD 0
331 002512 000000 .WORD 0
332 002514 000000 .WORD 0
333 .
334 002516 000000 DRVCNT: .WORD 0 ;DRIVE COUNT FOR DRIVES UNDER TEST
335 002520 000000 DIFAUG: .WORD 0 ;DIFFERENCE AUGMENT FOR SEEK
336 002522 000000 OLD CVL: .WORD 0 ;OLD CYLINDER
337 002524 000000 NEW CVL: .WORD 0 ;NEW CYLINDER
338 002526 000000 CUR CVL: .WORD 0 ;CURRENT CYLINDER
339 002528 000000 DES DIF: .WORD 0 ;DESIRED DIFFERENCE
340 002530 000000 DES SIG: .WORD 0 ;DESIRED SIGN
341 002532 000000 DES SHD: .WORD 0 ;DESIRED HEAD
342 002534 000000 DES SEC: .WORD 0 ;DESIRED SECTOR
343 002536 000000 TEMP0: .WORD 0 ;TEMPORARY STORAGE
344 002540 000000 TEMP1: .WORD 0 ;TEMPORARY STORAGE
345 002542 000000 TEMP2: .WORD 0 ;TEMPORARY STORAGE
346 002544 000000 TEMP3: .WORD 0 ;TEMPORARY STORAGE
347 002546 000000 TEMP4: .WORD 0 ;TEMPORARY STORAGE
348 002548 000000 TEMP5: .WORD 0 ;TEMPORARY STORAGE
349 002550 000000 TEMP6: .WORD 0 ;TEMPORARY STORAGE
350 002552 000000 TEMP7: .WORD 0 ;TEMPORARY STORAGE
351 002554 000000 TEMP8: .WORD 0 ;TEMPORARY STORAGE
352 002556 000000 .WORD 0
353 .
354 ; TIMER STOPAGE
    
```

```
353 002562 000000 OPIN: .WORD 0 ;ONE CYLINDER FORWARD INNER
354 002564 000000 OPINU: .WORD 0 ;UPPER
355 002566 000000 OFMD: .WORD 0 ;ONE CYLINDER FORWARD MIDDLE
356 002568 000000 OFMDU: .WORD 0 ;UPPER
357 002570 000000 OFOUT: .WORD 0 ;ONE CYLINDER FORWARD OUTER
358 002572 000000 OFOUTU: .WORD 0 ;UPPER
359 002574 000000 OFIN: .WORD 0 ;ONE CYLINDER REVERSE INNER
360 002576 000000 OFINU: .WORD 0 ;UPPER
361 002578 000000 OFMD: .WORD 0 ;ONE CYLINDER REVERSE MIDDLE
362 002580 000000 OFMDU: .WORD 0 ;UPPER
363 002582 000000 OFOUT: .WORD 0 ;ONE CYLINDER REVERSE OUTER
364 002584 000000 OFOUTU: .WORD 0 ;UPPER
365 002586 000000 OFIN: .WORD 0 ;12R CYLINDER FORWARD INNER
366 002588 000000 OFINU: .WORD 0 ;UPPER
367 002590 000000 OFMD: .WORD 0 ;12R CYLINDER FORWARD OUTER
368 002592 000000 OFMDU: .WORD 0 ;UPPER
369 002594 000000 OFOUT: .WORD 0 ;12R CYLINDER REVERSE INNER
370 002596 000000 OFOUTU: .WORD 0 ;UPPER
371 002598 000000 OFIN: .WORD 0 ;12R CYLINDER REVERSE OUTER
372 002600 000000 OFINU: .WORD 0 ;UPPER
373 002602 000000 OFMD: .WORD 0 ;256 CYLINDER FORWARD
374 002604 000000 OFMDU: .WORD 0 ;UPPER
375 002606 000000 OFOUT: .WORD 0 ;256 CYLINDER REVERSE
376 002608 000000 OFOUTU: .WORD 0 ;UPPER
377 002610 000000 OFIN: .WORD 0 ;ONE CYLINDER REVERSE
378 002612 000226 EXOCVYL: .WORD 150. ;EXPECTED TIME ONE CYLINDER
379 002614 000386 EXHCVYL: .WORD 75. ;EXPECTED TIME 12R CYLINDER
380 002616 001756 EXACVYL: .WORD 1000. ;EXPECTED TIME 256 CYLINDER
381 002618 000372 EXROT: .WORD 250. ;EXPECTED ROTATION TIME
382 002620 000004 EPRVEC: .WORD 4 ;ERROR VECTOR USED WHEN AUTO SIZING
383 002622 000000 ;
384 002624 000000 ; MISCELLANEOUS COUNTERS
385 002626 000000 PASCNT: .WORD 0 ;PASS COUNTER (LOCAL TO A TEST)
386 002628 000000 COUNT: .WORD 0 ;A COUNTER (LOCAL TO A TEST)
387 002630 000000 ERRPNT: .WORD 0 ;ERROR POINTER
388 002632 000100 ERRCNT: .BLKW 64. ;STORAGE FOR ERROR COUNTERS
389 002634 000000 PASNUM: .WORD 0 ;PASS NUMBER FOR PROGRAM
390 002636 000000 PSEFNM: .WORD 0 ;COUNTER FOR PARAMETER SET NUMBER IN USE
391 002638 000000 LOCERR: .WORD 0 ;LOCAL ERROR COUNTER
392 002640 000000 NOERCT: .BYTE 0 ;INHIBIT ERROR COUNTING FLAG
393 002642 000000 TRPFLC: .WORD 0 ;HARDWARE TRAP OCCURRENCE
394 002644 000000 PWRFLC: .WORD 0 ;POWER FAILURE OCCURRENCE
395 002646 000000 ;
396 002648 000000 ; RAD SECTOR TABLES AND POINTERS
397 002650 000000 BSRVAL: .WORD 0 ;BAD SECTORS FILES VALID FLAG
398 002652 000000 ;
399 002654 000076 BSRFIL: .BLKW 76 ;SOFTWARE BAD SECTOR FILE
400 002656 000076 FBSFIL: .BLKW 76 ;FACTORY RAD SECTOR FILE
401 002658 000000 ;
402 002660 000200 IBUFF: .BLKW 200 ;INPUT BUFFER
403 002662 000200 OBUFF: .BLKW 200 ;OUTPUT BUFFER
404 002664 000000 ;
405 002666 000000 PAT1: .WORD 0 ;PATTERN 1 (ALL ZEROS)
406 002668 177777 .WORD 177777
407 002670 177777 .WORD 177777
408 002672 177777 .WORD 177777
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409 004476 052525 .WORD 052525
410 004478 052525 .WORD 052525
411 004480 052525 .WORD 052525
412 004482 052525 .WORD 052525
413 004484 177777 .WORD 177777
414 004486 052525 .WORD 052525
415 004488 052525 .WORD 052525
416 004490 052525 .WORD 052525
417 004492 177777 .WORD 177777
418 004494 177777 .WORD 177777
419 004496 177777 .WORD 177777
420 004498 177777 .WORD 177777
421 004500 177777 .WORD 177777
422 004502 000000 .WORD 000000
423 004504 000000 PAT3: .WORD 000000
424 004506 000000 .WORD 000000
425 004508 000000 .WORD 000000
426 004510 177777 .WORD 177777
427 004512 177777 .WORD 177777
428 004514 177777 .WORD 177777
429 004516 000000 .WORD 000000
430 004518 000000 .WORD 000000
431 004520 177777 .WORD 177777
432 004522 177777 .WORD 177777
433 004524 000000 .WORD 000000
434 004526 177777 .WORD 177777
435 004528 000000 .WORD 000000
436 004530 177777 .WORD 177777
437 004532 000000 .WORD 000000
438 004534 177777 .WORD 177777
439 004536 000000 .WORD 000000
440 004538 000000 PAT4: .WORD 000000
441 004540 052525 .WORD 052525
442 004542 052525 .WORD 052525
443 004544 052525 .WORD 052525
444 004546 125252 .WORD 125252
445 004548 125252 .WORD 125252
446 004550 052525 .WORD 052525
447 004552 052525 .WORD 052525
448 004554 052525 .WORD 052525
449 004556 125252 .WORD 125252
450 004558 052525 .WORD 052525
451 004560 052525 .WORD 052525
452 004562 125252 .WORD 125252
453 004564 052525 .WORD 052525
454 004566 052525 .WORD 052525
455 004568 125252 .WORD 125252
456 004570 000000 .WORD 000000
457 004572 155555 PAT5: .WORD 155555
458 004574 133333 .WORD 133333
459 004576 066666 .WORD 066666
460 004578 000000 .WORD 000000
461 004580 000000 PAT6: .WORD 000000
462 004582 121105 .WORD 121105
463 004584 150442 .WORD 150442
464 004586 064221 .WORD 064221
465 004588 132110 .WORD 132110
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582 007127 101 045104 041440 P210E: .ASCIZ /ADJ CYL INTERFERENCE/
583 007154 053117 045104 041440 P210E: .ASCIZ /OVERWRITE/
584 007166 042523 045505 052040 SRTMRES: .ASCIZ /SEEK TIMES/
585 007202 050160 047111 046104 SRTMRES: .ASCIZ /STROBE ROTATION TIME/
586 007276 050160 047111 046104 SRTMRES: .ASCIZ /STROBE IN 100'S OF MICRO SEC/
587 007276 050160 047111 046104 SRTMRES: .ASCIZ /APPROX/
588 007300 047111 042516 000122 LABTN: .ASCIZ /INNER/
589 007300 047111 042516 000122 LABTN: .ASCIZ /MIDDLE/
590 007300 044515 042104 042104 LABMID: .ASCIZ /OUTER/
591 007323 185 050130 041505 LABEXP: .ASCIZ /EXPECTED/
592 007333 030060 020061 054503 LABOCF: .ASCIZ /001 CYL FWD/
593 007350 030060 020061 054503 LABOCF: .ASCIZ /001 CYL REV/
594 007364 031061 020070 054503 LABHCF: .ASCIZ /12R CYL FWD/
595 007400 031061 020070 054503 LABHCF: .ASCIZ /12R CYL REV/
596 007414 032462 020065 054503 LABACF: .ASCIZ /25S CYL FWD/
597 007430 032462 020065 054503 LABACF: .ASCIZ /25S CYL REV/
598 007444 042110 020123 045006 HDMVFP: .ASCIZ /HDS FAILED TO MOVE IN 10 TRIES/
599 007503 183 046131 050040 CVLPER: .ASCIZ /CVL PORTION OF HDS DIFFER WHEN READ FROM TRK 0 & 1/
600 007567 110 040505 020104 HAMS21: .ASCIZ /HEAD ALIGN. RSET WRT LCK TO SEL HD 0, SET FOR HD 1/
601 007552 043520 021040 HAMS22: .ASCIZ /TYPE "CT" & "CONT" TO CONTINUE TESTING/
602 007724 041127 051501 046040 OPR003: .ASCIZ /ABOVE CONDITIONS MET/
603 007751 041127 051501 046040 OPR003: .ASCIZ /WAS LOAD DEPRESSED/
604 007774 044103 020113 051104 OPR1: .ASCIZ /CHK DRV IS UNLDED, COVER OPN, AND WRT LCKED /
605 007802 045103 020113 051104 OPR2: .ASCIZ /CLOSE COVER & RST WRT LCK /
606 007816 051120 051505 020123 OPR5: .ASCIZ /PRESS LOAD & WAIT FOR LOAD LIGHT /
607 007816 051120 051505 020123 OPR6: .ASCIZ /PRESS LOAD & WAIT FOR RDV /
608 010120 047111 044505 053117 OPR8: .ASCIZ /REMOVE ADD PLGS EXCPT /
609 010120 047111 044505 053117 OPR8: .ASCIZ /REMOVE ADD PLG /
610 010120 047111 044505 053117 OPR8: .ASCIZ /IN ALL DRVS/
611 010263 047111 052523 043106 OPR10: .ASCIZ /INSUFFICIENT DRVS FOR DRV SEL ERR TST/
612 010300 050122 042514 020105 OPR11: .ASCIZ /PLCE ADD PLGS AS BEFORE/
613 010346 050122 042514 020105 OPR11: .ASCIZ /RESET WRT LCK /
614 010416 047111 000040 053117 OPR1A: .ASCIZ /ON /
615 010416 047111 000040 053117 OPR1B: .ASCIZ /ON DRV /
616 010422 047111 042040 053122 OPR1B: .ASCIZ /UNDER TEST/
617 010434 047111 042504 053117 OPR004: .ASCIZ /HDS NOT LCK /
618 010462 044504 043106 000040 DIFWD: .ASCIZ /DIFF /
619 010470 043523 020116 000 SGNWD: .ASCIZ /SGN /
620 010470 043523 020116 000 HBD: .ASCIZ /HD /
621 010470 043523 020116 000 SCWD: .ASCIZ /CYL /
622 010470 043523 020116 000 CVLWD: .ASCIZ /CYL /
623 010513 106 047522 020115 FRWD: .ASCIZ /FROM /
624 010513 106 047522 020115 FRWD: .ASCIZ /BYPASSED /
625 010533 047522 043506 040120 RFPSPM: .ASCIZ /RFPSPM TRACE SEQ (IN SEQ CALLED)/
626 010577 104 053122 051440 STAMES: .ASCIZ /DRV STAT/
627 010610 040502 020104 042523 BSNSTR: .ASCIZ /RAD SEC FILES NOT STRD. ALL SEC ASSUMED GOOD./
628 010666 047524 020124 047503 TCERR: .ASCIZ /TOT COMPARE ERRS: /
629 010711 104 053122 051040 MDRDV: .ASCIZ /DRV RDV /
630 010723 047524 052116 042440 MCERR: .ASCIZ /CONT ERR /
631 010744 046504 040524 041440 MDCCR: .ASCIZ /DATA CRC /
632 010755 110 051104 047040 MHP: .ASCIZ /HDR NOT FND/
633 010771 104 052101 020101 MDLT: .ASCIZ /DATA LATE/

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640 011003 110 051104 047040 MHFCRC: .ASCIZ /SHDR NOT FND/HDR CRC/OPI&
641 011033 104 053122 042440 MDRERR: .ASCIZ /DRV ERR /
642 011044 042523 042514 052103 MHSTA: .ASCIZ /SELECTED HD /
643 011063 047524 046117 041440 MWOLCK: .ASCIZ /VOL CHG /
644 011063 047524 046117 041440 MWOLCK: .ASCIZ /COVER OPN /
645 011105 102 052522 044123 MBHSTA: .ASCIZ /RRUSH HME /
646 011120 051127 020124 041514 MWLSTA: .ASCIZ /WRT LCK /
647 011120 051127 020124 041514 MWLSTA: .ASCIZ /HDS OUT ERR /
648 011133 110 051504 047440 MHSTA: .ASCIZ /DRV SEL ERR /
649 011133 110 051504 047440 MHSTA: .ASCIZ /DRV STATE /
650 011157 051104 053122 051440 MDRVST: .ASCIZ /SPIN TIMEOUT /
651 011172 050123 047111 052040 MSPERR: .ASCIZ /WRT DAT ERR /
652 011210 051127 020124 040507 MWGERR: .ASCIZ /SEEK TIMEOUT /
653 011210 051127 020124 040507 MWGERR: .ASCIZ /HEAD CUR ERR /
654 011243 102 040505 020124 MWGERR: .ASCIZ /WRT DAT ERR /
655 011261 127 052122 042040 MWDRPP: .ASCIZ /OP INCOMPLETE /
656 011276 050117 044440 041516 MWDRPP: .ASCIZ /SHDR/DAT ERR /
657 011276 050117 044440 041516 MWDRPP: .ASCIZ /OP INCOMPLETE /
658 011332 042110 020123 047516 MFLERR: .ASCIZ /SHDR NOT FND/DAT LATE &
659 011332 042110 020123 047516 MFLERR: .ASCIZ /NON-EXSTNT MEM /
660 011360 047516 020516 054105 MNEERR: .ASCIZ /NON-EXSTNT MEM /
661 011400 054503 020124 000 MCYLOC: .ASCIZ /CYL /
662 011445 183 046131 042114 MDRST: .ASCIZ /COULD NOT RETRIEVE DRIVE STATUS/
663 011445 183 046131 042114 MDRST: .ASCIZ /UNKN DRV STATE-NO RDV,NO ERR,HDS OUT/
664 011512 040506 046111 052040 MRLPAL: .ASCIZ /FAIL TO RELD HDS AFTER ERR CLEAR/
665 011553 127 044524 042524 MWRTRAB: .ASCIZ /WRITE ABORTED/
666 011634 042440 051177 051177 MWRTRAB: .ASCIZ /PER LMT EXCEEDED - UNIT DROPPED/
667 011643 207 177777 000 MWRTRAB: .ASCIZ /ERROR/
668 011643 207 177777 000 BELL: .ASCIZ /<207><377><377>
669
670
671 011647 111 020123 000 RESE3: .ASCIZ /IS /
672 011653 040 041123 00040 RESE4: .ASCIZ /SR /
673
674
675 011660 044440 020116 000 RESE5: .ASCIZ /CONDITIONS /
676 011665 05 040 043117 00040 RESE6: .ASCIZ /IN /
677 011672 052123 020101 020105 STATE2: .ASCIZ /OP /
678 011714 052123 020101 020105 STATE2: .ASCIZ /STATE 2 /
679 011714 052123 020101 020105 STATE2: .ASCIZ /STATE 2 /
680 011722 042523 045505 053440 CDRDV: .ASCIZ /SEEK W/O MOTION&
681 011744 044506 051522 020124 C10MS: .ASCIZ /FIRST 3 MS/
682 011744 044506 051522 020124 C10MS: .ASCIZ /FIRST 3 MS/
683 011763 103 041531 042514 CS00MS: .ASCIZ /CYCLE UP/
684 011774 040504 040524 040400 CAPDT: .ASCIZ /DATA XFER/
685 012006 020065 042523 042103 C5SPC: .ASCIZ /5 SECS/
686
687
688 012016 047045 052045 047045 FMTOP1: .ASCIZ /&N1&N1&T06&S&T01&N/
689 012045 045 022516 022524 FMTOP2: .ASCIZ /&N1&N1&S1&T01&N/
690 012067 045 022516 022524 FMTOP3: .ASCIZ /&N1&N1&S1&T01&N/
691 012110 052045 022516 022524 FMT1: .ASCIZ /&T /
692 012110 052045 022516 022524 FMT1: .ASCIZ /&T /
693 012124 000 000 FMT1: .ASCIZ /&T /
694 012124 000 000 FMT1: .ASCIZ /&T /
695 012143 047045 000116 FMT1: .ASCIZ /&N /
696 012143 047045 000116 FMT1: .ASCIZ /&N /
697 012143 045 022516 022524 FMT6: .ASCIZ /&N1&N1&T01/
698 012163 045 022516 022524 FMT6: .ASCIZ /&N1&N1&S4&T&S4&T&S2&T/
699 012225 045 022516 022524 FMT7: .ASCIZ /&N1&N1&S2&T06&S2&T06&S3&T03&S2&T01&N/

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700 012275 045 022516 022524 FMT8: .ASCIZ /%N*T%06*S2*06*S2*06*S2*06/
701 012327 045 022516 000124 FMT9: .ASCIZ /%NRT/
702 012334 052044 047445 000061 FMT11: .ASCIZ /%R01/
703 012342 052044 047445 000063 FMT12: .ASCIZ /%R03/
704 012350 047044 051444 030461 FMT13: .ASCIZ /%N*S11*T%03*S11*T%03*S11*T%01*S11*T%01/
705 012414 047044 052044 052045 FMT14: .ASCIZ /%N*S11*T%03*S11*T%06*S11*T%06/
706 012446 047044 051444 030461 FMT15: .ASCIZ /%N*S11*T%03*S11*T%06*S11*T%06/
707 012513 047044 052044 022460 FMT17: .ASCIZ /%N*S5%06/
708 012535 045 030523 022460 FMT18: .ASCIZ /%SIO*T%N*S11*06*N/
709 012557 045 030523 030523 FMT19: .ASCIZ /%N*S13*T%S4*T%S4*T%S4*T%N/
710 012654 052044 051444 031133 FMT20: .ASCIZ /%N*S13*T%S4*T%S4*T%S4*T%AD6*N/
711 012654 052044 051444 022462 FMT21: .ASCIZ /%T*S2%06*S14%06*S4%06*N/
712 012654 052044 051444 031061 FMT22: .ASCIZ /%T*S12%06*S14%06*N/
713 012677 045 022516 030523 FMT23: .ASCIZ /%N*S11*T%03*S11*T%01*S11*T%02/
714 012738 045 022516 022524 FMT24: .ASCIZ /%NRT%01*N/
715 012747 045 022516 000124 FMT25: .ASCIZ /%NRT/
716 012754 047044 042045 022462 FMT26: .ASCIZ /%N%D2*T/
717 012764 047044 051444 022461 FMT27: .ASCIZ /%N*S11*T%04*T%03*N/
718 013010 047044 052044 043045 FMT28: .ASCIZ /%NRT%03*T%03*N/
719 013019 045 022516 022524 FMT29: .ASCIZ /%NRT*T/
720 013040 ENDMOD
725
726 013940 RGNMOD GLRFRP
727 ; ERR1 R3 POINTS TO RESULT MESSAGE
728 ; RESULT: (R3)
729 ;
730 ; ERR2 R3 POINTS TO RESULT NAME
731 ; RESULT: (R3) IS SB 0
732 ;
733 ; ERR3 R3 POINTS TO RESULT NAME
734 ; RESULT: (R3) IS 0 SR 1
735 ;
736 ; ERR4 R3 POINTS TO RESULT NAME
737 ; R4 POINTS TO RESULT CONDITIONS
738 ; RESULT: (R3) IS 1 SB 0 (R4)
739 ;
740 ; ERR5 R3 POINTS TO RESULT NAME
741 ; R4 POINTS TO RESULT CONDITIONS
742 ; RESULT: (R3) IS 0 SR 1 (R4)
743 ;
744 ; ERR6 RESULT ROUTINE DETERMINES WHICH ERROR(S) ARE SET AND
745 ; REPORTS ALL
746 ; RESULT: "ERROR" IS 1 SB 0
747 ;
748 ; ERR7 DRIVE STATE ERROR REPORT
749 ; R3 CONTAINS EXPECTED STATE
750 ; RESULT: DRIVE STATE IS (%STAT) SR (R3)
751 ;
752 ;
753 ; ERR8 HEAD POSITIONING ERROR REPORT
754 ; NEWCVL CONTAINS EXPECTED CYLINDER
755 ; HDWRD1 CONTAINS BAD CYLINDER
756 ; RESULT: CYLINDER IS (HDWRD1) SR (NEWCVL)
757 ;
758 ; ERR9 UTILITY RESULT REPORT
759 ; R3 POINTS TO RESULT NAME
    
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760 ; R4 POINTS TO VALUE 1
761 ; R5 POINTS TO VALUE 2
762 ; RESULT: (R3-NAME) IS (R4-VALUE 1) SR (R5-VALUE 2)
763 ;
764 ; ERR10 COMPARE ERROR REPORT
765 ; R3 CONTAINS THE BAD WORD NUMBER
766 ; R4 POINTS TO BAD WORD
767 ; R5 POINTS TO GOOD WORD
768 ; RESULT: WORD (R3) IS (R4) SR (R5)
769 ;
770 ;
771 013040 RGNMSG ERR1 NQEFCT ;TEST IF ERROR COUNTING INHIBITED
772 013040 105737 003067 TSTR ;YES - SKIP
773 013044 001069 167606 INC ;ELSE BUMP ERROR COUNT
774 013046 005277 000001 JSE ;STORE R1
775 013052 010146 021516 1S: MOV R1,(SP) ;REPORT OPERATION
776 013054 004737 021516 JSE ;SET PARAM NUMBER
777 013060 010521 000001 MOV #1,(R1)+ ;INSERT MESSAGE ADDRESS POINTER
778 013064 010521 022304 MOV R3,(R1)+ ;REPORT RESULTS
779 013066 004737 022304 JSE ;REPORT REMAINDER
780 013072 004737 022512 JSP PC,PPTRES ;STORE R1
781 013075 012601 016122 MOV #SP,R1 ;GO CHECK IF ERROR COUNT EXCEEDED
782 013100 004737 016122 JSP PC,CKERCM
783 013104 ENDMMSG
784 013104 L1000C: EMT CSMG
785 013104 104023
786 ;
787 013106 RGNMSG ERR2 NQEFCT ;RUMP ERROR COUNT
788 013106 005277 167546 INC ;STORE R1
789 013114 010146 021516 JSE ;REPORT OPERATION
790 013120 012721 000003 MOV #5,(R1)+ ;SET PARAM NUMBER
791 013124 010321 000001 MOV R3,(R1)+ ;INSERT NAME ADD POINTER
792 013126 010521 000001 MOV #1,(R1)+ ;SET IS VALUE
793 013134 005277 022304 CLR ;SET SB VALUE
794 013140 004737 022512 JSE ;REPORT RESULTS
795 013144 012601 016122 JSP PC,PPTRES ;REPORT REMAINDER
796 013146 004737 016122 MOV #SP,R1 ;STORE R1
797 013152 ENDMMSG
798 013152 L10001: EMT CSMG
799 013152 104023
800 ;
801 013154 RGNMSG ERR3 NQEFCT ;RUMP ERROR COUNT
802 013154 005277 167500 INC ;STORE R1
803 013160 010146 021516 JSE ;REPORT OPERATION
804 013166 004737 000003 MOV #5,(R1)+ ;SET PARAM NUMBER
805 013172 010321 000001 MOV R3,(R1)+ ;INSERT NAME ADD POINTER
806 013174 010521 000001 CLR ;SET IS VALUE
807 013178 005277 022304 MOV #1,(R1)+ ;SET SB VALUE
808 013206 004737 022512 JSE ;REPORT RESULTS
809 013212 012601 016122 JSP PC,PPTRES ;REPORT REMAINDER
810 013214 012601 016122 MOV #SP,R1 ;STORE R1
811 013220 ENDMMSG
    
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008 013712 0060C5 ROR P5 ;SHIFT BIT POINTER TO NEXT PIT
009 013714 001372 BNE J5 ;LOOP IF NOT 0
010 013716 000405 RR J5 ;ELSE REPORT REMAINDER
011 013720 015411 002174 6S: MOV R6,PPRES ;INSERT NAME ADDRESS
012 013722 000737 JSP R6 ;REPORT RESULTS
013 013724 000737 BR 4S ;GET NEXT BIT
014 013732 004737 7S: PC,PPREM ;REPORT REMAINDER
015 013736 005737 TST #P3 ;TEST IF ANY NEW STATUS
016 013740 001414 BEQ J5 ;NO - SKIP
017 013744 000000 PRINTR #FMT17,#STAMES,TEMP3
018 013746 002546 MOV TEMP3,-(SP)
019 013750 012746 MOV #STAMES,-(SP)
020 013754 000003 MOV #3,-(SP)
021 013760 010600 MOV SP,R0
022 013764 010600 EMT CSMSG
023 013766 104014 ADD #10,SP
024 013774 032737 000010 002466 15S: BIT #DCKERR,T.CS ;TEST IF DATA CHECK ERROR
025 014002 001453 002000 002466 REQ #DPTERR,T.CS ;NO - SKIP
026 014004 032737 BIT #DPTERR,T.CS ;TEST IF OPT SET
027 014006 001047 BNE J5 ;YES - SKIP
028 014014 005537 002436 CLR MORECE ;CLEAR COMPARE ERROR COUNT
029 014020 012701 MOV #12R,R1 ;SET COMPARE LENGTH
030 014024 012703 MOV #12R,R5 ;SET WORD COUNT
031 014028 012703 MOV #IBUFF,R4 ;SET GOOD WORD POINTER
032 014034 012704 MOV #IBUFF,R4 ;SET TEST WORD POINTER
033 014040 021514 18S: CVP (R5),(R4) ;CHECK WORD
034 014044 001427 EQV #GOOD,1 ;GOOD - SKIP
035 014048 001427 CMP MORECE,#10. ;TEST IF COMPARE LIMIT REACHED
036 014052 003021 BGT J5 ;YES - SKIP
037 014054 000000 PRINTR #FMT15,#WORD,R3,#RESE3,(R4),#RESE4,(R5)
038 014056 011546 MOV #R3,-(SP)
039 014060 011446 MOV #RESE4,-(SP)
040 014064 011647 MOV #R4,-(SP)
041 014070 010346 MOV #RESE3,-(SP)
042 014074 006005 MOV #R3,-(SP)
043 014078 012746 MOV #FMT15,-(SP)
044 014102 012746 MOV #7,-(SP)
045 014106 010600 MOV SP,R0
046 014110 000000 EMT CSMSG
047 014114 062706 ADD #20,SP
048 014118 005237 20S: INC MORECE ;BUMP ERROR COUNTER
049 014122 022524 19S: CMP (R5)+,(R4)+ ;BUMP POINTERS
050 014126 005501 INC #R4,SP ;BUMP COUNTER
051 014130 001343 DEC P1 ;DEC LENGTH COUNT
052 014134 001343 RNE J5 ;LOOP IF NOT DONE
053 014138 005737 25S: TST MORECE ;TEST IF COMPARE ERRORS
054 014142 001701 BEQ J5 ;NO - SKIP
055 014144 000200 MOV #12R,R1 ;SET COMPARE LENGTH
056 014146 000200 PRINTR #FMT27,#TCERR,MORECE,#RESE6,R1
057 014148 010146 MOV #R6,-(SP)
058 014152 002436 MOV #MORECE,-(SP)
059 014156 010666 MOV #TCERR,-(SP)
060 014162 010310 MOV #FMT27,-(SP)
    
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061 014166 000005 MOV #5,-(SP)
062 014172 010600 MOV SP,R0
063 014174 104014 000014 EMT CSMSG
064 014176 062706 ADD #14,SP
065 014202 012605 27S: MOV #R4,SP ;RESTORE R5, 4, 3, 1
066 014204 012604 MOV #R4,SP
067 014206 012603 MOV #R3,SP
068 014210 004737 016122 MOV #R3,SP
069 014214 004737 JSP PC,CKERLM ;GO CHECK IF ERROR COUNT EXCEEDED
070 014218 000000 ENDMMSG L10005:
071 014220 104023 EMT CSMSG
072 014222 005277 64434 BGNMSG ERR7
073 014224 004737 INC #ERR7 ;BUMP ERROR COUNT
074 014226 004737 MOV #R3,SP ;STORE R3
075 014228 004737 JSP PC,PPTOP ;REPORT OPERATION
076 014232 000003 MOV #3,(R1)+ ;SET PARAM NUMBER
077 014234 011157 000003 MOV #MDRVS1,(R1)+ ;INSERT NAME ADD POINTER
078 014236 002562 MOV #R1,(R1)+ ;INSERT SR VALUE
079 014240 010311 MOV #R1,(R1)+ ;INSERT SR VALUE
080 014244 004737 JSP PC,PPRES ;REPORT RESULTS
081 014248 004737 JSP PC,PPREM ;REPORT REMAINDER
082 014252 012603 016122 MOV #R3,SP ;RESTORE R3
083 014256 004737 JSP PC,CKERLM ;GO CHECK IF ERROR COUNT EXCEEDED
084 014260 000000 ENDMMSG L10006:
085 014264 104023 EMT CSMSG
086 014270 005277 66364 BGNMSG ERR8
087 014272 010346 INC #ERR8 ;BUMP ERROR COUNT
088 014274 010346 MOV #R3,SP ;STORE R3
089 014276 004737 MOV #R3,SP ;STORE R3
090 014280 004737 JSP PC,PPTOP ;REPORT OPERATION
091 014284 000003 MOV #3,(R1)+ ;SET PARAM NUMBER
092 014288 011400 MOV #MDRVS1,(R1)+ ;INSERT NAME ADD POINTER
093 014292 000003 MOV #R1,(R1)+ ;GET HEADER WORD
094 014296 000003 MOV #7,R3 ;SET SHIFT COUNT
095 014300 000241 3S: CLC (R1) ;ALIGN CHAP FOR PRINTING
096 014304 006013 RDP #R1 ;AS IS VALUE
097 014308 001374 BNE J5
098 014312 005721 TST (R1)+ ;BUMP PARAM POINTER
099 014316 003737 MOV #NEWVL,(R1) ;INSERT SR VALUE
100 014320 025374 JSP PC,PPPRES ;REPORT RESULTS
101 014324 022512 JSP PC,PPREM ;REPORT REMAINDER
102 014328 012603 MOV #R3,SP ;RESTORE R3
103 014332 004737 016122 MOV #R3,SP ;RESTORE R3
104 014336 004737 JSP PC,CKERLM ;GO CHECK IF ERROR COUNT EXCEEDED
105 014340 000000 ENDMMSG L10007:
106 014344 104023 EMT CSMSG
107 014364 005277 66270 BGNMSG ERR9
108 014368 010146 INC #ERR9 ;BUMP ERROR COUNT
109 014370 000000 MOV #R1,-(SP) ;STORE R1
    
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887	014372	004737	021516	JSP	PC,PCRTOP	;REPORT OPERATION
888	014402	016321	000000	MOV	(R1)+	;SET PARAM NUMBER
889	014404	010421		MOV	R3,(R1)+	;INSERT NAME ADD POINTER
890	014406	010421		MOV	R4,(R1)+	;SET IS VALUE
891	014408	004737	022304	MOV	PC,PCRTOP	;SET SR VALUE
892	014414	004737	022512	JSP	PC,PCRTOP	;REPORT RESULTS
893	014420	012601		JSP	(SP)+,R1	;REPORT REMAINDER
894	014422	004737	015122	MOV	(SP)+,R1	;RESTORE R1
895	014424	004737		JSP	PC,CKEFLM	;GO CHECK IF ERROR COUNT EXCEEDED
896	014426					
897	014428			ENDMSG		
898	014430	104023		L10010:		
899	014432	010146		BGNMSG	EMT CSMSC	
900	014434	005737	002436	MOV	R1-(SP)	;STORE R1
901	014436	001051		TST	MORECE	;TEST IF 2ND RAD LINE
902	014438	005737		RNF	38	;YES - SKIP
903	014440	005737	156214	INC	PC,PCRTOP	;BUMP ERROR COUNT
904	014442	004737	021516	JSP	PC,PCRTOP	;REPORT OPERATION
905	014444			PPINTR	#EMT5,#BASADD,RLBAS,#DRVNAM,<R,RLDRV+1>	;REPORT ID
906	014446	005046		CLR	(-SP)	
907	014448	002455		BRSP	RLDRV+1,(SP)	
908	014450	002455		MOV	#DRVNAM,-(SP)	
909	014452	002455		MOV	RLBAS,-(SP)	
910	014454	005422		MOV	#BASADD,-(SP)	
911	014456	002455		MOV	#EMT5,-(SP)	
912	014458	000000		MOV	#5,-(SP)	
913	014460	010600		MOV	SP,RO	
914	014462	104014		EMT	CSPNTR	
915	014464	062706	000014	ADD	#4,SP	
916	014466			PPINTR	#MPSLT,#MWORD,P3,#RESE3,(R4),#RESE4,(R5)	
917	014468	011546		MOV	(R5)-,(SP)	
918	014470	011446	011453	MOV	#RESE4,-(SP)	
919	014472	011446	011647	MOV	(R4)-,(SP)	
920	014474	011446		MOV	#RESE3,-(SP)	
921	014476	006005		MOV	R3,-(SP)	
922	014478	005157		MOV	#MWORD,-(SP)	
923	014480	000010		MOV	#MPSLT,-(SP)	
924	014482	010600		MOV	#10,-(SP)	
925	014484	104014		MOV	SP,RO	
926	014486	062706		EMT	CSPNTR	
927	014488	000421	000022	ADD	#2,SP	
928	014490			BR	45	
929	014492			35:	PRINTB	#EMT15,#MWORD,P3,#RESE3,(R4),#RESE4,(R5);REPORT DATA
930	014494	011546		MOV	(R5)-,(SP)	
931	014496	011446	011653	MOV	#RESE4,-(SP)	
932	014498	011446		MOV	(R4)-,(SP)	
933	014500	011446	011647	MOV	#RESE3,-(SP)	
934	014502	010346		MOV	R3,-(SP)	
935	014504	006005		MOV	#MWORD,-(SP)	
936	014506	005157		MOV	#MPSLT,-(SP)	
937	014508	000010		MOV	#10,-(SP)	
938	014510	010600		MOV	SP,RO	
939	014512	104014		EMT	CSPNTR	
940	014514	062706		ADD	#2,SP	
941	014516	000421	000020	BR	45	
942	014518		002436	45:	INC	MORECE
943	014520	005237				;TNC COMPARE ERROR COUNT

1008	014630	012601		MOV	(SP)+,R1	;RESTORE R1
1009	014632	004737	016122	JSP	PC,CKEFLM	;GO CHECK IF ERROR COUNT EXCEEDED
1010	014634			ENDMSG		
1011	014636			L10011:		
1012	014638	104023		ENDMOD	EMT CSMSC	
1013	014640			ENDMOD	.EVEN	
1014	014642			BGNMOD	HPTCCDE	
1015	014644	000005		BGNHW	.WORD L10012-LSHW/2	
1016	014646	004400			.WORD 174400	;CSR BASE ADDRESS DEFAULT
1017	014648	000160			.WORD 140	;VECTOR DEFAULT
1018	014650	000340			.WORD 240	;PRIORITY DEFAULT
1019	014652	000000			.WORD 1	;DRIVE NUMBER DEFAULT
1020	014654	000001			.WORD 1	;RLI1 CONTROLLER
1021	014656			ENDHW		
1022	014658			L10012:		
1023	014660			ENDMOD		
1024	014662			BGNMOD	SPTCCDE	
1025	014664	000006		BGNHW	.WORD L10013-LSSW/2	
1026	014666	000000		MISHW:	.WORD 0	
1027	014668					;BIT 0 = USE ALL CYLINDERS
1028	014670					;BIT 1 = USE ALL SECTORS
1029	014672					;BIT 3 = EXECUTE DRIVE SELECT TEST
1030	014674					;BIT 5 = EXECUTE HEAD ALIGNMENT
1031	014676					;BIT 4 = DROP DRIVE IF NO RESPONSE
1032	014678					;BIT 12 = HEAD SELECT SUPPLIED FLAG
1033	014680					;BIT 13 = HILIMIT SPECIFIED FLAG
1034	014682					;BIT 15 = DO MANUAL INTERVENTION
1035	014684	000000		L0LIMW:	.WORD 0	
1036	014686	000000		H1LIMW:	.WORD 255.	
1037	014688	000000		HEADW:	.WORD 0	
1038	014690	000024		ERLIMW:	.WORD 20.	;ERROR LIMIT
1039	014692	000012		DCLIMW:	.WORD 10.	;COMPARE ERROR LIMIT
1040	014694			ENDHW		
1041	014696			L10013:		
1042	014698			ENDMOD		
1043	014700			BGNMOD	DSPCODE	
1044	014702	000016		DISPATCH	.WORD 14	
1045	014704	022776			.WORD 14	
1046	014706	033450			.WORD T1	
1047	014708	024704			.WORD T3	
1048	014710	025512			.WORD T4	
1049	014712	026110			.WORD T5	
1050	014714	027302			.WORD T6	
1051	014716	027366			.WORD T7	
1052	014718	040500			.WORD TR	
1053	014720	030642			.WORD T9	
1054	014722				.WORD T10	
1055	014724				.WORD T11	
1056	014726				.WORD T12	

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(6) 014724 031160 .WORD T13
(5) 014726 031372 .WORD T14
1051 ENDMOD
1052 RGNMOD INITCODE
1053 RGNINIT
1054 (3) 014730 012700 000340 SETPRI #340
1055 (3) 014730 104041 MOV #340,R0
1056 (3) 014734 104051 EMT #340,R0
1057 (3) 014736 104051 EMT CSMANI ;CHECK IF MANUAL INTERVENTION ALLOWED
1058 (2) 014740 103403 BCOMPLETE 1S ;YES - SKIP
1059 (2) 014742 100014 014656 RCS 1S
1060 (2) 014750 005037 002424 1S: CLR SSINDX ;CLEAR SUBROUTINE STACK INDEX
1061 (2) 014754 012700 000034 MOV #EF.PWR,R0 ;CLEAR INTERVENTION FLAGS
1062 (3) 014754 104050 EMT CSREFG ;POWER FAILURE
1063 (2) 014762 103004 BCOMPLETE 4S ;NO, GO CHECK NEW PASS
1064 (2) 014764 002012 003072 R: CLR SSUNIT,PWRFLG ;SET POWER FAIL FLAG
1065 (3) 014774 104050 RP PWCON ;GO SERVICE POWER FAIL
1066 (3) 014774 000040 4S: READEF #EF.START,R0 ;CHECK IF START
1067 (3) 015000 104050 MOV #EF.START,R0
1068 (2) 015002 103043 BCOMPLETE RESTART ;NO - SKIP
1069 (2) 015002 103043 BCC RESTART
1070 (2) 015004 012700 002516 ; ON START INITIALIZE TO START AT FIRST DRIVE, CLEAR INTERNAL
1071 (2) 015012 005037 003062 RSTRT: CLR PASNUM ;SET UP UNIT COUNT
1072 (2) 015016 012700 002662 MOV #ERRCNT,R0 ;CLEAR PASS NUMBER
1073 (2) 015022 012700 000100 MOV #R1,R1 ;SET A COUNT
1074 (2) 015030 005301 1S: CLR #R1 ;CLEAR A ERROR COUNTER STORAGE AREA
1075 (2) 015032 001375 DEC R1
1076 (2) 015034 012737 002660 BNE #RRCNT-2,ERRPOINT ;LOOP TILL ALL CLEARED
1077 (2) 015036 104041 003064 MOV #1,PSETNM ;INIT ERROR POINTER
1078 (2) 015050 012737 104041 MOV #1,HADONE ;SET PARAM SELECT TO INITIAL VALUE
1079 (2) 015056 032737 020300 MOV #HICVL,MISWIW ;PRESET HEAD ALIGN DONE FLAG
1080 (2) 015064 001063 MOV #2,MISWIW ;TEST IF HI LIMIT SET
1081 (2) 015074 032737 000377 014662 3S: MOV #377,HILIMW ;YES - SKIP
1082 (2) 015102 001062 MOV #LOCVL,MISWIW ;ELSE INIT HILIMIT
1083 (2) 015104 005037 014660 BIT #5S ;TEST IF LO LIMIT SET
1084 (2) 015110 004432 CLR #LIMTW ;YES - SKIP
1085 (2) 015112 000037 5S: RESTART: MOV #LIMTW,SETDON ;ELSE CLEAR LO LIMIT
1086 (3) 015112 104050 READEF #EF.RESTART,R0 ;CHECK IF RESTART
1087 (3) 015120 103734 MOV #EF.RESTART,R0
1088 (2) 015122 103734 EMT CSREFG ;NO - SKIP
1089 (3) 015122 012700 000036 BCOMPLETE RSTPT
1090 (3) 015122 CONTINUE: RCS RSTRT
1091 (3) 015122 READEF #EF.CONTINUE,R0 ;TEST IF CONTINUE
1092 (3) 015122 MOV #EF.CONTINUE,R0
    
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(3) 015126 104050 EMT CSREFG
1090 (2) 015130 103452 RCOMPLETE PWCON
1091 (2) 015132 103452 RCS PWCON
1092 (3) 015132 012700 000035 ; ON CONTINUE PICK UP UNIT LAST UNDER TEST
1093 (3) 015136 104050 READEF #EF.NEW,R0 ;CHECK IF STARTING NEW PASS
1094 (3) 015140 103403 EMT CSREFG
1095 (2) 015142 103403 BCOMPLETE PASNEW
1096 (2) 015142 005737 002516 NXPAS: TST DRVCNT ;TEST IF ALL UNITS CHECKED
1097 (2) 015146 001033 RNE SETDON ;NO - SKIP
1098 (2) 015154 012737 002660 PASNEW: INC PASNUM ;ELSE BUMP PASS COUNT
1099 (2) 015162 012737 002012 MOV #ERRCNT-2,ERRPOINT ;INIT THE ERROR POINTER
1100 (2) 015170 012737 104041 MOV #1,PSETNM ;GET ALL DRIVES
1101 (2) 015176 005337 003064 SETDON: INC PSETNM ;SET PARAM SELECT TO INITIAL
1102 (2) 015184 005337 002516 DEC DRVCNT ;NEXT SET OF PARAMETERS
1103 (2) 015186 062737 000032 ADD #2,ERRPOINT ;DOWN COUNT DRIVE TOTAL
1104 (2) 015214 013700 003064 MOV #ERRBAS,R2 ;UPDATE THE ERROR POINTER
1105 (2) 015220 012702 002450 MOV #R2,R2 ;SET UP TO GET PARAMETERS
1106 (2) 015224 104042 GPHARD R0
1107 (3) 015226 010001 EMT CSREFG
1108 (2) 015230 103406 RCOMPLETE 7S ;SKIP IF GOOD PARAM
1109 (2) 015232 005737 003072 RCS 7S
1110 (2) 015236 001741 TST PWRFLG ;RECENT POWER FAILURE
1111 (2) 015240 005337 003072 DEC NXPAS ;ACCOUNT FOR DRIVE
1112 (2) 015244 000736 BR NXPAS
1113 (2) 015246 012122 7S: MOV (R1)+(R2)+ ;STORE PARAMETERS CSR
1114 (2) 015250 012122 MOV (R1)+(R2)+ ;VECTOR
1115 (2) 015254 012122 TST (R1)+ ;BUMP PAST PRIORITY
1116 (2) 015256 012122 MOV (R1)+(R2)+ ;DRIVE
1117 (2) 015256 012746 000340 PWCON: SETVEC RLVEC,#INTHLR,#340 ;SET UP VECTOR
1118 (2) 015258 012746 016064 MOV #340,-(SP)
1119 (2) 015260 013746 002452 MOV #INTHLR,-(SP)
1120 (2) 015272 012746 000003 MOV #3,-(SP)
1121 (2) 015276 104037 EMT CSREFG
1122 (2) 015280 000010 ADD #10,SP
1123 (2) 015304 012700 000000 SETPRI #0 ;SET PRIORITY
1124 (2) 015304 012700 000000 MOV #0,R0
1125 (2) 015312 013702 002450 EMT CSRPRT
1126 (2) 015312 MOV #RLBAS,R2 ;SET RL BASE ADDRESS POINTER
1127 (2) 015312 ; CHECK IF DOING AUTO SIZE AND DROP DRIVE IF NOT READY AND
1128 (2) 015316 005737 003062 ; ERROR SETS ON GET STATUS.
1129 (2) 015322 001135 TST PASNUM ;TEST IF PASS 0
1130 (2) 015324 032737 000020 014656 RNE #22S,MISWIW ;NO - SET
1131 (2) 015332 031531 REQ #22S ;TEST IF DOING AUTO SIZE
1132 (2) 015332 MOV #22S ;NO - SKIP
1133 (2) 015332 ;CHECK IF UNIRUS ADDRESS IS THERE BEFORE WE CHECK DRIVE READY
    
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1130 015334 005037 003070 CLR TPFLG ;TRAP OCCURANCE
1131 015340 012746 000340 SETVEC ERRVEC,#TRPHAN,#340 ;SET TRAP VECTOR
1132 015344 012746 016056 MOV #TRPHAN,-(SP)
1133 015350 013746 002652 MOV ERRVEC,-(SP)
1134 015354 012746 000003 MOV #6,-(SP)
1135 015360 062706 000010 EMT C$#1,SP
1136 015366 005762 000000 ADD #10,SP
1137 015370 005737 000000 TST RLCS(R2) ;ACCESS BUS
1138 015374 001937 003070 TPFLG ;TRAP OCCUR??
1139 015380 001937 000001 BNE #5,SP ;YES, DON'T INVESTIGATE FURTHER
1140 015384 013705 002454 MOV RLDV,R5 ;GET DRIVE NUMBER
1141 015388 010562 000200 BIS #CRDYSK,R5 ;INSERT CONT READY
1142 015392 012762 000000 MOV R5,RLCS(R2) ;LOAD IN DRIVE NUMBER
1143 015396 001072 000000 BNE #20,SP ;CHECK IF DRIVE IS READY
1144 015400 012762 000003 MOV #CRSTAT,PLDA(R2) ;ELSE INSERT GET STATUS
1145 015404 012770 000004 BIS #R5 ;LOAD R5 WITH GET STATUS FUNCTION
1146 015408 042705 000200 BIC #CRDYSK,R5 ;CLEAR CONTROLLER READY
1147 015412 010562 000000 MOV R5,RLCS(R2) ;LOAD CS REG
1148 015416 012700 000004 WAITMS #4 ;WAIT 4 MS
1149 015420 012700 000004 MOV #4,R0
1150 015424 032762 002000 EMT C$#R0,SP
1151 015428 001452 000000 RIT #OPERR,RLCS(R2);TEST IF OPT SET
1152 015432 001452 000000 BFO #5,SP ;NO - SKIP
1153 015436 013700 002652 CLRVEC ERRVEC,R0
1154 015440 104036 000000 EMT C$CVC,SP
1155 015444 012746 005640 PRINTF #FM24,#DRVNAV
1156 015448 012746 012749 MOV #DRVNAV,-(SP)
1157 015452 012746 000002 MOV #2,-(SP)
1158 015456 010600 MOV #2,(SP)
1159 015460 104017 000006 MOV SP,R0
1160 015464 062706 000006 EMT C$PRTF,SP
1161 015468 005046 002454 ADD #CRASADD,RLAS,#DRVNAV,<R,RLDV+1>
1162 015472 013746 002454 CLR RLDV+1,(SP)
1163 015476 013746 002450 MOV #DRVNAV,-(SP)
1164 015480 013746 002450 MOV RLBAS,(SP)
1165 015484 012746 005622 MOV #BASADD,-(SP)
1166 015488 012746 012749 MOV #DRVNAV,-(SP)
1167 015492 012746 000003 MOV #FM5,-(SP)
1168 015496 010600 MOV #R5,(SP)
1169 015500 104017 000006 MOV SP,R0
1170 015504 062706 000014 EMT C$PRTF,SP
1171 015508 012746 012127 ADD #4,SP
1172 015512 012746 000001 PRINTF #FM3,-(SP)
1173 015516 010600 MOV #1,-(SP)
1174 015520 104017 000004 MOV SP,R0
1175 015524 062706 000004 EMT C$PRTF,SP
1176 015528 013700 003064 ADD #4,SP
1177 015532 104053 000000 DDDU PSETNM,R0 ;DROP DRIVE
1178 015536 104053 000000 MOV PSETNM,R0
1179 015540 104053 000000 EMT C$DDU,SP
1180 015544 104053 000000 DCCLN
    
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1181 015606 104044 20S: EMT C$DCCLN
1182 015610 013700 002652 CLRVEC ERRVEC,R0
1183 015614 104036 22S: MOV ERRVEC,R0
1184 015616 104036 EMT C$CVC,SP
1185 015616 104051 MANUAL CSMANI ;MANUAL INTERVENTION ALLOWED
1186 015620 163004 BNCOMPLT #5 4S ;NO
1187 015622 005737 003062 TST PASHUM ;YES, CHECK PASS NUMBER
1188 015626 001000 BNE #2,SP ;NO, FIRST PASS, NEED DRIVE UP
1189 015630 004037 BR #5 ;FIRST PASS, PROGRAM WILL INSTRUCT USER
1190 015632 005737 003072 ;CHECK IF POWER FAILURE WAIT IS NEEDED
1191 015636 001434 4S: TST PWRFLG ;NEEDED??
1192 015640 013705 002454 BEQ #5,SP ;NO, SKIP
1193 015644 013705 002454 MOV RLDV,R5 ;DRIVE SELECT
1194 015648 010562 000000 BIS #CRDYSK,R5 ;SET CRDY
1195 015652 012701 000074 MOV R5,RLCS(R2) ;SELECT DRIVE
1196 015656 032762 000001 MOV #R0,R1 ;SIXTY SECOND TIMER
1197 015660 001020 000000 RIT #CRDYSK,RLCS(R2) ;DRIVE UP YFT
1198 015664 001020 000000 BNE #5,SP ;YES START TEST
1199 015670 012700 000012 WAITMS #10 ;WAIT A SECOND
1200 015674 012700 000012 MOV #10,R0
1201 015678 005301 000012 EMT C$#R0,SP
1202 015682 001367 000012 DEC R1 ;SIXTY GONE BY
1203 015686 001367 000012 BNE #9,SP ;NO
1204 015690 012746 005673 PRINTF #FM24,#NOPWR
1205 015694 012746 012747 MOV #NOPWR,-(SP)
1206 015698 012746 000002 MOV #FM24,-(SP)
1207 015702 012746 000002 MOV #2,-(SP)
1208 015706 010600 MOV SP,R0
1209 015710 104017 000006 EMT C$PRTF,SP
1210 015714 062706 000006 ADD #4,SP
1211 015718 000673 BR #10,SP
1212 015722 015730 8S:
1213 015726 015730 ENDINIT L10014:
1214 015730 104011 EMT C$INIT
1215 015734 015732 ENDMOD
1216 015738 015732 BGNMOD CLNCODE
1217 015742 015732 BGNCLN
1218 015732 012746 000340 SETVEC ERRVEC,#TRPHAN,#340
1219 015736 012746 016056 MOV #340,-(SP)
1220 015740 013746 002652 MOV #TRPHAN,-(SP)
1221 015744 012746 000003 MOV ERRVEC,-(SP)
1222 015748 012746 000003 MOV #3,-(SP)
    
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(3) 015752 104037 EMT CSSVEC
(3) 015754 062706 000010 ADD #10,SP
1182
1193 015760 012700 SETPRI #7 ;SET PRORITY TO 7
(3) 015760 012700 000007 EMT
(3) 015760 012700 000007 EMT
1184 015765 032762 000200 2S: BIT #CRDVMASK,RLCS(R2) ;TEST IF CONTROLLER READY
1195 015774 001407 REQ 3S ;NO LOOP UNTIL READY
1196 015776 053762 002454 000000 BIT #DRDY,RLCS(R2) ;SET DRIVE NUMBER
1197 016004 032762 000001 000000 BIT #DRDYMASK,RLCS(R2) ;TEST IF DRIVE BUSY
1198 016012 061003 RNE 5S ;NO - SKIP
1199 016014 012700 000003 WAITMS #3 ;WAIT 300 MS
(3) 016014 012700 000003 EMT
(3) 016020 104026 5S: MOV #3,RC ;RELEASE VEC
(3) 016022 013700 002452 EMT
(3) 016025 104036 003072 CLPVEC RLVEC,RO
(3) 016030 005737 003072 EMT
1202 016034 001407 003072 TST PWRFLG ;PWR FAIL SET
1203 016036 005337 003072 BEQ 7S ;NO
1204 016042 013700 002652 7S: MOV #ERRVEC,RO
(3) 016048 104036 EMT
1205 016050 ENDCLN
(3) 016050 L10015: EMT
(3) 016050 104012 EMT
1206
1207 016052 016052 RGNDU NOP
1208 016054 000240 ENDDU
(3) 016054 L10016: EMT
(3) 016054 104055 EMT
1210
1211 016056 ENDMOD
1212 016058 BGNMOD GLBSUR
1213
1214 016056 005237 003070 TRPHAN: INC TPPFLG
1215 016062 000002 RTI
1216
1217 016064 BGNDRV INTHLR
1218 ; INTERRUPT HANDLER. ABORTS WAIT TIMEP AND STORES ALL RL11 REGS
(3) 016064 ; ABORTWAIT
(3) 016064 104021 EMT
1220 016066 012237 002466 MOV (R2)+,T,CS ;STORE RL REGISTERS
1221 016072 012437 003470 MOV (R2)+,B,DA
1222 016074 011337 002474 MOV (R2)+,T,MP
1223 016102 011337 002474 MOV (R2)+,T,MP ;SET DONE FLAG
1224 016106 012737 177777 002430 MOV B-1,DONE ;RESTORE R2
1225 016114 013702 002450 MOV RLBAS,R2
1226
1227 016120 000002 ENDSRV
(2) 016120 L10017: RTI
1228
1229 ; ERROP LIMIT CHECKING ROUTINE
1230 016122 027737 164532 014666 ; DROPS DRIVE IF ERROR LIMIT EXCEEDED
; CKERLM: CMP #ERRPOINT,ERLIMW ;TEST IF ERROR LIMIT EXCEEDED
    
```

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1231 016130 002453 RL7 1S ;NO - SKIP
1232 016132 016132 INLOOP ;CHECK IF IN ERROR LOOP
(3) 016132 104020 EMT
1233 016134 103451 BCOMPLETE 1S ;YES - SKIP
(2) 016134 103451 BCS 1S
1234 016136 PRINTF #FMT25,ERLIMW,#MEXERS
(3) 016136 012746 011571 MOV #MEXERS,-(SP)
(3) 016142 012746 013665 MOV ERLIMW,-(SP)
(3) 016146 012746 012754 MOV #M475,-(SP)
(3) 016152 012746 000003 MOV #3,-(S6)
(3) 016156 010500 MOV SP,PC
(4) 016162 062706 000010 EMT
1235 016166 ADD #10,SP
(11) 016166 PRINTF #FMT5,#RASADD,RLBAS,#DRVNAM,<R,RLDRV*1>
(10) 016170 005046 CLR -(SP)
(9) 016174 052746 002455 MOV #RDRV+1,(SP)
(8) 016200 011374 005433 MOV #DRVNAM,-(SP)
(7) 016204 012746 002450 MOV RLBAS,-(SP)
(6) 016208 012746 005522 MOV #RASADD,-(SP)
(5) 016214 012746 000005 MOV #M15,-(SP)
(4) 016220 016000 MOV SP,PC
(4) 016224 104027 000014 EMT
(3) 016230 062706 000014 ADD #4,SP
1236 016230 PRINTF #FMT3,-(SP)
(7) 016230 012746 012127 MOV #1,-(SP)
(6) 016234 012746 000001 MOV #1,-(SP)
(4) 016240 010500 EMT
(4) 016242 104017 CSEMTF
(4) 016244 062706 000004 ADD #4,SP
1237 016250 DDDU PSETRM,RO ;DROP DRIVE
(3) 016254 104053 EMT
1238 016256 DOCLN ;GO TO CLEAN UP
(3) 016256 104044 EMT
1239 016260 000207 1S: RTS
1240
1241 ; READ AND STORE ALL RL11 REGISTERS
1242 016262 016237 000000 002466 ; READRL: MOV PLCSR(R2),T,CS ;GET CS REG
1243 016270 016237 000002 002470 MOV RLB(R2),T,BA ;GET BUS ADDRESS REG
1244 016276 016237 000004 002474 MOV RLD(R2),T,DA ;GET DISK ADDRESS
1245 016304 016237 000006 002474 MOV RIMP(R2),T,MP ;GET MULTI-PURPOSE REG
1246 016312 000207 PC ;RETURN
1247
1248 ; WAIT FOR CONTROLLER TIMEOUT TO FORCE INTERRUPT ROUTINE
1249 016314 011646 WAITIN: MOV (SP)-,(SP) ;MAKE ROOM FOR ERROR POINTER
1250 016316 005066 000002 000000 CLP 2(SP) ;CLEAR FOR POINTER
1251 016322 032762 000200 000000 BIT #CRDVMASK,RLCS(R2) ;TEST IF CONTROLLER READY
1252 016330 001420 BEQ 4S ;NO - SKIP TO WAIT
1253 016332 004737 016262 JSE PC,READRL ;READ ALL RL REGS
1254 016336 005737 002430 TST DONE ;TEST IF INTERRUPT OCCURRED
1255 016344 001433 000002 000002 ;NO - GO SET NO INTERRUPT ERR FLAG
1256 016346 012766 000002 002466 ;ELSE SET TO SLOW ERROR POINTER
1257 016352 032737 002000 002466 1S: BIT #OPTERR,T,CS ;TEST IF OPTI SET
1258 016360 001403 000002 BEQ 2S ;NO - SKIP
1259 016362 012766 006032 000002 MOV #MDRPS,2(SP) ;SET MESSAGE FOR NO DRIVE RESPONSE
    
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1260 016370 000207          2S:   RTS      PC          ;RETURN
1261 016372          4S:   WAITMS  #3,RO      ;WAIT 300 MS FOR TIMEOUT
1262 016374          (3)   EMT      C$WTM
1263 016376          (3)   EMT      #1,RO      ;RDYVMSK,RLCS(R2)
1264 016400          000200 000000  BIT      #RDYVMSK,RLCS(R2) ;YES - TEST IF READY NOW SET
1265 016406          001006        BNE     PC          ;SKIP
1266 016414          001115 000002  MOV     #C$HNG,2(SP) ;SET MESSAGE FOR CONTROLLER HUNG
1267 016422          001162        BR     BR          ;SKIP
1268 016424          005737 002430 3S:   BNE     DONE      ;CHECK IF INTERRUPT OCCURRED
1269 016430          001345        BNE     PC          ;SKIP TO SET TO SLOW
1270 016432          004737 016262 5S:   PC     READRL    ;READ RL REGS
1271 016436          007561 006362 000002  MOV     #M$OINT,2(SP) ;ELSE SET NO INTERRUPT FLAG
1272 016444          000751        BR     BR          ;GO TO RETURN
1273
1274          016446 005037 002426  ;ISTINT: OPERATION AND TEST INITIALIZE ROUTINE
1275          016452 105037 003067        CLR     OPFLAG    ;CLEAR OPERATION FLAGS
1276          016458 050337 002436        CLR     NOERCT    ;RESET INTERRUPT COUNTING
1277          016462 000207        RTS     PC          ;RESET MOPP COMPARE ERRORS
1278
1279          016464 013746 002550  ;GSTATR: GET STATUS AND GET STATUS WITH RESET ROUTINE
1280          016470 012737 000913 002550  MOV     #G$STAT,DRSET,TEMP4 ;STORE TEMP4
1281          016476 000412        BR     BR          ;SET FOR RESET
1282          016500 013746 002550  GSTATC: MOV     TEMP4,-(SP) ;STORE TEMP4
1283          016506 000003 002550  MOV     #G$STAT,TEMP4 ;SET FOR NO RESET
1284          016512 000404        BR     BR          GSTATG
1285          016514 013746 002550  GSTAT:  MOV     TEMP4,-(SP) ;STORE TEMP4
1286          016520 005037 002550  CLR     TEMP4      ;SET FOR SAVE L. AND T. REGS
1287          016524 013703 002424  GSTATG: MOV     R3,-(SP) ;STORE R3
1288          016532 005723        TST     SR$INDEX,R3 ;GET SUBROUTINE INDEX
1289          016534 000004 002260  MOV     4(SP),SUBSTK(R3) ;RUMP IT FOR NEXT ENTRY
1290          016536 006437 002260  MOV     SR$INDEX,R3 ;INSERT THIS CALL
1291          016540 010046        MOV     R3,SSINDEX ;ADJUST IT TO CALLING LOCATION
1292          016542 010046        MOV     R0,-(SP) ;STORE IT BACK
1293          016544 010046        MOV     R0,-(SP) ;STORE R0
1294          016546 000002 002440  MOV     R3,-(SP) ;STORE R3
1295          016548 000010 002550  BIT     #RSET,TEMP4 ;TEST IF DRIVE RESET
1296          016552 001453        BEQ     BEQ       ;NO - SKIP
1297          016554 032737 040000 000000  MOV     #R$VERR,RLCS(R2) ;TEST IF DRIVE ERROR SET
1298          016556 001403        BEQ     BEQ       ;NO - SKIP
1299          016558 001403        WAITMS #3,RO      ;WAIT FOR 300 MS FOR DRIVE TO SETTLE
1300          016606          (3)   EMT      #3,RO
1301          016612 104027 000003  MOV     #C$WTM
1302          016614 000062 000062 49S:  EMT      #C$WTM
1303          016620 004737 016514 50S:  JSR     PC,GSTAT  ;SET WAIT FOR 5 SEC
1304          016624 017260        BR     BR          ;GET DRIVE STATUS
1305          016626 032737 000001 002466 3S:   BNE     #RDYVMSK,T.CS ;TEST IF DRIVE READY
1306          016630 004737 000020 002474 5S:   BNE     #G$STAT,T.MP ;YES - GO DD CLEAR
1307          016634 001010        BIT     #RSET,TEMP4 ;ELSE TEST IF HEADS OUT
1308          016636 001010        BNE     51S      ;YES - BYPASS RELOAD WAIT FLAG SETTING
1309          016642 032737 140000 002474 51S:  MOV     #SPDSTAT+HCRSTAT,DRSTAT,T.MP ;TEST IF DRIVE HAS ERROR
1310          016644          (3)   EMT      #3,RO      ;THAT CAUSED HEADS TO
1311          016646          (3)   EMT      #3,RO      ;UNLOAD
    
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1312 016654 001441          BEQ     5S        ;NO - SKIP
1313 016656 052737 040000 002426 5S:   #RELDWT,OPFLAG ;ELSE SET WAIT FLAG
1314 016664 000435          BR     BR        ;SKIP TO CLEAR
1315 016666 032737 040000 002466 51S:  BIT     #DR$VERR,T.CS ;TEST IF DRIVE ERROR NOW
1316 016672 001031          BNE     5S      ;YES - SKIP TO CLEAR
1317 016674 001031          WAITMS #3,RO      ;WAIT FOR DRIVE TO GET ERROR, RDY, OR HC
1318          016676          (3)   EMT      #1,RO
1319          016702 104027 000001  MOV     #C$WTM
1320          016704 005304        EMT      #1,RO
1321          016706 005304        EMT      #1,RO
1322          016710 012703 011445  MOV     #W$UNDEF,R3 ;MESSAGE FOR UNDEFINED STATE
1323          016714 1000166 ERR1  ERRHPD 1000166 ERR1
1324          016716 104443        TRA     #ERRCODE
1325          016718 013040        .WORD 1001
1326          016720 013040        .WORD ERR1
1327          016722 000554        BR     BR        ;EXIT
1328          016724 005737 002550 11S:  TST     TEMP4      ;TEST IF SAVE REGISTERS
1329          016726 005737        BNE     5S      ;NO - SKIP
1330          016732 012701 000004  MOV     #4,R1      ;SET SAVE COUNT
1331          016734 012703 002466  MOV     #L.MP+2,R3 ;SET ADDRESS OF FIRST SAVE
1332          016736 012703 002466  MOV     4(R3),-(SP) ;PUT REG ON STACK
1333          016738 012703 002466  DEC     R3         ;DEC COUNT
1334          016742 001375        BNE     R3        ;LOOP UNTIL ALL SAVED
1335          016744 001375        BNE     R3        ;SET FOR GET STATUS
1336          016746 001375        MOV     #G$STAT,L.DA ;SET FOR GET STATUS
1337          016750 000403 000003 002462  BR     BR        ;SKIP
1338          016752 000403 002550 002462 5S:   MOV     TEMP4,L.DA ;INSERT PRESET FOR STATUS
1339          016754 013737 002550 6S:   MOV     TEMP4,L.DA
1340          016756 000503 002430 6S:   CLR     DONE      ;CLEAR INTERRUPT FLAG
1341          016758 013737 002430  MOV     #RDYVMSK,RLCS ;SET UP TO GET STATUS
1342          016760 002900 002456  BIT     #R$TSTA,L.CS ;CLEAR FOR DRIVE 4 - 7 SPEC'D
1343          016762 006104 002456  BIC     #R$TSTA,L.CS
1344          016764 002456  MOV     L.DA,RLCS(R2) ;LOAD RL REGS
1345          016766 002456  MOV     L.CS,RLCSR(R2) ;LOAD CS REG
1346          016768 002456  WAITMS #1,RO      ;WAIT 100 US FOR INTERRUPT
1347          016770 104027 000001  MOV     #C$WTM
1348          016772 104027 002430  EMT      #1,RO
1349          016774 005737 002430  TST     DONE      ;CHECK IF INTERRUPT OCCURRED
1350          016776 013737 002474 4S:   BNE     1S      ;NO - SKIP
1351          016778 013737 002502  MOV     T.MP,T.STAT ;STORE MP REGISTER
1352          016780 017770 002502  RIC     #C<STAMSK>,T.STAT ;CLEAR ALL BUT STATE
1353          016782 032737 000010 002462  BIT     #RSET,L.DA ;TEST IF RESET WAS SPECIFIED
1354          016784 001474        BNE     3S      ;NO - SKIP TO EXIT
1355          016786 032737 040000 002426  BIT     #RELDWT,OPFLAG ;TEST IF RELOAD WAIT FLAG SET
1356          016788 001424        BEQ     BEQ       ;NO - SKIP
1357          016790 012701 001130  MOV     #R0,R1      ;SET WAIT COUNT FOR 60 SECONDS
1358          016792 000001 000000 13S:  BIT     #RDYVMSK,RLCS(R2) ;TEST IF DRIVE NOW READY
1359          016794 001016        BNE     1S      ;YES - SKIP
1360          016796 001016        WAITMS #1,RO      ;CALL WAIT
1361          016798 012700 000001  MOV     #1,RO
1362          016800 005361        EMT      #C$WTM
1363          016802 001361        DEC     R1        ;DEC COUNT
1364          016804 001361        BNE     13S     ;LOOP IF NOT 0
1365          016806 004737 016514  JSR     PC,GSTAT  ;GET DRIVE STATUS
1366          016808 012703 011512 3S:   MOV     #R$REAL,R3 ;ERROR RETURN
1367          016810 012703 011512  MOV     #R$REAL,R3 ;SET RESULT MESSAGE POINTER
1368          016812 1000366 ERR1  ERRHPD 1000366 ERR1
    
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(3) 017149 104443 TRAP TSERCODE
(5) 017144 023424 -WORD 10003
(5) 017144 013040 -WORD ERR1
359 017146 00442 BR 14S ;GO TO EXIT
1360 017150 000012 12S: WAITUS #10, R0 ;WAIT FOR IMS
(3) 017154 104027 MOV #10, R0
(3) 017154 004737 EMT CSWTU ;GET DRIVE STATUS
1363 017164 032750 JSR PC, GSTAT ;GET DRIVE STATUS
1364 017172 001432 BIT #ANVERR, T.CS ;TEST IF ANY ERROR
1365 017172 032737 BEQ 3S ;NO - SKIP
1366 017172 001403 BIT #VCSTAT, T.MP ;CHECK IF VOLUME CHECK RESET
1367 017202 012703 BEQ 7S ;YES - SKIP
1368 017210 000416 MOV #VNCNST, R3 ;SET REASON POINTER
1369 017212 032737 RP 2S ;EXIT
1370 017222 001404 BIT #DRVERR, T.CS ;CHECK IF DRIVE ERROR
1371 017222 001404 BR 9S ;NO - SKIP
(3) 017222 104443 TRAP TSERCODE
(5) 017222 023424 -WORD 10004
(5) 017222 013040 -WORD ERR6
1373 017232 012703 BR 14S ;EXIT
1374 017236 004403 MOV #UNKERR, R3 ;SET REASON POINTER
1375 017240 004737 JSR PC, WAITIN ;WAIT FOR INTERRUPT
1377 017246 012603 BR 2S ;STORE REASON POINTER FOR RETURN
(3) 017246 104443 TRAP TSERCODE
(5) 017246 023424 -WORD 10002
(5) 017246 013040 -WORD ERR1
1378 017254 005037 CLR ERPSWI ;CLEAR FOR ERROR RETURN
1379 017260 005737 TST TEMP4 ;TEST IF REGISTERS WERE SAVED
1380 017264 001404 BNE 10S ;NO - SKIP
1381 017264 012703 MOV #L, CS, R3 ;SET POINTER TO RESTORE
1382 017272 012701 MOV #4, R1 ;SET REGISTER COUNT
1383 017276 012623 MOV (SP)+, (R3)+ ;RESTORE REG
1384 017300 005301 DEC B1 ;DEC COUNT
1385 017302 005737 BNE 20S ;LOOP UNTIL ALL ARE RESTORED
1386 017304 162737 SUB #2, SSINDX ;REMOVE ENTRY FROM SUBROUT STACK
1387 017312 012601 MOV (SP)+, R1 ;RESTORE R1
1388 017314 012600 MOV (SP)+, R0 ;RESTORE R0
1389 017316 012600 MOV (SP)+, R3 ;RESTORE R3
1390 017320 012637 MOV (SP)+, TEMP4 ;RESTORE TEMP4
1391 017324 005737 TST ERPSWI ;TEST IF ERROR RETURN
1392 017330 001403 BEQ 99S ;YES - SKIP
1393 017334 002440 ADD ERPSWI, (SP) ;ADD IN ERROR RETURN
1394 017336 000207 RTS PC ;SET ERROR RETURN ADDRESS
1395 017340 000000 MOV #4, (SP), (SP)
1396 017344 000207 RTS PC
1497 017346 010346 STMSEK: MOV R3, -(SP) ;STORE REGISTERS
1498 017350 013703 MOV SSINDX, R3 ;GET SUBROUTINE INDEX
1499 017354 001404 TST #3 ;BUMP UP FOR NEXT ENTRY
1500 017356 016663 MOV 2(SP), SUBSTK(R3) ;INSERT THIS CALL
1501 017364 162763 SUB #4, SUBSTK(R3) ;ADJUST IT TO CALLING LOCATION
    
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1502 017372 010337 MOV R3, SSINDX ;STORE IT BACK
1503 017376 010446 MOV RC, -(SP) ;SET FOR SEEK
1504 017400 010446 MOV #2, ERPSWI ;SET FOR NO ERROR RETURN
1505 017410 004737 JSR PC, RDVCHK ;CHECK IF DRIVE READY
1506 017414 017574 BSS 65S
1507 017414 017574 MOV #L, CS, R4 ;GET POINTER TO L REGS
1508 017422 012704 MOV #R, CS, R4 ;SET FOR SEEK
1509 017426 001404 BIT #RDRV, (R4) ;INSERT DRIVE NUMBER
1510 017430 005714 BIT #RIT0, (R4)+ ;CLEAR FOR DRIVE 4 - 7 SPEC'D
1511 017436 005024 CLR (R4)+ ;CLEAR BUS ADDRESS
1512 017436 005024 MOV #DIF, (R4) ;LOAD DIFFERENCE
1513 017444 012714 MOV #7, R3 ;SET COUNT FOR SHIFT TO ALIGN
1514 017450 006314 ASL (R4) ;ALIGN DIFFERENCE IN DA
1515 017452 005303 DEC R3
1516 017454 001375 BNE 3S
1517 017456 005737 TST DESSGN ;TEST IF SIGN SET
1518 017462 001404 BEQ 5S ;NO - SKIP
1519 017464 005714 BIT #DIRBIT, (R4) ;INSERT SIGN
1520 017470 005737 BNE 5S ;TEST IF HEAD 0
1521 017474 001404 BEQ 7S ;YES - SKIP
1522 017476 005714 BIT #HSEL, (P4) ;INSERT HEAD BIT
1523 017500 005724 BIT #MRSET0, (R4)+ ;INSERT MARKER BIT
1524 017502 005737 CLR DONE ;CLEAR INTERRUPT FLAG
1525 017506 012901 MOV #10, R1 ;SET WAIT COUNT FOR 800US
1526 017510 006010 MOV -(R4), RLDA(R2) ;LOAD RL REGISTERS
1527 017516 014462 MOV -(R4), RLRA(R2)
1528 017522 014462 MOV -(R4), RLCS(R2)
1529 017526 000000 TST DONE ;CHECK IF INTERRUPTED
1530 017530 001404 BNE 65S ;YES - SKIP
1531 017534 005301 DEC R1 ;DEC WAIT COUNT
1532 017538 001404 BEQ 13S ;IF 0 - SKIP
1533 017544 #1 WAITUS ;
1534 017544 #1 MOV #1, R0
1535 017550 104027 EMT CSWTU ;GO CHECK DONE
1536 017554 004737 JSR PC, WAITIN ;GO WAIT FOR TIMEOUT
1537 017560 012603 MOV (SP)+, R3 ;GET RESULT MESSAGE POINTER
1538 017564 010443 ERRHRD 10004, ERR1
1539 017568 023433 TRAP TSERCODE
1540 017566 013040 -WORD 10011
1541 017570 005037 CLR ERPSWI ;CLEAR FOR ERROR ERROR RETURN
1542 017574 162737 SUB #2, SSINDX ;REMOVE ENTRY FROM SUBROUT STACK
1543 017602 012604 MOV (SP)+, R4 ;RESTORE REGS
1544 017604 012600 MOV (SP)+, R0
1545 017610 005293 MOV (SP)+, R3
1546 017614 001403 BEQ 99S ;TEST IF ERROR RETURN
1547 017616 06371E ADD ERPSWI, (SP) ;YES - SKIP
1548 017622 000207 RTS PC ;ADD IN ERROR RETURN
1549 017624 000000 MOV #4, (SP), (SP) ;SET ERROR RETURN ADDRESS
1550 017630 000207 RTS PC
1628 ; DRIVE READY TEST ROUTINE. CHECKS DEIVE IS READY. IF NOT, WAIT
    
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1629          1176332  0103446
1630 0176332  0103446
1631 0176440  0047233
1632 0176440  0047233
1633 0176442  0166663 000002 002260
1634 0176506  1627633 000004 002260
1635 0176506  0103446 002424
1636 0176506  0103446
1637 0176644  0101446
1638 0176666  0104446
1639 0176666  0104446
1640 0176776  0127011 000002 002440
1641 0177002  0047337 016514
1642 0177066  0209446
1643 0177110  0327337
1644 0177116  0010445 000001 002466
1645 0177200  0127000 000001
1646 0177200  1040277
1647 0177200  0013664
1648 0177332  0127063 010711
1649 0177332  0127063 011755
1650 0177332  0127063
1651 0177442  1044443
1652 0177442  0234432
1653 0177444  0234432
1654 0177506  0053661
1655 0177506  0127011 000062
1656 0177506  0047337 016514
1657 0177600  0209446
1658 0177600  000001 002466
1659 0177700  0010005
1660 0177720  0127000 000001
1661 0177720  1040277
1662 0200005  0053661
1663 0200002  0013664
1664 0200004  0327337 100000 002466 35:
1665 0200004  0327337
1666 0200142  0014405
1667 0200142  1044443
1668 0200166  0234432
1669 0200226  0023447
1670 0200226  0050337 002662
1671 0200332  1627337 002440 45:
1672 0200332  1627337 000002 002424 55:
1673 0200400  0127063
1674 0200400  0126000
1675 0200400  0126000
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1767 0200400  0126000
    
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1675          1176332  0103446
1676 0176332  0103446
1677 0176440  0047233
1678 0176440  0047233
1679 0176442  0166663 000002 002550
1680 0176506  1627633 000004 002550
1681 0176506  0103446 002424
1682 0176506  0103446
1683 0176644  0101446
1684 0176666  0104446
1685 0176666  0104446
1686 0176776  0127011 000002 002260
1687 0177002  0047337 000004 002260
1688 0177066  0209446 002424
1689 0177110  0327337
1690 0177116  0010446
1691 0177200  0104446
1692 0177200  0127011 000002 002440
1693 0177332  0047337 002550
1694 0177332  0047337
1695 0177442  1044443
1696 0177442  0234432
1697 0177444  0234432
1698 0177506  0053661
1699 0177506  0127011 000062
1700 0177600  0047337 016514
1701 0177600  0209446
1702 0177700  000001 002466
1703 0177720  0010005
1704 0177720  0127000 000001
1705 0177720  1040277
1706 0200005  0053661
1707 0200002  0013664
1708 0200004  0327337 100000 002466 35:
1709 0200004  0327337
1710 0200142  0014405
1711 0200142  1044443
1712 0200166  0234432
1713 0200226  0023447
1714 0200226  0050337 002662
1715 0200332  1627337 002440 45:
1716 0200332  1627337 000002 002424 55:
1717 0200400  0127063
1718 0200400  0126000
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1767 0200400  0126000
    
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1933 0211004 005737 002440 TST ERPSWI ;TEST IF ERROR RETURN
1934 021010 001403 ;YES - SKIP
1935 0211004 005737 002440 ADD ERPSWI,(SP) ;ADD IN ERROR RETURN
1936 0210116 000207 RTS PC ;
1937 021020 017616 000000 99S: MOV @ (SP),(SP) ;SET ERROR RETURN ADDRESS
1938 021024 000207 RTS PC ;
1939 ;
1940 ; GET POSITION ROUTINE. READS A HEADER FROM CURRENT CYLINDER
1941 ; (WHERE IT IS PRESENTLY POSITIONED) AND STORES CYLINDER
1942 ; NUMBER IN CIPCYL.
1943 MOV R3,(SP) ;STORE REGISTERS
1944 MOV SSINDX,R3 ;GET SUBROUTINE INDEX
1945 TST (R3)+ ;BUMP IT FOR NEXT ENTRY
1946 021034 005723 000002 002260 2(SP),R3 ;INSERT THIS CALL
1947 021036 012663 000004 002260 #4,SUBSTK(R3) ;ADJUST IT TO CALLING LOCATION
1948 021052 010137 002424 MOV R3,SSINDX ;STORE IT BACK
1949 MOV PC,-(SP) ;
1950 021060 010046 002012 JSR PC,XRDHD ;DO READ HEADER
1951 021066 021116 65S ;
1952 021070 013703 002474 MOV HDWRD1,R3 ;GET HEADER WORD
1953 021074 042783 024777 BIC #CIPCYL,R3 ;CLEAR ALL BUT CYLINDER
1954 021104 006203 000007 4S: MOV #7,R5 ;SET SHIFT COUNT
1955 ASR R3 ;SHIFT TO RIGHT JUSTIFY
1956 021106 005305 65S ;
1957 021110 011337 002526 RNE R3,CURCYL ;STORE AS CURRENT CYLINDER
1958 021116 162737 000002 65S: SUB #2,SSINDX ;REMOVE ENTRY FROM SUBROUT STACK
1959 021124 012605 002424 MOV (SP)+,R5 ;RESTORE REGISTERS
1960 021130 012605 002424 MOV (SP)+,R3 ;
1961 021132 005737 002440 TST ERPSWI ;TEST IF ERROR RETURN
1962 021136 001403 002440 RFE 99S ;YES - SKIP
1963 021140 000207 002440 ADD ERPSWI,(SP) ;ADD IN ERROR RETURN
1964 021146 017616 000000 99S: MOV @ (SP),(SP) ;SET ERROR RETURN ADDRESS
1965 021152 000207 RTS PC ;
1966 ;
1967 ; READ ALL HEADERS ROUTINE. 40 HEADERS ARE READ AND STORED
1968 ; IN Ibuff.
1969 MOV R3,-(SP) ;STORE REGISTERS
1970 MOV SSINDX,R3 ;GET SUBROUTINE INDEX
1971 TST (R3)+ ;BUMP IT FOR NEXT ENTRY
1972 021164 016663 000002 002260 2(SP),SUBSTK(R3) ;INSERT THIS CALL
1973 021172 022763 000004 002260 #4,SUBSTK(R3) ;ADJUST IT TO CALLING LOCATION
1974 021180 010046 002424 MOV R3,SSINDX ;STORE IT BACK
1975 021204 010146 65S ;
1976 021206 010146 000002 MOV R4,-(SP) ;
1977 021210 010446 000002 MOV R4,-(SP) ;SET POP NO ERROR RETURN
1978 021214 012703 000050 MOV #46,R1 ;SET HEADER COUNT
1979 021218 012703 000002 MOV #40,DPFLAG ;SET 40 HDR OP FLAG
1980 021224 012703 002426 RIS #IBUFF,R3 ;SET POINTER TO STORE HDRS
1981 021232 012703 002426 MOV #0,R4 ;SET BASE ADDRESS
1982 021236 012703 000002 WMO #R1MP,R4 ;WAKE UP POINT TO MP REG
1983 021242 012737 002456 MOV #10,L,C ;LOAD FOR READ HEADER, NO INTERRUPT
1984 021246 000010
    
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2017 021254 053737 002454 002456 RIS RLDV,L,CS ;INSERT DRIVE NUMBER
2018 021262 042737 002000 002456 BIC #BIT0,L,CS ;CLEAR FOR DRIVE 4 - 7 SPEC'D
2019 021270 005037 002460 CLR L,RA ;CLEAR RA
2020 021274 005037 002462 CLR L,DA ;CLEAR DA
2021 021300 005037 002534 TST DTSHD ;TEST IF HEAD 0
2022 021304 001463 000002 RFE 3S ;YES - SKIP
2023 021306 052737 000020 002462 BIS #DSEL,L,DA ;ELSE INSERT HEAD 0
2024 021312 013762 002462 3S: MOV L,DA,RLDA(R2) ;LOAD RLDA REG
2025 021316 005037 002462 MOV L,DA,RLBA(R2) ;LOAD RLBA
2026 021330 032762 000200 BIT #CIPCYL,RLCS(R2) ;TEST IF CONTROLLER READY
2027 021336 001003 000002 RNE 6S ;YES - SKIP
2028 021340 004737 017632 JSR PC,PDVCHK ;ELSE CHECK READY
2029 021346 013762 002456 6S: MOV L,CS,RLCS(R2) ;LOAD RLCS REG
2030 021354 012700 077777 6S: MOV #7777,RC ;SET COUNT FOR WAIT
2031 021360 032762 000200 7S: BIT #CRDYMSK,RLCS(R2) ;CHECK THAT OPERATION COMPLETED
2032 021366 001116 000002 RFE 6S ;YES - SKIP
2033 021370 005300 000000 DEC RC ;DEC COUNT
2034 021372 001372 000000 RNE 7S ;SKIP IF NOT YET 0
2035 021374 004737 016314 JSR PC,READRL ;ELSE GET ALL REGISTERS
2036 021404 012603 000000 MOV (SP)+,R3 ;GET RESULT MESSAGE POINTER
2037 021406 ERPHRD 10025,ERR1 ;
2038 (3) 021408 104443 TRAP TSERCODE ;
2039 (5) 021412 013040 .WORD ERR1 ;
2040 021414 005037 002440 CLR ERPSWI ;CLEAR FOR ERROR RETURN
2041 021420 004737 002466 9S: RFE 6S ;TEST FOR ANY ERRORS
2042 021426 100000 000000 BEL L,CS ;NO - SKIP
2043 021430 ERPHRD 10026,ERR6 ;
2044 (5) 021432 104443 TRAP TSERCODE ;
2045 (5) 021434 013340 .WORD ERR6 ;
2046 021436 005037 002440 CLR ERPSWI ;CLEAR FOR ERROR RETURN
2047 021442 004050 12S: MOV (R4),(R3)+ ;STORE HEADER WORDS
2048 021446 011423 000000 MOV (R4),(R3)+ ;
2049 021450 011423 000000 MOV (R4),(R3)+ ;
2050 021454 005303 65S: DEC R1 ;DEC HEADER COUNT
2051 021458 162737 000002 65S: SUB #2,SSINDX ;REMOVE ENTRY FROM SUBROUT STACK
2052 021464 012604 002424 MOV (SP)+,R4 ;RESTORE REGISTERS
2053 021466 012604 002424 MOV (SP)+,R3 ;
2054 021472 012603 002440 MOV (SP)+,R3 ;
2055 021474 005737 002440 TST ERPSWI ;TEST IF ERROR RETURN
2056 021500 001403 002440 RFE 99S ;YES - SKIP
2057 021504 000207 002440 ADD ERPSWI,(SP) ;ADD IN ERROR RETURN
2058 021510 017616 000000 99S: MOV @ (SP),(SP) ;SET ERROR RETURN ADDRESS
2059 021514 000207 RTS PC ;
2060 ;
2061 ; REPORT OPERATION ROUTINE. PRINTS SUBROUTINE TRACE SEQUENCE AND
2062 ; OPERATION BEING PERFORMED PORTION OF ALL
    
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2294      021516 010446      RPTOP:  ERROP MESSAGES
2295      021520 005437      MOV      #0,-(SP)
2296      021524 001433      TST     SSINDEX      ;TEST SURROUTINE INDEX 0
2297      021524 001433      BFG     15            ;SKIP IF 0
2298      021526 012704      MOV     #2,R4        ;SET INDEXED TO FIRST ENTRY
2299      021532 010534      PRINTR #R4,#SEGMES  ;PRINT "SURROUTINE CALL SEQ"
2300      021536 012746      MOV     #R4,#SEGMS  ;PRINT "SURROUTINE CALL SEQ"
2301      021542 012746      MOV     #2,-(SP)
2302      021546 010600      MOV     SP,R0
2303      021552 104014      EMT     C&PNTB
2304      021556 062746      ADD     #6,SP
2305      021556 016446      PRINTR #R4,SUBSTK(R4) ;PRINT CALLING LOCATION
2306      021556 012746      MOV     SUBSTK(R4),-
2307      021556 012746      MOV     #2,-(SP)
2308      021572 010600      MOV     #2,-(SP)
2309      021572 104014      MOV     SP,R0
2310      021574 104014      EMT     C&PNTB
2311      021574 000006      ADD     #6,SP
2312      021602 062704      ADD     #2,R4
2313      021602 062704      CMP     R4,SSINDEX  ;RUMP INDEX
2314      021612 003761      BLE     3S           ;CHECK IF ALL PRINTED
2315      021614 012746      PRINTB #R4,ERHEAD,#TSLAB ;LOOP IF NOT ALL PRINTED YET
2316      021620 013746      MOV     #TSLAB,-(SP) ;PRINT ERROR HEADER
2317      021624 012746      MOV     #R4,-(SP)
2318      021624 012746      MOV     #3,-(SP)
2319      021634 010600      MOV     SP,R4
2320      021636 104014      EMT     C&PNTB
2321      021640 062704      BIT     #CFKOP,OPFLAG ;CLEAR SK & RD OR WRT FLAG
2322      021652 013701      L,CS,R1             ;GET COMMAND EXECUTED
2323      021656 042701      BIT     #177741,R1  ;STRIP ALL RUT FUNCTION CODE
2324      021662 012704      CME     #R1         ;TEST IF SEEK OPERATION
2325      021670 010070      RIS     #SEEKOP,OPFLAG ;NO - SKIP
2326      021676 022701      CMP     #2,R1       ;ELSE SET SEEK FLAG
2327      021702 001003      BNE     2S         ;TEST IF WRITE
2328      021704 012746      RIS     #RDWOP,OPFLAG ;NO - SKIP
2329      021712 022701      CMP     #4,R1       ;SET RD OR WRT FLAG
2330      021716 001003      BNE     20S        ;TEST IF READ
2331      021720 052737      RIS     #RDWOP,OPFLAG ;NO - SKIP
2332      021726 016146      PRINTR #R4,#OPER,OPMSG(R1) ;SET RD OR WRT FLAG
2333      021732 012746      MOV     #R4,-(SP)  ;PRINT OPERATION
2334      021736 012746      MOV     #R4,-(SP)
2335      021746 010600      MOV     SP,R4
2336      021750 104014      EMT     C&PNTB
2337      021752 062704      ADD     #6,SP
2338      021762 001007      BIT     #4,R4       ;CHECK IF GET STATUS
2339      021764 001007      BNE     4S         ;NO - SKIP
2340      021772 001403      BIT     #RSET,L,DA  ;TEST IF RESET INCLUDED
2341      022000 006436      BEQ     4S         ;NO - SKIP
2342      022000 006436      MOV     #R1,R1     ;SET TO PRINT WITH RESET
2343      022000 006436      BR      9S
    
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2344      022002 032737      4S:    BIT     #0,OPFLAG  ;TEST IF ANY OTHER OPERATION
2345      022010 001424      BEQ     5S         ;NO - SKIP
2346      022016 012701      MOV     OPFLAG,R4
2347      022022 032704      MOV     #0,R1       ;SET UP TO DETERMINE WHICH ONE
2348      022026 001003      BIT     #R1,R4      ;PRESET THE POINTER
2349      022026 001003      BNE     6S         ;CHECK THE BIT
2350      022032 006264      TFR     (R1)+      ;IF SET - SKIP
2351      022034 006772      ASP     R4         ;RUMP POINTER
2352      022036 016146      6S:    PRINTB #R4,OPMSG(R1)
2353      022042 012746      MOV     #R4,-(SP)
2354      022046 012746      MOV     #2,-(SP)
2355      022052 010600      MOV     SP,R0
2356      022056 062704      EMT     C&PNTB
2357      022062 032737      ADD     #6,SP
2358      022070 001415      BIT     #R4,OPFLAG ;TEST IF 40 HEADER OPERATION
2359      022074 001415      BEQ     8S         ;NO - SKIP
2360      022076 012701      PRINTR #R4,R1      ;ELSE PRINT IT
2361      022076 016146      9S:    MOV     #R4,OPMSG(R1)
2362      022102 012746      MOV     #R4,-(SP)
2363      022106 012746      MOV     #2,-(SP)
2364      022114 104014      MOV     SP,R4
2365      022116 062706      EMT     C&PNTB
2366      022122 000006      ADD     #6,SP
2367      022134 000434      BIT     #CFKOP,OPFLAG ;SKIP
2368      022138 001430      BEQ     10S        ;TEST IF SEEK
2369      022144 013746      PRINTR #R4,#FRMWD,OLDCYL,#DIFWD,DESDIF,#SGNWD,DESSGN,#HDWD,DESHD
2370      022150 013746      MOV     #R4,-(SP)
2371      022154 013746      MOV     #R4,-(SP)
2372      022158 013746      MOV     #R4,-(SP)
2373      022162 013746      MOV     #R4,-(SP)
2374      022166 013746      MOV     #R4,-(SP)
2375      022170 013746      MOV     #R4,-(SP)
2376      022174 013746      MOV     #R4,-(SP)
2377      022178 013746      MOV     #R4,-(SP)
2378      022182 013746      MOV     #R4,-(SP)
2379      022186 013746      MOV     #R4,-(SP)
2380      022190 013746      MOV     #R4,-(SP)
2381      022194 013746      MOV     #R4,-(SP)
2382      022198 013746      MOV     #R4,-(SP)
2383      022202 010600      MOV     SP,R0
2384      022206 104014      EMT     C&PNTB
2385      022210 032704      ADD     #6,SP
2386      022214 001424      BIT     #RDWOP,OPFLAG ;TEST IF READ OR WRITE SET
2387      022218 001424      BEQ     15S        ;NO - SKIP
2388      022222 013746      PRINTR #R4,#CWLWD,CURCYL,#HDWD,DESHD,#SECWD,DESSEC
2389      022226 013746      MOV     #R4,-(SP)
2390      022230 013746      MOV     #R4,-(SP)
2391      022234 013746      MOV     #R4,-(SP)
2392      022238 013746      MOV     #R4,-(SP)
2393      022242 013746      MOV     #R4,-(SP)
2394      022246 013746      MOV     #R4,-(SP)
2395      022250 013746      MOV     #R4,-(SP)
2396      022254 013746      MOV     #R4,-(SP)
2397      022258 013746      MOV     #R4,-(SP)
2398      022262 013746      MOV     #R4,-(SP)
2399      022266 013746      MOV     #R4,-(SP)
2400      022270 013746      MOV     #R4,-(SP)
2401      022274 013746      MOV     #R4,-(SP)
2402      022278 013746      MOV     #R4,-(SP)
2403      022282 010600      MOV     SP,R0
2404      022286 104014      EMT     C&PNTB
    
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2385 022746 010546 CLRPARM: MOV #P5, -(SP) ;STORE R5
2386 022750 012701 002504 MOV #R5SPARM,R1 ;GET ADDRESS OF RLOCK
2387 022754 013761 000005 2S: MOV #R5 ;SET COUNT
2388 022760 005021 000005 CLR (R1)+ ;CLEAR WORD
2389 022762 005305 DEC R5 ;DEC COUNT
2390 022764 001375 BNE #R5 ;LOOP UNTIL 0
2391 022766 013765 002504 MOV #R5SPARM,R1 ;RESET POINTER
2392 022770 013765 000005 MOV (SP)+,R5 ;RESTORE R5
2393 022774 000207 RTS PC
2394 ENDMOD
2395
2396 022776
    
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2398 022776 BGNMOD HRDWTST
2399 .SBTTL *TEST 1 BASIC INTERFACE (PART 1)
2400 RENTST ;TEST01
2401
2402 022776 TST PASNUM ;CHECK IF FIRST PASS T1:
2403 023002 005737 003062 BNE #R5 ;EXIT IF NO
2404 023004 005737 014656 TST #R5 ;CHECK IF MANUAL INTERVENTION
2405 023010 005737 000005 BFO #R5 ;NO - EXIT TEST
2406 023012 012737 006225 002434 2S: MOV #R5ST,ERHEAD ;LOAD ER HEAD
2407 023020 012746 007774 PPINTF #FMTOPI,#OPRI,#OPRIA,#RASADD,RLBAS,#DRVNAM,<B,RLDRV+1>
2408 (13) 023020 005046 CLR -(SP)
2409 (13) 023022 153712 002455 RLDV+1,(SP)
2410 (13) 023024 005633 002455 HDRNAM, -(SP)
2411 (11) 023032 013746 002450 MOV RLRAS, -(SP)
2412 (10) 023036 012746 005622 MOV #RASADD, -(SP)
2413 (9) 023040 012746 013416 MOV #OPRIA, -(SP)
2414 (8) 023042 012746 007774 MOV #OPRI, -(SP)
2415 (7) 023052 012746 012016 MOV #FMTOPI, -(SP)
2416 (5) 023056 012746 004007 MOV #7, -(SP)
2417 (3) 023060 010600 MOV SP, R0
2418 (4) 023064 104017 EMT CSPTF
2419 (4) 023066 052706 000020 ADD #20, SP
2420 023072 005037 004086 CLR #RUFF ;CLEAR FOR RESPONSE
2421 023076 GWNIL OPR002,ORUFF,1,NO
2422 (3) 023078 104043 EMT CSWAM
2423 (3) 023100 004040 RP 1000CS
2424 (4) 023102 004066 .WORD ORUFF
2425 (5) 023104 004120 .WORD TSCODE
2426 (5) 023110 004001 .WORD OPR002
2427 (3) 023112 .WORD I
2428 023112 005737 004066 1000CS: TST ORUFF ;TEST RESPONSE YES
2429 023114 004737 001740 BFO #R5 ;YES - SKIP
2430 023120 004737 016446 1S: JSR PC, TSTINT ;INITIALIZE TEST
2431 023124 004737 016500 JSR PC, CSTATC ;GO GET STATUS (NO RESET)
2432 023130 032737 000040 002474 BTT #R5 ;CHECK IF COVER OPEN SET
2433 023140 001005 BNE #R5 ;YES - SKIP
2434 023142 012703 011072 MOV #NCOSTA, R3 ;SET NAME POINTER
2435 (3) 023146 104443 ERRHRD 102,ERR3
2436 (5) 023150 000145 TRAP TSCODE
2437 (5) 023152 013154 .WORD ICI
2438 023154 032737 000010 002474 7S: .WORD ERR3
2439 023162 001005 BIT #RHSTAT, T.MP ;TEST IF BRUSHES HOME
2440 023164 013703 011105 BNE #R5 ;YES - SKIP
2441 023170 104443 MOV #RHSTA, R3 ;SET POINTER FOR BRUSH HOME ERROR
2442 (3) 023172 001146 ERRHRD 102,ERR3
2443 (5) 023174 013154 TRAP TSCODE
2444 (5) 023176 032737 .WORD ERR3
2445 023204 001005 BIT #WLSTAT, T.MP ;TEST IF WRITE LOCK SET
2446 023206 012703 011120 BNE #R5 ;YES - SKIP
2447 023210 001005 MOV #WLSTA, R3 ;SET NAME POINTER
2448 023212 104443 ERRHRD 103,ERR3
2449 (3) 023212 TRAP TSCODE
    
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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 16:20 PAGE 2-1
CZRLCB.P11 23-OCT-78 14:39 *TEST 1          BASIC INTERFACE (PART 1)          SEQ 0081

(5) 023214 000147      .WORD 103
(5) 023226 013154      .WORD ERR3
2437 023220 085737      002502      11S:      T5STAT          ;TEST IF STATE ZERO
4438 023224 081737          BEQ          ;YES - SKIP
2439 023226 005003          CLR          ;SET STATE EXPECTED
2430 023230          ERRHRD 104,ERR7
(5) 023230          TRAP 202,ERR2
(5) 023234          .WORD 202
2431 023236 014920          .WORD ERR7
2432 023236 004737      016464      15S:      JSP          ;DO DRIVE RESET
2433 023238 023244          .WORD 65S
4434 023244          .WORD 65S
(3) 023244          ENDST
2435 023244          L10020:      EMT          CSETST

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 16:20 PAGE 2-2
CZRLCB.P11 23-OCT-78 14:39 *TEST 2          BASIC INTERFACE (PART 2)          SEQ 0082

2437          .SBTTL *TEST 2          BASIC INTERFACE (PART 2)
2438          BGNST          ;TEST 2
(3) 023246          T2:
2440 023246 005737      003062      TST          PASNUM          ;TEST IF PASS 0
2441 023254 081075          BNE          ;NO - SKIP
2442 023254 065737      014656      TST          WISWIM          ;TEST IF MANUAL INTERVENTION
2443 023260 100072          RPL          65S          ;NO - SKIP
2444 023262 012737      006225      002434      2S:      PRINTF          ;SET ERROR HEADER
2445 023270          MOV          #M1STAT,ERHEAD ;SET ERROR HEADER
(3) 023270          CLR          -(SP)          ;REQUEST CLOSE
(3) 023272 153716      002455      BTSP          RLDV+1,(SP)
(17) 023276 012746      005633      MOV          #DRVNAM,(SP)
(10) 023306 012746      005622      MOV          #R1AS,(SP)
(9) 023312 012746      010416      MOV          #RASADD,(SP)
(8) 023316 012746      010052      MOV          #OPPIA,(SP)
(7) 023316 012746      012516      MOV          #FTOP1,(SP)
(6) 023326 012746      006007      MOV          #7,(SP)
(3) 023332 010600          MOV          SP,R0
(4) 023334 104317          EMT          CSPTF
(4) 023336 062706      000020      ADD          #20,SP
2446 023342 005037      004066      CLP          ORUFF          ;COVER AND RESET WRITE LOCK
2447 023346          GMANIL          OPR002,ORUFF,1,NO ;CLEAR FOR RESPONSE
2448 023346          EMT          CSWIM
(3) 023350          RP          10000S
(4) 023352 004066          .WORD          ORUFF
(5) 023354 007724          .WORD          TSCODE
(5) 023356 000061          .WORD          OPR002
(3) 023356          .WORD          I
2449 023362 005737      004366      10000S:      TST          ORUFF          ;TEST IF RESPONSE YES
2450 023362 001740          BNE          ;NO - SKIP
2451 023370 004737      016445      JSR          PC,TSTINT          ;INITIALIZE TEST
2452 023374 004737      016464      JSR          PC,GSTATR          ;GET STATUS WITH RESET
2453 023400          65S
2454 023400          BEQ          #COSTAT,T.MP          ;TEST IF COVER OPEN RESET
2455 023410 001405          .WORD          65S          ;YES - SKIP
2456 023412 012703      011072      MOV          #COSTA,R3          ;SET NAME MESSAGE POINTER
2457 023416          ERRHRD 201,ERR2
(5) 023420          TRAP 202,ERR2
(5) 023420          .WORD 202
(5) 023422 013105          .WORD          ERR2
2458 023424 032737      020000      002474      9S:      BIT          #M1STAT,T.MP          ;TEST IF WRITE LOCK RESET
2459 023424 001405          BEQ          ;YES - SKIP
2460 023424 012703      011120      MOV          #M1STA,R3          ;SET NAME MESSAGE POINTER
2461 023440          ERRHRD 202,ERR2
(3) 023440          TRAP          TSCODE
(5) 023444          .WORD          202
2462 023446          .WORD          ERR2
2463 023446          .WORD          65S
2464 023446          ENDST
(3) 023446          L10021:      EMT          CSETST

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2465 023450          SRTTL          *TEST 3          HEAD LOADING
2466 (3) 023450          BGNTEST          ;TEST03
2467 023450          TST          PASNUM          ;TEST IF PASS 0          T3::
2468 023450          TST          4S          ;NO SKIP
2469 023450          TST          4MSWIW          ;TEST IF MANUAL INTERVENTION
2470 023450          RMI          5S          ;YES - SKIP
2471 023462          104332          005737          003062          4S:
2472 023464          001274          004737          016446          EXIT          CSEXIT
2473 023470          004737          004737          016446          .WORD          L10022-
2474 023474          004737          016464          JSP          PC,TSTAT          ;INITIALIZE TEST
2475 023500          005737          002502          JSP          PC,GSTAT          ;GET STATUS
2476 023506          001440          TSTAT          ;TEST IF STATE ZERO
2477 023510          023510          005346          REQ          2S          ;YES - SKIP
2478 023512          023512          153716          PPINTF          #FMTOPI,#OPR5,#OPRIA,#BASADD,RLRAS,#DRVNAM,CR,RLDRV*1) ;REQUEST DRIVE BE
2479 023516          012746          005633          CLR          -(SP)
2480 023522          013746          002455          BISR          RLDV*1,(SP)
2481 023526          012746          005633          MOV          #DRVNAM,-(SP)
2482 023532          013746          002450          MOV          RLAS,-(SP)
2483 023536          012746          010416          MOV          #BASADD,-(SP)
2484 023542          012746          010120          MOV          #OPRIA,-(SP)
2485 023546          012746          012016          MOV          #OPR1,-(SP)
2486 023552          010600          000020          MOV          SP,PO
2487 023554          104017          000020          EMT          CSEMT
2488 023556          062706          004066          ADD          #2,(SP)
2489 023562          905037          004066          GMANIL          OPR002,OBUFF,1,NO ;CLEAR FOR RESPONSE
2490 023566          104043          000020          EMT          CSEMAN
2491 023570          000404          100005          BR          100005
2492 023574          000120          .WORD          OBUF
2493 023576          007724          .WORD          TSCODE
2494 023600          000001          .WORD          OPR002
2495 023602          000001          .WORD          1
2496 023606          005737          004066          10000S: TST          ORUFF          ;TEST IF RESPONSE YES
2497 023610          001740          004066          REQ          1S          ;NO - SKIP
2498 023612          005046          004066          PPINTF          #FMTOPI,#OPR3,#OPRIA,#BASADD,RLRAS,#DRVNAM,CR,RLDRV*1)
2499 023616          012746          005633          CLR          -(SP)
2500 023622          013746          002450          BISR          RLDV*1,(SP)
2501 023626          012746          010416          MOV          #DRVNAM,-(SP)
2502 023632          012746          010120          MOV          RLAS,-(SP)
2503 023636          012746          012016          MOV          #BASADD,-(SP)
2504 023642          012746          000007          MOV          #OPRIA,-(SP)
2505 023646          010600          000020          MOV          #OPR1,-(SP)
2506 023652          104017          000020          MOV          SP,PO
2507 023654          062706          000020          EMT          CSEMT
2508 023656          062706          000020          ADD          #2,(SP)
2509 023662          012703          000001          MOV          #CYCLEUP,OPFLAG ;SET CYCLE UP FLAG
2510 023664          012703          000001          MOV          #1,R3 ;SET EXPECTED STATE VALUE
2511 023666          012737          002434          MOV          #NSTACHG,ERHEAD ;SET ERROR HEADER
2512 023670          012701          000454          MOV          #3CC,.R1 ;SET WAIT COUNT FOR 30 SECONDS
    
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2488 023706          004737          016500          3S: JSR          FC,GSTAT          ;GET STATUS
2489 023712          024702          002502          T35S          TSTAT          ;TEST IF STATE IS STILL 0
2490 023720          001020          002502          RNE          10S          ;NO - SKIP
2491 023722          005301          002502          DEC          R1          ;DEC WAIT COUNT
2492 023724          001404          002502          BEQ          6S          ;EXIT IF WAIT DONE
2493 023726          012700          000001          WAITMS          MOV          #1,R0
2494 023732          104026          000001          EMT          CSEMT
2495 023734          000764          004066          BP          3S          ;CLEAR FOR RESPONSE
2496 023736          005937          004066          GMANIL          OPR003,OBUFF,1,NO
2497 023742          104043          000020          EMT          CSEMAN
2498 023744          000404          100005          BR          100005
2499 023746          000120          .WORD          OBUF
2500 023750          000120          .WORD          TSCODE
2501 023752          007751          .WORD          OPR003
2502 023754          000001          .WORD          1
2503 023756          005737          004066          10001S: TST          ORUFF          ;TEST IF RESPONSE YES
2504 023762          001024          004066          RNE          11S          ;YES - REPORT
2505 023764          000651          002502          BR          1S          ;CHECK IF NOW STATE 1
2506 023766          024702          002502          CMP          13S,TSTAT          ;YES - SKIP
2507 023772          001405          002502          BEQ          11S          ;YES - SKIP
2508 023774          104443          004443          ERPHRD          303,ERR7
2509 023776          000455          004443          TRAP          TSEPCODE
2510 023778          000455          014220          .WORD          301
2511 023780          014220          .WORD          ERE7
2512 023782          104332          000455          EXIT          TST
2513 023784          000455          000455          EMT          CSEXIT
2514 023786          000636          000454          .WORD          L10022-
2515 023788          005301          004066          DEC          R1          ;SET WAIT FOR 30 SECONDS
2516 023790          001404          000002          BEQ          #300,.R1          ;SET EXPECTED STATE VALUE
2517 023792          024702          016500          JSP          PC,GSTAT          ;GET STATUS
2518 023794          020337          002502          CWP          R3,TSTAT          ;CHECK IF STATE 2
2519 023796          001435          002502          BEQ          20S          ;YES - SKIP
2520 023798          101005          002502          BHI          17S          ;CHECK IF NO CHANGE - YES - SKIP
2521 023800          104443          004443          ERPHRD          303,ERR7
2522 023802          000455          004443          TRAP          TSEPCODE
2523 023804          000455          014220          .WORD          302
2524 023806          014220          .WORD          ERE7
2525 023808          104332          000636          EXIT          TST
2526 023810          000636          000636          .WORD          L10022-
2527 023812          005301          004066          DEC          R1          ;DEC WAIT COUNT
2528 023814          001404          004066          BEQ          #1,R0          ;SKIP IF 0
2529 023816          012700          000001          WAITMS          MOV          #1,R0
2530 023818          104026          000002          EMT          CSEMT
2531 023820          000764          000764          BP          14S          ;SET EXPECTED STATE VALUE
2532 023822          024702          016500          JSP          PC,GSTAT          ;GET STATUS
2533 023824          020337          002502          CWP          R3,TSTAT          ;CHECK IF STATE 2
2534 023826          001435          002502          BEQ          20S          ;YES - SKIP
2535 023828          101005          002502          BHI          17S          ;CHECK IF NO CHANGE - YES - SKIP
2536 023830          104443          004443          ERPHRD          303,ERR7
2537 023832          000455          004443          TRAP          TSEPCODE
2538 023834          000455          014220          .WORD          303
2539 023836          014220          .WORD          ERE7
2540 023838          024702          002502          BIT          #SPDSTAT,T.MP          ;TEST IF SPINDLE TIMEOUT
    
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2520 024076 001010 BNE 19$ ;YES - SKIP
2521 024100 012737 MOV #SPERR,ERHEAD ;SET ERROR HEADER
2522 024106 012703 MOV #MSPERR,R3 ;SET NAME MESSAGE POINTER
2523 024112 104443 TRAP #ERR3 ;SERCODE
2524 024114 000460 .WORD 304
2525 024116 013154 .WORD ERR3
2526 024120 104032 EXIT CSEXIT
2527 024122 000566 .WORD L10022-
2528 024124 012737 MOV #M1STAT,ERHEAD ;SET ERROR HEADER
2529 024126 011672 MOV #STATE2,R4 ;SET CONDITION MESSAGE POINTER
2530 024128 011105 MOV #MHSTAT,R3 ;SET NAME MESSAGE POINTER
2531 024130 032737 BIT #RHSTAT,T.MP ;TEST IF BRUSH HOME STILL SET
2532 024132 000010 BNE 22$ ;YES - SKIP
2533 024134 104443 TRAP #ERR5 ;SERCODE
2534 024136 000461 .WORD 305
2535 024138 013272 .WORD ERR5
2536 024140 104032 EXIT CSEXIT
2537 024142 000566 .WORD L10022-
2538 024144 012761 MOV #M1STAT,ERHEAD ;SET WAIT COUNT FOR 5 SECONDS
2539 024146 004737 JSP PC,GSTATC ;GET STATUS
2540 024148 016500 J36$
2541 024150 000010 BIT #PHSTAT,T.MP ;TEST IF BRUSH HOME RESET
2542 024152 000143 BNE 27$ ;YES - SKIP
2543 024154 005301 DEC #1 ;DEC WAIT COUNT
2544 024156 001404 BEQ 26$ ;SKIP IF ZERO
2545 024158 000001 WAITMS #1
2546 024160 MOV #1,R0
2547 024162 EMT CSEMT
2548 024164 104032 TRAP #ERR4 ;SERCODE
2549 024166 000462 .WORD 306
2550 024168 013222 .WORD ERR4
2551 024170 104032 EXIT CSEXIT
2552 024172 000460 .WORD L10022-
2553 024174 004737 JSP PC,GSTATC ;SET WAIT COUNT 30 SECONDS
2554 024176 000010 J36$ ;GET STATUS
2555 024178 032737 BIT #RHSTAT,T.MP ;TEST IF BRUSH HOME SET AGAIN
2556 024180 000010 BNE 28$ ;YES - SKIP
2557 024182 005301 DEC #1 ;ELSE DEC WAIT COUNT
2558 024184 001404 BEQ 27$ ;SKIP IF 0
2559 024186 000001 WAITMS #1
2560 024188 MOV #1,R0
2561 024190 EMT CSEMT
2562 024192 TRAP #ERR5 ;SERCODE
2563 024194 104443 .WORD 307
2564 024196 000463 .WORD ERR5
2565 024198 013272 .WORD ERR5
2566 024200 EXIT TST
    
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(3) 024300 104032 EMT CSEXIT
(3) 024302 000460 .WORD L10022-
(3) 024304 012737 MOV #NSTACHG,ERHEAD ;SET ERROR HEADER
(3) 024306 000003 MOV #R3,R3 ;SET EXPECTED STATE VALUE
(3) 024308 004737 JSP PC,GSTATC ;GET STATUS
(3) 024310 024702 T36$S
(3) 024312 024337 CMP #1,T.STAT ;CHECK IF STATE 3
(3) 024314 001405 BEQ 32$ ;YES - SKIP
(3) 024316 104443 TRAP #ERR7 ;SERCODE
(3) 024318 000464 .WORD 308
(3) 024320 014220 .WORD ERR7
(3) 024322 104032 EXIT TST
(3) 024324 000340 .WORD L10022-
(3) 024326 012737 MOV #M1STAT,ERHEAD ;SET ERROR HEADER
(3) 024328 011762 MOV #STATE3,R4 ;SET CONDITION MESSAGE POINTER
(3) 024330 011131 MOV #MHSTAT,R3 ;SET NAME MESSAGE POINTER
(3) 024332 004737 JSP PC,GSTATC ;GET STATUS
(3) 024334 032737 T36$S
(3) 024336 000020 BIT #HOSTAT,T.MP ;TEST IF HEADS OUT SET
(3) 024338 001005 BNE 38$ ;YES - SKIP
(3) 024340 104443 TRAP #ERR5 ;SERCODE
(3) 024342 000465 .WORD 309
(3) 024344 013272 .WORD ERR5
(3) 024346 104032 EXIT TST
(3) 024348 000340 .WORD L10022-
(3) 024350 012737 MOV #M1STAT,ERHEAD ;SET ERROR HEADER
(3) 024352 011762 MOV #STATE3,R4 ;SET CONDITION MESSAGE POINTER
(3) 024354 011131 MOV #MHSTAT,R3 ;SET NAME MESSAGE POINTER
(3) 024356 004737 JSP PC,GSTATC ;GET STATUS
(3) 024358 032737 T36$S
(3) 024360 000020 BIT #HOSTAT,T.MP ;TEST IF HEADS OUT SET
(3) 024362 001005 BNE 38$ ;YES - SKIP
(3) 024364 104443 TRAP #ERR5 ;SERCODE
(3) 024366 000466 .WORD 310
(3) 024368 013272 .WORD ERR5
(3) 024370 104032 EXIT CSEXIT
(3) 024372 000340 .WORD L10022-
(3) 024374 032737 BIT #DRVERR,T.CS ;TEST IF DRIVE ERROR SET
(3) 024376 001007 BNE 40$ ;YES - SKIP
(3) 024378 012703 MOV #MVOLCK,R3 ;SET NAME MESSAGE POINTER
(3) 024380 104443 TRAP #ERR5 ;SERCODE
(3) 024382 000467 .WORD 311
(3) 024384 013272 .WORD ERR5
(3) 024386 104032 EXIT CSEXIT
(3) 024388 000340 .WORD L10022-
(3) 024390 012761 MOV #M1STAT,ERHEAD ;SET WAIT COUNT FOR 300 MS
(3) 024392 006250 MOV #NSTACHG,ERHEAD ;SET ERROR HEADER
(3) 024394 000004 MOV #R3,R3 ;SET EXPECTED STATE VALUE
(3) 024396 016500 JSP PC,GSTATC ;GET STATUS
(3) 024398 024737 T36$S
(3) 024400 CMP #4,R3 ;CHECK IF STATE 4
(3) 024402 BEQ 43$ ;YES - SKIP
    
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2588 024520 005301 DEC R1 ;DEC WAIT COUNT
2589 024524 0014C4 RFO #1 ;SKIP IF 0
2590 024524 000001 WAITUS #1,RO
(3) 024524 012700 MOV #1,RO
(3) 024530 104027 FMT CSMTU
2591 024534 00A764 BP #1,S
(3) 024534 104443 ERPHRD T,ERR7
(5) 024536 000470 TRAP T,ERRCODE
2593 024540 014220 .WORD #12
2594 024544 104032 EMT CSEXIT
(3) 024544 000136 .WORD L10022-
2595 024549 000454 MOV #3,R1 ;SET WAIT COUNT FOR 30 MS
2596 024556 000095 JSP #3,R1 ;SET EXPECTED STATE VALUE
2597 024556 004737 JSK PC,GSTATC ;GET STATUS
2598 024562 024700 T365S R3,T,STAT ;CHECK IF STATE 5
2599 024570 001413 REQ #5 ;YES - SKIP
2600 024572 005301 DEC R1 ;DEC WAIT COUNT
2601 024574 001404 RFO #1 ;ELSE SKIP
2602 024576 000001 WAITUS #1,RO
(3) 024602 104027 EMT CSMTU
2603 024604 00A764 BP #1,S
(3) 024606 104443 ERPHRD T,ERR7
(5) 024610 000471 TRAP T,ERRCODE
2605 024612 014220 .WORD #13
(3) 024614 104032 EMT CSEXIT
(3) 024616 000064 .WORD L10022-
2606 024620 000120 MOV #8,R1 ;SET WAIT FOR 8 MS
2607 024624 004737 JSK PC,GSTATC ;GET STATUS
2608 024632 T365S
2609 024632 000001 BIT #DRDVMASK,T,CS ;CHECK IF DRIVE READY
2610 024640 001404 BNF #2 ;YES - SKIP
2611 024644 009301 DEC R0 ;DEC COUNT
2612 024646 001404 RFO #0 ;SKIP IF 0
2613 024646 000001 WAITUS #1,RO
(3) 024654 104027 EMT CSMTU
2614 024654 00A764 BP #1,S
(3) 024656 012737 MOV #UISTST,EPHEAD ;SET ERROR HEADER
2615 024664 012700 MOV #STAT5,R4 ;SET CONDITION MESSAGE POINTER
2616 024670 010711 MOV #DRDV,6 ;SET NAME MESSAGE POINTER
2617 024674 104443 ERPHRD T,ERR7
(3) 024676 000470 TRAP T,ERRCODE
(5) 024700 013272 .WORD #14
2619 024702 62S:
2620 024702 T365S:
2621 024702 END73:
2622 024702 L10022:
(3) 024702 104001 EMT CSETST
    
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2624 024704 SBTTL *TEST 4 HEAD UNLOADING
2625 024704 BGNSTST *TEST 4 HEAD UNLOADING
(3) 024704 TST #0 ;TEST 0
2626 024710 005737 RNE #0 ;TEST IF FIRST PASS T4:
2627 024712 005737 TST #1 ;NO - SKIP
2628 024716 100402 BMI #0 ;TEST IF MANUAL INTERVENTION
2629 024720 104032 EXIT TST ;YES - SKIP
(3) 024722 000566 .WORD L10023-
2631 024724 10S:
2632 024724 BGNSUB
(3) 024724 104002 EMT C$BSUR T4.1:
2633 024726 012737 MOV #INSTACHG,ERHEAD ;SET ERROR HEADER
2634 024734 004737 JSP #3,R1 ;INITIALIZE TEST
2635 024738 016464 JSP PC,GSTATR ;GET STATUS
2636 024744 025400 T365S
2637 024746 000001 BIT #DRDVMASK,T,CS ;CHECK IF DRIVE READY
2638 024752 001040 BNF #3 ;YES - SKIP
2639 024756 005046 CLR #FMTOP1,#OPR6,#OPR1A,#BASADD,RLBAS,#DRVNM,<R,RLDRV+1>
(13) 024760 153716 BTS# RLDV+1,(SP)
(11) 024764 024746 MOV #DRVNM4,-(SP)
(11) 024768 024746 MOV #RLBAS,-(SP)
(10) 024774 012746 MOV #BASADD,-(SP)
(9) 025000 012746 MOV #OPR1A,-(SP)
(7) 025004 012746 MOV #OPR6,-(SP)
(6) 025014 012746 MOV #FMTOP1,-(SP)
(3) 025020 010600 MOV #7,-(SP)
(4) 025024 103717 EMT CS$BNTF
(4) 025028 103717 ADD #20,SP
2640 025030 005037 CLR #DBUFF ;CLEAR PCR RESPONSE
2641 025034 104043 EMT G$MANIL
(3) 025038 00A404 EMT G$CMAN
(4) 025040 004066 .WORD #0005
(5) 025042 000120 .WORD #0002
(5) 025044 000001 .WORD #OPR02
(3) 025050 1000G$:
2642 025050 005737 TST #DBUFF ;TST RESPONSE YES
2643 025054 001740 REQ #1 ;NO - SKIP
2644 025056 052737 3S:
2645 025064 000010 4S:
(13) 025064 005737 RTS #ULOAD,OPFLAG ;SET UNLOAD OPERATION
(12) 025068 035746 CLR #FMTOP1,#OPR3,#OPR1A,#BASADD,RLBAS,#DRVNM,<R,RLDRV+1>
(12) 025072 012746 BTS# RLDV+1,(SP)
(11) 025076 012746 MOV #DRVNM4,-(SP)
(11) 025080 012746 MOV #RLBAS,-(SP)
(10) 025084 012746 MOV #BASADD,-(SP)
(9) 025100 012746 MOV #OPR1A,-(SP)
(8) 025112 012746 MOV #OPR6,-(SP)
(7) 025116 012746 MOV #FMTOP1,-(SP)
(6) 025122 012746 MOV #7,-(SP)
    
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(3) 025126 010600 MOV SP,RO
(3) 025130 024377 EMT CSETSP
(3) 025134 000020 ADD #2,SP
2647 025136 012703 MOV #R3
2648 025142 000144 MOV #100,R4
2649 025146 011704 MOV #100,R1
2650 025150 018500 JSP PC,GSFATC
2651 025156 025400 5S: T465S
2652 025160 020337 R3,T,STAT
2653 025164 011939 CMP #5,T,STAT
2654 025174 000005 002502 BNE #5,T,STAT
2655 025176 005304 8S: DEC R4
2656 025180 001004 BNE #01
2657 025184 005301 DEC #1
2658 025188 014044 BEQ #100,R4
2659 025192 012704 000144 WAITUS #1
2660 025196 012700 MOV #1,RO
2661 025200 104027 EMT CSETU
2662 025204 000754 BR #5S
2663 025208 005037 7S: CLP OBUFF,CLEAR FOR RESPONSE
2664 025212 025326 GWMNIL OPR003,OBUFF,1,NO
2665 025216 104043 EMT CSGMAN
2666 025220 000404 BR #10001S
2667 025224 000120 .WORD OBUFF
2668 025228 000120 .WORD TSCODE
2669 025232 007751 .WORD OPR003
2670 025236 000001 .WORD 1
2671 025240 000001 10001S:
2672 025244 005737 004066 TST OBUFF
2673 025248 001706 BEQ #4S
2674 025252 025250 9S: ERPHRD 402,ERR7
2675 025256 104443 TPAP TSPRCODE
2676 025260 000521 .WORD 402
2677 025264 014220 .WORD ERR7
2678 025268 104032 EXIT SUB
2679 025272 000120 EMT CSEXIT
2680 025276 012703 000007 MOV #7,R3
2681 025280 012701 005670 MOV #3000,R1
2682 025284 016500 12S: JSP PC,GSSTAT
2683 025288 025400 T465S
2684 025292 020337 R3,T,STAT
2685 025296 001413 BEQ #100,R3
2686 025300 001404 BEQ #100,R1
2687 025304 000001 WAITUS #1
2688 025308 012700 MOV #1,RO
2689 025312 000764 EMT CSETU
2690 025316 000764 BR #12
2691 025320 104443 16S: ERPHRD 402,ERR7
2692 025324 000926 TRAP TSPRCODE
2693 025328 014220 .WORD 402
2694 025332 014220 .WORD ERR7
2695 025336 014220 EXIT SUR
    
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(3) 025330 104032 EMT CSEXIT
(3) 025334 000954 .WORD L10024-
2696 025338 005003 18S: CLR R3
2697 025342 012703 MOV #600,R1
2698 025346 016500 20S: JSP PC,GSSTAT
2699 025350 025400 T465S
2700 025354 005737 002502 TST T,STAT
2701 025358 001311 BEQ #1
2702 025362 001404 BEQ #2S
2703 025366 012700 WAITUS #1
2704 025370 000001 MOV #1,RO
2705 025374 000764 EMT CSETU
2706 025378 000764 BR #20S
2707 025382 104443 22S: ERPHRD 402,ERR7
2708 025386 000433 TPAP TSPRCODE
2709 025390 014220 .WORD 402
2710 025394 025400 24S: .WORD ERR7
2711 025398 012737 000002 002440 24S: MOV #2,FRPSWI
2712 025402 025406 ENDSUP
2713 025406 104003 L10024:
2714 025410 005046 26S: EMT CSESR
2715 025414 153716 BPRINT #PRINTP1,#OPR6,#OPR1A,#BASADD,RLBAS,#DRVNAM,CB,RLDRV+1
2716 025418 002455 CLP -(SP)
2717 025422 002455 BISR RLDV+1,(SP)
2718 025426 002455 MOV #DRVNAM-(SP)
2719 025430 002455 MOV #RLBAS-(SP)
2720 025434 005022 MOV #BASADD-(SP)
2721 025438 010416 MOV #OPR1A-(SP)
2722 025442 017162 MOV #OPR6-(SP)
2723 025446 000007 MOV #PRINTP1-(SP)
2724 025450 010600 MOV #7,-(SP)
2725 025454 010600 SP,RO
2726 025458 000020 EMT CSETSP
2727 025462 004066 26S: CLP OBUFF,CLEAR FOR RESPONSE
2728 025466 104043 GWMNIL OPR002,OBUFF,1,NO
2729 025470 000404 BR #1000S
2730 025474 000120 .WORD OBUFF
2731 025478 000120 .WORD TSCODE
2732 025482 007751 .WORD OPR002
2733 025486 000001 .WORD 1
2734 025490 005737 004066 10000S: TST OBUFF
2735 025494 001740 BEQ #26S
2736 025498 025510 ENDTST
2737 025504 104001 L10023: EMT CSETST
    
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2705 .SBTTL *TEST 5 DRIVE SELECT
2706 BGWSTST ;TEST05
2707 025512 012737 000002 002440 MOV #2,ERRSWI ;SET FOR NO ERROR RETURN T5:1
2708 025520 005737 003062 002440 TST PASNUM ;TEST IF FIRST PASS
2709 025522 001173 000000 002440 BNE #0 ;NO - SKIP
2710 025534 001567 000004 014656 BIT #DPRSEL,MISWIW ;TEST IF SELECT TESTS
2711 025536 001567 000000 014656 BEQ EXT05 ;NO - SKIP
2712 025536 005046 000000 014656 PRINTF #FMTOP1,#OPR7,#OPR1A,#BASADD,RLBAS,#DRVNM,<B,RLDRV+1>;REQUEST REMOVE A
2713 025540 153716 002455 CLR RDRV+1,(SP)
2714 025544 012746 005633 BSR RDRVNM,-(SP)
2715 025550 013746 002450 MOV RLBAS,-(SP)
2716 025554 012746 005627 MOV #BASADD,-(SP)
2717 025560 012746 010416 MOV #OPR1A,-(SP)
2718 025564 012746 010215 MOV #OPR7,-(SP)
2719 025570 012746 012016 MOV #FMTOP1,-(SP)
2720 025574 012746 000097 MOV #7,-(SP)
2721 025580 010600 000000 MOV SP,R0
2722 025602 104017 000000 FMT C$PNTF
2723 025604 062706 000020 ADD #20,SP
2724 025614 005037 004066 CLR ORUFF ;CLEAR FOR RESPONSE
2725 025614 104043 000000 GMANIL OPR002,OBUFF,1,NO
2726 025616 000404 000000 EMT C$GMAN
2727 025620 000404 000000 BR 10000S
2728 025624 000150 000000 .WORD ORUFF
2729 025624 000724 000000 .WORD T$CODE
2730 025626 000001 000000 .WORD OPR002
2731 025630 005737 004066 10000S: TST ORUFF ;TEST RESPONSE YES
2732 025634 001740 000000 BEQ SS ;NO - SKIP
2733 025636 012737 006362 002434 3S: MOV #TOSERR,ERHEAD ;SET ERROR HEADER MESSAGE
2734 025640 004737 018500 JSR PC,G$TATC ;INITIALIZE TEST
2735 025644 026036 000000 T$04S: PC,G$TATC ;DO SELECT AND GET STATUS
2736 025654 013737 002454 002540 MOV RDRV,TEMPO ;STORE ORIGINAL DRIVE NUMBR
2737 025656 012746 000000 MOV RDRV,R1 ;PUT IT IN R1
2738 025660 012746 000004 MOV #4,R4 ;SET COUNT FOR NUMBER OF PLUGS
2739 025674 062701 000400 LPT05: ADD #40,R1 ;BUMP TO NEXT DRIVE
2740 025680 022701 002000 CMP #2000,R1 ;CHECK IF TOO LARGE
2741 025684 005001 000000 BNE 4S ;NO - SKIP
2742 025706 010137 002454 4S: CLR R1 ;ELSE CLEAR TO DRIVE 0
2743 025710 012746 010432 MOV R1,RLDRV ;PUT IT BACK IN RLDRV
2744 025714 012746 010422 PRINTF #FMTOP3,#OPR8,<B,RLDRV+1>,#OPR1B,#UNDST
2745 025720 012746 010422 MOV #OPR1B,-(SP)
2746 025724 005046 002455 CLR RDRV+1,(SP)
2747 025726 153716 002455 BSR RDRV+1,(SP)
2748 025730 012746 012067 MOV #FMTOP3,-(SP)
2749 025736 012746 000005 MOV #5,-(SP)
2750 025746 010600 000000 MOV SP,R0
2751 025752 062706 000014 EMT C$PNTF
2752 025752 062706 000014 ADD #14,SP ;INSERT PLUG REQUEST
    
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2731 025756 005037 004066 CLR ORUFF ;CLEAR FOR RESPONSE
2732 025762 104043 000000 GMANIL OPR002,OBUFF,1,NO
2733 025764 000404 000000 EMT C$GMAN
2734 025766 000404 000000 BR 10001S
2735 025770 000120 000000 .WORD ORUFF
2736 025772 000724 000000 .WORD T$CODE
2737 025774 000001 000000 .WORD OPR002
2738 025776 000001 000000 .WORD 1
2739 025776 005737 004066 10001S: TST ORUFF ;TEST RESPONSE YES
2740 025780 001744 000000 BEQ SS ;NO - SKIP
2741 025804 005037 004066 BGNSUP TST ORUFF ;TEST RESPONSE YES
2742 025806 004737 016500 JSR PC,G$TATC ;GET STATUS - REPORT ANY ERROR T5.1:
2743 025810 012737 000002 002440 60S: MOV #2,ERRSWI ;INIT ERROR SWITCH
2744 025814 012737 000002 002440 60S: MOV #2,ERRSWI ;INIT ERROR SWITCH
2745 026022 005037 004066 ENDSUB L10026:
2746 026024 104003 000000 EMT C$ESUR
2747 026026 005304 000000 DEC R4 ;DEC COUNT
2748 026028 001337 002540 002454 BNE LPT05 ;LOOP IF NOT ZERO
2749 026030 013737 002540 002454 MOV #FMTOP1,RLDRV ;ELSE RESTORE RLDRV
2750 026036 012746 010263 4S: PRINTF #FMTP1,#OPR8,#OPR9
2751 026040 012746 010244 MOV #OPR9,-(SP)
2752 026044 012746 012110 MOV #OPR8,-(SP)
2753 026048 012746 000003 MOV #FMTP1,-(SP)
2754 026052 010600 000000 MOV #3,-(SP)
2755 026056 104017 000000 MOV SP,R0
2756 026060 083706 000010 EMT C$PNTF
2757 026064 005037 004066 CLR ORUFF ;CLEAR FOR RESPONSE
2758 026074 104043 000000 GMANIL OPR002,OBUFF,1,NO
2759 026078 000404 000000 EMT C$GMAN
2760 026082 000404 000000 BR 10000S
2761 026086 000120 000000 .WORD ORUFF
2762 026090 000724 000000 .WORD T$CODE
2763 026094 000001 000000 .WORD OPR002
2764 026098 000001 000000 .WORD 1
2765 026106 005737 004066 10000S: TST ORUFF ;TEST RESPONSE YES
2766 026110 001751 000000 BEQ 4S ;NO - SKIP
2767 026114 104001 000000 EXT05: EMT C$SETST
2768 026114 104001 000000 ENDTST L10025:
2769 026114 104001 000000 EMT C$SETST
    
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(5) 026624 013040      .WORD   ERR1
(6) 026626 104032      EXIT    C$EXIT
(7) 026628 000962      EMT    L10030-
28336 026632          12$: WAITMS #2,PC          ;WAIT FOR DSE TO SET
(8) 026634 012700 000002      MOV     #2,PC
(9) 026636 004737 016514      JSP    PC,GSTAT          ;GET STATUS
28338 026644 026704          ACS     #DSESTAT,T.MP    ;TEST IF DRIVE SELECT ERROR SET
28340 026646 032737 000400 002474      BIT    #DSEERR,R3        ;YES - SKIP
28342 026648 001077          BNE    #SET NAME MESSAGE POINTER
28344 026650 012703 011142      ERHRD  603,ERR3
(10) 026652 104443      TRAP   T$ERRCODE
(11) 026654 001132      .WORD  602
(12) 026656 013154      .WORD  ERR3
28346 026670          EXIT   SUB
(13) 026672 104032      EMT    C$EXIT
(14) 026674 000562          .WORD  L10030-
28348 026700 005304          DEC    R4
(15) 026702 001304          BNE    #LOAD IN DIFFERENT ADDRESS
(16) 026704 012737 000002 002440 60$: ENDSUB  R4
(17) 026706 012737          MOV    #2,ERRSWI        ;LOOP IF NOT ZERO
(18) 026708 010403          EMT    #INIT ERROR SWITCH
(19) 026710 104003          PRINTF C$ESUR #OPR11    ;REQUEST PLUG CHANGE
(20) 026712 012746 010346      MOV    #OPR11,(-SP)
(21) 026714 012746 012327      MOV    #PMT9,(-SP)
(22) 026716 012746 000002      MOV    #2,(-SP)
(23) 026718 104032          EMT    C$EXIT
(24) 026720 027066 000006      ADD    #6,SP
(25) 026722 005037 004966      CLP    O$BUFF,OBUFF,1,NO ;CLEAR FOR RESPONSE
(26) 026724 104043      CMANIL C$GMAN
(27) 026726 000404      BR    10000$
(28) 026728 004066      .WORD  ORUFF
(29) 026730 000730      .WORD  T$CODE
(30) 026732 000734      .WORD  O$P002
(31) 026734 000001      .WORD  1
28350 026736 005737 004066      10000$: TST    O$UFF          ;TEST RESPONSE YES
(32) 026738 001753          BEQ    15$              ;NO - SKIP
(33) 026740 104001      LCLEXT: EMT    C$ETST
(34) 026742 104001      ENDSUB:
(35) 026744 104001      L10027:

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2847 026770          SBTTL  *TEST 7          INITIAL STATE
(3) 026772          RGNTST ;TEST 07
2849 026774 012737 006347 002434      MOV    #INIT$ERHEAD    ;SET ERROR HEADER
(4) 026776 004737 016446      JSP    PC,T$INT         ;INITIALIZE TEST
2850 026778 012700 000012      WAITUS #1,PC          ;WAIT 1 MS
(5) 026780 104027          EMT    C$WTO
(6) 026782 004737 016500      JSP    PC,GSTAT        ;GET STATUS
2852 026784 027066 000001 002466      BSS    #DRDVM$Y,T.CS    ;CHECK IF DRIVE READY
(7) 026786 001003          BNE    #YES-SKIP
(8) 026788 012703 010711      MOV    #M$DRDY,R3      ;SET NAME MESSAGE POINTER
(9) 026790 000427          BR     #GO REPORT
(10) 026792 012703 000005      3$:   MOV    #5,R3          ;SET EXPECTED STATE VALUE
2854 026794 020337 002502      CMP    R3,T$STAT       ;CHECK IF STATE OK
(11) 026796 001405          BEQ    #YES-SKIP
(12) 026798 012704          ERHRD  701,ERR7       ;ELSE REPORT STATE ERROR
(13) 026800 001275      TRAP   T$ERRCODE
(14) 026802 014220      .WORD  701
(15) 026804 014220      .WORD  ERR7
2856 026806          EXIT   T$
(16) 026808 104032      EMT    C$EXIT
(17) 026810 000222          .WORD  L10031-
2858 026812 000156          MOV    #1,MP,R1        ;GET MP REG
(18) 026814 032701 000020      BIT    #HOSTAT,R1      ;CHECK HEADS OUT
(19) 026816 001003          BNE    #YES-SKIP
(20) 026818 012703 011131      MOV    #M$HSTA,R3      ;SET NAME MESSAGE PTR
(21) 026820 000405          BR     #GO REPORT
(22) 026822 001007          BNE    #CHECK BRUSH HOME SET
(23) 026824 001007          BNE    #YES-SKIP
(24) 026826 012703 011105      MOV    #M$HSTA,R3      ;SET NAME MESSAGE PTR
(25) 026828 104443      ERHRD  703,ERR3       ;REPORT ERROR
(26) 026830 001276      TRAP   T$ERRCODE
(27) 026832 013154      .WORD  703
(28) 026834 013154      .WORD  EPR3
2860 026836          EXIT   T$
(29) 026838 104032      EMT    C$EXIT
(30) 026840 000156          .WORD  L10031-
2862 026842 005737 014656      10$:  TST    #M$SWI          ;TEST IF MANUAL INTERVENTION RUN
(31) 026844 000034          BPL    #NO-SKIP
(32) 026846 005737 003062      TST    #M$NUM          ;CHECK IF FIRST PASS
(33) 026848 001031          BNE    #NO-SKIP
(34) 026850 032701 000100      BIT    #H$STAT,R1      ;ELSE CHECK HD 0 SELECTED
(35) 026852 001421          BEQ    #YES-SKIP
(36) 026854 012703 011044      MOV    #M$HSTA,R3      ;SET NAME MESSAGE PTR
(37) 026856 012704 011763      MOV    #C$CVLUP,R4     ;SET CONDITION POINTER
(38) 026858 000427          BR     #REPORT ERROR
(39) 026860 104443      ERHRD  703,ERR4
(40) 026862 001277      TRAP   T$ERRCODE
(41) 026864 013222      .WORD  703
(42) 026866 013222      .WORD  EPR4
2866 026868          EXIT   T$
(43) 026870 104032      EMT    C$EXIT
(44) 026872 000112          .WORD  L10031-
2868 026874 000100 001000      13$:  BIT    #V$STAT,R1      ;CHECK VOL CHECK SET

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2885 027174 001003 BNE 155 ;YES-SKIP
2886 027174 012703 MOV #VOLCK,R3 ;ELSE-SET NAME MESSAGE PTR
2887 027202 000733 BR 95 ;GO REPORT
2888 027202 032703 040000 002466 155: BIT #DRVERR,T.CS ;TEST DRIVE ERROR SET
2889 027202 012703 BNE 165 ;YES-SKIP
2890 027202 012703 MOV #DRERR,R3 ;ELSE-SET NAME MESSAGE PTR
2891 027202 000733 BR 95 ;GO REPORT
2892 027222 032703 020000 165: BIT #LSTAT,R1 ;CHECK WRITE LOCK STATUS
2893 027222 001405 BEQ 175 ;SKIP IF RESET
2894 027250 012703 011120 MOV #MLSTA,R3 ;ELSE-SET NAME MESSAGE PTR
2895 027234 104443 ERRHRD 705 ;ERR2
2896 027234 013106 TRAP TSERCODE ;ERR2
2897 027242 042701 021177 175: .WORD ERR2
2898 027242 005701 BIT #21177,R1 ;CLEAR STATUS EXCEPT FOR ERROR BITS
2899 027242 001405 BEQ 185 ;CHECK IF ANY ERROR SET
2900 027252 104443 ERRHRD 704 ;ERR6
2901 027252 001300 TRAP TSERCODE ;NO-SKIP
2902 027252 013342 .WORD ERR6 ;ELSE REPORT ALL ERRORS
2903 027260 104032 EXIT TST ;EXIT
2904 027264 000015 EMT CSXIT
2905 027264 000015 .WORD L10031- ;
2906 027274 042701 002466 195: MOV #CS,R1 ;GET CS REG
2907 027274 005701 BIC #141777,R1 ;CLEAR ALL BUT ERROR BITS
2908 027276 001365 TST P1 ;TEST IF ANY ERROR SET
2909 027300 BNE 185 ;YES-SKIP TO REPORT
2910 027300 45: ENDTST
2911 027300 65: L10031: EMT CSETST
2912 027300 104001 (3)
    
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2909 027302 .SMTL *TEST 8 INITIAL RESET STATE
2910 027302 BENTST ;TEST 8
2911 027302 (3)
2912 027310 012737 006347 002434 MOV #INITST,ERHEAD ;INITIALIZE TEST
2913 027310 004737 016446 JSR PC,TSTINT ;INITIALIZE TEST
2914 027314 004737 016464 JSR PC,GSTATR ;GET STATUS WITH RESET
2915 027320 005737 014656 655: TST #1 ;CHECK IF MAN INTERVENTION WAS RUN
2916 027320 100016 003062 RPL #PANSUM ;CHECK IF 1ST PASS
2917 027330 001010 000100 002474 BNE 45 ;NO-SKIP
2918 027330 001407 BEQ #SSTAT,T.MP ;CHECK HD SELECT STILL 0
2919 027334 012703 011044 MOV #MHSTA,R3 ;SET NAME MESSAGE PTR
2920 027334 012704 011763 MOV #CCVLOC,R4 ;SET CONDITION POINTER
2921 027356 104443 ERRHRD 801 ;ERR4 ;REPORT ERROR
2922 027356 001441 TRAP TSERCODE
2923 027364 013222 .WORD 801 ;ERR4
2924 027364 45: .WORD ERR4
2925 027364 65: ENDTST
2926 027364 104001 (3) L10032: EMT CSETST
2927 027364 (3)
    
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2935      0273666          .SBTTL          *TEST 9          DRIVE READY
          0273666          RGNTST          ;TEST 9
2936      0273666          012737          006375          002434          MOV          #T09ERR,ERHEAD          ;SET ERROR HEADER
2937      0273774          012701          002524          MOV          #NEWCVL,R1          ;GET POINTER TO DESIRED LOC
2938      027400          005021          CLR          (R1)+          ;CLEAR NEW CVL
2939      027402          005021          CLR          (R1)+          ;CLEAR CURRENT CVL
2940      027404          005021          CLR          (R1)+          ;DIFFERENCE
2941      027406          005011          CLR          (R1)          ;SIGN
2942      027410          004737          016446          JSR          PC,TSTINT          ;INITIALIZE TEST
2943      027414          004737          016464          JSR          PC,GSTATR          ;GET STATUS WITH RESET
2944      027420          027664          GSS
2945      027422          004737          JSR          PC,POSHSB          ;POSITION HEAD SELECTED BIT
2946      027426          010537          002530          MOV          RS,DESHD          ;STORE AS DESIRED HEAD
2947      027432          004737          017346          JSR          PC,SIMSEK          ;EXECUTE SIMPLE SEEK
2948      027436          027664          GSS
2949      027440          012703          010711          MOV          #MDDRV,R3          ;SET NAME MESSAGE PTR
2950      027444          012704          011722          MOV          #CDRDV,R4          ;SET CONDITION POINTER
2951      027450          004737          016514          JSR          PC,GSTAT          ;GET STATUS
2952      027454          027664          GSS
2953      027456          032737          002466          BIT          #DRVMASK,T.CS          ;TEST READY SET
2954      027464          001405          BEQ          #0,ERR4          ;NO-SKIP
2955      027466          027664          ERHRD          104443          TRAP          TSERCODE          ;REPORT READY ERROR
          027470          001605          .WORD          901
          027472          013222          .WORD          ERR4
2956      027474          104032          EXIT          TST          ;EXIT
          027476          000166          .WORD          CSEXT
          027500          012704          000121          MOV          #L10033-,          ;SET WAIT COUNT
2957      027504          004737          016514          JSR          PC,GSTAT          ;GET STATUS
2958      027510          027664          GSS
2959      027512          012703          000005          MOV          #5,R3          ;SET EXPECTED STATE VALUE
2960      027516          023703          002502          CMP          #1,STAT,R3          ;CHECK STATE IS 5
2961      027524          001405          BEQ          #0,ERR7          ;YES-SKIP
2962      027526          027664          ERHRD          902,ERR7          ;ELSE REPORT
          027530          001606          .WORD          902
          027532          014220          .WORD          ERR7
2963      027534          104032          EXIT          TST
          027536          000166          .WORD          CSEXT
          027540          012703          010711          MOV          #MDDRV,R3          ;CHECK READY SET
2964      027544          032737          000001          002466          7S:          BIT          #DRVMASK,T.CS          ;YES-SKIP
2965      027550          001013          BNE          L5          ;ELSE DEC WAIT COUNT
2966      027552          005301          DEC          R3          ;SKIP IF 0
2967      027556          001404          BEQ          #0,ERR5
2968      027562          012700          000001          MOV          #1,R0          ;REPORT READY ERROR
2969      027566          104027          EMT          CSETU
2970      027570          000747          BR          R0
2971      027574          104443          9S:          ERHRD          903,ERR5          ;REPORT READY ERROR
          027576          001607          TRAP          TSERCODE
          .WORD          903
    
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          027572          013272          .WORD          ERR5
2973      027574          104032          EXIT          TST
          027576          000066          .WORD          CSEXT
          027580          012703          000033          .WORD          L10033-
2974      027600          005737          002466          12S:          TST          T.CS          ;TEST IF ANY ERROR
2975      027604          100005          BEQ          #0,ERR6          ;NO-SKIP
2976      027606          027664          ERHRD          904,ERR6          ;REPORT READY ERROR
2977      027610          001610          TRAP          TSERCODE
          027612          013342          .WORD          904
          027614          001405          BEQ          #0,ERR6
2978      027616          104032          EXIT          TST
          027618          000946          .WORD          CSEXT
          027620          012703          010711          MOV          #MDDRV,R3          ;SET NAME MESSAGE PTR
2979      027624          004737          020530          JSR          PC,POSHSB          ;POSITION HEAD SELECT BIT FOR TEST
2980      027630          020537          002534          CMP          RS,DESHD          ;CHECK IF CORRECT HEAD SELECTED
2981      027634          001413          BEQ          #0,ERR2          ;YES-SKIP
2982      027636          009377          002534          MOV          R3,DESHD          ;ELSE TEST IF 1 DESIRED
2983      027642          001405          BEQ          #0,ERR2          ;NO-REPORT SB 1
2984      027644          104443          9S:          ERHRD          905,ERR3          ;REPORT READY ERROR
          027646          001611          TRAP          TSERCODE
          027648          023754          .WORD          905
          027652          000010          .WORD          ERR3
2986      027654          104032          EXIT          TST
          027656          000010          .WORD          CSEXT
          027660          001612          17S:          ERHRD          906,ERR2          ;REPORT READY ERROR
          027662          013106          TRAP          TSERCODE
          .WORD          906
          .WORD          ERR2
2988      027664          20S:          .WORD          903
2989      027664          65S:          .WORD          903
2990      027664          ENDTST          L10033:          EMT          CSETST
          027664          104001          .WORD          903
    
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3063
3064
3065      030210      BRTTL *TEST 11      HEAD ALIGNMENT SUPPORT
3066      030210      BGNSTST *TEST 11
3067      030210      032737 000010 014656      BIT      #HDLATGN,MISWIW }CHECK IF RUN HEAD ALIGNMENT T11.1:
3068      030210      001411      RFO      1S          }NO-EXIT
3069      030220      001008      RST      PASNUM     }TEST IF PASS 0
3070      030220      023737 002454 002432      BNE      1S          }NO-EXIT
3071      030230      001008      CMP      RLDV,HADONE }TEST IF HEAD ALIGN DONE THIS DRIVE
3072      030230      001008      BNE      2S          }NO-SKIP
3073      030242      104032      JMP      2115S      }GO CHECK WRITE LOCK
3074      030242      009374      EXIT     TST
3075      030242      013737 002454 002432 2S:      EMT      CSEXT
3076      030254      005046      WORD    L10036-1
3077      030254      012746      MOV     RLDV,HADONE }SET HEAD ALIGN DONE FLAG
3078      030254      005633      PRINTF #FMT5,#RASADD,RLBAS,#DRVNAM,<R,RLDRV+1>
3079      030254      013746      CLR     -(SP)
3080      030254      005633      BSR     #DRVNAM,-(SP)
3081      030254      013746      MOV     RLBAS,-(SP)
3082      030254      005633      MOV     #RASADD,-(SP)
3083      030254      005633      MOV     #FMT5,-(SP)
3084      030254      005633      MOV     #5,-(SP)
3085      030254      010600      MOV     SP,RO
3086      030254      104017      EMT     CSPTNF
3087      030254      062706      END     1S,SP
3088      030254      012746      PRINTF #FMT9,#HAMES1 }TYPE INSTRUCTIONS
3089      030254      012746      MOV     #HAMES1,-(SP)
3090      030254      012327      MOV     #FMT9,-(SP)
3091      030254      016600      MOV     #2,-(SP)
3092      030254      104017      MOV     SP,RO
3093      030254      062706      EMT     CSPTNF
3094      030254      012746      ADD     #6,SP
3095      030254      007652      PRINTF #FMT9,#HAMES2
3096      030254      012327      MOV     #HAMES2,-(SP)
3097      030254      016600      MOV     #2,-(SP)
3098      030254      104017      MOV     SP,RO
3099      030254      062706      EMT     CSPTNF
3100      030254      000006      ADD     #6,SP
3101      030254      104002      BGNSTUR
3102      030254      004737      EMT     CSBSUR T11.1:
3103      030254      005037      JSR     PC,TSTINT }INITIALIZE TEST
3104      030254      002456      CLR     DONE        }CLEAR DONE
3105      030254      000010      MOV     RLDV,L,CS   }SET UP FOR GET STATUS
3106      030254      000013      RIS     #GETSTAT,L,CS
3107      030254      000013      MOV     #GETSTAT,DRSET,L,DA
3108      030254      000004      MOV     L,DA,RLDA(R2)
3109      030254      000004      MOV     #0,RLCSR(R2) }DO GET STATUS
3110      030254      000006      WAITMS #50,RO      }WAIT FOR INTERRUPT
3111      030254      000006      MOV     #50,RO
3112      030254      104026      EMT     CSPTNF
3113      030254      002430      TST     DONE
3114      030254      001747      BEQ     JS          }CHECK IF DONE
3115      030254      001747      BEQ     JS          }NO-GO CLR CONTROLLER
    
```

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3089
3090
3091      030452      012737 000021 002462 10S:      MOV     #HSELIMBSETC,L,DA }LOAD FOR HEAD 1
3092      030452      002737 020000 002474      BIT     #WSTAT,T,MP   }CHECK IF WRITE LOCK SET
3093      030452      001003      BNE     12S          }YES-SKIP
3094      030470      042737 000020 002462      RIC     #HSEL,L,DA    }ELSE CLEAR TO HEAD 0
3095      030476      013737 002454 002456 12S:      MOV     RLDV,L,CS     }LOAD IN DRIVE NUMBER
3096      030504      052737 001006 002456      DIS     #SEEK,L,CS    }SET FOR SEEK
3097      030520      013762 002456 000004      MOV     L,CS,RLCSR(R2) }LOAD & EXECUTE SEEK
3098      030520      013762 002456 000000      MOV     L,CS,RLCSR(R2)
3099      030526      012700 000036      WAITMS #30,RO      }WAIT FOR INTERRUPT
3100      030526      104026      MOV     #30,RO
3101      030534      000715      EMT     CSPTNF
3102      030536      000715      BR      JS          }LOOP
3103      030536      104003      ENDSUB L10037:
3104      030540      000540      T11S:      EMT     CSESUB
3105      030540      000540      BGNSTUR
3106      030540      104002      EMT     CSBSUR T11.2:
3107      030542      004737      JSR     PC,TSTINT }INITIALIZE TEST
3108      030542      004737      JSR     PC,GSTATR  }CLEAR DRIVE
3109      030542      032939      RIS     #WSTAT,T,MP   }CHECK WRITE LOCK RESET
3110      030542      001425      BEQ     19S          }YES-SKIP
3111      030564      012746 010377 19S:      PRINTF #FMT9,#OPR12 }REQUEST WRITE LOCK RESET
3112      030570      012746 012327      MOV     #OPR12,-(SP)
3113      030574      012746 000002      MOV     #FMT9,-(SP)
3114      030600      106600      MOV     #2,-(SP)
3115      030604      062706      MOV     SP,RO
3116      030604      005037      ADD     #6,SP
3117      030614      104043      CLR     DBUFF        }CLEAR FOR RESPONSE
3118      030616      000404      GWMNIL OPR002,DBUFF,1,NO }GET RESPONSE
3119      030620      004066      EMT     CSWMAN
3120      030622      000120      .WORD  DBUFF
3121      030624      000120      .WORD  TSCODE
3122      030626      000001      .WORD  OPR002
3123      030630      005737 004066 10000S:      TST     DBUFF        }WAS ANSWER YES
3124      030630      001753      REQ     19S          }NO-REPEAT REQUEST
3125      030636      19S:
3126      030636      ENDSUB L10040:
3127      030636      104003      EMT     CSESUB
3128      030640      20S:
3129      030640      ENDSUB L10036:
3130      030640      104001      EMT     CSETST
    
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(12) 030642          SRTTL      *TEST 12      HEAD SWITCHING
(11) 030642          RGWTST      ;TEST 12
(10) 030642          012737 006425 002434  MOV      #T12ERR,ERHEAD ;SET ERROR HEADER          T12::
(9) 030659          015701 002524          MOV      #NEWCVL,R1 ;SET POINTER TO DESIRED LOCATION
(8) 030659          005031          CLR      (R1)+ ;CLEAR NEW CYLINDER
(7) 030659          005031          CLR      (R1)+ ;CLEAR CURRENT CYL-
(6) 030660          005031          CLR      (R1)+ ;CLEAR DIFFERENCE
(5) 030662          005031          CLR      (R1)+ ;CLEAR SIGN
(4) 030664          012721 000001          MOV      #1,(R1)+ ;SET FOR HEAD 1
(3) 030670          T124S:
(2) 030670          BCNS08
(1) 030670          104002          EMT      CSBSUB ;INITIALIZE TEST          T12.1:
(0) 030672          004737          JSR      PC,TSTINT ;GET STATUS WITH RESET
(0) 030675          004737          JSR      PC,GSTATR ;GET STATUS WITH RESET
(0) 030704          004737          JSR      PC,SIMSEK ;DO SEEK
(0) 030710          003136          GOS      #MORDV,R3 ;SET NAME MESSAGE PTR
(0) 030703          012703          MOV      #MORDV,R4 ;SET CONDITION POINTER
(0) 030704          004737          JSR      PC,GSTAT ;GET STATUS
(0) 030728          003136          GOS      #MORDVMSK,T.CS ;CHECK IF READY
(0) 030730          032737          BIT      #5 ;NO-SKIP
(0) 030736          001405          BEQ      ERR4 ;REPORT READY ERROR
(0) 030740          104443          TRAP    #5,ERR4
(0) 030742          002261          .WORD  1201 ;
(0) 030744          013222          .WORD  ERR4 ;
(0) 030746          104032          EMT      CSEXIT ;EXIT
(0) 030750          000188          .WORD  L10042-.
(0) 030752          012701          MOV      #81,R1 ;SET WAIT COUNT
(0) 030756          004737          JSR      PC,GSTAT ;GET STATUS
(0) 030762          003136          GOS      #5,R3 ;SET EXPECTED STATE VALUE
(0) 030764          027337          CMP      #3,T.STAT ;CHECK IF STATE IS 5
(0) 030774          001405          BEQ      #5,ERR7 ;YES-SKIP
(0) 030776          001405          BEQ      #5,ERR7 ;REPORT STATE ERROR
(0) 030776          104443          TRAP    #5,ERR7
(0) 030776          002261          .WORD  1202 ;
(0) 031000          014236          .WORD  ERR7 ;
(0) 031004          104032          EMT      CSEXIT ;EXIT
(0) 031006          004130          .WORD  L10042-.
(0) 031010          012703          MOV      #MORDV,R3 ;SET NAME MESSAGE PTR
(0) 031013          000001          BNE     #MORDVMSK,T.CS ;CHECK DRIVE READY
(0) 031014          005301          DEC     R1 ;YES-SKIP
(0) 031026          001404          BEQ     #0,ERR9 ;DEC WAIT COUNT
(0) 031030          012700          WAITUS #1,R0 ;SKIP IF 0
    
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(3) 031034          104027          EMT      CSWTU ;
(2) 031036          000747          BR      #6 ;
(1) 031040          104443          ERRHRD 1203,ERR5 ;REPORT READY ERROR
(0) 031042          002263          TRAP    #5,ERR5
(0) 031044          013272          .WORD  1203 ;
(0) 031046          104032          EMT      CSUB ;EXIT
(0) 031050          000686          .WORD  L10042-.
(0) 031052          005737          TST     T.CS ;TEST IF ANY ERROR
(0) 031056          100005          BPL     #5,ERR6 ;NO-SKIP
(0) 031058          005737          ERRHRD 1204,ERR6 ;REPORT ALL ERRORS
(0) 031060          104443          TRAP    #5,ERR6
(0) 031062          002264          .WORD  1204 ;
(0) 031064          013342          .WORD  ERR6 ;
(0) 031066          104032          EMT      CSUB ;EXIT
(0) 031070          000946          .WORD  L10042-.
(0) 031072          012703          MOV      #MST1,R3 ;SET NAME MESSAGE PTR
(0) 031074          004737          JSR      PC,POSISR ;POSITION HEAD SELECT BIT
(0) 031076          005705          CMP     #2,DESHD,R5 ;CHECK IF CORRECT HEAD SELECTED
(0) 031106          001413          BEQ     #2,ERR3 ;YES-SKIP
(0) 031110          005737          TST     #0,DESHD ;WAS HEAD 0 SELECTED
(0) 031114          001405          BEQ     #0,ERR3 ;YES-SKIP
(0) 031116          104443          ERRHRD 1205,ERR3 ;REPORT HEAD S8 1
(0) 031120          002265          TRAP    #5,ERR3
(0) 031122          013154          .WORD  1205 ;
(0) 031124          013154          .WORD  ERR3 ;
(0) 031126          104032          EMT      CSEXIT ;EXIT
(0) 031130          000010          .WORD  L10042-.
(0) 031132          104443          ERRHRD 1206,ERR2 ;ELSE REPORT HEAD S8 0
(0) 031134          002266          TRAP    #5,ERR2
(0) 031136          013106          .WORD  1206 ;
(0) 031138          013106          .WORD  ERR2 ;
(0) 031136          20S:
(0) 031136          ENDSUB
(0) 031136          L10042:
(0) 031140          104003          EMT      CSBSUB ;CHECK IF HD 0 WAS DONE
(0) 031142          005737          DESHD  #255 ;YES-SKIP
(0) 031144          001404          BEQ     #255,ERR10 ;YES-SKIP
(0) 031146          005737          CLR     DESHD ;ELSE SET TO HEAD 0
(0) 031148          001137          JMP     #0,ERR10 ;REDO TEST
(0) 031150          25S:
(0) 031150          ENDTST
(0) 031150          L10041:
(0) 031150          104001          EMT      CSETST
    
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3192
3193
3194
3195      031160      SRTTL *TEST 13      READ HEADER (PART 1)
3196      031160      RCNTST      ;TEST 13
3197      031160      012737      006437      002434      MOV      #T13ERR,ERHEAD      T13:
3198      031160      015701      002524      MOV      #NSWCYL,R1      ;SET ADDRESS OF DESTRED LOCATIONS
3199      031174      005021      CLP      (R1)+      ;CLEAR NEW CVL
3200      031174      005021      CLP      (R1)+      ;CLEAR CURRENT CVL
3201      031176      005021      CLP      (R1)+      ;CLEAR DIFF
3202      031190      005021      CLP      (R1)+      ;CLEAR SIGN
3203      031204      T134S:      CLP      (R1)+      ;CLEAR HEAD
3204      031204      RCNSUB:
3205      031204      104002      EMT      C$RSUR      T13.1:
3206      031206      004737      JSR      PC,T$TINT      ;GET STATUS ;INITIALIZE TEST
3207      031212      004737      JSR      PC,G$STATR      ;GET STATUS W/RESET
3208      031220      004957      JSR      PC,S$IMSEK      ;DO SEEK
3209      031224      001304      GOS      60S
3210      031226      012701      MOV      #R1,R1      ;SET WAIT COUNT
3211      031236      003130      JSR      PC,R$D$WAIT      ;WAIT FOR READY
3212      031240      004737      JSR      PC,XRDHDC      ;DO READ HEADER
3213      031244      011703      MOV      #MHSTA,R3      ;SET NAME MESSAGE PTR
3214      031252      004737      JSR      PC,POS$HW1      ;POSITION HS BIT IN HD WRD 1
3215      031256      020537      CMP      R5,D$ESHD      ;CHECK IF HEAD CORRECT
3216      031264      001410      ER$RHRD      1301,E$RR3      ;REPORT SR 1
3217      031266      104443      TRAP      T$ERRCODE
3218      031266      002425      .WORD      1301
3219      031270      013154      .WORD      ER3
3220      031272      104032      EMT      C$EXIT
3221      031274      000010      .WORD      1302,E$ERR2      ;REPORT SR 0
3222      031276      104443      TRAP      T$ERRCODE
3223      031300      002426      .WORD      1302
3224      031302      013166      .WORD      ERR2
3225      031304      15S:
3226      031304      60S:
3227      031304      ENDSUB      L10044:
3228      031304      104003      EMT      C$ESUB
3229      031306      005737      TST      D$ESHD      ;TEST IF HEAD 1 DONE
3230      031312      001007      BNE      20S      ;YES-SKIP
3231      031314      015737      MOV      #1,D$ESHD      ;ELSE SET TO HEAD 1
3232      031322      000001      HD$WRD1,TEMP0      ;STORE HDR WORD 1
3233      031326      000725      RR      T134S      ;DO TEST AGAIN
3234      031332      042737      BIC      #CHDCYL,TEMP0      ;CLEAR ALL BUT CYLINDER IN 1ST HEADER
3235      031340      100177      BIC      #CHDCYL,HD$WRD1      ;CLEAR ALL BUT CYL IN 2ND HEADER
3236      031346      023737      CMP      TEMP0,HD$WRD1      ;COMPARE IF EQUAL
    
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3237      031352      001405      BEQ      22S
3238      031352      012703      007503      MOV      #CVLPER,R3      ;YES-SKIP
3239      031352      104443      ER$RHRD      1306,E$ERR1      ;SET NAME MESSAGE PTR
3240      031364      002432      TRAP      T$ERRCODE      ;REPORT HEAD ALIGNMENT PROBLEM
3241      031366      013040      .WORD      1306
3242      031370      003130      .WORD      ERR1
3243      031370      22S:
3244      031370      ENDTST      L10043:
3245      031370      104001      EMT      C$SETST
    
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3306 031646 BGNMOD HRDPRM
3307 031646 BGNHRD
(3) 031646 000025
(4) 031646
(4) 031650 004130
(4) 031652 031764
(4) 031655 000001
3309 031658 GPRMA CSMSG,CSR,0,160000,177776,YES
(4) 031658 000031
(4) 031660 031722
(4) 031663 160000
(4) 031664 177776
3310 031666 GPRMA VECMSG,VECT,0,0,776,YES
(4) 031666 001031
(4) 031670 031736
(4) 031672 000008
(4) 031674 000776
3311 031676 GPRMD BRMSG,PRIOR,0,340,0,7,YES
(4) 031676 002032
(4) 031678 031736
(4) 031702 000346
(4) 031704 000000
(4) 031706 000007
3312 031708 GPRMD DRMSG,DRSR,1,340,0,7,YES
(4) 031710 003032
(4) 031712 031756
(4) 031714 003400
(4) 031720 000007
3313
3314 031722 ENDRD
(3) 031722 L10047: .EVEN
3315
3316 031722 052502 020123 042101 CSMSG: .ASCIZ /BUS ADDRESS/
(4) 031722 021704 061502 000123
3317 031736 042526 052103 051117 VECMSG: .ASCIZ /VECTOR/
(4) 031744 000
3318 031745 042512 020122 042514 BRMSG: .ASCIZ /RR LEVEL/
(4) 031756 051104 053111 000105 DRMSG: .ASCIZ /DRIVE/
3319 031764 046122 036461 000 CNTYPE: .ASCIZ /RL1/
3321 031771 ENDMOD .EVEN
3322
3324 031772 BGNMOD SFTPRM
3325 031772 BGNSTPT .WORD L10050-L$SOFT/2
(3) 031772
3326
3332 031774 GPRML SELQ,WISWI,4,YES
(4) 031774 000130
(4) 031776 032002
(4) 032000 000004
3333 032002 GPRML ALGNQ,WISWI,10,YES
(4) 032002 000130
(4) 032004 032001
    
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(4) 032006 000010
3335 032010 GPRML .WORD 10
(4) 032010 000130 MANG,WISWI,100000,YES
(4) 032012 032130 .WORD TSCODE
(4) 032014 100000 .WORD MANG
. WORD 100000
3336 032016 3S: GPRMD ERLIMQ,ERLIM,D,377,0,377,YES
(4) 032016 004052 .WORD TSCODE
(4) 032022 006177 .WORD ERLIMQ
(4) 032024 000000 .WORD 377
(4) 032026 000377 .WORD T$LOLIM
3352 032030 GPRML AUTOQ,WISWI,20,YES
(4) 032032 000130 .WORD TSCODE
(4) 032034 032216 .WORD AUTOQ
(4) 032036 000020 .WORD 20
3353 032036 ENDSFT .EVEN
(3) 032036 L10050:
3354
3356 032036 054105 041505 052125 SELQ: .ASCIZ /EXECUTE DRIVE SELECT TESTS/
(4) 032042 050105 042104 052111
(4) 032050 050105 042523 042514
(4) 032060 052103 052040 051505
(4) 032065 051524 000
3361 032076 042524 044040 052503 ALGNQ: .ASCIZ /EXECUTE HEAD ALIGNMENT SUPPORT/
(4) 032104 020104 046101 043511
(4) 032112 046516 047105 020124
(4) 032126 052523 050120 051117
3363 032130 054105 041505 052125 MANG: .ASCIZ /EXECUTE MANUAL INTERVENTION TESTS/
(4) 032136 020105 046515 052516
(4) 032148 046101 044440 052116
(4) 032152 051105 042526 052116
(4) 032160 047511 020116 042524
3371 032166 052123 000153 043111 ERLIMQ: .ASCIZ /SPECIFY ERROR LIMIT/
(4) 032170 050105 041505 047522
(4) 032206 020122 044514 044515
3372 032218 050117 042040 AUTOQ: .ASCIZ /DROP DRIVE IF NO RESPONSE/
(4) 032224 044527 047526 044440
(4) 032232 020106 047516 051040
(4) 032240 051505 047520 051516
(4) 032246 000105
3376 032250 ENDMOD .EVEN
3377
3378
3380 032514 .-=32514
3381
3382
3383
3384
3385
3386
;AREA RESERVED AS PATCH AREA FOR DIAGNOSTICS.
; -32514 WAS SELECTED AS "LASTAD" TO PROVIDE APT TO LSI-11 COMPATIBILITY.
; BIT 7 OF "LASTAD" MUST BE CLEARED TO ACHIEVE A VALID MAILBOX ADDRESS
; WHEN RUNNING ON THE LSI-11 UNDER APT.
    
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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 16:20 PAGE 3-2  
CZRLCB.PT2 22-NOV-78 16:19 \*TEST 14 READ HEADER (PART 2)

SEQ 0113

3387  
3389  
3399  
3400 032514 LASTAD  
(3) 032514 LSLAST:: -EVEN

ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 16:20 PAGE 4  
CZRLCB.SUP 23-OCT-78 09:52 DIAGNOSTIC SUPERVISOR -- LOW CORE SET UP

SEQ 0114

3402			.SBTTL	DIAGNOSTIC SUPERVISOR -- LOW CORE SET UP
14273	063310	000900	.WORD	0 ;SPACE FOR USER POOL POINTER
14274	063312	000900	.WORD	0 ;SIZE
14275	063314	000000	.WORD	0 ;CHECKSUM (NOT CURRENTLY USED)
14276	063316	000000	.WORD	0 ;SIZE OF H.W. PTAB. ALLOCATION
14277		063322	END.SUPV	=+2
14278		000200	.END	200







ASSEMBLY ROUTINES MACV11 30A(1052) 22-NOV-78 16:20 PAGE 5-4  
CZRLCB.SUP 23-OCT-78 09:52 SYMBOL TABLE

SEQ 0119

ERRORS DETECTED: 0

DSKZ: CZRLCB, DSKZ: CZRLCB/EO: PART1=CZRLCB/ML, CZRLCB.PT1, CZRLCB.P11, CZRLCB.PT2, CZRLCB.SUP  
RUN-TIME: 50.491 SECONDS  
RUN-TIME RATIO: 177/101=1.7  
CORE USED: 16K (31 PAGES)