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IDENTIFICATION

PRODUCT CODE MAINDEC-11-DZQKC-E-D
PRODUCT NAME 11 FAMILY INSTRUCTION EXERCISER
DATE CREATED MARCH 21, 1975
MAINTAINER DIAGNOSTIC GROUP
AUTHOR J. ADAMS

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- 1.0 ABSTRACT
THIS DIAGNOSTIC PROGRAM IS DESIGNED TO BE A COMPREHENSIVE CHECK OF THE PDP11/05 AND PDP11/20 PROCESSORS. THE PROGRAM EXECUTES EACH INSTRUCTION IN ALL ADDRESS MODES AND INCLUDES TESTS FOR TRAPS AND THE TELETYPE INTERRUPT SEQUENCE. THE PROGRAM DOES NOT TEST INSTRUCTIONS NOT COMMON TO THE 11/20 OR 11/05. THE PROGRAM RELOCATES THE TEST CODE THROUGHOUT MEMORY 0-28K.
- 2.0 REQUIREMENTS
- 2.1 EQUIPMENT
PDP11 FAMILY CENTRAL PROCESSOR
OPTIONAL - KW11-L (LINE CLOCK)
- 2.2 STORAGE
THE PROGRAM USES ALL OF THE FIRST 4K OF MEMORY (EXCLUDING THAT AREA OF MEMORY RESERVED FOR THE LOADERS).
- 2.3 PRELIMINARY PROGRAMS
NONE, HOWEVER, THE EMT AND TRAP INSTRUCTION SHOULD BE VERIFIED BEFORE RUNNING.
- 3.0 LOADING AND STARTING PROCEDURE
LOAD PROGRAM USING ABS LOADER
IF THE CONSOLE TTY IS A SERIAL LA30 OR A VT05 FILLER CHARACTERS MAY BE REQUIRED. DEPOSIT INTO LOCATION 1002 (FILLS) A 4400.
LOAD ADDRESS = 200
PRESS START
SET OPERATING SWITCHES
PASS COUNT IS PRINTED AFTER EACH PASS (SEE SEC 6.4)
"DZQKC DONE" IS PRINTED WHEN DONE (SEE SEC 7.1)
- 4.0 SWITCH SETTINGS
- SW15 HALT ON ERROR... THIS SWITCH WHEN SET WILL HALT THE PROCESSOR WHEN AN ERROR IS DETECTED. THE PC+2 AND THE CURRENT STATUS AT THE TIME OF THE ERROR IS STORED ON THE STACK (R6). IF THIS SWITCH IS SET BEFORE AN ERROR IS DETECTED THE PROGRAM HALTS AS DESCRIBED ABOVE. THE PROGRAM MAY BE HALTED AFTER THE ERROR TYPEOUT OCCURS BY SETTING SW15 AFTER THE TYPEOUT BEGINS.
- SW14 LOOP SUBTEST... THIS SWITCH WHEN SET LOOPS THE CURRENT SUBTEST RUNNING REGARDLESS OF ERROR.
- SW13 INHIBIT ERROR PRINTOUT - THIS SWITCH WHEN SET INHIBITS THE ERROR PRINTOUT.
- SW12 INHIBIT RELOCATION... THIS SWITCH WHEN SET CAUSES THE PROGRAM TO BE EXECUTED ONLY IN THE FIRST 4K OF MEMORY. THIS SWITCH CANNOT BE SET WHEN THE PROGRAM IS RUNNING.
- SW11 INHIBIT SUBTEST ITERATION... THIS SWITCH WHEN SET INHIBITS SUBTEST REITERATION. NORMALLY EACH SUBTEST IS EXECUTED 8 TIMES BEFORE THE NEXT SUBTEST IS RUN.

SETTING SW11 CAUSES EACH TEST TO BE EXECUTED ONCE BEFORE STARTING THE NEXT SUBTEST.

SW10 RING BELL ON ERROR,.. THIS SWITCH WHEN SET WILL RING THE BELL WHEN AN ERROR IS DETECTED.

SW7 INHIBIT ALL BUT ERROR TYPEOUTS,..THIS SWITCH WHEN RESET (0) INHIBITS THE END OF PASS TYPEOUT (ICNT=XXXX) AND THE END OF PROGRAM TYPEOUT (DZQKC DONE)

5.0 ERRORS

IF AN ERROR IS DETECTED THE PROGRAM WILL TRAP TO THE ERROR HANDLING ROUTINE (ERROR), IF ENABLED THIS ROUTINE WILL BYTE THE PC AND THE PROCESSOR STATUS AT THE TIME OF THE ERROR, ALSO (IF REQUIRED) THE ORIGINAL PC (WHERE THE PC WAS RELOCATED FROM).

5.0.1

ERROR PRINTOUT FORMAT

ICNT=AAAA PC=BBBBBB PSW=DDDDDD

OR

ICNT=AAAA PC=BBBBBB PSW=DDDDDD PC RELOCATED FROM CCCC

WHERE: AAAA=PASS COUNT
 BBBBBB=PC AT THE TIME OF THE ERROR
 CCCCC=PC OF THE ORIGINAL CODE RELOCATED
 DDDDD=PSW AT THE TIME OF THE ERROR.

5.1

PARITY ERROR DETECTION

IF A PARITY ERROR IS DETECTED THE PROGRAM WILL TYPE A MESSAGE "PARITY ERROR" AND SCAN MEMORY FOR THE PARITY ERROR, WHEN THE FAILING ADDRESS IS LOCATED THE PROGRAM WILL HALT WITH THE VALUE OF THE ADDRESS+2 IN R0.

5.2

ERROR LOOPING

THE SUBTEST DETECTING THE ERROR MAY BE LOOPED INDEFINITELY BY SETTING SW14, SETTING SW13 WILL INHIBIT THE TYPEOUT AND ALLOW SCOPING THE FAULTY SIGNAL(S).

5.3

UNPREDICTED ERRORS

THE PROGRAM MAY ON OCCASSION DETECT A MEMORY ERROR THE RESULTS OF WHICH WERE NOT PREDICTABLE IN WHICH CASE THE PROGRAM MAY BEHAVE UNPREDICTABLY. WHEN THIS HAPPENS THE USER MUST RETRACE THE PROGRAM STEPS TO RESOLVE WHERE THE ERROR OCCURRED. THE FOLLOWING ITEMS SHOULD BE CONSIDERED AND MAY BE OF USE WHEN RETRACING A FAILURE OF THIS NATURE.

1. HALT THE PROGRAM (IF NECESSARY)
2. EXAMINE RELR1
 ADDRESS RELR1 (1006) CONTAINS THE UNRELOCATED VALUE OF THE PC OF THE LAST TEST THAT WAS SUCCESSFULLY EXECUTED.

3. EXAMINE FACTOR
ADDRESS FACTOR (1004) CONTAINS THE RELOCATION FACTOR.
4. EXAMINE ALL LOCATIONS STARTING WITH THE ADDRESS SPECIFIED IN R1/R11 (IF PSW BIT11 = 0/1) COMPARING THEIR CONTENTS WITH THE CONTENTS OF THE CORRESPONDING UNRELOCATED CODE (SPECIFIED IN 1006) AS SHOWN IN THE LISTING. EXAMINE AND COMPARE UNTIL EITHER A DIFFERENCE IN INSTRUCTION (I.E., THE ERROR) OR THE NEXT 'SCOPE' IS SEEN.

IF THE PROGRAM TRAPS AND HALTS AT A TRAP/INTERRUPT VECTOR+2 (NOTE: THE PDP-11/45 WILL DISPLAY THE ADDRESS OF THE HALT+2 I.E., A FALSE TRAP TO 4 WILL DISPLAY 10).

1A. EXAMINE THE STACK (R6)

THE TOP WORD ON THE STACK CONTAINS THE PC AT THE TIME OF THE TRAP. IF THE PC IS GREATER THAN 20000, THEN

2A. EXAMINE LOCATION 1002 (FACTOR)

THIS LOCATION CONTAINS THE PROGRAM RELOCATION FACTOR WHICH, WHEN SUBTRACTED FROM THE PC GIVES THE PC OF THE ORIGINAL CODE.

6.0 SUBROUTINE ABSTRACTS

6.1 SCOPEA
THE SCOPEA ROUTINE IS ENTERED BY THE SCOPE (EMT) INSTRUCTION AND IS EXECUTED AT THE START OF EACH SUBTEST. THE ROUTINE MONITORS SW14, SW11 AND SW 8 AND TAKES APPROPRIATE ACTION. ALSO, THIS ROUTINE STORES IN R1/R11 THE FIRST ADDRESS OF THE SUBTEST BEING ENTERED.

6.2 ERROR
THE ERROR ROUTINE IS ENTERED BY THE HLT (TRAP) INSTRUCTION, AND IS EXECUTED WHEN A PREDICTABLE ERROR IS DETECTED. THIS ROUTINE MONITORS SW15, SW13, AND SW10.

6.3 RELOC
THE RELOC ROUTINE IS ENTERED BY A MOV RELOC,PC INSTRUCTION. THIS ROUTINE RELOCATES THE PROGRAM CODE THROUGHOUT MEMORY, AND 'JUMPS' TO THE RELOCATED CODE AFTER IT HAS BEEN MOVED SUCCESSFULLY. IF THE CODE CANNOT BE RELOCATED (BECAUSE OF INSUFFICIENT MEMORY) THE ROUTINE 'JUMPS' TO THE NEXT SECTION OF UNRELOCATED PROGRAM CODE. THE CODE MOVED IS LESS THAN 1K (4000) BYTES). AT THE START AND END OF EACH SECTION OF CODE TO BE MOVED ARE A SECTION OF CODE WHICH ESTABLISHES THE FIRST ADDRESS OF THE CODE TO BE MOVED, AND SETS A SCOPE POINTER (R1/R11) AND, ALSO A SECTION WHICH ESTABLISHES THE LAST ADDRESS AND 'JUMPS' TO THE RELOCATION (RELOC) ROUTINE. EACH SECTION OF CODE IS

IDENTIFIED AS SHOWN BELOW:

:0000000000000000FIRST ADDRESS TO BE RELOCATED0000000000

CODE TO BE MOVED AND EXECUTED

:0000000000000000LAST ADDRESS OF CODE TO BE RELOCATED 0000000000

THE RELOC ROUTINE DOES NOT RELOCATE PROGRAM CODE INTO THE LAST 1000(8) BYTES OF MEMORY, THUS PRESERVING THE LOADERS.

6.4

END
THIS ROUTINE IS ENTERED AT THE COMPLETION OF EACH PASS IT SETS UP (LOADS NEW PROCESSOR STATUS) FOR THE NEXT PASS; AND PRINTS THE PASS COUNT

ICNT=XXXX

7.0

MISCELLANEOUS

7.1

EXECUTION TIME

THE EXECUTION TIME IS HIGHLY VARIABLE (DEPENDENT ON PROCESSOR, TYPE OF MEMORY, AND AMOUNT OF MEMORY). HOWEVER, WHEN THE PROGRAM IS RUNNING SUCCESSFULLY THERE IS A NOTICEABLE 'FLICKER' DISPLAYED IN THE CONSOLE LIGHT PATTERN THE 'FLICKER' WILL DIM WHEN 'T' BIT TRAP PASSES (EVERY ODD PASS) ARE RUNNING, THE PROGRAM SHOULD BE RUN FOR A MINIMUM OF:

2 PASSES ICNT=2 11/05 OR 11/20

SOME TYPICAL TIMES FOLLOW:

8.0

PROGRAM DESCRIPTION

THE PROGRAM IS DIVIDED INTO FOUR SECTIONS OF POSITION INDEPENDENT RELOCATABLE TEST CODE. EACH SECTION IS APPROXIMATELY 1K WORDS LONG. (EXCEPT SECTION A).

SECTION 0 THIS SECTION TEST THE UNARY INSTRUCTION SET EXECUTING EACH UNARY INSTRUCTION IN EACH ADDRESS MODE (EXCLUDING UNARY INSTRUCTIONS USING ADDRESS MODE 7).

SECTION 1 THIS SECTION TESTS THE UNARY INSTRUCTIONS USING ADDRESS MODE 7 AND BINARYS IN ALL ADDRESS MODES (EXCLUDING BINARY BYTE OPS USING ADDRESS MODE 7).

SECTION 2 THIS SECTION TEST BINARY BYTE OPS USING ADDRESS MODE 7, JMP, JSR AND PROGRAM TRAP (IOT, TRAP AND EMT) INSTRUCTIONS.

SECTION A FOLLOWING SECTION 2 IS A ROUTINE TO ASCERTAIN WHICH CP THE PROGRAM IS RUNNING ON. THE RESULTS ARE USED BY THE FOLLOWING CODE TO CHECK THE ADDITIONAL INSTRUCTIONS/FEATURES OF THE 11/40 AND 11/45.

SECTION 3 THIS SECTION CHECKS THAT EACH BIT IN THE PROCESSOR STATUS WORD (PSW) CAN BE SET CLEARED, RESERVED INSTRUCTION, AND ODD ADDRESS TRAPS.

FOLLOWING SECTION 3 ARE TWO ROUTINES TO CHECK THE TELETYPE PRINTER LOGIC AND A ROUTINE TO START THE KW11-L LINE CLOCK. IF THE KW11-L IS AVAILABLE THE PRIORITY ARBITRATION LOGIC IS TESTED.

AFTER EACH INDIVIDUAL SECTION HAS BEEN EXECUTED THE "RELOC" ROUTINE WILL RELOCATE THE SECTION THROUGHOUT ALL MEMORY UP TO 28K. WHEN THE SECTION HAS BEEN RELOCATED AND EXECUTED IN ALL MEMORY THE "RELOC" ROUTINE WILL RETURN THE PROGRAM TO THE NEXT UNRELOCATED SECTION.

RELOCATION AND EXECUTION OF ALL SECTIONS THROUGHOUT ALL MEMORY CONSTITUTES A SINGLE PASS.

UPON COMPLETION OF A PASS OF THE PROGRAM THE PROGRAM RESTARTS USING A NEW PROCESSOR STATUS DEPENDING ON THE TYPE OF PROCESSOR AND THE PASS COUNT.

8.1

STACK POINTER

THE STACK POINTER IS SET AT 500.

NOTE: IF THE PROGRAM IS RUNNING IN EITHER USER OR SUPERVISOR MODE (NOT APPLICABLE IF 11/20 OR 11/05) THE USER/SUPERVISOR STACK POINTER IS SET TO 500 AND THE KERNEL STACK POINTER IS SET TO 600. THE KERNEL STACK POINTER IS USED ONLY FOR THE SCOPE, HLT, TTY, AND KW11-L (IF AVAILABLE TRAP/INTERUPT ROUTINES.

8.2

POWER FAILURE

A POWER FAIL SERVICE ROUTINE IS INCORPORATED IN THE TEST. WHEN USING THIS PROGRAM THE POWER SHOULD BE TURNED OFF WHEN RUNNING TO CHECK THE POWER FAIL LOGIC. WHEN THE POWER FAILS THE PROGRAM WILL TYPE:

POWER FAILED

AND RESTART THE PROGRAM AT THE BEGINNING. (START)

9.0

USER DEFINED RELOCATION LIMITS

THE PROGRAM WILL REQUEST A LOWER AND UPPER LIMIT FOR RELOCATION. THE LIMITS MUST BE BETWEEN 20000 AND 157776. THE PROGRAM WILL EXECUTE IN THE LOWER 4K (0-17776) AND THE LIMITS SPECIFIED.

THE STARTING ADDRESS IS 204.

TO RETAIN PREVIOUSLY SPECIFIED LIMITS START AT 210.

.NLIST SEQ;MD,BC
.LIST ME
.ABS
.TITLE FRONT END
;CONTAINS DEFINITIONS, REGISTER ASSIGNMENTS AND MACRO CALLS

;GENERAL REGISTER ASSIGNMENTS

```
000000 R0=X0
000001 R1=X1
000002 R2=X2
000003 R3=X3
000004 R4=X4
000005 R5=X5
000006 SP=X6
000007 PC=X7
000008 R10=X8
000009 R11=X1
000010 R12=X2
000011 R13=X3
000012 R14=X4
000013 R15=X5
```

;STATUS REGISTER (PSW) BIT ASSIGNMENTS

```
000001 C=1 ;C BIT
000002 V=2 ;V BIT
000003 Z=4 ;Z BIT
000010 N=10 ;N BIT
000020 T=20 ;T BIT
000040 PRTY7=340 ;PRIORITY LEVEL 7
000300 PRTY6=300 ;PRIORITY LEVEL 6
000200 PRTY4=200 ;PRIORITY LEVEL 4
```

;VECTOR ADDRESSES

```
000004 ERRVEC=4 ;ADDRESS OF ERROR VECTOR
000010 RESVEC=10 ;ADDRESS OF RESERVED INST, TRAP VECTOR
000014 TBITVEC=14 ;ADDRESS OF 'T' BIT TRAP VECTOR
000014 TRTVEC=14 ;ADDRESS OF 'TRACE' TRAP VECTOR
000014 BPTVEC=14 ;ADDRESS OF 'BREAKPOINT' TRAP VECTOR
000020 IOTVEC=20 ;ADDRESS OF IOT TRAP VECTOR
000024 PFVEC=24 ;ADDRESS OF POWER FAIL TRAP VECTOR
000030 EMTVEC=30 ;ADDRESS OF EMT VECTOR
000034 TRAPVEC=34 ;ADDRESS OF TRAP VECTOR
000064 TPVEC=64 ;ADDRESS OF TTY PRINTER INTERRUPT VECTOR
000100 LKVEC=100 ;ADDRESS KW11-L LINE CLOCK INT, VECTOR
000240 PIRVEC=240 ;ADDRESS OF PIRQ VECTOR
000244 FPEVEC=244 ;ADDRESS OF FLOATING POINT INT, VECTOR
000250 HMVEC=250 ;ADDRESS OF MEM MGMT ERROR TRAP VECTOR
```

;REGISTER ADDRESSES

```
177776 PSW= 177776 ;ADDRESS OF STATUS REGISTER
177774 SLR= 177774 ;ADDRESS OF STACK LIMIT REGISTER
177772 PIRQ= 177772 ;ADDRESS OF PROGRAM INTERRUPT REQUEST
```

```
177770 UBREAK= 177770 ;ADDRESS OF MICRO BREAK REGISTER
177546 LKS= 177546 ;ADDRESS OF KW11-L STATUS REG,
177560 TKS= 177560 ;ADDRESS OF KEYBOARD CSR
177562 TKB= 177562 ;ADDRESS OF KEYBOARD BUFFER
177564 TPS= 177564 ;ADDRESS OF TELEPRINTER CSR
177566 TPB= 177566 ;ADDRESS OF TELEPRINTER BUFFER
177572 SRB= 177572 ;ADDRESS OF MEM MGMT REGISTER SR0
177570 SWR= 177570 ;ADDRESS OF CONSOL SWITCH REGISTER
177570 DISPLAY=177570 ;ADDRESS OF CONSOL DISPLAY REGISTER
177514 LPS= 177514 ;ADDRESS OF LINE PRINTER STATUS REG
177516 LPB= 177516 ;ADDRESS OF LINE PRINTER DATA DUFFER
```

;INITIAL STACK POINTER SETTING

```
000500 STKPTR= 500 ;PROGRAM STACK PTR
000600 KPTR=600 ;KERNEL STACK PTR (USED BY KERNEL WHEN
;PROGRAM IS RUNNING IN OTHER THAN KERNEL
;MODE (NOT APPLICABLE TO 11/00,11/20)
```

;MISCELLANEOUS BIT ASSIGNMENTS

```
100000 BIT15=100000
040000 BIT14=40000
020000 BIT13=20000
000400 BIT8=400
000100 BIT6=100
```

;INSTRUCTION EQUATES

```
104400 HLT=TRAP ;HLT IS A TRAP INST TO THE ERROR ROUTINE
104000 SCOPE=EMT ;SCOPE IS AN EMT TRAP
```

```
000046 016542 ;=46
000052 000052 LOGICAL
000052 040000 ;=92
000200 000200 BIT14
000200 ;=200
000200 012707 002066 MOV #START,PC ;GO TO START OF TEST
000204 012707 002100 MOV #START1,PC ;GO GET LOWER/UPPER RELOCATION BOUNDARY
000210 012707 002224 MOV #STARTS,PC ;START WITH LAST TYPED BOUNDARY LIMITS
```

;ROUTINE TO SAVE REGISTERS ON THE STACK

```

000214 012667 000016      ICALLED BY SAVE MACRO OR JSR      PC,$SAVR
000220 010546              $SAVR:  MOV      (SP+,1$)      ;SAVE RETURN PC
000222 010446              MOV      X5,-(SP)
000224 010346              MOV      X4,-(SP)
000226 010246              MOV      X3,-(SP)
000230 010146              MOV      X2,-(SP)
000232 010046              MOV      X1,-(SP)
000234 012707              MOV      X0,-(SP)
000236 000000              MOV      (PC+,PC)      ;RETURN
                                ;CONTAINS RETURN ADDRESS

                                ;ROUTINE TO RESTORE REGISTERS SAVED ON THE STACK
                                ICALLED BY RESTORE MACRO OR JSR      PC,$RSTR
000240 012667 000016      $RSTR:  MOV      (SP+,1$)      ;SAVE RETURN PC
000244 012600              MOV      (SP+,X0)
000246 012601              MOV      (SP+,X1)
000250 012602              MOV      (SP+,X2)
000252 012603              MOV      (SP+,X3)
000254 012604              MOV      (SP+,X4)
000256 012605              MOV      (SP+,X5)
000260 012707              MOV      (PC+,PC)      ;RETURN
000262 000000              ;CONTAINS RETURN ADDRESS

                                ;=610
000610 012737 000620 000024  ;POWER FAIL SUBROUTINE
000616 000000      PDWN:  MOV      #PUP,#PFVEC
                                HALT

000620 012737 000610 000024  ;POWER UP SUBROUTINE
000626 012706 000600      PUP:   MOV      #PDWN,#PFVEC      ;RESTORE POWER FAIL TRAP TO POWER
000632 005027              MOV      #KPTR,SR      ;DOWN ROUTINE ABOVE
000634 000000              CLR      (PC+)         ;SET STACK PTR
000636 005267 177772      1$:   WORD      0          ;KILL TIME
000642 001375              2$:   INC      1$
000644 004767 000362              BNE     2$
000650 000656              JSR     PC,PRINT      ;PRINT MESSAGE BEGINING AT FOLLOWING ADRS
000652 000137 002066              PFAIL: JMP      @#START      ;RESTART TEST

000656 005015 047520 042527  PFAIL: .ASCIZ <15><12>'POWER FAILED'<15><12>
000664 020122 040506 046111
000672 042105 005015 000

                                ;=740
000740 012737 000002 000006  ;NOTE: THIS CODE USED ONLY BY THE XOR TESTER.
000746 000261              ;TO USE CODE PLACE 776 (BR ,-2) IN SCOPEA
000750 005737 177060      FORXOR: MOV      #RTI,#ERRVEC+2 ;SET TIME OUT TRAP TO RETURN
                                SEC
                                TST      @#177060 ;SET C
                                ;IF A TIME OUT OCCURS THEN WHEN NEXT
                                ;INSTRUCTION IS EXECUTED 'C' WILL BE SET
                                ;AND IF NO TIME OUT 'C' WILL BE CLEARED
000754 103401              BCS     1$             ;BRANCH IF 'C' SET (TIMED OUT)
000756 011601              MOV     (SP+,R1)      ;ADDRESS OF NEXT SUBTEST TO R1
  
```

```

000760 005037 000006      1$:   CLR      @#ERRVEC+2 ;RESTORE TIME OUT TRAP
000764 010116              MOV     R1,(SP)      ;GET RETURN ADDRESS BACK TO SUBTESTS
000766 000240              NOP
000770 000002              RTI      ;RETURN EITHER TO LAST OR NEXT SUBTEST

                                ;=776
000776 000000      TICKS: .WORD      0          ;CONTAINS CLOCK TICK COUNT
                                ;=1000
001000 000000      ICNT:  0
001002 000000      $FILLS: .WORD      0          ;CONTAINS PASS COUNT
                                ;CONTAINS FILLS COUNT IN ODD BYTE
                                ;AND FILLER CHARACTER IN EVEN BYTE
001004 000000      FACTOR: 0 -          ;CONTAINS RELOCATION FACTOR
                                ;SUBTRACT # IN FACTOR FROM PC TO GET PC OF ORIGINAL CODE
001006 000000      RELR1: 0
                                ;CONTAINS RELOCATED R1 (THE R1 OF THE
                                ;ORIGINAL CODE MOVED)
001010 000000      FRSTADI: .WORD      0        ;CONTAINS FIRST ADRS OF CODE TO BE MOVED
001012 000000      FRSTMEN: .WORD      0        ;CONTAINS LOWER RELOCATION BOUNDARY ADDRESS
001014 000751              BR      FORXOR      ;BRANCH TO XOR TESTER CODE

                                ;SCOPE (EMT) SERVICE ROUTINE
                                ;THIS ROUTINE ALLOWS THE SUBTEST TO BE CONTINUOUSLY LOOPED, ITEMATED
                                ;(OR NOT ITERATED) BEFORE BEGINNING NEXT SUBTEST
001016 000240      SCOPEA: NOP
001020 032766 004000 000002      BIT      #4000,2(SP) ;WAS REGISTER SET BIT SET ON TRAP
001026 001403              BEQ     2$           ;BRANCH IF NOT
001030 052737 004000 177776      BIS     #4000,@#PSW ;RETAIN REGISTER SET
001036 032737 040000 177570      2$:   BIT      #4000,@#SWR ;CHECK BIT 14 (CONTINUOUS LOOP)
001044 001416              BEQ     SCOPEC
001046 010116      SCOPEB: MOV      R1,(SP)      ;LOAD RETURN ADDRESS
001050 010137 001006      MOV     R1,@#RELR1
001054 163737 001004 001006      SUB     @#FACTOR,@#RELR1 ;RELR1 CONTAINS UNRELOCATED R1
001062 032737 000400 177570      BIT      #400,@#SWR ;LOAD PDP11/45 MICRO BREAK REG?
001070 001403              BEQ     1$
001072 113737 177570 177770      MOVB   @#SWR,@#UBREAK ;LOAD MICRO BREAK REG WITH SWR-7
001100 000002      1$:   RTI      ;RETURN TO SUBTEST
001102 032737 004000 177570      SCOPEC: BIT      #4000,@#SWR ;SUBTEST ITERATION DESIRED?
001110 001006              BNE     SCOPEE      ;BRANCH IF NO ITERATION DESIRED?
001112 005327              DEC     (PC+)        ;DECREMENT SUBTEST ITERATION COUNT
001114 000040      SCOPED: 40
                                ;CONTAINS SUBTEST ITERATION COUNT
001116 001353              BNE     SCOPEB
001120 012767 000040 177766      SCOPEF: MOV      #40,SCOPEB ;RESET ITERATION COUNT
001126 011601      SCOPEE: MOV     (SP+,R1) ;GET ADDRESS OF NEXT TEST
001130 000746              BR      SCOPEB

                                ;ROUTINE TO RELOCATE PROGRAM CODE
001132 032737 010000 177570      RELOC: BIT      #10000,@#SWR ;CHECK IF RELOCATION DESIRED (BIT12)
001140 001031              BNE     3$           ;BRANCH IF NO RELOCATION DESIRED
001142 013700 001010      MOV     @#FRSTAD,R0 ;GET FIRST ADDRESS OF CODE TO BE MOVED
001144 010005              MOV     R0,R5        ;SAVE
001150 010204              MOV     R2,R4        ;GET LAST ADDRESS OF CODE TO BE MOVED
001152 160504              SUB     R5,R4        ;R4 CONTAINS # OF WORDS TO RELOCATE
001154 010203              MOV     R2,R3        ;SAVE LAST ADDRESS OF CODE TO BE MOVED
001156 005737 001004      TST     @#FACTOR      ;FIRST RELOCATION IS TO 20000
001162 001004              BNE     10$
001164 010237 001230      MOV     R2,@#RETPC ;SAVE RETURN PC TO NEXT SECTION OF CODE
  
```



```

001170 013702 001012      MOV    0#FRSTMEN,R2 ;SET FIRST ADDRESS
001174 002024      ADD    R2,R4         ;R4 CONTAINS LAST MEMORY ADDRESS
                                ;TO BE USED
001176 020437 002140      CMP    R4,#LSTMEN  ;CHECK IF SUFFICIENT MEMORY REMAINS
001202 101011      BHI   4$           ;RELOCATE PROGRAM CODE
001204 012022      MOV    (R0)+,(R2)+ ;CHECK IF DONE
001206 020003      CMP    R0,R3
001210 001375      BNE   1$
001212 024042      CMP    -(R0),-(R2) ;CHECK THAT CODE WAS RELOCATED
001214 001401      BEQ   ,+4         ;PROPERLY
001216 104400      HLT   ;ERROR! CODE NOT RELOCATED PROPERLY
001220 020005      CMP    R0,R5      ;CHECK IF FINISHED CHECKING
001222 001373      BNE   2$
001224 010207      MOV    R2,RC
001226 011707      MOV    (PC),PC    ;GO EXECUTE RELOCATED CODE
001230 000000      RETPC; 0         ;RETURN TO NEXT SECTION OF CODE
                                ;CONTAINS PC OF NEXT SECTION OF CODE

;ROUTINE TO PRINT ASCII MESSAGE, MESSAGE MUST TERMINATE WITH A 0 BYTE;
001232 010046      .PRINT; MOV    R0,-(SP) ;SAVE R0 ON THE STACK
001234 017600      MOV    #2(SP),R0  ;GET MESSAGE ADDRESS
001240 002766 000002 000002  ADD    #2,2(SP)    ;ADJUST RETURN PC

001246 112046      1$;  MOV    (R0)+,-(SP) ;PUSH CHAR ON THE STACK
001250 001003      BNE   2$         ;BRANCH IF NOT TERMINATOR
001252 005726      TST   (SP)+      ;POP TERMINATOR OFF THE STACK
001254 012600      MOV    (SP)+,R0  ;RESTORE R0
001256 000207      RTS   PC        ;RETURN

001260 004767 000026      2$;  JSR    PC,5$   ;TYPE CHARACTER
001264 122726 000012      3$;  CMP    #12,(SP)+ ;CHECK IF CHAR WAS A LINE FEED
001270 001366      BNE   1$        ;BRANCH IF NOT LINE FEED

001272 016746 177504      MOV    $FILLS,-(SP) ;GET # OF FILLERS REQUIRED AFTER
                                ;LINE FEED AND FILLER CHARACTER
001276 105366 000001      4$;  DECB   1(SP)    ;DECREMENT FILLERS COUNT
001302 002770      BLT   3$        ;BRANCH IF NO MORE FILLERS NEEDED
001304 004767 000002      JSR    PC,5$    ;TYPE FILLER CHARACTER
001310 000772      BR    4$

001312 105737 177564      5$;  TST    0#TPS    ;WAIT FOR OUTPUT DEVICE
001316 100375      BPL   ,+4       ;TO BECOME READY
001320 116637 000002 177566  MOV    2(SR),0#TPB ;TYPE CHARACTER
001326 000207      RTS   PC

                                NULL=0
;ROUTINE TO PLACE ASCII VALUE OF AN ADDRESS IN TO ADDRESS MESSAGE
001330 004767 176660      SFORM0; JSR    PC,$SAVR ;GO SAVE REGISTERS ON THE STACK
001334 012704 001662      MOV    #DIGITS,R4 ;ADDRESS WHERE ASCII VALUES ARE STORED
001340 005003      CLR    R3        ;WORKING & INDEX REGISTER
001342 010201      MOV    R2,R1     ;SAVE
001344 006302      1$;  ASL    R2        ;FIRST DIGIT TO R3
001346 006103      ROL   R3
001350 012700 000006      MOV    #6,R0     ;DIGIT COUNT
    
```

```

001354 000404      BR    3$         ;PRINT FIRST DIGIT
001356 006302      2$;  ASL    R2
001360 006103      ROL   R3
001362 005301      DEC   R1
001364 001374      BNE   2$
001366 012701 000003      3$;  MOV    #3,R1    ;DIGIT SHIFT COUNT
001372 116324 001652      MOV    DIGTAB(0),(4)+ ;LOAD DIGIT INTO MESSAGE
001376 005003      CLR   R3        ;CLEAR INDEX
001400 005300      DEC   R0        ;DEC DIGIT COUNT
001402 001365      BNE   2$
001404 004767 176630      JSR    PC,$RESTR ;RESTORE REGISTERS FROM STACK
001410 000207      RTS   PC

;ERROR SERVICE CALLED BY TRAP (HLT) INSTRUCTION
001412 005737 177570      ERROR; TST    0#SWR ;HALT ON ERROR?
001416 100002      BPL   ,+6
001420 000000      HALT ;ERROR PC IS THE TOP WORD
001422 000002      RTI  ;ON THE STACK
001424 032737 020000 177570  BIT    #20000,0#SWR ;PRINT OUT DESIRED?
001432 001073      BNE   1$        ;BRANCH IF NO PRINTOUT
001434 011627      MOV    (SP+),(PC)+ ;SAVE PC
001436 000000      .WORD 0        ;CONTAINS SAVED PC
001440 016627 000002      MOV    2(SP),(PC)+ ;GET STATUS ON TRAP
001444 000000      .WORD 0        ;CONTAINS STATUS (PSW) AT TIME OF TRAP
001446 004767 176542      JSR    PC,$SAVR ;GO SAVE REGISTERS ON THE STACK
001452 013702 001000      MOV    #1CNT,R2  ;GET PASS COUNT
001456 004767 177646      JSR    PC,$FORM0 ;GO TO FORMAT ROUTINE
001462 016767 000176 000212  MOV    DIGITS+2,PASSES ;LOAD ASCII VALUES
001470 016767 000172 000206  MOV    DIGITS+4,PASSES+2
001476 004767 177530      JSR    PC,,PRINT ;PRINT MESSAGE BEGINING AT FOLLOWING ADRS
001502 001672      PASCNT
001504 016702 177726      MOV    11$,R2   ;GET PC OF ERROR CALL
001510 005742      TST   -(R2)    ;DECREMENT PC TO HLT
001512 004767 177612      JSR    PC,$FORM0 ;GO TO FORMAT ROUTINE
001516 004767 177510      JSR    PC,,PRINT ;PRINT MESSAGE BEGINING AT FOLLOWING ADRS
001522 001707      ERPC
001524 004767 177502      JSR    PC,,PRINT ;PRINT MESSAGE BEGINING AT FOLLOWING ADRS
001530 001662      DIGITS
001532 004767 177474      JSR    PC,,PRINT ;PRINT MESSAGE BEGINING AT FOLLOWING ADRS
001536 001714      STATUS
001540 016702 177700      MOV    12$,R2  ;GET STAU AT TIME OF ERROR
001544 004767 177560      JSR    PC,$FORM0 ;GO TO FORMAT ROUTINE
001550 004767 177456      JSR    PC,,PRINT ;PRINT MESSAGE BEGINING AT FOLLOWING ADRS
001554 001662      DIGITS
001556 016702 177654      MOV    11$,R2  ;GET PC OF ERROR
001562 005742      TST   -(R2)
001564 005737 001004      TST   0#FACTOR
001570 001412      BEQ   10$
001572 163702 001004      SUB   0#FACTOR,R2 ;FORM PC OF ORIGINAL CODE
001576 004767 177526      JSR    PC,$FORM0 ;GO TO FORMAT ROUTINE
001602 004767 177424      JSR    PC,,PRINT ;PRINT MESSAGE BEGINING AT FOLLOWING ADRS
001606 001721      ERRPC0
001610 004767 177416      JSR    PC,,PRINT ;PRINT MESSAGE BEGINING AT FOLLOWING ADRS
001614 001662      DIGITS
    
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001616
001616 004767 176416 10S: JSR PC, $RESTR ;RESTORE REGISTERS FROM STACK
001622 032737 002000 177570 1S: BIT #200, #SHR ;RING BELL ON ERROR
001630 001403 BEQ 2S
001632 004767 177374 JSR PC, PRINT ;PRINT MESSAGE BEGINING AT FOLLOWING ADRS
001636 001747 BELL
001640 005737 177570 2S: TST #SWR ;HALT AFTER PRINT OUT
001644 100001 BPL ,+4
001646 000000 HALT
001650 000002 RTI

;DIGIT TABLE
001652 030460 DIGTAB: "01
001654 031462 "23
001656 032464 "45
001660 033466 "67
001662 030060 030060 030060 DIGITS: ,ASCIZ '000000 '
001670 000040
001672 005015 PASCNT: ,ASCII <15><12>
001674 044440 047103 036524 ,ASCII ' ICNT='
001702 030060 030060 000 PASSES: ,ASCIZ '0000'
001707 040 041520 000075 ERRPC: ,ASCIZ ' PC='
001714 051520 036527 000 STATUS: ,ASCIZ 'PSW='
001721 120 020103 042522 ERRPC0: ,ASCIZ 'PC RELOCATED FROM !
001726 047514 040503 042524
001734 020104 051106 046517
001742 000040
001744 005015 000 $CRLF: ,ASCIZ <15><12>
001747 007 000 BELL: ,ASCIZ <7>
001752 ,EVEN

;ROUTINE TO GET TYPED OCTAL ADDRESS AND CONVERT TO OCTAL, CALL:
;
; JSR R5, RECD
; ,WORD 0 ;CONVERTED DATA IS PLACED HERE
; RECD: MOV R0, -(SP) ;SAVE R0 ON THE STACK
; CLR (R5) ;CLEAR OLD DATA
001752 010046 177560 1S: TSTB #TKS ;WAIT FOR USER TO TYPE CHARACTER
001754 005015 BPL 1S
001756 105737 177562 MOV B, R0 ;GET CHARACTER
001762 100375 BIC #200, R0 ;STRIP MSB
001764 113700 177562 CMPB #177, R0 ;CHECK IF RUBOUT
001770 042700 000200 BNE 2S ;BRANCH IF NOT RUBOUT
001774 122700 000177 MOV B, #\;#TRB ;TYPE \
002000 001010 CLC ;CLEAR CARRY
002002 112737 000134 177566 ROR (R5) ;SHIFT LAST TYPED CHARACTER
002010 000241 ASR (R5) ;OUT OF DATA WORD
002012 006015 ASR (R5)
002014 006215 BR 1S ;GO WAIT FOR NEXT CHARACTER
002016 006215
002020 000756

002022 110037 177566 2S: MOV B, R0;#TPB ;ECHO CHARACTER TYPED
002026 122700 000015 CMPB #15, R0 ;CHECK IF CARRIAGE RETURN
002032 001005 BNE 3S ;BRANCH IF NOT CARRIAGE RETURN
002034 004767 177172 JSR PC, PRINT ;PRINT MESSAGE BEGINING AT FOLLOWING ADRS
002040 001744 $CRLF

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002042 005725 TST (R5)+ ;STEP RETURN ADDRESS
002044 000205 RTS R5 ;RETURN

002046 042700 177770 3S: BIC #177770, R0 ;STRIP NON-ESSENTIAL BITS
002052 006315 ASL (R5) ;SHIFT LAST CHARACTER 3 PLACES
002054 006315 ASL (R5) ;LEFT
002056 006315 ASL (R5)
002060 050015 BIS R0, (R5) ;AND INSERT NEW CHARACTER
002062 000735 BR 1S ;WAIT FOR NEXT CHARACTER

002064 000002 RTI ;RETURN

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          ,TITLE D2QKC-C BASIC 11 FAMILY INSTRUCTION EXER,
002066 005037 177776      START: CLR  #PSW          ;KERNEL MODE
002072 005000             CLR  R0           ;CLEAR R0-R9
002074 005001             CLR  R1
002076 005002             CLR  R2
002100 005003             CLR  R3
002102 005004             CLR  R4
002104 005005             CLR  R5
002106 012706 000600     MOV  #KPTR,SR      ;SET KERNEL STACK PTR

          ;ROUTINE TO DETERMINE LAST MEMORY ADDRESS
002112 012737 002132 000004  MOV  #1, #ERRVEC
002120 005037 000006     CLR  #ERRVEC+2
002124 005000             CLR  R0
002126 005720             TST  (R0)+          ;FILL TIME OUTWHEN END OF MEMORY
002130 000776             BR   .-2
002132 162700 000002     1S: SUB  #2,R0
002136 010027             MOV  R0,(PC)+      ;SET VALUE INTO LSTMEM
002140 000000             LSTMEM, #WORD 0          ;CONTAINS VALUE OF LAST MEMORY ADDRESS
002142 162737 004000 002140  SUB  #400, #LSTMEM ;SET PROTECTION FOR LOADERS
002150 012737 020000 001012  MOV  #2000, #FRSTMEM ;SET LOWER BOUNDARY AT 20000
002156 000422             BR   START3      ;GO TO START 3
002160             START1:
002160 004767 177046     JSR  PC, #PRINT      ;PRINT MESSAGE BEGINING AT FOLLOWING ADRS
002164 016576             MSG1
002166 004567 177560     JSR  R5, RECD      ;GET LOWER LIMIT
002172 000000             1S: #WORD 0          ;CONTAINS TYPED LOWER LIMIT
002174 016737 177772 001012  MOV  1S, #FRSTMEM ;SET IN LOWER LIMIT
002202 004767 177024     JSR  PC, #PRINT      ;PRINT MESSAGE BEGINING AT FOLLOWING ADRS
002206 016613             MSG2
002210 004567 177536     JSR  R5, RECD      ;GET UPPER LIMIT
002214 000000             2S: #WORD 0          ;CONTAINS UPPER LIMIT
002216 016737 177772 002140  MOV  2S, #LSTMEM

002224 005037 001000     START3: CLR  #N1CNT      ;CLEAR PASS COUNT
002230 012737 000006 000004  START2: #ERRVEC+2, #ERRVEC ;SET ERROR TRAP TO HALT AT 6
002236 012706 000500     MOV  #STKPTR, SP    ;SET STACK PTR
002242 013737 001000 177570  MOV  #N1CNT, #DISP ;DISPLAY PASS COUNT
002250 012737 001016 000030  MOV  #SCOPEA, #EMTVEC ;SET EMT(SCOPE) TRAP VECTOR
002256 012737 001412 000034  MOV  #ERROR, #TRAPVEC ;SET TRAP (HLT) VECTOR
002264 012737 000200 000036  MOV  #200, #TRAPVEC+2 ;PRIORITY LEVEL 4 ON TRAP

          ;0000000000000000 FIRST ADDRESS TO BE RELOCATED 00000000
002272 010700             REL0: MOV  PC, R0          ;GET PC
002274 005740             TST  -(R0)         ;R0 CONTAINS THE ADDRESS OF REL0
002276 010037 001010     MOV  R0, #FRSTAD   ;SAVE
002302 010700             MOV  PC, R0          ;GET CURRENT PC
002304 162700 002304     SUB  #, R0          ;SUBTRACT RELOCATION FACTOR
002310 010037 001004     MOV  R0, #FACTOR   ;SAVE RELOCATION FACTOR
002314 010701             MOV  PC, R1          ;SET NEW SCOPE PTR

          ;CHECK BRANCH INSTRUCTIONS
002316 000257             CCC  C00           ;CC'S=0000
002320 103407             BCS  C00           ;SAME AS BLO

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002322 102406             BVS  C00
002324 001405             BEQ  C00
002326 100404             BMI  C00
002330 002403             BLT  C00
002332 003402             BLE  C00
002334 101401             BLOS C00
002336 101001             BMI  .+4
002340 104400             HLT

          CC0: HLT ;ONE OF THE ABOVE BRANCHES FAILED

          ;CONTINUE
002342 000270             SEN
002344 100003             BPL  CC1
002346 002002             BGE  CC1
002350 003001             BGT  CC1
002352 002401             BLT  .+4
002354 104400             HLT

          CC1: HLT ;ONE OF THE ABOVE BRANCHES FAILED

          ;CONTINUE
002356 000262             SEV
002360 102003             BVC  CC2
002362 002402             BLT  CC2
002364 003401             BLE  CC2
002366 002001             BGE  .+4
002370 104400             HLT

          CC2: HLT ;ERROR! ONE OF THE ABOVE BRANCHES FAILED

          ;CONTINUE
002372 000261             SEC
002374 103002             BCC  CC3
002376 101001             BMI  CC3
002400 003001             BGT  .+4
002402 104400             HLT

          CC3: HLT ;ERROR! ONE OF THE ABOVE BRANCHES FAILED

          ;CONTINUE
002404 000264             SEZ
002406 001003             BNE  CC4
002410 003002             BGT  CC4
002412 101001             BMI  CC4
002414 003401             BLE  .+4
002416 104400             HLT
002420 104400             SCOPE

          ;TEST UNARY CONDITION CODES
          ;CLR
002422 000277             CLR  R0
002424 000244             SCC
002426 005000             CLR  R0
002430 103404             BCS  CLR0
002432 102403             BVS  CLR0
002434 001002             BNE  CLR0
002436 100401             BMI  CLR0
002440 003401             BLE  .+4
002442 104400             HLT

          CLR0: HLT ;ERROR! INCORRECT CC'S AFTER CLR

002444 000277             SCC

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002446	000244	CLC		
002450	005700	TST	R0	IR0=0,CC'S=0100
002452	103404	BCS	TST0	
002454	102403	BVS	TST0	
002456	001002	BNE	TST0	
002460	100401	BMI	TST0	
002462	101401	BLOS	,+4	
002464	104400	HLT		TST0: ERROR! INCORRECT CC'S AFTER TST
002466	000257	CCC		
002470	000266	+SEZ:SEV		
002472	005100	COM	R0	IR0=-1,CC'S=1001
002474	103004	BCC	COM0	
002476	102403	BVS	COM0	
002500	001402	BEQ	COM0	
002502	100001	BPL	COM0	
002504	002401	BLT	,+4	
002506	104400	HLT		COM0: ERROR! INCORRECT CC'S AFTER COM
002510	000261	SEC		
002512	005500	ADC	R0	IR0=000000,CC'S=0101
002514	103003	BCC	ADC0	
002516	102402	BVS	ADC0	
002520	001001	BNE	ADC0	
002522	002001	BGE	,+4	
002524	104400	HLT		ADC0: ERROR! INCORRECT CC'S AFTER ADG
002526	000261	SEC		
002530	006000	ROR	R0	IR0=100000,CC'S=1010
002532	103404	BCS	ROR0	
002534	102003	BVC	ROR0	
002536	001402	BEQ	ROR0	
002540	100001	BPL	ROR0	
002542	003001	BGT	,+4	
002544	104400	HLT		ROR0: ERROR! INCORRECT CC'S AFTER ROR
002546	000277	SCC		
002550	000242	CLV		
002552	005300	DEC	R0	IR0=077777,CC'S=0011
002554	103004	BCC	DEC0	
002556	102003	BVC	DEC0	
002560	001402	BEQ	DEC0	
002562	100401	BMI	DEC0	
002564	003401	BLE	,+4	
002566	104400	HLT		DEC0: ERROR! INCORRECT CC'S AFTER DEC
002570	000257	CCC		
002572	005200	INC	R0	IR0=100000,CC'S=1010
002574	103404	BCS	INC0	
002576	102003	BVC	INC0	
002600	001402	BEQ	INC0	
002602	100001	BPL	INC0	
002604	003001	BGT	,+4	
002606	104400	HLT		INC0: ERROR! INCORRECT CC'S AFTER INC

002610	000277	SCC		
002612	000242	CLV		
002614	005400	NEG	R0	IR0=100000,CC'S=1011
002616	103003	BCC	NEG0	
002620	102002	BVC	NEG0	
002622	001401	BEQ	NEG0	
002624	002001	BGE	,+4	
002626	104400	HLT		NEG0: ERROR! INCORRECT CC'S AFTER NEG
002630	000261	SEC		
002632	006300	ASL	R0	IR0=000000,CC'S=0111
002634	103004	BCC	ASL0	
002636	102003	BVC	ASL0	
002640	001002	BNE	ASL0	
002642	100401	BMI	ASL0	
002644	101401	BLOS	,+4	
002646	104400	HLT		ASL0: ERROR! INCORRECT CC'S AFTER ASL
002650	006100	ROL	R0	IR0=000001,CC'S=0000
002652	103402	BCS	ROL0	
002654	003401	BLE	ROL0	
002656	002001	BGE	,+4	
002660	104400	HLT		ROL0: ERROR! INCORRECT CC'S AFTER ROL
002662	006200	ASR	R0	IR0=000000,CC'S=0111
002664	103003	BCC	ASR0	
002666	102002	BVC	ASR0	
002670	001001	BNE	ASR0	
002672	002401	BLT	,+4	
002674	104400	HLT		ASR0: ERROR! INCORRECT CC'S AFTER ASR
002676	000277	SCC		
002700	005600	SBC	R0	IR0=-1,CC'S=1001
002702	103002	BCC	SBC0	
002704	102401	BVS	SBC0	
002706	003401	BLE	,+4	
002710	104400	HLT		SBC0: ERROR! INCORRECT CC'S AFTER SBC
002712	005400	NEG	R0	IR0=000001,CC'S=00001
002714	000300	SWAB	R0	IR0=000400,CC'S=0100
002716	103403	BCS	SWAB0	
002720	102402	BVS	SWAB0	
002722	001001	BNE	SWAB0	
002724	002001	BGE	,+4	
002726	104400	HLT		SWAB0: ERROR! INCORRECT CC'S AFTER SWAB
002730	104000	SCOPE		
002732	005000	JCHECK REGISTER SELECTION		
002734	000277	CLR	R0	
002736	006100	SCC		
002740	010002	ROL	R0	IR0=1
002742	006302	MOV	R0,R2	
002744	010203	ASL	R2	IR2=2
		MOV	R2,R3	

002746	006303		ASL	R3		R3=4
002750	010304		MOV	R3,R4		
002752	006304		ASL	R4		R4=10
002754	010405		MOV	R4,R5		
002756	006305		ASL	R5		R5=20
002760	010546		MOV	R5,*(SP)		SET BITS SET IN REGISTERS
002762	004116		BIS	R4,(4SP)		INTO STACK ADDRESS
002764	003316		BIS	R3,(4SP)		
002766	002216		BIS	R2,(4SP)		
002770	000016		BIS	R0,(4SP)		
002772	022726	000037	CMP	#37,(SP)+		
002776	001401		BEQ	,+4		HERE SET
003000	104400		HLT			MISSING BIT(S) REPRESENT INCORRECT REGISTER SELECTION

ICHECK THAT ALL BITS CAN BE SET & CLEARED IN ALL REGISTERS

003002	000257		CCC			
003004	112700	000377	MOV	#377,R0		SET ALL BITS (MOV EXTENDS SIGN)
003010	006100		ROL	R0		ROTATE A 0 THROUGH ALL BIT
003012	103776		BCS	1\$		POSITIONS
003014	005200		INC	R0		FINAL RESULT IS -1
003016	001401		BEQ	,+4		
003020	104400		HLT			ERROR!
003022	012700	000020	MOV	#16,R0		SET SHIFT COUNT
003026	005002		CLR	R2		
003030	000241		SEC			
003032	006002		ROR	R2		ROTATE 1 THROUGH ALL BIT POSITS
003034	005300		DEC	R0		DECREMENT SHIFT COUNT
003036	001374		BNE	2\$		
003040	005102		COM	R2		R2 SHOULD CONTAIN -1
003042	001401		BEQ	,+4		
003044	104400		HLT			ERROR! CHECK R2 SHOULD = 0
003046	012703	100000	MOV	#100000,R3		
003052	006203		ASR	R3		EXTEND 1 BIT THROUGH ALL POSITIONS
003054	103376		BCC	3\$		
003056	005203		INC	R3		
003060	001401		BEQ	,+4		
003062	104400		HLT			ERROR!
003064	112704	177401	MOV	#177401,R4		R4=1
003070	000404		ADD	R4,R4		HAS THE AFFECT OF SHIFTING A BIT
003072	103376		BCC	4\$		THROUGH ALL POSITIONS
003074	005704		TST	R4		RESULT SHOULD BE 0
003076	001401		BEQ	,+4		
003100	104400		HLT			
003102	012705	000001	MOV	#1,R5		
003106	006305		ASL	R5		
003110	102376		BVC	5\$		
003112	006305		ASL	R5		
003114	103002		BCC	6\$		
003116	005705		TST	R5		

003120	001401		BEQ	,+4		
003122	104400		HLT			
003124	005002		CLR	R2		
003126	005102		COM	R2		R2=-1
003130	010203		MOV	R2,R3		
003132	000257		CCC			
003134	006002		ROR	R2		R2=LOOP COUNT
003136	006202		ASR	R2		
003140	010304		MOV	R3,R4		
003142	005302		DEC	R2		DECREMENT LOOP COUNT
003144	001375		BNE	7\$		
003146	005203		INC	R3		CHECK R3
003150	001002		BNE	8\$		
003152	005204		INC	R4		CHECK R4
003154	001401		BEQ	,+4		
003156	104400		HLT			
003160	032737	000020	177776	ICHECK TRANSFER OF REGISTER DATA BETWEEN THE GS AND GD REGISTERS (11/45)		
003166	001052		BIT	#20,0#PSW		CHECK IF 'I' BIT IS SET
003170	010146		BNE	7\$		SKIP TEST IF 'I' BIT SET
003172	010627		MOV	R1,*(SP)		SAVE SCOPE PTR
003174	000000		MOV	SP,(PC)+		SAVE STACK PTR
003176	010727		.WORD	0		CONTAINS SAVED STACK PTR
003200	000000		.WORD	0		LOAD DATA, THE CURRENT PC IS USED AS
003202	005267	177772	177766	INC	2\$	DATA. IF THIS TEST FAILS 2\$ CON-
003206	016700		MOV	2\$,R0		TAINS THE DATA BEING USED,
003212	010001		MOV	R0,R1		MAKE ODD TO CHECK BIT 0
003214	010102		MOV	R1,R2		LOAD GD REGISTER 0
003216	010203		MOV	R2,R3		TRANSFER GS REG 0 TO GD REG 1
003220	010304		MOV	R3,R4		AND GS REG 1 TO GD REG 2
003222	010405		MOV	R4,R5		ETC...
003224	192737	000340	177776	BISB	#340,0#PSW	SET PRIORITY LEVEL 7
003232	010506		MOV	R5,SP		TRANSFER GS REG 5 TO GD STK PTR
003234	010627		MOV	SP,(PC)+		TRANSFER GS STK PTR TO MEMORY
003236	000000		.WORD	0		CONTAINS GS STACK PTR
003240	016706	177730	177776	MOV	1\$,SP	RESTORE STK PTR NEEDED FOR HLT/SCOPE
003244	142737	000340	177766	BICB	#340,0#PSW	SET PRIORITY LEVEL 0
003252	026700		CMP	4\$,R0		COMPARE GS/GD STKPTR WITH GS REG 0
003256	001004		BNE	5\$		BRANCH IF THEY WERE NOT =
003260	006367	177714	ASL	2\$		SHIFT TEST DATA UNTIL = 000000
003264	001350		BNE	3\$		
003266	000411		BR	6\$		
003270	010046		MOV	R0,*(SP)		GET GS REG 0
003272	010146		MOV	R1,*(SP)		ETC...
003274	010246		MOV	R2,*(SP)		
003276	010346		MOV	R3,*(SP)		
003300	010446		MOV	R4,*(SP)		
003302	010546		MOV	R5,*(SP)		
003304	104400		HLT			ERROR! DATA IN GS STK PTR NOT = GS REG 0 GS REG 0-GS REG 5 ARE ON THE STACK

003306	016706	177662		MOV	15,SP	RESTORE STACK PTR
003312	012601		65:	MOV	(SP)+,R3	RESTORE SCOPE PTR
003314	104000		75:	SCOPE		
)TEST UNARY WORD INSTRUCTIONS USING ADDRESS MODE 1						
003316	000401			BR	,+4	
003320	000000			,WORD	0	RESERVE ADDRESS FOR TESTS
003322	010702			MOV	PC,R2	
003324	102702	000004		SUB	#4,R2	R2 POINTS TO RESERVED WORD
003330	005012			CLR	(R2)	PRESET (R2)
003332	000261			SEC		
003334	006012			ROR	(R2)	(R2)=100000,CC=1010
003336	101402			BLOS	ROR1	
003340	100001			BPL	ROR1	
003342	002001			BGE	,+4	
003344	104400		ROR1:	HLT		ERROR: INCORRECT CC'S AS SHOWN ABOVE
003346	000257			CCC		
003350	000261			SEC		
003352	005312			DEC	(R2)	(R2)=077777,CC=0011
003354	103001			BCC	DEC1	
003356	003401			BLE	,+4	
003360	104400		DEC1:	HLT		ERROR: INCORRECT CC'S AS SHOWN ABOVE
003362	000257			CCC		
003364	000261			SEC		
003366	005512			ADC	(R2)	(R2)=100000,CC=1010
003370	103403			BCS	ADC1	
003372	102002			BVC	ADC1	
003374	100001			BPL	ADC1	
003376	001001			BNE	,+4	
003400	104400		ADC1:	HLT		ERROR: INCORRECT CC'S AS SHOWN ABOVE
003402	006112			ROL	(R2)	(R2)=000000,CC=0111
003404	103003			BCC	ROL1	
003406	102002			BVC	ROL1	
003410	001001			BNE	ROL1	
003412	100001			BPL	ROL1	
003414	104400		ROL1:	HLT		ERROR: INCORRECT CC'S AS SHOWN ABOVE
003416	006112			ROL	(R2)	(R2)=000001,CC=0000
003420	101402			BLOS	ROL1A	BRANCH IF C OR Z IS SET
003422	102401			BVS	ROL1A	
003424	100001			BPL	ROL1A	
003426	104400		ROL1A:	HLT		
003430	006212			ASR	(R2)	(R2)=000000,CC=0111
003432	103003			BCC	ASR1	
003434	102002			BVC	ASR1	
003436	001001			BNE	ASR1	
003440	100001			BPL	,+4	
003442	104400		ASR1:	HLT		ERROR: INCORRECT CC'S AS SHOWN ABOVE

003444	006012			ROR	(R2)	(R2)=100000,CC=1010
003446	103403			BCS	ROR1A	
003450	102002			BVC	ROR1A	
003452	001401			BEQ	ROR1A	
003454	100401			BMI	,+4	
003456	104400		ROR1A:	HLT		
003460	000261			SEC		
003462	005212			INC	(R2)	(R2)=100001,CC=1001
003464	103003			BCC	INC1	
003466	102402			BVS	INC1	
003470	001401			BEQ	INC1	
003472	100401			BMI	,+4	
003474	104400		INC1:	HLT		ERROR: INCORRECT CC'S AS SHOWN ABOVE
003476	005612			SBC	(R2)	(R2)=100000,CC=1000
003500	103403			BCS	SBC1	
003502	102402			BVS	SBC1	
003504	001401			BEQ	SBC1	
003506	100401			BMI	,+4	
003510	104400		SBC1:	HLT		ERROR: INCORRECT CC'S AS SHOWN ABOVE
003512	000261			SEC		
003514	005612			SBC	(R2)	(R2)=077777,CC=0010
003516	103403			BCS	SBC1A	
003520	102002			BVC	SBC1A	
003522	001401			BEQ	SBC1A	
003524	100001			BPL	,+4	
003526	104400		SBC1A:	HLT		ERROR: INCORRECT CC'S AS SHOWN ABOVE
003530	000261			SEC		
003532	005512			ADC	(R2)	(R2)=100000,CC=1010
003534	100401			BMI	,+4	
003536	104400			HLT		
003540	000261			SEC		
003542	006312			ASL	(R2)	(R2)=000000,CC=0111
003544	103003			BCC	ASL1	
003546	102002			BVC	ASL1	
003550	001001			BNE	ASL1	
003552	100001			BPL	,+4	
003554	104400		ASL1:	HLT		ERROR: INCORRECT CC'S AS SHOWN ABOVE
003556	005112			COM	(R2)	(R2)=177777,CC=1001
003560	103002			BCC	COM1	
003562	102401			BVS	COM1	
003564	100401			BMI	,+4	
003566	104400		COM1:	HLT		ERROR: INCORRECT CC'S AS SHOWN ABOVE
003570	000250			CLN		
003572	005712			TST	(R2)	(R2)=177777,CC=1000
003574	103403			BCS	TST1	
003576	102402			BVS	TST1	
003600	100001			BPL	TST1	

003602	001001		BNE	,+4	
003604	104400	TST1:	HLT		!ERROR! INCORRECT CC'S AS SHOWN ABOVE
003606	000262		SEV		
003610	005412		NEG	(R2)	!(R2)=000001,CC=0000
003612	103002		BCC	NEG1	
003614	102401		BVS	NEG1	
003616	001001		BNE	,+4	
003620	104400	NEG1:	HLT		!ERROR! INCORRECT CC'S AS SHOWN ABOVE
003622	005312		DEC	(R2)	!(R2)=000000,CC=0101
003624	103001		BCC	DEC1A	
003626	001401		BEQ	,+4	
003630	104400	DEC1A:	HLT		!ERROR! INCORRECT CC'S AS SHOWN ABOVE
003632	104000		SCOPE		
!CHECK UNARY BYTE INSTRUCTIONS USING ADDRESS MODE 1					
003634	000401		BR	,+4	!RESERVE A WORD
003636	000000		.WORD	0	!ADDRESS RESERVED FOR TESTS
003640	010703	020004	MOV	PC,R3	
003642	102703		SUB	#4,R3	!R3 POINTS TO EVEN BYTE OF WORD
003644	010304		MOV	R3,R4	!R4 POINTS TO ODD BYTE OF WORD
003650	005204		INC	R4	
003652	005013		CLR	(R3)	!PRESET DATA
003654	000261	1S:	SEC		
003656	105513		ADCB	(R3)	!ADD CARRY TO EVEN BYTE
003660	100402		BMI	2S	!UNTIL EVEN BYTE BECOMES NEGATIVE
003662	105214		INCB	(R4)	!INCREMENT ODD BYTE
003664	000773		BR	1S	
003666	102401	2S:	BVS	,+4	!(R3)=077600=[0774][200],CC=1010
003670	104400		HLT		
003672	000242		CLV		
003674	105214		INCB	(R4)	!(R3)=100200=[1000][200],CC=1010
003676	103402		BCC	INCB1	
003700	102001		BVC	INCB1	
003702	100401		BMI	,+4	
003704	104400	INCB1:	HLT		!ERROR! INCORRECT CC'S AS SHOWN ABOVE
003706	106114		ROLB	(R4)	!(R3)=000200=[0000][200],CC=0111
003710	103002		BCC	ROLB1	
003712	102001		BVC	ROLB1	
003714	001401		BEQ	,+4	
003716	104400	ROLB1:	HLT		!ERROR! INCORRECT CC'S AS SHOWN ABOVE
003720	105614		SBCB	(R4)	!(R3)=177600=[1774][200],CC=1001
003722	103002		BCC	SBCB1	
003724	102401		BVS	SBCB1	
003726	100401		BMI	,+4	
003730	104400	SBCB1:	HLT		!ERROR! INCORRECT CC'S AS SHOWN ABOVE
003732	106313		ASLB	(R3)	!(R3)=177400,CC=0111
003734	103002		BCC	ASLB1	
003736	102001		BVC	ASLB1	

003740	001401		BEQ	,+4	
003742	104400	ASLB1:	HLT		!ERROR! INCORRECT CC'S AS SHOWN ABOVE
003744	105413		NEGB	(R3)	!(R3)=177400,CC=0100
003746	103402		BCC	NEGB1	
003750	102401		BVS	NEGB1	
003752	001401		BEQ	,+4	
003754	104400	NEGB1:	HLT		!ERROR! INCORRECT CC'S AS SHOWN ABOVE
003756	000277		SCC		
003760	105313		DECB	(R3)	!(R3)=177777,CC=1001
003762	103002		BCC	DECB1	
003764	102401		BVS	DECB1	
003766	001001		BNE	,+4	
003770	104400	DECB1:	HLT		!ERROR! INCORRECT CC'S AS SHOWN ABOVE
003772	000241		CLC		
003774	106013		RORB	(R3)	!(R3)=177577,CC=0011
003776	103002		BCC	RORB1	
004000	102001		BVC	RORB1	
004002	100001		BPL	,+4	
004004	104400	RORB1:	HLT		!ERROR! INCORRECT CC'S AS SHOWN ABOVE
004006	000241		CLC		
004010	105114		COMB	(R4)	!(R3)=000177,CC=0101
004012	103002		BCC	COMB1	
004014	102401		BVS	COMB1	
004016	001401		BEQ	,+4	
004020	104400	COMB1:	HLT		!ERROR! INCORRECT CC'S AS SHOWN ABOVE
004022	106213	1S:	ASRB	(R3)	!SHIFT EVEN BYTE UNTIL V CLEARS
004024	102002		BVC	2S	
004026	105514		ADCB	(R4)	!AND ADD CARRY TO ODD BYTE
004030	000774		BR	1S	
004032	103401	2S:	BCC	ASRB1	
004034	001401		BEQ	,+4	
004036	104400	ASRB1:	HLT		!ERROR! INCORRECT CC'S AS SHOWN ABOVE
004040	106214		ASRB	(R4)	!(R3)=000400,CC=0011
004042	106214		ASRB	(R4)	
004044	103002		BCC	ASRB1A	
004046	102001		BVC	ASRB1A	
004050	001001		BNE	,+4	
004052	104400	ASRB1A:	HLT		!ERROR! INCORRECT CC'S AS SHOWN ABOVE
004054	105314		DECB	(R4)	!(R3)=000000,CC=0100
004056	001401		BEQ	,+4	
004060	104400		HLT		!ERROR! INCORRECT CC'S AS SHOWN ABOVE
004062	000261		SEC		
004064	106014		RORB	(R4)	!(R3)=100000,CC=1010
004066	103402		BCC	RORB1A	
004070	102001		BVC	RORB1A	
004072	100401		BMI	,+4	

004074	104400	RORB1A:	HLT			;ERROR! INCORRECT CC'S AS SHOWN ABOVE
004076	000242		CLV			
004100	105314		DECB	(R4)		; (R3)=077400,CC=0100
004102	102401		BVS	,+4		
004104	104400		HLT			
004106	000261		SEC			
004110	105313		DECB	(R3)		; (R3)=077777,CC=1001
004112	103002		BCC	DECB1A		
004114	102401		BVS	DECB1A		
004116	100401		BMI	,+4		
004120	104400	DECB1A:	HLT			;ERROR! INCORRECT CC'S AS SHOWN ABOVE
004122	000277		SCC			
004124	000313		SWAB	(R3)		; (R3)=177577=[1774][177],CC=0000
004126	103402		BCC	SWAB1		
004130	102401		BVS	SWAB1		
004132	100001		BPL	,+4		
004134	104400	SWAB1:	HLT			;ERROR! INCORRECT CC'S AS SHOWN ABOVE
004136	105714		TSTB	(R4)		; (R3)=177577=[1774][177],CC=1000
004140	103402		BCC	TSTB1		
004142	102401		BVS	TSTB1		
004144	100401		BMI	,+4		
004146	104400	TSTB1:	HLT			;ERROR! INCORRECT CC'S AS SHOWN ABOVE
004150	105014		CLRB	(R4)		; (R3)=000177=[0000][177],CC=0100
004152	001401		BEQ	,+4		
004154	104400		HLT			
004156	106313		ASLB	(R3)		; (R3)=000376 ,CC=1010
004160	103402		BCC	ASLB1A		
004162	102001		BVC	ASLB1A		
004164	100401		BMI	,+4		
004166	104400	ASLB1A:	HLT			;ERROR! INCORRECT CC'S AS SHOWN ABOVE
004170	105113		COMB	(R3)		; (R3)=000001,CC=0001
004172	103002		BCC	COMB1A		
004174	102401		BVS	COMB1A		
004176	100001		BPL	,+4		
004200	104400	COMB1A:	HLT			;ERROR! INCORRECT CC'S AS SHOWN ABOVE
004202	000313		SWAB	(R3)		; (R3)=000400, CC=0100
004204	001401		BEQ	,+4		
004206	104400		HLT			
004210	105213		INCB	(R3)		
004212	000261		SEC			
004214	105613		SBCB	(R3)		; (R3)=000400,CC=0100
004216	001401		BEQ	,+4		
004220	104400		HLT			
004222	022713	000400	CHP	#400,(R3)		;CHECK REMAINING RESULT
004226	001401		BEQ	,+4		
004230	104400		HLT			

004232	104000		SCOPE			
004234	000401		ICHECK	UNARY WORD OPS USING ADDRESS MODES 2 AND 4 (AUTO INC/DEC)		
004236	000000		BR	,+4		
004240	010704		,WORD	0		;ADDRESS RESERVED FOR TESTS
004242	102704	020004	MOV	PC,R4		
004246	010405		SUB	#4,R4		;R4 AND R5 POINT TO
004250	005015		MOV	R4,R5		;RESERVED WORD
			CLR	(R5)		;PRESET DATA=0
004252	000277		SCC			
004254	000244		CLE			
004256	005725		TST	(R5)+		; (R5)=000000,CC=0100
004260	103402		BCC	TST2		
004262	102401		BVS	TST2		
004264	001401		BEQ	,+4		
004266	104400	TST2:	HLT			;ERROR! INCORRECT CC'S AS SHOWN ABOVE
004270	005145		COM	-(R5)		; (R5)=177777,CC=1001
004272	103001		BCC	COM4		
004274	100401		BMI	,+4		
004276	104400	COM4:	HLT			;ERROR! INCORRECT CC'S AS SHOWN ABOVE
004300	000241		CLC			
004302	006024		ROR	(R4)+		; (R4)=077777,CC=0011
004304	103002		BCC	ROR2		
004306	102001		BVC	ROR2		
004310	100001		BPL	,+4		
004312	104400	ROR2:	HLT			;ERROR! INCORRECT CC'S AS SHOWN ABOVE
004314	000257		CCC			
004316	005244		INC	-(R4)		; (R4)=100000,CC=1010
004320	102002		BVC	INC4		
004322	001401		BEQ	INC4		
004324	100401		BMI	,+4		
004326	104400	INC4:	HLT			;ERROR! INCORRECT CC'S AS SHOWN ABOVE
004330	000261		SEC			
004332	000324		SWAB	(R4)+		; (R4)=000200,CC=1000
004334	103401		BCC	SWAB2		
004336	100401		BMI	,+4		
004340	104400	SWAB2:	HLT			;ERROR! INCORRECT CC'S AS SHOWN ABOVE
004342	005425		NEG	(R5)+		; (R5)=177600,CC=1001
004344	103001		BCC	NEG2		
004346	100401		BMI	,+4		
004350	104400	NEG2:	HLT			;ERROR! INCORRECT CC'S AS SHOWN ABOVE
004352	005044		CLR	-(R4)		; (R4)=000000,CC=0100
004354	001401		BEQ	,+4		
004356	104400		HLT			
004360	000261		SEC			
004362	006045		ROR	-(R5)		; (R5)=100000,CC=1010

004364	000261		SEC		
004366	005225		ADC	(R5)+	;(R5)=100001,CC=1000
004370	102401		BVS	ADC2	
004372	100401		BMI	,+4	
004374	104400		ADC2:	HLT	!ERROR! INCORRECT CC'S AS SHOWN ABOVE
004376	000262		SEV		
004400	006224		ASR	(R4)+	;(R4)=140000,CC=1001
004402	103002		BCC	ASR2	
004404	102401		BVS	ASR2	
004406	100401		BMI	,+4	
004410	104400		ASR2:	HLT	!ERROR! INCORRECT CC'S AS SHOWN ABOVE
004412	000262		SEV		
004414	006144		ROL	-(R4)	;(R4)=100001, CC=1001
004416	103002		BCC	ROL4	
004420	102401		BVS	ROL4	
004422	100401		BMI	,+4	
004424	104400		ROL4:	HLT	!ERROR! INCORRECT CC'S AS SHOWN ABOVE
004426	005645		SBC	-(R5)	;(R5)=100000,CC=1000
004430	103001		BCC	,+4	
004432	104400		HLT		!ERROR! 'C' BIT FAILED TO CLEAR
004434	005325		DEC	(R5)+	;(R5)=077777,CC=0010
004436	103402		BCS	DEC2	
004440	102001		BVC	DEC2	
004442	100001		BPL	,+4	
004444	104400		DEC2:	HLT	!ERROR! INCORRECT CC'S AS SHOWN ABOVE
004446	006324		ASL	(R4)+	;(R4)=177776,CC=1010
004450	102401		BVS	,+4	
004452	104400		HLT		
004454	006344		ASL	-(R4)	;(R4)=177774,CC=1001
004456	103003		BCC	ASL4	
004460	102402		BVS	ASL4	
004462	001401		BEQ	ASL4	
004464	100401		BMI	,+4	
004466	104400		ASL4:	HLT	!ERROR! INCORRECT CC'S AS SHOWN ABOVE
004470	022724	177774	CMP	#177774,(R4)+	
004474	001401		BEQ	,+4	
004476	104400		HLT		
004500	020405		CMP	R4,R5	
004502	001401		BEQ	,+4	
004504	104400		HLT		
004506	104000		SCOPE		
!CHECK UNARY BYTE ORS USING ADDRESS MODES 2 AND 4					
004510	000401		BR	,+4	!RESERVE A WORD
004512	000000		,WORD	0	!RESERVED WORD
004514	010705		MOV	PC,R5	
004516	162705	000004	SUB	#4,R5	!R5 POINTS TO EVEN BYTE OF RESERVED WORD
004522	010500		MOV	R5,R0	

004524	010002		MOV	R0,R2	
004526	005202		INC	R2	!R2 POINTS TO ODD BYTE OF RESERVED WORD
004530	005010		CLR	(R0)	!PRESET
004532	000277		SCC		
004534	000241		CLC		
004536	105125		COMB	(R5)+	;(R5)=000377,CC=1001
004540	103002		BCC	COMB2	
004542	102401		BVS	COMB2	
004544	100401		BMI	,+4	
004546	104400		COMB2:	HLT	!ERROR! INCORRECT CC'S AS SHOWN ABOVE
004550	105542		ADCB	-(R2)	;(R2)=000000,CC=0101
004552	001401		BEQ	,+4	
004554	104400		HLT		!ERROR! INCORRECT RESULT AS SHOWN ABOVE
004556	105525		ADCB	(R5)+	;(R5)=000400,CC=0000
004560	103401		BCS	ADCB2	
004562	001201		BNE	,+4	
004564	104400		ADCB2:	HLT	!ERROR! INCORRECT CC'S AS SHOWN ABOVE
004566	000263		+SEC:SEV		
004570	106045		RORB	-(R5)	;(R5)=100000,CC=1001
004572	103003		BCC	RORB4	
004574	102402		BVS	RORB4	
004576	001401		BEQ	RORB4	
004600	100401		BMI	,+4	
004602	104400		RORB4:	HLT	!ERROR! INCORRECT CC'S AS SHOWN ABOVE
004604	000277		SCC		
004606	106122		ROLB	(R2)+	;(R2)=100001,CC=0000
004610	103403		BCS	ROLB2	
004612	102402		BVS	ROLB2	
004614	001401		BEQ	ROLB2	
004616	100001		BPL	,+4	
004620	104400		ROLB2:	HLT	!ERROR! INCORRECT CC'S AS SHOWN ABOVE
004622	000257		CCC		
004624	106225		ASRB	(R5)+	;(R5)=140001, CC=1010
004626	103402		BCS	ASRB2	
004630	102001		BVC	ASRB2	
004632	100401		BMI	,+4	
004634	104400		ASRB2:	HLT	!ERROR! INCORRECT CC'S AS SHOWN ABOVE
004636	105242		INCB	-(R2)	;(R2)=140002,CC=0000
004640	000277		SCC		
004642	106222		ASRB	(R2)+	;(R2)=140001,CC=0000
004644	103402		BCS	ASRB2A	
004646	102401		BVS	ASRB2A	
004650	100001		BPL	,+4	
004652	104400		ASRB2A:	HLT	!ERROR! INCORRECT CC'S AS SHOWN ABOVE
004654	000266		+SEZ:SEV		!SET Z,V
004656	106345		ASLB	-(R5)	;(R5)=100001,CC=1001
004660	103003		BCC	ASLB4	

004662	102402		BVS	ASLB4	
004664	001401		BEQ	ASLB4	
004666	100401		BMI	,+4	
004670	104400		ASLB4:	HLT	;ERROR! INCORRECT CC'S AS SHOWN ABOVE
004672	105322		DECB	(R2)+	; (R0)=077401=[0774][001], CC=0010
004674	103002		BCC	DECB2	
004676	102001		BVC	DECB2	
004700	100001		BPL	,+4	
004702	104400		DECB2:	HLT	;ERROR! INCORRECT CC'S AS SHOWN ABOVE
004704	105645		SBCB	-(R5)	; (R0)=077400, CC=0100
004706	103402		BCS	SBCB4	
004710	102401		BVS	SBCB4	
004712	001401		BEQ	,+4	
004714	104400		SBCB4:	HLT	;ERROR! INCORRECT CC'S AS SHOWN ABOVE
004716	105442		NEGB	-(R2)	; (R0)=10400, CC=1001
004720	103002		BCC	NEGB4	
004722	102401		BVS	NEGB4	
004724	100401		BMI	,+4	
004726	104400		NEGB4:	HLT	;ERROR! INCORRECT CC'S AS SHOWN ABOVE
004730	105725		TSTB	(R3)+	; (R0)=100400, CC=0100
004732	103401		BCS	TSTB2	
004734	001401		BEQ	,+4	
004736	104400		TSTB2:	HLT	
004740	105722		TSTB	(R2)+	; (R0)=100400, CC=1000
004742	001401		BEQ	TSTB2A	
004744	100401		BMI	,+4	
004746	104400		TSTB2A:	HLT	
004750	000261		SEC		
004752	000342		SWAB	-(R2)	; (R0)=000201, CC=1000
004754	103401		BVS	SWAB4	
004756	100401		BMI	,+4	
004760	104400		SWAB4:	HLT	
004762	000277		SCC		
004764	105225		INCB	(R5)+	; (R0)=000601=[0004][201], CC=0000
004766	103003		BCC	INCB2	
004770	102402		BVS	INCB2	
004772	001401		BEQ	INCB2	
004774	100001		BPL	,+4	
004776	104400		INCB2:	HLT	
005000	022227	000601	CMP	(R2)+, #000601	;CHECK END RESULT
005004	001401		BEQ	,+4	
005006	104400		HLT		
005010	020205		CMP	R2, R5	;CHECK REGISTERS
005012	001401		BEQ	,+4	
005014	104400		HLT		
005016	104000		SCOPE		

;CHECK UNARY WORD OPS USING ADDRESS MODES 3 AND 5					
005020	000402		BR	,+6	;RESERVE 2 WORDS
005022	000000		,WORD	0	;1 FOR THE ADDRESS
005024	000000		,WORD	0	;AND 1 FOR DATA
005026	010703	000004	MOV	PC, R3	
005030	162703		SUB	#4, R3	
005034	005013		CLR	(R3)	;PRESET DATA
005036	010300		MOV	R3, R0	;R0 POINTS TO DATA WORD
005040	005743		TST	-(R3)	
005042	010013		MOV	R0, (R3)	
005044	010304		MOV	R3, R4	
005046	000257		CCC		
005050	005733		TST	0(R3)+	; (R0)=000000, CC=0100
005052	001401		BEQ	,+4	
005054	104400		HLT		
005056	000261		SEC		
005060	006053		ROR	0-(R3)	; (R0)=100000, CC=1010
005062	103402		BVS	ROR5	
005064	102001		BVC	ROR5	
005066	100401		BMI	,+4	
005070	104400		ROR5:	HLT	
005072	000257		CCC		
005074	006234		ASR	0(R4)+	; (R0)=140000, CC=1010
005076	102001		BVC	ASR3	
005100	100401		BMI	,+4	
005102	104400		ASR3:	HLT	
005104	000250		CLN		
005106	006333		ASL	0(R3)+	; (R0)=100000, CC=1001
005110	103002		BCC	ASL3	
005112	102401		BVS	ASL3	
005114	100401		BMI	,+4	
005116	104400		ASL3:	HLT	
005120	000277		SCC		
005122	005354		DEC	0-(R4)	; (R0)=077777, CC=0010
005124	103003		BCC	DEC5	
005126	102002		BVC	DEC5	
005130	001401		BEQ	DEC5	
005132	100001		BPL	,+4	
005134	104400		DEC5:	HLT	
005136	005453		NEG	0-(R3)	; (R0)=100001, CC=1001
005140	103002		BCC	NEG5	
005142	102401		BVS	NEG5	
005144	100401		BMI	,+4	
005146	104400		NEG5:	HLT	
005150	000262		SEV		
005152	005134		COM	0(R4)+	; (R0)=077776, CC=0001

005154	103001	BCC	COM3		
005156	102001	BVC	COM3	HLT	HLT
005160	104400				
005162	005233	INC	0-(R3)+		;(R0)=077777, CC=0001
005164	103001	BCC	INC3		
005166	100001	BPL	INC3	HLT	HLT
005170	104400				
005172	005554	ADC	0-(R4)		;(R0)=100000, CC=1010
005174	103402	BCC	ADC5		
005176	102001	BVC	ADC5		
005200	100401	BMI	ADC5	HLT	HLT
005202	104400				
005204	000257	CCC	0-(R4)+		;(R0)=000000, CC=0111
005206	006134	ROL	ROL3		
005210	103002	BCC	ROL3		
005212	102001	BVC	ROL3		
005214	001401	BEQ	ROL3	HLT	HLT
005216	104400				
005220	005253	INC	0-(R3)		;(R0)=000001, CC=0001
005222	005654	SBC	0-(R4)		;(R0)=000000, CC=0100
005224	103401	BCC	SBC5		
005226	001401	BEQ	SBC5	HLT	HLT
005230	104400				
005232	104000	SCDPE			
;CHECK UNARY BYTE OPS USING ADDRESS MODES 3 AND 5					
005234	000403	BR	0		;RESERVE 3 WORDS
005236	000200	.WORD	0		;1 FOR EVEN BYTE ADDRESS
005240	000000	.WORD	0		;1 FOR ODD BYTE ADDRESS
005242	000000	.WORD	0		;AND 1 FOR DATA
005244	010702	MOV	PC,R2		
005246	005742	YST	-(R2)		;BACK R2 UP TO
005250	005742	YST	-(R2)		;DATA WORD
005252	010200	MOV	R2,R0		;R0 POINTS TO THE DATA WORD
005254	005010	CLR	(R0)		;PRESET DATA
005256	005742	YST	-(R2)		;BACK R2 UP TO
005260	005742	YST	-(R2)		;EVEN BYTE ADDRESS WORD
005262	010022	MOV	R0,(R2)+		;LOAD ADDRESS
005264	005200	INC	R0		;ODD BYTE ADDRESS
005266	010022	MOV	R0,(R2)+		;LOAD ODD BYTE ADDRESS
005270	010200	MOV	R2,R0		;RESET R0
005272	010205	MOV	R2,R5		
005274	105152	COMB	0-(R2)		;(R0)=177400, CC=1001
005276	103001	BCC	COMB5		
005300	100401	BMI	COMB5	HLT	HLT
005302	104400				
005304	105752	TSTB	0-(R2)		;(R0)=177400, CC=0100
005306	001401	BEQ	COMB5	HLT	HLT

005310	104400	HLT			
005312	000262	SEV			
005314	106255	ASRB	0-(R5)		;(R0)=177400, CC=1001
005316	103002	BCC	ASRB5		
005320	102401	BVS	ASRB5		
005322	100401	BMI	ASRB5	HLT	HLT
005324	104400				
005326	105232	INCB	0-(R2)+		;(R0)=177401, CC=0000
005330	103001	BCC	INCB3		
005332	100001	BPL	INCB3	HLT	HLT
005334	104400				
005336	000241	CLC			
005340	106055	RORB	0-(R5)		;(R0)=177400, CC=0111
005342	103003	BCC	RORB5		
005344	102002	BVC	RORB5		
005346	001001	BNE	RORB5		
005350	100001	BPL	RORB5	HLT	HLT
005352	104400				
005354	106332	ASLB	0-(R2)+		;(R0)=177000, CC=1001
005356	103002	BCC	ASLB3		
005360	102401	BVS	ASLB3		
005362	100401	BMI	ASLB3	HLT	HLT
005364	104400				
005366	105552	ADCB	0-(R2)		;(R0)=177400, CC=1000
005370	103401	BCC	ADCB5		
005372	100401	BMI	ADCB5	HLT	HLT
005374	104400				
005376	000277	SCC			
005400	106135	ROLB	0-(R5)+		;(R0)=177401, CC=0000
005402	101402	BLOS	ROLB3		;BRANCH IF C OR Z IS SET
005404	102401	BVS	ROLB3		
005406	100001	BPL	ROLB3	HLT	HLT
005410	104400				
005412	000352	SWAB	0-(R2)		;(R0)=000777, CC=1000
005414	100401	BMI	SWAB	HLT	HLT
005416	104400				
005420	000261	SEC			
005422	105635	SBCB	0-(R5)+		;(R0)=000377, CC=0100
005424	103401	BCC	SBCB3		
005426	001401	BEQ	SBCB3	HLT	HLT
005430	104400				
005432	105432	NEGB	0-(R2)+		;(R0)=000001
005434	105352	DECB	0-(R2)		;(R0)=000000, CC=0101
005436	103001	BCC	DECB5		
005440	001401	BEQ	DECB5	HLT	HLT

005442	104400		DECB5:	HLT	
005444	104000			SCOPE	
005446	005027				
005450	000000		ICHECK	UNARY WORD OPS USING ADDRESS MODE 6 (PC)	
005452	010700			CLR (PC)+	IPRESET DATA = 0
005454	024040		UWM6:	.WORD 0	IPRESERVED FOR DATA
005456	000277			MOV PC,R0	
005460	006167	177764		CMP =(R0),=(R0)	IR0 POINTS TO DATA WORD
005464	103403			SCC	
005466	102402			ROL UWM6	IR(R0)=000001,CC=0000
005470	001401			BVS ROL6	
005472	100001			BEQ ROL6	
005474	104400		ROL6:	BPL .+4	
				HLT	
005476	005167	177766		COM UWM6	IR(R0)=177776,CC=1001
005502	103002			BCC COM6	
005504	102401			BVS COM6	
005506	100401			BMI .+4	
005510	104400		COM6:	HLT	

005512	006267	177732	ASR	UWM6	;(R0)=177777, CC=1010
005516	103402		BCS	ASR6	
005520	102001		BVC	ASR6	
005522	100401		BMI	,+4	
005524	104400		ASR6:	HLT	
005526	000277		SCC		
005530	005467	177714	NEG	UWM6	;(R0)=000001, CC=0001
005534	103003		BCC	NEG6	
005536	102402		BVS	NEG6	
005540	001401		BEQ	NEG6	
005542	100001		BPL	,+4	
005544	104400		NEG6:	HLT	
005546	000277		SCC		
005550	006067	177674	ROR	UWM6	;(R0)=100000, CC=1001
005554	103003		BCC	ROR6	
005556	102402		BVS	ROR6	
005560	001401		BEQ	ROR6	
005562	100401		BMI	,+4	
005564	104400		ROR6:	HLT	
005566	005667	177656	SBC	UWM6	;(R0)=077777, CC=0010
005572	103402		BCS	SBC6	
005574	102001		BVC	SBC6	
005576	100001		BPL	,+4	
005600	104400		SBC6:	HLT	
005602	000242		CLV		
005604	005267	177640	INC	UWM6	;(R0)=100000, CC=1011
005610	103403		BCS	INC6	
005612	102002		BVC	INC6	
005614	001401		BEQ	INC6	
005616	100401		BMI	,+4	
005620	104400		INC6:	HLT	
005622	006267	177622	ASR	UWM6	;(R0)=140000, CC=1010
005626	000261		SEC		
005630	006367	177614	ASL	UWM6	;(R0)=100000, CC=1001
005634	103002		BCC	ASL6	
005636	102401		BVS	ASL6	
005640	100401		BMI	,+4	
005642	104400		ASL6:	HLT	
005644	005367	177600	DEC	UWM6	;(R0)=077777, CC=0011
005650	103002		BCC	DEC6	
005652	102001		BVC	DEC6	
005654	100001		BPL	,+4	
005656	104400		DEC6:	HLT	
005660	005567	177564	ADC	UWM6	;(R0)=100000, CC=1010
005664	103402		BCS	ADC6	

005666	102001		BVC	ADC6	
005670	100401		BMI	,+4	
005672	104400		ADC6:	HLT	
005674	000242		CLV		
005676	000367	177546	SWAB	UWM6	
005702	100401		BMI	,+4	
005704	104400		HLT		
005706	022710	000200	CMP	#200, (R0)	
005712	001401		BEQ	,+4	
005714	104400		HLT		
005716	104000		SCOPE		
005720	012700	006262	;CHECK UNARY BYTE OPS (EVEN/ODD) USING ADDRESS MODE 6 (PC)		
005724	063700	001004	MOV	#UBM6,R0	
005730	005067	000326	ADD	##FACTOR,R0	;R0 POINTS TO ADDRESS OF DATA
005734	000277		CLR	UBM6	;CLEAR DATA
005736	000244		SCC		
005740	105767	000316	CLZ		
005744	103403		TSTB	UBM6	
005746	102402		BCS	TSTB6	
005750	001001		BVS	TSTB6	
005752	100001		BNE	TSTB6	
005754	104400		BPL	,+4	
			TSTB6:	HLT	
005756	000257		CCC		
005760	105767	000277	TSTB	UBM6+1	;TEST ODD BYTE
005764	001401		BEQ	,+4	
005766	104400		HLT		
005770	105667	000266	SBCB	UBM6	;(R0)=000000, CC=0100
005774	103402		BCS	SBCB6	
005776	102401		BVS	SBCB6	
006000	001401		BEQ	,+4	
006002	104400		SBCB6:	HLT	
006004	000261		1\$:	SEC	
006006	105267	000250	INCB	UBM6	;LOOP UNTIL (R0)=077600, CC=1011
006012	100403		BMI	2\$	
006014	105567	000243	ADCB	UBM6+1	;INCB INST INCREMENTS EVEN BYTE
006020	000771		BR	1\$;ADCB INCREMENTS ODD BYTE
006022	103001		BCC	INCB6	
006024	102401		BVS	,+4	
006026	104400		INCB6:	HLT	
006030	106367	000226	ASLB	UBM6	;(R0)=077400, CC=0111
006034	103003		BCC	ASLB6	
006036	102002		BVC	ASLB6	
006040	001001		BNE	ASLB6	
006042	100001		BPL	,+4	
006044	104400		ASLB6:	HLT	
006046	000242		CLV		
006050	105567	000207	ADCB	UBM6+1	;(R0)=100000, CC=1010

006054	103402		BCS	ADCB6	
006056	102001		BVC	ADCB6	
006060	100401		BMI	,+4	
006062	104400		ADCB6:	HLT	
006064	000261		SEC		
006066	106067	000171	RORB	UBM6+1	;(R0)=140000, CC=1010
006072	103402		BCS	RORB6	
006074	102001		BVC	RORB6	
006076	100401		BMI	,+4	
006100	104400		RORB6:	HLT	
006102	105167	000154	COMB	UBM6	;(R0)=140377 CC=1001
006106	103002		BCC	COMB6	
006110	102401		BVS	COMB6	
006112	100401		BMI	,+4	
006114	104400		COMB6:	HLT	
006116	000262		SEV		
006120	105467	000137	NECB	UBM6+1	;(R0)=040377, CC=0001
006124	103002		BCC	NECB6	
006126	102401		BVS	NECB6	
006130	100001		BPL	,+4	
006132	104400		NECB6:	HLT	
006134	106167	000123	ROLB	UBM6+1	;(R0)=100777, CC=1010
006140	103402		BCS	ROLB6	
006142	102001		BVC	ROLB6	
006144	100401		BMI	,+4	
006146	104400		ROLB6:	HLT	
006150	106267	000106	ASRB	UBM6	;(R0)=100777, CC=1001
006154	103002		BCC	ASRB6	
006156	102401		BVS	ASRB6	
006160	100401		BMI	,+4	
006162	104400		ASRB6:	HLT	
006164	105267	000072	INCB	UBM6	;(R0)=100400, CC=0101
006170	103002		BCC	INCB6A	
006172	102401		BVS	INCB6A	
006174	001401		BEQ	,+4	
006176	104400		INCB6A:	HLT	
006200	105367	000057	DECB	UBM6+1	;(R0)=100000, CC=1001
006204	103003		BCC	DECB6A	
006206	102402		BVS	DECB6A	
006210	001401		BEQ	DECB6A	
006212	100401		BMI	,+4	
006214	104400		DECB6A:	HLT	
006216	000367	000040	SWAB	UBM6	;(R0)=000200, CC=1000
006222	103401		BCS	SWAB6	
006224	100401		BMI	,+4	
006226	104400		SWAB6:	HLT	

006230	106167	020026	ROLB	UBM6	;(R0)=000000, CC=0111
006234	103002		BCC	ROLB6A	
006236	102001		BVC	ROLB6A	
006240	001401		BEQ	,+4	
006242	104400		ROLB6A:	HLT	
006244	005767	020012	TST	UBM6	;(R0)=000000, CC=0100
006250	103402		BCS	TST6	
006252	102401		BVS	TST6	
006254	001401		BEQ	,+4	
006256	104400		TST6:	HLT	
006260	000401		BR	,+4	!RESERVE A WORD
006262	000000		UBM6:	.WORD	0
006264	104000			SCOPE	!WORD RESERVED FOR DATA
006266	010702		MOV	PC,R2	
006270	062702	000012	ADD	#12,R2	
006274	012707	001132	MOV	#RELOC,PC	!GO RELOCATE PROGRAM CODE
006300	000240		NOP		!PROGRAM RETURNS HERE+2

10000000000000 LAST ADDRESS OF CODE TO BE RELOCATED 0000000000

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11111111111111111111 FIRST ADDRESS TO BE RELOCATED 1111111111
REL1: MOV PC,R0 ;GET PC
      TST -(R0) ;R0 CONTAINS THE ADDRESS OF REL1
      MOV R0,#FRSTAD ;SAVE
      MOV PC,R0 ;GET CURRENT PC
      SUB #,R0 ;SUBTRACT RELOCATION FACTOR
      MOV R0,#FACTOR ;SAVE RELOCATION FACTOR
      MOV PC,R1 ;SET NEW SCOPE PTR

;CHECK UNARY WORD OPS USING ADDRESS MODE 7
      BR UW7 ;RESERVE 3 WORDS FOR ADDRESSES & DATA
      ,WORD 0 ;CONTAINS ADDRESS OF UWM7
UWM7: ,WORD 0 ;CONTAINS DATA
      ,WORD 0 ;CONTAINS ADDRESS OF UWM7

UW7: MOV PC,R0
      TST -(R0)
      TST -(R0)
      CLR -(R0) ;CLEAR TEST DATA
      MOV R0,R2
      MOV R2,-(R0) ;SET UP ADDRESS
      TST (R0)+ ;MOVE R0 TO NEXT ADDRESS
      TST (R0)+
      MOV R2,(R0) ;SET NEXT ADDRESS
      MOV R2,R0 ;SET R0 POINTING TO DATA
      SCC
      CLC
      TST @2(2) ;(R0)=000000, CC=0100
      BEQ ,+4
      HLT

      SCC
      SBC @-2(2) ;(R0)=177777, CC=1001
      BCC SBC7
      BVS SBC7
      BMI ,+4
      HLT SBC7:

      SCC
      CLC
      ASL @2(2) ;(R0)=177776, CC=1001
      BCC ASL7
      BVS ASL7
      BMI ,+4
      HLT ASL7:

      CCC
      DEC @2(2) ;(R0)=177775, CC=1000
      BCS DEC7
      BVS DEC7
      BMI ,+4
      HLT DEC7:

006302 010700
006304 005740
006306 010037 001010
006312 010700
006314 102700 006314
006320 010037 001004
006324 010701

006326 000403
006330 000000
006332 000000
006334 000000

006336 010700
006340 005740
006342 005740
006344 005040
006346 010022
006350 010240
006352 005720
006354 005720
006356 010210
006360 010200
006362 000277
006364 000244
006366 005772 000002
006372 001401
006374 104400

006376 000277
006400 005672 177776
006404 103002
006406 102401
006410 100401
006412 104400

006414 000277
006416 000241
006420 006372 000002
006424 103002
006426 102401
006430 100401
006432 104400

006434 000257
006436 005372 000002
006442 103402
006444 102401
006446 100401
006450 104400

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      SEV
      ASR @-2(2) ;(R0)=177776, CC=1001
      BCC ASR7
      BVS ASR7
      BMI ,+4
      HLT ASR7:

      CLC
      SEV
      ROR @-2(2) ;(R0)=077777, CC=0000
      BLOS ROR7 ;BRANCH IF C OR Z IS SET
      BVS ROR7
      BPL ,+4
      HLT ROR7:

      SEV
      NEG @2(2) ;(R0)=100001, CC=1001
      BCC NEG7
      BVS NEG7
      BMI ,+4
      HLT NEG7:

      CLN
      SWAB @-2(2) ;(R0)=000600, CC=1000
      BCS SWAB7
      BMI ,+4
      HLT SWAB7:

      SEV
      COM @2(2) ;(R0)=177177, CC=1001
      BCC COM7
      BVS COM7
      BMI ,+4
      HLT COM7:

      SWAB
      BMI @2(2) ;(R0)=077776, CC=1000
      HLT ,+4

      SCC
      ADC @-2(2) ;(R0)=077777, CC=0000
      BCS ADC7
      BVS ADC7
      BPL ,+4
      HLT ADC7:

      INC
      BVC @2(2) ;(R0)=100000, CC=1010
      BMI INC7
      HLT ,+4

      CCC
      ROL @-2(2) ;(R0)=000000, CC=0111

006452 000262
006454 006272 177776
006460 103002
006462 102401
006464 100401
006466 104400

006470 000241
006472 000262
006474 006072 177776
006500 101402
006502 102401
006504 100001
006506 104400

006510 000262
006512 005472 000002
006516 103002
006520 102401
006522 100401
006524 104400

006526 000250
006530 000372 177776
006534 103401
006536 100401
006540 104400

006542 000262
006544 005172 000002
006550 103002
006552 102401
006554 100401
006556 104400

006560 000372 000002
006564 100401
006566 104400

006570 000277
006572 005572 177776
006576 103402
006600 102401
006602 100001
006604 104400

006606 005272 000002
006612 102001
006614 100401
006616 104400

006620 000257
006622 006172 177776

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006626	103002		BCC	ROL7	
006630	102001		BVC	ROL7	
006632	001401		BEQ	,+4	
006634	104400		HLT		
006636	104000		SCOPE		
:CHECK UNARY BYTE OPS USING ADDRESS MODE 7					
006640	005720		TST	(R0)+	
006642	005210		INC	(R0)	:WORD FOLLOWING UHM7 CONTAINS ADDRESS
006644	005740		TST	-(R0)	:OF ODD BYTE, R0 POINTS TO DATA WORD
006646	005010		CLR	(R0)	:PRESET DATA
006650	010701		MOV	PC,R1	:SET SCOPE PTR
:NOTE: #2(2) REFERENCES THE ODD BYTE, AND #-2(2) REFERENCES THE EVEN BYTE,					
006652	000263		+SECISEV		:SET C AND V
006654	105672	000002	SBCB	#2(2)	:(R0)=177400, CC=1001
006660	103003		BCC	SBCB7	
006662	102402		BVS	SBCB7	
006664	001401		BEQ	SBCB7	
006666	100401		BMI	,+4	
006670	104400		HLT		
:SET CONDITION CODES					
006672	000277		SCC		:(R0)=177401, CC=0000
006674	105572	177776	ADCB	#-2(2)	
006700	103403		BCC	ADCB7	
006702	102402		BVS	ADCB7	
006704	001401		BEQ	ADCB7	
006706	100001		BPL	,+4	
006710	104400		HLT		
:CLEAR CARRY					
006712	105172	177776	COMB	#-2(2)	:(R0)=177776, CC=1001
006716	103002		BCC	COMB7	
006720	102401		BVS	COMB7	
006722	100401		BMI	,+4	
006724	104400		HLT		
:SET C AND V					
006726	000241		CLC		:(R0)=077776, CC=0011
006730	106072	000002	RORB	#2(2)	
006734	103002		BCC	RORB7	
006736	102001		BVC	RORB7	
006740	100001		BPL	,+4	
006742	104400		HLT		
:CLEAR CARRY					
006744	105272	000002	INCB	#2(2)	:(R0)=100376, CC=1011
006750	103002		BCC	INCB7	
006752	102001		BVC	INCB7	
006754	100401		BMI	,+4	
006756	104400		HLT		
:SET CONDITION CODES					
006760	105372	177776	DECB	#-2(2)	:(R0)=100375, CC=1001
006764	103002		BCC	DECB7	
006766	102401		BVS	DECB7	
006770	100401		BMI	,+4	
006772	104400		HLT		

006774	106372	000002	ASLB	#2(2)	:(R0)=000375, CC=0111
007000	103002		BCC	ASLB7	
007002	102001		BVC	ASLB7	
007004	001401		BEQ	,+4	
007006	104400		HLT		
:CLEAR CARRY					
007010	000241		CLC		:(R0)=000376, CC=1001
007012	106272	177776	ASRB	#-2(2)	
007016	103002		BCC	ASRB7	
007020	102401		BVS	ASRB7	
007022	100401		BMI	,+4	
007024	104400		HLT		
:SET CONDITION CODES					
007026	105472	000002	NEGB	#2(2)	:(R0)=000376, CC=0100
007032	103402		BCC	NEGB7	
007034	102401		BVS	NEGB7	
007036	001401		BEQ	,+4	
007040	104400		HLT		
:SET CONDITION CODES					
007042	000262		SEV		:(R0)=00374, CC=1001
007044	106172	177776	ROLB	#-2(2)	
007050	103002		BCC	ROLB7	
007052	102401		BVS	ROLB7	
007054	100401		BMI	,+4	
007056	104400		HLT		
:SET CONDITION CODES					
007060	105272	177776	INCB	#-2(2)	:(R0)=000375, CC=1001
007064	105272	177776	INCB	#-2(2)	:(R0)=000376, CC=1001
007070	105572	177776	ADCB	#-2(2)	:(R0)=000377, CC=1000
007074	105172	177776	COMB	#-2(2)	:(R0)=000000, CC=0100
007100	001401		BEQ	,+4	
007102	104400		HLT		
007104	104000		SCOPE		
:CHECK BINARY OPS USING ADDRESS MODE 0					
007106	000277		SCC		:SET CONDITION CODES
007110	010700		MOV	PC,R0	:R0=PC, CC=X001
007112	103002		BCC	MOV0	
007114	102401		BVS	MOV0	
007116	001001		BNE	,+4	
007120	104400		HLT		
:SET V					
007122	010002		MOV	R0,R2	:R2=R0
007124	000262		SEV		:SET V
007126	100002		SUB	R0,R2	:R2=000000, CC=0100
007130	103402		BCC	SUB0	
007132	102401		BVS	SUB0	
007134	001401		BEQ	,+4	
007136	104400		HLT		
:SET R3					
007140	000244		CLC		:R2=R3=000000, CC=0100
007142	010203		MOV	R2,R3	
007144	103401		BCC	MOV0A	


```

007146 001401 BEQ    ,+4
007150 104400 MOV0A: HLT

007152 000257 CCC
007154 000272 +SEVISEN
007156 020203 CMP    R2,R3    ;R2=R3=000000, CC=0100
007160 103403 BCS   CMP0
007162 102402 BVS   CMP0
007164 001001 BNE   CMP0
007166 100001 BPL   ,+4
007170 104400 CMP0: HLT

007172 010002 MOV    R0,R2    ;R0=R2
007174 010203 MOV    R2,R3    ;R0=R2=R3
007176 060203 ADD    R2,R3    ;R3=R2+R0
007200 006302 ASL   R2        ;R2=2*R0
007202 020203 CMP    R2,R3    ;R2=R3=2*R0
007204 001401 BEQ    ,+4
007206 104400 HLT        ;ERROR! CHECK ADD INSTRUCTION
    
```

ITHE FOLLOWING SUBTEST SHIFTS A BIT THROUGH R2 AND R5 AND DOES A
 BIT TEST (BIT) USING R2 AND R5.

```

007210 005002 CLR    R2
007212 005202 INC    R2
007214 000402 BR     2$
007216 006302 1$: ASL   R2
007220 100407 BMI   4$
007222 010205 2$: MOV   R2,R5
007224 000277 SCC
007226 030205 BIT    R2,R5    ;R2=R5
007230 103002 BCC   3$
007232 102401 BVS   3$
007234 001370 BNE   1$
007236 104400 3$: HLT
007240 010205 4$: MOV   R2,R5
007242 000257 CCC
007244 030205 BIT    R2,R5
007246 100401 BMI   ,+4
007250 104400 HLT

007252 005002 CLR    R2
007254 000277 SCC
007256 050002 BIS   R0,R2
007260 103002 BCC   B1$0
007262 102401 BVS   B1$0
007264 001001 BNE   ,+4
007266 104400 BIS0: HLT

007270 010003 MOV    R0,R3
007272 000277 SCC
007274 000244 CLZ
007276 040003 BIC   R0,R3
007300 103003 BCC   B1C0
007302 102402 BVS   B1C0
    
```

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007304 001001 BNE   B1C0
007306 100001 BPL   ,+4
007310 104400 HLT

007312 010004 MOV    R0,R4
007314 005104 COM   R4
007316 040004 BIC   R0,R4
007320 005104 COM   R4
007322 020004 CMP   R0,R4
007324 001401 BEQ   ,+4
007326 104400 HLT

007330 010004 MOV    R0,R4
007332 005104 COM   R4
007334 010403 MOV   R4,R3
007336 050003 BIS   R0,R3
007340 103001 BCC   B1$0A
007342 100401 BMI   ,+4
007344 104400 BIS0A: HLT
007346 005203 INC   R3
007350 001401 BEQ   ,+4
007352 104400 HLT
007354 010304 MOV   R3,R4    ;R3=R4=0
007356 005103 COM   R3    ;R3=17777
007360 000241 SEC
007362 000004 ROR   R4    ;SET C
007364 060304 ADD   R3,R4    ;R4=100000
007366 103003 BCC   ADD0    ;R3=17777,R4=07777, CC=0011
007370 102002 BVC   ADD0
007372 001401 BEQ   ADD0
007374 100001 BPL   ,+4
007376 104400 HLT
007400 010700 MOV   PC,R0
007402 022020 CMP   (R0+),(R0)+
007404 020007 CMP   R0,PC
007406 001401 BEQ   ,+4
007410 104400 HLT

007412 010700 MOV   PC,R0
007414 062700 000010 ADD   #10,R0
007420 010002 MOV   R0,R2
007422 020700 CMP   PC,R0
007424 001002 BNE   CMP0A
007426 020200 CMP   R2,R0
007430 001401 BEQ   ,+4
007432 104400 CMP0A: HLT
007434 104000 SCOPE

;CHECK BINARY BYTE OPS USING ADDRESS MODE 0.
007436 012703 125252 MOV   #125252,R3
007442 010304 125000 MOV   R3,R4    ;R3=R4=125252
007444 140304 BICB  R3,R4    ;R3=125252, R4=125000
007446 022704 125000 CMP   #125000,R4
007452 001401 BEQ   ,+4
    
```

007454	104400		HLT		!ERROR! BICB FAILED
007456	005004		CLR	R4	!R3=129252, R4=0
007460	150304		BISB	R3,R4	!R3=129252, R4=000292
007462	022704	000292	CHP	#252,R4	
007466	001401		BEQ	.,+4	
007470	104400		HLT		!ERROR! BISB FAILED
007472	110404		MOVB	R4,R4	!R4=177652
007474	022704	177652	CHP	#177652;R4	!MOVB EXTENDS THE SIGN
007500	001401		BEQ	.,+4	
007502	104400		HLT		!ERROR! MOVB FAILED
007504	132704	177525	BITB	#177525;R4	
007510	001401		BEQ	.,+4	
007512	104400		HLT		!ERROR! BITB FAILED
007514	105104		COMB	R4	!R4=177525
007516	110404		MOVB	R4,R4	!R4=000125
007520	022704	000125	CHP	#125,R4	
007524	001401		BEQ	.,+4	
007526	104400		HLT		
007530	150304		BISB	R3,R4	!R3=129252, R4=000377
007532	105204		INCB	R4	
007534	005704		TST	R4	
007536	001401		BEQ	.,+4	
007540	104400		HLT		
007542	104000		SCOPE		

!CHECK BINARY OPS USING ADDRESS MODE 1

007544	000402		BR	.,+6	!RESERVE TWO WORDS
007546	000000		.WORD	0	!RESERVED FOR SOURCE DATA
007550	000000		.WORD	0	!RESERVED FOR DESTINATION DATA
007552	010704		MOV	PC,R4	
007554	005744		TST	-(R4)	
007556	005044		CLR	-(R4)	!R4 POINTS TO DESTINATION DATA
007560	010403		MOV	R4,R3	
007562	005043		CLR	-(R3)	!R3 POINTS TO SOURCE DATA
007564	005113		COM	(R3)	!(R3)=177777
007566	005214		INC	(R4)	!(R4)=000001
007570	000262		SEV		!SET V
007572	061314		ADD	(R3),(R4)	!(R3)=177777,(R4)=000000, CC=0101
007574	103002		BCC	ADD1	
007576	102401		BVS	ADD1	
007600	001401		BEQ	.,+4	
007602	104400		HLT		
		ADD1:			
007604	000277		SCC		
007606	000250		CLN		
007610	021314		CHP	(R3),(R4)	!(R3)=177777,(R4)=000000, CC=1000
007612	103403		BCC	CHP1	

007614	102402		BVS	CHP1	
007616	001401		BEQ	CHP1	
007620	100401		BMI	.,+4	
007622	104400		HLT		
		CHP1:			
007624	000277		SCC		
007626	000244		CLZ		
007630	031314		BIT	(R3),(R4)	!(R3)=177777,(R4)=000000, CC=0101
007632	103002		BCC	BIT1	
007634	102401		BVS	BIT1	
007636	001401		BEQ	.,+4	
007640	104400		HLT		
		BIT1:			
007642	000277		SCC		
007644	000245		+CLCICLZ		
007646	005114		COM	(R4)	!(R4)=177777
007650	161314		SUB	(R3),(R4)	!(R3)=177777,(R4)=000000, CC=0100
007652	103402		BCC	SUB1	
007654	102401		BVS	SUB1	
007656	001401		BEQ	.,+4	
007660	104400		HLT		
		SUB1:			
007662	105013		CLRB	(R3)	!(R3)=177400
007664	000313		SWAB	(R3)	!(R3)=000377
007666	000270		SEN		
007670	011314		MOV	(R3),(R4)	!(R3)=(R4)=000377
007672	100001		BPL	.,+4	
007674	104400		HLT		
007676	000314		SWAB	(R4)	!(R3)=000377,(R4)=177400
007700	000263		+SECISEV		!SET C & V
007702	051314		BIS	(R3),(R4)	!(R3)=000377,(R4)=177777, CC=1001
007704	103002		BCC	BIS1	
007706	102401		BVS	BIS1	
007710	100401		BMI	.,+4	
007712	104400		HLT		
		BIS1:			
007714	041314		BIC	(R3),(R4)	!(R3)=000377,(R4)=177400, CC=1001
007716	103002		BCC	BIC1	
007720	102401		BVS	BIC1	
007722	100401		BMI	.,+4	
007724	104400		HLT		
		BIC1:			
007726	000262		SEV		!SET V
007730	021314		CHP	(R3),(R4)	!(R3)=000377,(R4)=177400, CC=0001
007732	103003		BCC	CHP1A	
007734	102402		BVS	CHP1A	
007736	001401		BEQ	CHP1A	
007740	100001		BPL	.,+4	
007742	104400		HLT		
		CHP1A:			
007744	005013		CLR	(R3)	!(R3)=000000
007746	000261		SEC		
007750	006013		ROR	(R3)	!(R3)=100000
007752	011314		MOV	(R3),(R4)	!(R3)=(R4)=100000

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007754 005114 COM (R4) ;(R4)=077777
007756 161314 SUB (R3,(R4) ;(R3)=100000,(R4)=177777,CC=1011
007760 103002 BCC SUB1A
007762 102001 BVC SUB1A
007764 100401 BMI .+4
007766 104400 SUB1A: HLT

007770 000277 SCC
007772 161314 SUB (R3,(R4) ;(R3)=100000,(R4)=077777,CC=0000
007774 101402 BLOS SUB1B ;BRANCH IF C OR Z IS SET
007776 102401 BVS SUB1B
010000 100001 BPL .+4
010002 104400 SUB1B: HLT

010004 011314 MOV (R3,(R4) ;(R3)=100000,(R4)=100000,CC=1000
010006 001401 BEQ MOV1
010010 100401 BMI .+4
010012 104400 MOV1: HLT

010014 061314 ADD (R3,(R4) ;(R3)=100000,(R4)=000000,CC=0111
010016 103003 BCC ADD1A
010020 102002 BVC ADD1A
010022 001001 BNE ADD1A
010024 100001 BPL .+4
010026 104400 ADD1A: HLT

010030 005113 COM (R3) ;(R3)=077777
010032 011314 MOV (R3,(R4) ;(R4)=077777
010034 061314 ADD (R3,(R4) ;(R3)=077777,(R4)=177776,CC=1010
010036 103402 BCS ADD1B
010040 102001 BVC ADD1B
010042 100401 BMI .+4
010044 104400 ADD1B: HLT

010046 062714 000002 ADD #2,(R4)
010052 005714 TST (R4) ;CHECK FINAL RESULT
010054 001401 BEQ .+4
010056 104400 HLT
010060 104000 SCOPE

;CHECK BINARY BYTE OPS USING ADDRESS MODE 1
010062 000402 BR .+6
010064 000000 ,WORD 0
010066 000000 ,WORD 0
010070 010705 MOV PC,R5
010072 005745 TST -(R5)
010074 005045 CLR -(R5) ;(R5)=000000
010076 010502 MOV R5,R2
010100 005042 CLR -(R2) ;(R2)=000000
010102 005202 INC R2 ;R2 POINTS TO ODD BYTE
010104 105112 COMB (R2) ;(R2)=177400

010106 000277 SCC
010110 111215 MOVB (R2,(R9) ;(R2)=177400,(R5)=000377,CC=1001

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010112 103005 BCC MOVB1
010114 102404 BVS MOVB1
010116 001403 BEQ MOVB1
010120 100002 BPL MOVB1
010122 105215 INCB (R5) ;CHECK RESULT
010124 001401 BEQ .+4
010126 104400 MOVB1: HLT

010130 106312 ASLB (R2) ;SHIFT (R2) UNTIL
010132 102376 BVC .-2 ;(R2)=000000
010134 106012 RORB (R2) ;(R2)=100000
010136 105315 DECB (R5) ;(R5)=00377
010140 106015 RORB (R5) ;(R5)=000177
010142 000257 CCC
010144 121512 CMPB (R5,(R2) ;(R5)=000177,(R2)=100000,CC=1010
010146 102001 BVC CMPB1
010150 100401 BMI .+4
010152 104400 CMPB1: HLT

010154 005003 CLR R3
010156 000261 SEC
010160 006003 ROR R3 ;R3=100000
010162 050315 BIS R3,(R5) ;(R5)=100177
010164 000273 +SEC;SEVISEN ;SET C,V, & N
010166 131215 BITB (R2),(R9) ;(R2)=100000,(R5)=100177,CC=0101
010170 103002 BCC BITB1
010172 102401 BVS BITB1
010174 001401 BEQ .+4
010176 104400 BITB1: HLT

010200 151215 BISB (R2),(R5) ;(R2)=100000,(R5)=100377,CC=1001
010202 103001 BCC BISB1
010204 100401 BMI .+4
010206 104400 BISB1: HLT

010210 141215 BICB (R2),(R5) ;(R2)=100000,(R5)=100177,CC=0001
010212 103002 BCC BICB1
010214 001401 BEQ BICB1
010216 100001 BPL .+4
010220 104400 BICB1: HLT

010222 105112 COMB (R2) ;(R2)=077400,(R5)=100177
010224 121215 CMPB (R2,(R9)
010226 001401 BEQ .+4
010230 104400 HLT

010232 141512 BICB (R5),(R2) ;(R5)=100177,(R2)=000000,CC=0100
010234 001002 BNE BICB1A
010236 105712 TSTB (R2)
010240 001401 BEQ .+4
010242 104400 BICB1A: HLT

010244 000402 BR .+6 ;RESERVE TWO WORDS FOR DATA
010246 000000 ,WORD 0 ;SOURCE DATA

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010250	000000		,WORD	0	DEST DATA
010252	010705		MOV	PC,R5	
010254	005745		TST	-(R5)	
010256	105045		CLRB	-(R5)	R5 POINTS TO DEST ODD BYTE
010260	010504		MOV	R5,R4	
010262	105044		CLRB	-(R4)	R4 POINTS TO DEST EVEN BYTE
010264	010403		MOV	R4,R3	
010266	105043		CLRB	-(R3)	R3 POINTS TO SOURCE ODD BYTE
010270	010302		MOV	R3,R2	
010272	105042		CLRB	-(R2)	R2 POINTS TO SOURCE EVEN BYTE

ICOMMENTS ARE LEAST SIGNIFICANT 4 BITS OF BYTES POINTED TO BY R2,R3
R4, AND R5 RESPECTIVELY AND THE REMAINING BITS ARE 0'S.

010274	000261		SEC		SET CARRY
010276	106112		ROLB	(R2)	(R2),(R3),(R4),(R5)
010300	111214		MOVB	(R2),(R4)	0001,0000,0000,0000
010302	106112		ROLB	(R2)	0001,0000,0001,0000
010304	111213		MOVB	(R2),(R5)	0010,0010,0001,0000
010306	106112		ROLB	(R2)	0100,0010,0001,0000
010310	111315		MOVB	(R3),(R5)	0100,0010,0001,0010
010312	106112		ROLB	(R2)	1000,0010,0001,0010
010314	106113		ROLB	(R3)	1000,0100,0001,0010
010316	151215		BISB	(R2),(R3)	1000,0100,0001,1010
010320	131512		BITB	(R5),(R2)	1000,0100,0001,1010
010322	001426		BEQ	BIN1	
010324	151314		BISB	(R3),(R4)	1000,0100,0101,1010
010326	131413		BITB	(R4),(R5)	1000,0100,0101,1010
010330	001423		BEQ	BIN1	
010332	105213		INCB	(R3)	1000,0101,0101,1010
010334	121314		CMPB	(R3),(R4)	1000,0101,0101,1010
010336	001020		BNE	BIN1	
010340	106113		ROLB	(R3)	1000,1010,0101,1010
010342	121315		CMPB	(R3),(R5)	1000,1010,0101,1010
010344	001015		BNE	BIN1	
010346	106212		ASRB	(R2)	0100,1010,0101,1010
010350	131214		BITB	(R2),(R4)	0100,1010,0101,1010
010352	001412		BEQ	BIN1	
010354	106015		RORB	(R5)	0100,1010,0101,0101
010356	121415		CMPB	(R4),(R5)	0100,1010,0101,0101
010360	001007		BNE	BIN1	
010362	105314		DECB	(R4)	0100,1010,0100,0101
010364	141214		BICB	(R2),(R4)	0100,1010,0000,0101
010366	001004		BNE	BIN1	
010370	111314		MOVB	(R3),(R4)	0100,1010,1010,0101
010372	106213		ASRB	(R3)	0100,0101,1010,0101
010374	141315		BICB	(R3),(R5)	0100,0101,1010,0101
010376	001401		BEQ	.+4	
010400	104400		HLT		
010402	104000		SCOPE		

ICHECK BINARY WORD OPS USING ADDRESS MODE 2 & 4

010404	010405		MOV	R4,R5	SET DESTINATION REGISTER
010406	012715	000001	MOV	#1,(R5)	

010412	012712	177777	MOV	#-1,(R2)	
010416	000257		CCC		
010420	000262		SEV		
010422	062225		ADD	(R2+),(R5)+	(R2)=177777,(R5)=000000,CC=0101
010424	103002		BCC	ADD2	
010426	102401		BVS	ADD2	
010430	001401		BEQ	.+4	
010432	104400		HLT		
010434	000262		SEV		SET V
010436	024527	000001	CMP	-(R5),#1	(R5)=000000,CC=1001
010442	103002		BCC	CMP2	
010444	102401		BVS	CMP2	
010446	100401		BMI	.+4	
010450	104400		HLT		
010452	054225		BIS	-(R2),(R5)+	(R2)=177777,(R5)=177777,CC=1001
010454	103001		BCC	BIS2	
010456	100401		BMI	.+4	
010460	104400		HLT		
010462	000277		SCC		
010464	000244		CLE		
010466	162245		SUB	(R2+),(R5)	(R2)=177777,(R5)=000000,CC=0100
010470	103402		BCC	SUB2	
010472	102401		BVS	SUB2	
010474	001401		BEQ	.+4	
010476	104400		HLT		
010500	005442		NEG	-(R2)	(R2)=000001
010502	005115		COM	(R5)	(R5)=177777
010504	000277		SCC		
010506	000250		CLN		
010510	042225		BIC	(R2+),(R5)+	(R2)=000001,(R5)=177776,CC=1001
010512	103003		BCC	BIC2	
010514	102402		BVS	BIC2	
010516	001401		BEQ	BIC2	
010520	100401		BMI	.+4	
010522	104400		HLT		
010524	012742	125252	MOV	#125252,(R2)	
010530	012245		MOV	(R2+),(R5)	
010532	005125		COM	(R5+)	(R5)=052525
010534	000262		SEV		
010536	034245		BIT	-(R2),(R5)	(R2)=125252,(R5)=052525,CC=0101
010540	103002		BCC	BIT2	
010542	102401		BVS	BIT2	
010544	001401		BEQ	.+4	
010546	104400		HLT		
010550	000262		SEV		
010552	052225		BIS	(R2+),(R5)+	(R2)=125252,(R5)=177777,CC=1001
010554	103002		BCC	BIS2A	
010556	102401		BVS	BIS2A	
010560	100401		BMI	.+4	

010562	104400		BIS2A:	HLT		
010564	042745	125252		BIC	#125252,(R5)	;(R5)=052525
010570	005125			COM	(R5)+	;(R5)=125252
010572	024245			CMP	-(R2),-(R5)	
010574	001401			BEQ	,+4	
010576	104400			HLT		
010600	005012			CLR	(R2)	
010602	005122			COM	(R2)+	;(R2)=177777
010604	162742	000001		SUB	#1,-(R2)	;(R2)=177776, CC=1000
010610	103402			BCS	SUB2A	
010612	102401			BVS	SUB2A	
010614	100401			BMI	,+4	
010616	104400		SUB2A:	HLT		
010620	104000			SCOPE		
010622	010702			MOV	PC,R2	!GET CURRENT PC
010624	010205			MOV	R2,R5	!MOVE TO R5
010626	124245		1S:	CMPB	-(R2),-(R5)	!COMPARE ALL PREVIOUS MEMORY ADDRESSES
010630	001401			BEQ	,+4	
010632	104400			HLT		!ERROR!
010634	020237	001010		CMP	R2,#FRSTAD	!CHECK FOR LOW LIMIT
010640	001372			BNE	1S	
010642	104000			SCOPE		
!CHECK BINARY BYTE OPS USING ADDRESS MODES 2 & 4,						
010644	000402			BR	,+6	!RESERVE TWO WORDS
010646	000000			,WORD	0	!SOURCE DATA
010650	000000			,WORD	0	!DESTINATION DATA
010652	010703			MOV	PC,R3	
010654	005743			YST	-(R3)	
010656	112743	000200		MOVb	#200,-(R3)	
010662	112743	000377		MOVb	#377,-(R3)	;(R3)=100377
010666	010304			MOV	R3,R4	
010670	112744	000177		MOVb	#177,-(R4)	
010674	112744	000000		MOVb	#0,-(R4)	;(R4)=077400
010700	001401			BEQ	,+4	
010702	104400			HLT		
010704	152324			BISB	(R3+,(R4)+	;(R3)=100377,(R4)=077777
010706	100401			BMI	,+4	
010710	104400			HLT		
010712	122324			CMPB	(R3+,(R4)+	
010714	103402			BCS	CMPB2	
010716	102001			BVC	CMPB2	
010720	100001			BPL	,+4	
010722	104400		CMPB2:	HLT		
010724	000261			SEC		
010726	134344			BITB	-(R3),-(R4)	
010730	103002			BCC	BITB2	
010732	102401			BVS	BITB2	

010734	001401			BEQ	,+4	
010736	104400		BITB2:	HLT		
010740	000244			CLE		
010742	144344			BICB	-(R3),-(R4)	;(R3)=100377,(R4)=077400
010744	001401			BEQ	,+4	
010746	104400			HLT		
010750	104000			SCOPE		
!CHECK BINARY WORD OPS USING ADDRESS MODES 3 & 5,						
010752	000404			BR	2S	!RESERVE SPACE FOR DATA AND ADDRESSES
010754	000000			,WORD	0	!CONTAINS ADDRESS OF SOURCE DATA
010756	000000			,WORD	0	!CONTAINS ADDRESS OF DEST DATA
010760	000000			,WORD	0	!CONTAINS SOURCE DATA
010762	000000			,WORD	0	!CONTAINS DEST DATA
010764	010701		2S:	MOV	PC,R1	
010766	010100			MOV	R1,R0	!SET SCOPE PTR
010770	024040			CMP	-(R0),-(R0)	!ADJUST R0
010772	010005			MOV	R0,R5	!R0 POINTS TO DEST DATA
010774	024545			CMP	-(R5),-(R5)	!SUB 4 FROM R5
010776	010015			MOV	R0,(R5)	!R0 POINTS TO ADDRESS OF DEST DATA
011000	010502			MOV	R5,R2	
011002	010004			MOV	R0,R4	!R4 POINTS TO DEST DATA
011004	005740			TST	-(R0)	
011006	010003			MOV	R0,R3	!R3 POINTS TO SOURCE DATA
011010	010042			MOV	R0,(R2)	!R2 POINTS TO ADDRESS OF SOURCE DATA
011012	005013			CLR	(R3)	!PRESET SOURCE DATA
011014	005014			CLR	(R4)	!PRESET DEST DATA
011016	000277			SCC		
011020	000244			CLE		
011022	163235			SUB	0,(R2)+,0,(R5)+	;(R3)=000000,(R4)=000000, CC=0100
011024	103402			BCS	SUB3	
011026	102401			BVS	SUB3	
011030	001401			BEQ	,+4	
011032	104400		SUB3:	HLT		
011034	052752	100000		BIS	#100000,0-(R2)	;(R3)=100000
011040	062755	000001		ADD	#1,0-(R5)	;(R4)=000001
011044	163235			SUB	0,(R2)+,0,(R5)+	;(R3)=100000,(R4)=100001, CC=1011
011046	103002			BCC	SUB3A	
011050	102001			BVC	SUB3A	
011052	100401			BMI	,+4	
011054	104400		SUB3A:	HLT		
011056	005414			NEG	(R4)	;(R4)=077777
011060	035255			BIT	0-(R2),0-(R5)	;(R3)=100000,(R4)=077777
011062	001401			BEQ	,+4	
011064	104400			HLT		
011066	023235			CMP	0,(R2)+,0,(R5)+	
011070	102401			BVS	,+4	
011072	104400			HLT		
011074	005152			COM	0-(R2)	
011076	000257			CCC		

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011100 063255 ADD @ (R2)+, @-(R5)
011102 102001 BVC ADD3
011104 100401 BMI ,+4
011106 104400 ADD3: HLT
011110 000261 SEC
011112 049235 BIC @-(R2), @ (R5)+ ; (R3)=077777, (R4)=100000
011114 103001 BCC BIC3
011116 100401 BMI ,+4
011120 104400 BIC3: HLT

011122 005155 COM @-(R5) ; (R4)=077777
011124 023235 CMP @ (R2)+, @ (R5)+ ; (R3)=077777, (R4)=077777
011126 001401 BEQ ,+4
011130 104400 HLT
011132 104000 SCOPE

;CHECK BINARY BYTE OPS USING ADDRESS MODES 3 & 5,
BR 1$ ;RESERVE SPACE FOR ADDRESSES & DATA
;WORD 0 ;CONTAINS ADDRESS OF SOURCE DATA (EVEN BYTE)
;WORD 0 ;CONTAINS ADDRESS OF SOURCE DATA (ODD BYTE)
;WORD 0 ;CONTAINS ADDRESS OF DEST DATA (EVEN BYTE)
;WORD 0 ;CONTAINS ADDRESS OF DEST DATA (ODD BYTE)
;WORD 0 ;CONTAINS SOURCE DATA
;WORD 0 ;CONTAINS DEST DATA

011152 010700 1$: MOV PC,R0
011154 024040 CMP @-(R0), @-(R0) ;R0=ADDRESS OF DEST DATA
011156 010003 MOV R0,R3 ;R3 " "
011160 010305 MOV R3,R5 ;R5 " "
011162 005743 TST @-(R3) ;SU0 2 FROM R3
011164 010043 MOV R0,@-(R3) ;R3 POINTS TO ADDRESS OF DEST DATA
011166 005213 INC (R3) ;ODD BYTE
011170 010043 MOV R0,@-(R3) ;EVEN BYTE
011172 010304 MOV R3,R4
011174 005740 TST @-(R0) ;R0=ADDRESS OF SOURCE DATA
011176 010044 MOV R0,@-(R4) ;R4 POINTS TO ADDRESS OF SOURCE DATA
;12000 005214 INC (R4) ;ODD BYTE
;11202 010044 MOV R0,@-(R4) ;EVEN BYTE

011204 000261 SEC ;SET CARRY
;12006 012734 177001 MOV #177001, @ (R4)+
;1212 112734 000200 MOVB #200, @ (R4)+ ;SOURCE DATA=100001
;1216 115433 MOVB @-(R4), @ (R3)+
;1220 115433 MOVB @-(R4), @ (R3)+ ;DEST DATA=000600
;1222 103401 BCS ,+4
;1224 104400 HLT ;ERROR! MOV DOES AFFECT C BIT IN PSW
;1226 022715 000600 CMP #600, (R9) ;CHECK DEST DATA
;1232 001401 BEQ ,+4
;1234 104400 HLT ;ERROR! INCORRECT RESULT
;1236 024343 CMP @-(R3), @-(R3) ;POINT R4 BACK TO EVEN BYTE
;1240 153433 B1SB @ (R4)+, @ (R3)+
;1242 153433 B1SB @ (R4)+, @ (R3)+ ;DEST DATA=100601
;1244 022715 100601 CMP #100601, (R5) ;CHECK RESULT
;1250 001401 BEQ ,+4

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011252 104400 HLT ;ERROR! INCORRECT DEST DATA AFTER B1SB
011254 145453 B1CB @-(R4), @-(R3)
011256 145453 B1CB @-(R4), @-(R3)
011260 133433 B1TB @ (R4)+, @ (R3)+
011262 001002 BNF BITB3
011264 133433 BITB @-(R4), @ (R3)+
011266 001001 BNE ,+4
011270 104400 BITB3: HLT

011272 123453 CMPB @ (R4)+, @-(R3)
011274 001002 BNE CHPB3
011276 123453 CMPB @ (R4)+, @-(R3)
011300 001401 BEQ ,+4
011302 104400 CHPB3: HLT
011304 104000 SCOPE

;CHECK BINARY OPS USING ADDRESS MODE 6
SDATA: BR ,+6 ;RESERVE TWO LOCATIONS
;WORD 0 ;RESERVED FOR SOURCE DATA
;WORD 0 ;RESERVED FOR DESTINATION DATA

011314 013702 001004 MOV @#FACTOR, R2 ;GET RELOCATION FACTOR AND USE AS AN
011320 010205 MOV R2, R5 ;INDEX VALUE TO POINT TO DATA
011322 005065 011312 CLR DDATA(5) ;PRESET DESTINATION DATA
011326 012762 000001 011310 MOV #1, SDATA(2) ;THIS ROUTINE PUT A 1 BIT INTO EVERY
011334 056265 011310 011312 1$: BIS SDATA(2), DDATA(5) ;OTHER BIT POSITION IN THE DEST-
011342 006362 011310 ASL SDATA(2) ;INATION ADDRESS (52525)
011346 006362 011310 ASL SDATA(2)
011352 103370 BCC 1$
011354 022765 052525 011312 CMP #52525, DDATA(5) ;CHECK RESULT
011362 001401 BEQ ,+4
011364 104400 HLT ;ERROR! INCORRECT RESULT
011366 012762 177777 011310 MOV #-1, SDATA(2)
011374 046562 011312 011310 BIC DDATA(5), SDATA(2) ;SOURCE DATA=125252
011402 036265 011310 011312 BIT SDATA(2), DDATA(5)
011410 001401 BEQ ,+4
011412 104400 HLT ;ERROR! BIT INST FAILED
011414 006365 011312 ASL DDATA(5) ;DDATA=125252
011420 026265 011310 011312 CMP SDATA(2), DDATA(5)
011426 001401 BEQ ,+4

011430 104400 HLT ;ERROR! CMP INST FAILED
011432 000257 CCC
011434 066265 011310 011312 ADD SDATA(2), DDATA(5)
011442 103002 ADD6
011444 102001 BCC ADD6
011446 100001 BVC ADD6
011450 104400 BPL ,+4
ADD6: HLT

011452 006362 011310 ASL SDATA(2) ;SDATA=52524
011456 166265 011310 011312 SUB SDATA(2), DDATA(5)
011464 103401 BCS SUB6
011466 001401 BEQ ,+4
011470 104400 SUB6: HLT

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011472	112700	000377		MOVB	#377,R0	R0#177777 (MOVB XR EXTENDS \$IGN)
011476	010062	011310		MOV	R0,SDATA(2)	
011502	012765	177777	011312	MOV	#-1,DDATA(5)	
011510	166500	011312		SUB	DDATA(5),R0	
011514	001401			BEQ	,+4	
011516	104400			HLT		
011520	066245	011310	011312	1\$:	ADD	SDATA(2),DDATA(5)
011526	006362	011310		ASL	SDATA(2)	
011532	005162	011310		COM	SDATA(2)	
011536	036245	011310	011312	BIT	SDATA(2),DDATA(5)	
011544	001401			BEQ	,+4	
011546	104400			HLT		
011550	005162	011310		COM	SDATA(2)	
011554	026245	011310	011312	CMP	SDATA(2),DDATA(5)	
011562	001401			BEQ	,+4	
011564	104400			HLT		
011566	026200	011310		CMP	SDATA(2),R0	
011572	001352			BNE	1\$	
011574	104000			SCOPE		

!CHECK BINARY BYTE OPS USING ADDRESS MODE 6
!NOTE: SDATAB(2), AND DDATAB(4) REFERENCE EVEN BYTE OF SOURCE & DEST DATA
!AND SDATAB(3), AND DDATAB(5) REFERENCE ODD BYTE OF SOURCE & DEST DATA

011576	013702	001004		MOV	#FACTOR,R2	!GET INDEX VALUE
011602	010204			MOV	R2,R4	!R2 FOR SOURCE EVEN BYTE INDEX, R4 FOR
011604	010403			MOV	R4,R3	!DEST ODD BYTE, R3 FOR SOURCE EVEN
011606	005203			INC	R3	!AND R5 FOR DEST ODD BYTE
011610	010305			MOV	R3,R5	
011612	000261			SEC		!SET CARRY
011614	012762	125252	011740	MOV	#125252,SDATAB(2)	
011622	112763	177125	011740	MOVB	#177125,SDATAB(3)	!SOURCE DATA = 092652
011630	016264	011740	011742	MOV	SDATAB(2),DDATAB(4)	
011636	092764	125125	011742	BIS	#125125,DDATAB(4)	!DEST DATA = 177777
011644	136263	011740	011740	BITB	SDATAB(2),SDATAB(3)	
011652	001401			BEQ	,+4	
011654	104400			BITB6:	HLT	
011656	146264	011740	011742	BICB	SDATAB(2),DDATAB(4)	
011664	103401			BCS	,+4	
011666	104400			HLT		!ERROR MOV,BIS,BITBIC DO NOT AFFECT !C!
011670	126364	011740	011742	CHPB	SDATAB(5),DDATAB(4)	
011676	001401			BEQ	,+4	
011700	104400			HLT		
011702	146365	011740	011742	BICB	SDATAB(3),DDATAB(5)	
011710	126265	011740	011742	CHPB	SDATAB(2),DDATAB(5)	
011716	001401			BEQ	,+4	
011720	104400			HLT		
011722	136564	011742	011742	BITB	DDATAB(3),DDATAB(4)	
011730	001401			BEQ	,+4	
011732	104400			HLT		

011734	104000			SCOPE		
011736	000406			BR	UB7	!RESERVE TWO WORDS
011740	000000			SDATAB1:	,WORD	0 !RESERVED FOR SOURCE DATA
011742	000000			DDATAB1:	,WORD	0 !RESERVED FOR DEST DATA
011744	000000			!CHECK BINARY WORD OPS USING ADDRESS MODE 7		
011746	000000			!R2=ADDRESS OF SOURCE DATA, AND R3= ADDRESS OF DEST DATA		
011750	000000			SBIN7:	,WORD	0 !CONTAINS ADDRESS OF SOURCE DATA
011752	000000			DBIN7:	,WORD	0 !CONTAINS ADDRESS OF DEST DATA
011754	010700			UB7:	MOV	PC,R0
011756	024040			CHP	-(R0),-(R0)	
011760	010002			MOV	R0,R2	
011762	024242			CHP	-(R2),-(R2)	
011764	010012			MOV	R0,(R2)	
011766	010203			MOV	R2,R3	
011770	024043			CHP	-(R0),-(R3)	
011772	010013			MOV	R0,(R3)	
011774	000261			SEC		
011776	012777	100000	177740	MOV	#100000,*SBIN7	!SOURCE DATA = 100000
012004	017777	177734	177734	MOV	*SBIN7,*DBIN7	!DEST DATA = 100000
012012	103001			BCC	MOV#	
012014	100401			BMI	,+4	
012016	104400			MOV7:	HLT	
012020	006377	177722		ASL	*DBIN7	!DEST DATA = 000000
012024	102001			BVC	,+4	
012026	001401			BEQ	,+4	
012030	104400			HLT		
012032	027777	177706	177706	CMP	*SBIN7,*DBIN7	!(R2)=100000,(R3)=000000
012040	103402			BCS	CHP7	
012042	102401			BVS	CHP7	
012044	100401			BMI	,+4	
012046	104400			CHP7:	HLT	
012050	167777	177670	177670	SUB	*SBIN7,*DBIN7	!(R2)=100000,(R3)=100000
012054	103003			BCC	SUB7	
012060	102002			BVC	SUB7	
012062	001401			BEQ	SUB7	
012064	100401			BMI	,+4	
012066	104400			SUB7:	HLT	
012070	006277	177650		ASR	*SBIN7	!(R2)=140000
012074	067777	177644	177644	ADD	*SBIN7,*DBIN7	!(R2)=140000,(R3)=040000
012102	103003			BCC	ADD7	
012104	102002			BVC	ADD7	
012106	001401			BEQ	ADD7	
012110	100001			SPL	,+4	
012112	104400			ADD7:	HLT	

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012114 047777 177624 177624 BIC  #SBIN7,#DBIN7 ;(R2)=140000,(R3)=000000
012122 001401 BEQ  ,+4
012124 104400 HLT
012126 057777 177612 177612 BIS  #SBIN7,#DBIN7 ;(R2)=140000,(R3)=140000
012134 100401 BMI  ,+4
012136 104400 HLT
012140 027777 177600 177600 CMP  #SBIN7,#DBIN7
012146 001401 BEQ  ,+4
012150 104400 HLT
012152 104000 SCOPE

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!SOME MISCELLANEOUS OPERATION INVOLVING THE PC
!NOTE: NONE OF THESE OPERATIONS SHOULD AFFECT THE PC

```

012154 005000 CLR  R0
012156 005067 000072 CLR  1$
012162 010707 MOV  PC,PC
012164 120707 CMPB PC,PC
012166 030707 BIT  PC,PC
012170 000007 ADD  R0,PC
012172 105707 TSTB PC
012174 005507 ADC  PC
012176 021007 CMP  (R0),PC
012200 131007 BITB (R0),PC
012202 062707 000000 ADD  #0,PC
012206 023707 001004 CMP  ##FACTOR,PC
012212 133707 001004 BITB ##FACTOR,PC
012216 000240 NOP

```

!THE NEXT TWO INSTRUCTION CAUSE THE PROGRAM TO JUMP TO THE UNRELOCATED
!CODE AND TO RETURN ON THE FOLLOWING INST (IF THE CODE IS RELOCATED)

```

012220 163707 001004 SUB  ##FACTOR,PC ;JUMPS TO UNRELOCATED CODE
012224 063707 001004 ADD  ##FACTOR,PC ;RETURNS
012230 000240 NOP
012232 024607 CMP  -(SP),PC
012234 132607 BITB (SP),PC
012236 026707 000012 CMP  1$,PC
012242 166707 000006 SUB  1$,PC
012246 046707 000002 BIC  1$,PC
012252 000401 BR   ,+4 ;BRANCH OVER 1$
1$: 0
SCOPE
012260 010702 MOV  PC,R2
012262 062702 000012 ADD  #12,R2
012266 012707 001132 MOV  #RELOC,PC ;GO RELOCATE PROGRAM CODE
012272 000240 NOP ;PROGRAM RETURNS HERE+2

```

!11111111111111111111 LAST ADDRESS OF CODE TO BE RELOCATED 111111111111

!22222222222222222222 FIRST ADDRESS TO BE RELOCATED 2222222222

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012274 010700 REL2: MOV  PC,R0 ;GET PC
012276 005740 TST  -(R0) ;R0 CONTAINS THE ADDRESS OF REL2
012300 010037 001010 MOV  R0,##FRSTAD ;SAVE

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012304 010700 MOV  PC,R0 ;GET CURRENT PC
012306 162700 012306 SUB  #,R0 ;SUBTRACT RELOCATION FACTOR
012312 010037 001004 MOV  R0,##FACTOR ;SAVE RELOCATION FACTOR
012316 010701 MOV  PC,R1 ;SET NEW SCOPE PTR
!CHECK BINARY BYTE OPS USING ADDRESS MODE 7
012320 000406 BR   BIN87 ;RESERVE SPACE FOR ADDRESSES & DATA
012322 000000 SBIN87: .WORD 0 ;CONTAINS ADDRESS OF SOURCE EVEN BYTE
012324 000000 .WORD 0 ;CONTAINS ADDRESS OF SOURCE ODD BYTE
012326 000000 .WORD 0 ;CONTAINS ADDRESS OF DEST EVEN BYTE
012330 000000 .WORD 0 ;CONTAINS ADDRESS OF DEST ODD BYTE
012332 000000 DBIN87: .WORD 0 ;CONTAINS SOURCE DATA
012334 000000 .WORD 0 ;CONTAINS DEST DATA
012336 010700 BIN87: MOV  PC,R0
012340 024040 CMP  -(R0),-(R0) ;R0 = ADDRESS OF DEST DATA
012342 010060 177772 MOV  R0,-6(R0) ;LOAD ADDRESS OF DEST EVEN BYTE DATA
012346 010060 177774 MOV  R0,-4(R0)
012352 005260 177774 INC  -4(R0) ;LOAD ADDRESS OF DEST ODD BYTE DATA

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012356	005740			TST	-(R0)	IR0=ADDRESS OF SOURCE DATA
012360	010000	177770		MOV	R0,-10(R0)	ILOAD ADDRESS OF SOURCE EVEN BYTE DATA
012364	010000	177772		MOV	R0,-6(R0)	ILOAD ADDRESS OF SOURCE ODD BYTE DATA
012370	005260	177772		INC		
012374	005002			CLR	R2	ISET INDEX REGISTERS
012376	012703	000002		MOV	#2,R3	I#SBINB7(2);#SBINB7(3) REFERENCE EVEN &
012402	012704	177774		MOV	#-4,R4	IODD BYTE SOURCE DATA; #DBINB7(4);#DBINB7(5)
012406	012705	177776		MOV	#-2,R5	IREFERENCE DEST EVEN& ODD BYTE DATA
012412	005020			CLR	(R0)+	IPRESET SOURCE DATA
012414	005010			CLR	(R0)	IPRESET DEST DATA
012416	013746	001004		MOV	#FACTOR,-(SP)	ISET RELOCATION FACTOR
012422	061602			ADD	(SP),R2	IAND ADD TO INDEX VALUES
012424	061603			ADD	(SP),R3	
012426	061604			ADD	(SP),R4	
012430	062605			ADD	(SP)+,R5	
012432	112773	177777	012322	MOVB	#-1,#SBINB7(3)	ISRC DATA = 177400
012440	132772	000377	012322	BITB	#379,#SBINB7(2)	ICHECK THAT EVEN BYTE WAS NOT AFFECTED
012446	001401			BEQ	,+4	I BY MOVB INSTRUCTION
012450	104400			HLT		
012452	157374	012332	012332	BISB	#SBINB7(3),#DBINB7(4)	
012460	105274			INCB	#DBINB7(4)	ICHECK THAT BIS SET ALL BITS
012464	001401			BEQ	,+4	
012466	104400			HLT		
012470	105375	012332		DECB	#DBINB7(5)	IDEST DATA = 177400
012474	005274	012332		INC	#DBINB7(4)	IDEST DATA = 177401
012500	127375	012322	012332	CMFB	#SBINB7(3),#DBINB7(5)	
012506	001401			BEQ	,+4	
012510	104400			HLT		
012512	147375	012322	012332	BICB	#SBINB7(3),#DBINB7(5)	
012520	001401			BEQ	,+4	
012522	104400			HLT		
012524	105073	012322		CLRB	#SBINB7(3)	ISRC DATA = 000000
012530	157473	012332	012322	BIS7:	BISB	#DBINB7(4),#SBINB7(3)
012536	106174	012332		ROLB	#DBINB7(4)	
012542	103372			BCC	BIS7	
012544	022772	177400	012322	CMF	#177400,#SBINB7(2)	ICHECK RESULT
012552	001401			BEQ	,+4	
012554	104400			HLT		
012556	000372	012322		SWAB	#SBINB7(2)	ISRC DATA = 000377
012562	112775	000200	012332	MOVB	#200,#DBINB7(5)	IDEST DATA = 100000
012570	147572	012332	012322	BIC7:	BICB	#DBINB7(5),#SBINB7(2)
012576	106075	012332		RORB	#DBINB7(5)	

012602	103372			BCC	BIC7	
012604	005772	012322		TST	#SBINB7(2)	
012610	001401			BEQ	,+4	
012612	104400			HLT		
012614	104000			SCOPE		
012616	012702	000001		OAERR:	MOV	#1,R2
012622	010703			MOV	PC,R3	ILOAD R2 WITH ODD #
012624	000401			BR	,+4	IRESERVE SPACE FOR A WORD
012626	000000			,WORD	0	IWILL CONTAIN AN ODD ADDRESS
012630	005723			TST	(R3)+	ISTEP R3 TO POINT TO WORD ABOVE
012632	010313			MOV	R3,(R3)	
012634	005213			INC	(R3)	IAND MAKE ODD
012636	012737	012764	000004	MOV	#1;#ERRVEC	ISET ODD ADDRESS & RESERVED INSTRUCTION
012644	063737	001004	000004	ADD	#FACTOR,#ERRVEC	
012652	013737	000004	000010	MOV	#ERRVEC,#RESVEC	I TO TRAP TO 15 BELOW
012660	000277			SCC		ISET ALL CC'S
012662	100212			SUB	R2,(R2)	
012664	104400			HLT		
012666	060222			ADD	R2,(R2)+	
012670	104400			HLT		
012672	006342			ASL	-(R2)	
012674	104400			HLT		
012676	106512			MFPD	(R2)	NOTE: MAY BE RESERVED
012700	104400			HLT		
012702	170412			CLRF	(R2)	
012704	104400			HLT		
012706	042202			BIC	(R2)+,R2	
012710	104400			HLT		
012712	164202			SUB	-(R2),R2	
012714	104400			HLT		
012716	155202			BISB	0-(R2),R2	
012720	104400			HLT		
012722	105532			ADCB	0-(R2)+	
012724	104400			HLT		
012726	163302			SUB	0-(R3)+,R2	
012730	104400			HLT		
012732	005733			TST	0-(R3)+	
012734	104400			HLT		
012736	106533			MFPD	0-(R3)+	
012740	104400			HLT		
012742	170453			CLRD	0-(R3)	
012744	104400			HLT		
012746	137702	177775		BITB	0,+1,R2	
012752	104400			HLT		
012754	105477	177773		NEGB	0,-1	
012760	104400			HLT		
012762	000406			BR	25	
012764	062716	000002		ADD	#2,(SP)	IADJUST RETURN PC
012770	052766	000017	000002	BIS	#17(2)(SP)	ISET CONDITION CODES ON RETURN
012776	000002			RTI		

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013000 012706 000500      2S1  MOV    #STMPTR,SP    ;RESET STACK PTR
013004 012737 000006 000004  MOV    #ERRVEC+2,##ERRVEC
013012 012737 000012 000010  MOV    #RESVEC+2,##RESVEC
013020 104000                SCOPE

;CHECK JMP INSTRUCTIONS

013022 010700                MOV    PC,R0
013024 002700 000012      ADD    #12,R0        ;SET ADDRESS FOR JMP INST
013030 000277                SCC                    ;SET CC'S
013032 000110                JMP    (R0)
013034 000402                BR     ,+6
013036 000250                CLN                    ;JMP INST JUMPS HERE
013040 000775                BR     ,+4

013042 103003                BCC   JMP1
013044 102002                BVC   JMP1
013046 001001                BNE   JMP1
013050 100001                BPL   ,+4
013052 104400      JMP1:  HLT                    ;ERROR! INCORRECT CC'S AFTER JMP

013054 005002                CLR    R2            ;SET INDICATOR
013056 010703                MOV    PC,R3
013060 000401                BR     ,+4            ;RESERVE WORD FOR JMP ADDRESS
013062 000000                .WORD 0              ;CONTAINS ADDRESS FOR JMP INST
013064 005723                TST   (R3)
013066 010313                MOV    R3,(R3)
013070 010300                MOV    R3,R0
013072 002713 000022      ADD    #22,(R3)      ;(R3) IS JMP ADDRESS
013076 010300                MOV    R3,R0
013100 000133                JMP    @ (R3)+
013102 000402                BR     ,+6            ;JUMP TO ADDRESS CONTAINED IN R3
013104 005102                COM   R2            ;COMPLEMENT INDICATOR
013106 000775                BR     ,+4
013110 005202                INC   R2            ;CHECK INDICATOR
013112 001003                BNE   JMP3
013114 005720                TST   (R0)
013116 020003                CMP   R0,R3        ;CHECK AUTO-INC R3
013120 001401                BEQ   ,+4
013122 104400      JMP3:  HLT

013124 005002                CLR    R2            ;SET INDICATOR
013126 010704                MOV    PC,R4
013130 010400                MOV    R4,R0        ;SET UP JMP REGISTER
013132 000402                BR     R1            ;SET UP CHECK REGISTER
013134 005102                COM   R2            ;COMPLEMENT INDICATOR
013136 000403                BR     R2
013140 022424      1S:  CMP   (R4)++,(R4)
013142 005724                TST   (R4)
013144 000144                JMP   -(R0)        ;R4=JMP ADDRESS
013146 005202                INC   R2            ;USE R4 AS ADDRESS
013150 001003                BNE   JMP#
013152 022220                CMP   (R0)++,(R0)
013154 020004                CMP   R0,R4        ;CHECK AUTO-DEC R4
    
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013156 001401                BEQ   ,+4
013160 104400      JMP4:  HLT

013162 010703                MOV    PC,R3
013164 000401                BR     ,+4            ;RESERVE WORD FOR JMP ADDRESS
013166 000000                .WORD 0              ;CONTAINS JUMP ADDRESS
013170 005723                TST   (R3)
013172 010313                MOV    R3,(R3)
013174 002723 000016      ADD    #16,(R3)
013200 010300                MOV    R3,R0        ;LOAD CHECK REGISTER
013202 000402                BR     R3
013204 005102                COM   R2
013206 000401                BR     R2
013210 000153                JMP   @-(R3)        ;JUMP TO 2S VIA 1S ABOVE
013212 005202                INC   R2            ;CHECK INDICATOR
013214 001003                BNE   JMP5
013216 005740                TST   -(R0)
013220 020003                CMP   R0,R3        ;CHECK AUTO-DEC R3
013222 001401                BEQ   ,+4
013224 104400      JMP5:  HLT

013226 000402                BR     R2
013230 005102                COM   R2            ;COMPLEMENT INDICATOR
013232 000402                BR     R3
013234 000167 177770      2S:  JMP   R1
013240 005202                3S:  INC   R2
013242 001401                BEQ   ,+4
013244 104400      JMP6:  HLT

013246 012767 013264 000020  MOV    #15,7S        ;SET UP JMP ADDRESS
013254 003767 001004 000012  ADD    @#FACTOR,7S  ;ADD RELOCATION FACTOR
013262 000402                BR     R2            ;GO TO JMP #7S INST
013264 005102                COM   R2            ;COMPLEMENT INDICATOR
013266 000403                BR     R3            ;GO TO CHECK ROUTINE
013270 000177 000000      2S:  JMP   @7S
013274 000000                7S:  .WORD 0        ;CONTAINS JMP ADDRESS
013276 005202                3S:  INC   R2            ;CHECK INDICATOR
013300 001401                BEQ   ,+4
013302 104400      JMP7:  HLT
013304 104000                SCOPE

;CHECK JSR INSTRUCTIONS
013306 013705 001004      JSRST: MOV    @#FACTOR,R5 ;GET RELOCATION FACTOR
013312 012702 013344      MOV    #35,R2        ;FORM DEST ADDR
013316 000502                ADD    R5,R2        ;ADD RELOCATION FACTOR
013320 000277                SCC                    ;PRESET CC'S
013322 000242                CLV
013324 004512                JSR   R5,(R2)      ;GO TO 3S VIA R2
013326 005702                1S:  TST   R2            ;CHECK INDICATOR
013330 001017                BNE   JSR1          ;R2 SHOULD=0
013332 023705 001004      CMP    @#FACTOR,R5  ;CHECK THAT RTS R5 RESTORED R5
013336 001014                BNE   JSR1A
013340 000414                BR     JSR1A
013342 000205                2S:  RTS   R5            ;EXIT TO SCOPE
    
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013344 103011 3$: BCC JSR1 ;CHECK THAT JSR DID NOT
013346 102410 BVS JSR1
013350 001007 BNE JSR1 ;AFFECT CC'S
013352 100006 BPL JSR1
013354 005002 CLR R2 ;CLEAR INDICATOR
013356 012704 013326 MOV #15,R4 ;GET UNRELOCATED RETURN ADDRESS
013362 001604 ADD (SP),R4 ;ADD RELOCATION FACTOR (OLD R5)
013364 020400 CMP R4,R5 ;CHECK THAT OLD R5 WAS PLACED ON THE
013366 001765 BEQ 2$ ;STACK.6 THAT NEW R5 CONTAINS RETURN PC
013370 104400 JSR1: HLT ;ERROR! ABOVE

013372 013704 001004 JSR1A: MOV ##FACTOR,R4 ;GET RELOCATION FACTOR
013376 005000 CLR R0 ;SET INDICATOR
013400 012705 013420 MOV #15,R5
013404 000405 ADD R4,R5 ;SET UP JSR DEFERRED ADRS
013406 010502 MOV R5,R2
013410 012715 013436 MOV #55,(R5)
013414 000415 ADD R4,(R5) ;((R5)=DEST ADRS
013416 000401 BR 2$ ;RESERVE WORD FOR ADDRESS
013420 000000 1$: ,WORD 0 ;CONTAINS DEST ADRS FOR JSR
013422 004435 2$: JSR R4,(R5)+ ;JSR TO 5$ VIA 1$ ABOVE
013424 005200 3$: INC R0 ;CHECK INDICATOR
013426 001013 BNE JSR3
013430 000413 BR JSR3A
013432 005100 4$: COM R0 ;COMPLEMENT INDICATOR
013434 000204 RTS 4 ;RETURN FROM SUBROUTINE
013436 012703 013424 5$: MOV #35,R3 ;GET UNRELOCATED RETURN ADDRESS
013442 001603 ADD (SP),R3 ;ADD RELOCATION FACTOR (OLD R4)
013444 020403 CMP R4,R3
013446 001003 BNE JSR3
013450 005722 TST (R2)+ ;CHECK AUTO-INC R5
013452 020205 CMP R2,R5 ;GO TO RTS
013454 001766 BEQ 4$ ;ERROR ABOVE
013456 104400 JSR3: HLT

013460 013704 001004 JSR3A: MOV ##FACTOR,R4
013464 010405 MOV R4,R5
013466 010703 MOV PC,R3
013470 000401 BR 2$
013472 000405 1$: BR 4$
013474 022323 2$: CMP (R3+),(R3)+
013476 000277 SCC ;GO TO 2$
013500 004443 JSR R4,(R3)
013502 104400 3$: HLT
013504 000414 BR JSR4A
013506 103012 4$: BCC JSR4
013510 102011 BVS JSR4
013512 001510 BNE JSR4
013514 100007 BPL JSR4
013516 012702 013502 MOV #35,R2 ;GET UNRELOCATED RETURN ADDRESS
013522 001602 ADD (SP),R2 ;ADD RELOCATION FACTOR (OLD R4)
013524 020204 CMP R2,R4 ;CHECK THAT CALCULATED RETURN
013526 001002 BNE JSR4 ;PC = NEW R4
013530 005724 TST (R4)+

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013532 000204 RTS R4
013534 104400 JSR4: HLT

013536 000401 JSR4A: BR 2$
013540 000405 1$: BR 3$
013542 010700 2$: MOV PC,R0
013544 004767 177770 JSR PC,1$
013550 100407 BMI JSR6A
013552 104400 HLT
013554 022020 3$: CMP (R0+),(R0)+
013556 020016 CMP R0,(SP) ;CHECK THAT RETURN ADDRESS IS ON THE
013560 001401 BEQ 4$ ;STACK
013562 104400 HLT
013564 000270 SEN ;SET N
013566 000207 RTS PC
013570 104000 JSR6A: SCOPE

;CHECK IOT TRAP (AND ROLB/ASLB)
013572 012737 013624 000020 MOV #IOT1,0$ IOTVEC
013600 063737 001004 000020 ##FACTOR,0$ IOTVEC ;ADD RELOCATION FACTOR
013606 000261 SEC ;SET CARRY
013610 013737 177776 000022 ##PSW,0$ IOTVEC+2 ;RETAIN CURRENT PSW ON TRAP
013616 005000 CLR R0 ;PRESET R0
013620 000004 IOT
013622 000403 BR IOT1A
013624 106100 IOT1: ROLB R0 ;ROTATE R0
013626 102376 BVC -2 ;UNTIL V SETS (R0#200)
013630 000002 RTI
013632 106300 IOT1A: ASLB R0 ;SHIFT SHOULD SET CARRY
013634 103004 BCC IOT1B
013636 102003 BVC IOT1B
013640 001002 BNE IOT1B
013642 005700 TST R0 ;R0 SHOULD =0
013644 001401 BEQ 4$
013646 104400 HLT ;ERROR! ROL/ASL FAILED TO SET CC'S PROPERLY
013650 012737 000022 000020 MOV #IOTVEC+2,0$ IOTVEC ;RESTORE IOT TRAP
013656 005037 000022 CLR ##IOTVEC+2 ;VECTOR
013662 104000 SCOPE

;CHECK EMT TRAP SEQUENCE
013664 013746 000030 MOV ##EMTVEC,-(SP) ;SAVE SCOPE PTR
013670 012737 013724 000030 #EMT1,0$ EMTVEC ;SET EMT TRAP VECTOR
013676 063737 001004 000030 ##FACTOR,0$ EMTVEC ;ADD RELOCATION FACTOR
013704 000262 SEV ;SET V
013706 013737 177776 000032 ##PSW,0$ EMTVEC+2 ;RETAIN CURRENT PSW ON TRAP
013714 000265 +SEB:SEC
013716 104000 EMT ;TRAP TO EMT1
013720 001433 BEQ EMT3C ;GO TO EMT3C
013722 104400 HLT ;ERROR! INCORRECT CC'S WERE SET ON RETURN
013724 102027 EMT1: BVC EMT3B ;V! SHOULD'VE SET ON EMT TRAP
013726 105100 COMB R0 ;R0#000377,CC'S#1001
013730 105500 ADCB R0 ;R0#000000,CC'S#0101
013732 106000 RORB R0 ;R0#000200,CC'S#1010

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213734 102023      BVC      EMT3B
213736 100022      BPL      EMT3B
213740 000257      CCC
213742 105400      NEGB     R0          ;R0=000200,CC'S=1010
213744 102017      BVC      EMT3B
213746 100016      BPL      EMT3B
213750 000242      CLV
213752 000261      SEC
213754 105300      DECB    R0          ;CLEAR 'V'
213756 102012      BVC      EMT1B    ;AND SET 'C'
213760 100411      BMI      EMT3B    ;R0=000177,CC'S=0011
213762 000242      CLV
213764 105200      INCB    R0          ;CLEAR 'V'
213766 103006      BCC      EMT1B    ;R0=000200,CC'S=1011
213770 102005      BVC      EMT3B
213772 100004      BPL      EMT1B
213774 000242      CLV
213776 106200      ASRB    R0          ;CLEAR 'V'
214000 102776      BVS     ,+2       ;SHIFT R0 UNTIL 'V' CLEARS
214002 000401      BR      ,+4
214004 104400      EMT1B: HLT
214006 000002      RTI      ;ERROR!
214010 105500      EMT1C: ADCB    R0          ;EXIT WITH R0=000377
214012 103003      BCC      EMT1D    ;R0=000000
214014 001002      BNE      EMT3D
214016 005700      TST     R0
214020 001401      BEQ     ,+4
214022 104400      EMT1D: HLT
214024 012637 000030      MOV     (SP+),@EMTVEC ;RESTORE SCOPE PTR
214030 005037 000032      CLR
214034 104000      SCOPE

;CHECK TRAP INSTRUCTION TRAP SEQUENCE
214036 013737 000034 000020      HLT=IOT ;REDEFINE HLT
214044 012737 014112 000034      MOV     @#TRAPVEC,@#IOTVEC ;SET IOT (HLT) TRAP VECTOR
214052 063737 021004 000034      MOV     #TRAP1,@#TRAPVEC ;SET TRAP VECTOR
214060 000270      ADD     @#FACTOR,@#TRAPVEC ;ADD RELOCATION FACTOR
214062 013737 177776 000036      SEN
214070 000261      MOV     @#PSW,@#TRAPVEC+2 ;RETAIN CURRENT PSW ON TRAP
214072 110700      SEC
214074 000264      MOVVB  PC,R0
214076 104400      SEZ
214100 103401      TRAP   ,+4       ;SET Z BIT
214102 000004      BCS    ,+4       ;TRAP TO TRAP1
214104 021401      HLT
214106 000004      BEQ   ,+4
214110 000412      HLT
214112 100401      TRAP1: BR      ,+4
214114 000004      BMI   ,+4       ;N BIT GOT SET ON TRAP
214116 062700 000004      HLT
214122 120016      ADD     #4,R0
214124 001401      CMPB   R0,(SP)   ;CHECK LOW BYTE OF RETURN PC ON
214126 000004      BEQ   ,+4       ;STACK
    
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214130 124646      CMPB   =(SP),-(SP)
214132 032626      BIT   (SP+),-(SP)+
214134 000002      RTI
214136 013737 000020 000034      TRAP1C: MOV     @#IOTVEC,@#TRAPVEC ;RESTORE TRAP (HLT) TRAP VECTOR
214144 012737 000200 000036      MOV     #PRTV4,@#TRAPVEC+2
214152 012737 020022 000020      MOV     #IOTVEC+2,@#IOTVEC
214160 005037 020022      CLR
214164 104000      SCOPE
214166 104400      HLT=TRAP ;RESTORE HLT TO A TRAP INST
214170 062702 000012      MOV     PC,R2
214174 012707 001132      ADD     #12,R2
214200 000240      MOV     #RELOC,PC ;GO RELOCATE PROGRAM CODE
214202 010701      NOP ;PROGRAM RETURNS HERE+2
214204 010701      ;2222222222222222 LAST ADDRESS OF CODE TO BE RELOCATED 222222222222
214206 010701      MOV     PC,R1 ;SET SCOPE PTR
214208 005767 164570      ;THE BELOW ROUTINE ASCERTAINS WHICH CP & CP OPTIONS THE PROGRAM IS RUN-
CPCHK1: TST     ICNT ;CHECK IF PASS 0
214210 001036      BNE    REL3 ;CHECK IF PASS 0
214212 012737 020002 000006      MOV     REL3 ;DO NOT EXECUTE ROUTINE IF NOT PASS 0
214220 012700 000003      MOV     #RT1,@#ERRVEC+2 ;SET UP ERROR TRAP TO RETURN
214224 000261      SEC
214226 005737 177772      TST     @#PIRQ ;R0#3 IF 11/45
214232 005600      SBC    R0 ;R0#2 IF 11/45
214234 000261      SEC
214236 105737 177777      TSTB   @#PSW+1 ;R0#1 IF 11/20
214242 005600      SBC    R0
214244 005037 177700      CLR     @#177700 ;R0#0 IF 11/05
214250 006300      ASL    R0 ;SHIFT INDICATOR
214252 010027      MOV     R0,(PC)+ ;SET CP INDICATOR
214254 000000      OPT.CP1 ;CONTAINS OPTION & CP INDICATORS
214256 005037 000006      ;EVEN BYTE1 0=11/05,2=11/20,4=11/40,6=11/45
214262 005037 020012      ;ODD BYTE1 200=MEM NGMT,100=EIB, 40=11/45 FLOATING POINT
SS:     CLR     @#ERRVEC+2 ;RESTORE ERROR TRAP TO HALT ON TRAP
214266 126727 177762 000004      CLR     @#RESVEC+2
214274 002404      CMPB   OPT,CP,#4 ;BRANCH IF 11/05 OR 11/20
214276 004767 164730      BLT    REL3
214302 016627      JSR    PC,PRINT ;PRINT MESSAGE BEGINING AT FOLLOWING ADRS
214304 000000      ILLTEST
                HALT

;3333333333333333 FIRST ADDRESS TO BE RELOCATED 333333333
214306 010700      REL3:  MOV     PC,R0 ;GET PC
214310 005740      TST     =(R0) ;R0 CONTAINS THE ADDRESS OF REL3
214312 010037 001010      MOV     R0,@#FRSTAD ;SAVE
214316 010700      MOV     PC,R0 ;GET CURRENT PC
214320 162700 014320      SUB     #,R0 ;SUBTRACT RELOCATION FACTOR
    
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014324 010037 001004      MOV      R0,#FACTOR      ;SAVE RELOCATION FACTOR
014330 010701              MOV      PC,R1           ;SET NEW SCOPE PTR

                                ;CHECK STACK OVERFLOW
014332 013767 177776 000306 OVFLW: MOV      #PSW,7$      ;SAVE STATUS IN 7$ BELOW
014340 005037 177776              CLR      #PSW           ;SET KERNEL MODE
014344 010746              MOV      PC,-(SP)       ;PUSH CURRENT PC ONTO STACK
014346 002716 000136              ADD      #2$,,(SP)      ;FORM ADDRESS OF 2$ BELOW
014352 011637 000004              MOV      (SP),#ERRVEC   ;SET ERROR VECTOR
014356 012737 000340 000006      MOV      #340,#ERRVEC+2 ;SET PRIORITY LEVEL 7 ON TRAP
014364 002716 000074              ADD      #41$-2$,(SP)   ;FORM ADDRESS OF 41$ BELOW
014370 012637 000020      MOV      (SP),#IOTVEC   ;SET IOT TRAP VECTOR TO 41$
014374 012746 000340      MOV      #340,-(SP)     ;
014400 011637 000022      MOV      (SP),#IOTVEC+2 ;SET PRIORITY LEVEL 7 ON IOT TRAP
014404 010746              MOV      PC,-(SP)       ;PUSH CURRENT PC ONTO THE STAK
014406 002716 000006      ADD      #6,(SP)        ;ADD OFFSET TO INST FOLLOWING RTI
014412 000002              RTI                    ;SET PRIORITY LEVEL 7,CLEAR 'I' BIT
                                ;AND EXECUTE FOLLOWING INST NEXT

014414 012703 000376      MOV      #376,R3        ;LOAD 376 INTO ADDRESS 376
014420 010313              MOV      R3,R3          ;
014422 010306              MOV      R3,SP         ;SET STACK PTR AT BOUNDARY

                                ;THE BELOW INSTRUCTIONS SHOULD NOT CAUSE AN OVERFLOW TRAP
014424 005716              TST      (SP)           ;BECAUSE TST IS A NON MODIFYING INST
014426 021666 177776              CMP      (SP+,-2(SP)    ;IS 0 IS COMPARE
014432 122737 000002 014254      CHPB    #2,#OPT,CP     ;CHECK IF 11/20 OR 11/05
014440 002411              BLT      12$           ;BRANCH IF 11/40 OR 11/45
014442 001404              BEQ      11$           ;BRANCH IF 11/20
014444 012767 000014 000144      MOV      #14,51$       ;CHANGE CHECK WORD IN 51$ IF 11/05
014452 000407              BR       10$           ;
014454 012767 000034 000134 11$: MOV      #34,51$       ;CHANGE CHECK WORD IN 51$ IF 11/20
014462 000403              BR       10$           ;
014464 012636              MOV      (SP)+,#(SP)    ;BECAUSE OF ADDRESS MODE 5
014466 054676 000000      BIS      -(SP),#(SP)    ;BECAUSE OF ADDRESS MODE 7
014472 005066 000004 10$: CLR      4(SP)         ;BECAUSE DEST ADDRESS IS > 376
014476 057636 000000      BIS      0(SP),#(SP)+  ;BECAUSE OF ADDRESS MODE 3
014502 000406              BR       3$           ;BRANCH OVER NON KERNEL MODE TESTS

                                ;ERROR SERVICE ROUTINE
014504 012600 2$: MOV      (SP)+,R0     ;SAVE PC OF INSTRUCTION THAT TRAPPED
014506 012602              MOV      (SP)+,R2     ;SAVE PSW
014510 012706 000500      MOV      #STKPTR,SP   ;SET STACK PTR
014514 104400              HLT                    ;ERROR! AN INSTRUCTION THAT WAS NOT
                                ;SUCCEEDED TO TRAP TRAPPED
                                ;R0 CONTAINS PC, R2 CONTAINS PSW

014516 000450              BR       6$           ;EXIT TEST
                                ;THE BELOW INSTRUCTIONS WILL CAUSE A STACK OVERFLOW
                                ;STACK PTR IS AT 376
014520 002737 000066 000004 3$: ADD      #4$-2$,#ERRVEC ;SET ERROR VECTOR TO 4$
014526 010306              MOV      R3,SP         ;SET STACK PTR AT 376
014530 012702 000001      MOV      #1,R2         ;
014534 005000              CLR      R0            ;
014536 005016              CLR      (SP)         ;SETS BIT 0 IN R0

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014540 006302              ASL      R2             ;SHIFT INDICATOR BIT
014542 105226              INCB    (SP)+         ;SETS BIT 1 IN R0
014544 006302              ASL      R2             ;
014546 000746              ADD      PC,-(SP)     ;SETS BIT 2 IN R0
014550 006302              ASL      R2             ;
014552 000004              IOT     2             ;SETS BIT 3 IN R0
014554 006302              ASL      R2             ;
014556 004767 000014      JSR      PC,40$       ;SETS BIT 4 IN R0
014562 006302              ASL      R2             ;NOTE! 11/05 WITHOUT ECO # KD11A-00005
                                ;DOES NOT SET BIT 4.
014564 050666 177776      BIS      SP,-2(SP)     ;SETS BIT 5 IN R0
014570 000407              BR       5$           ;

                                ;PROGRAM WILL TRAP HERE ON OVERFLOW TRAP
014572 050200 4$: BIS      R2,R0        ;SET APPROPRIATE BIT IN R0
014574 000002              RTI                    ;RETURN FROM TRAP

014576 000207 40$: RTS      PC

014600 012737 000022 000020 41$: MOV      #IOTVEC+2,#IOTVEC
014606 000002              RTI

                                ;CHECK THAT ABOVE INSTRUCTIONS DID TRAP
014610 012706 000500 5$: MOV      #STKPTR,SP   ;SET STACK PTR
014614 022700 50$: CMP      (PC)+,R0   ;EACH INSTRUCTION SET A BIT IN R0
014616 000000 51$: ,WORD 0           ;CONTAINS CHECK WORD
014620 001407              BEQ      6$           ;R0# 77 IF 40 OR 45,14 IF 25,34 IF 20
014622 105737 014254      TSTB    #DORT,CP     ;CHECK IF 11/05
014626 001003              BNE     52$           ;BRANCH IF NOT AN 11/05
014630 022700 000034      CMP      #34,R0       ;USE ECO KD11A-00005 CHECK WORD
014634 001401              BEQ     52$           ;
014636 104400 52$: HLT

                                ;EXIT ROUTINE
014640 012706 000600 6$: MOV      #KPTR,SR     ;SET KERNEL STACK PTR
014644 012746              MOV      (PC)+,-(SP)   ;PUSH OLD PSW ONTO STACK
014646 000000 7$: ,WORD 0           ;CONTAINS SAVED PSW
014650 010746              MOV      PC,-(SP)     ;PUSH CURRENT PC ONTO STACK
014652 002716 000006      ADD      #6,(SP)      ;ADD OFFSET
014656 000002              RTI
014660 012706 000500      MOV      #STKPTR,SP   ;SET STACK PTR
014664 012737 000006 000004      MOV      #ERRVEC+2,#ERRVEC
014672 104000              SCOPE

                                ;CHECK THAT ALL RESERVED INSTRUCTIONS TRAP (TO LOCATION 10)
014674 012737 000002 001114 RESTRP: MOV      #2,#SCOPE     ;LIMIT TO TWO ITERATIONS
014702 010701              MOV      PC,R1        ;SET SCOPE POINTER
014704 012702 015024      MOV      #55,R2       ;GET ADDRESS OF RESERVED INSTRUCTION TABLE
014710 003702 001004      ADD      #FACTOR,R2   ;
014714 122737 000004 014254      CHPB    #4,#OPT,CP     ;ADJUST TABLE ADDRESS IF 11/20, 11/05
014722 003402 11$: BLE     11$           ;55#11/45, 11/40 TABLE, 65#11/05
014724 002702 000036      ADD      #65-55,R2    ;11/20 TABLE
014730 132737 000040 014255 11$: BITB    #40,#DORT,CP+1 ;CHECK IF 11/45 FLOATING POINT IS AVAIL.
014736 001402              BEQ     ,+6           ;BRANCH IF NOT AVAILABLE

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014740 005067 000110 CLR 505 ;SET TABLE TERMINATOR AT GROUP 7
014744 012737 015002 000010 MOV #45,RESVEC ;SET RESERVED INSTRUCTION TRAP
014752 063737 001004 000010 ADD #FACTOR,RESVEC
014760 012203 15: MOV (R2+),R0 ;GET FIRST RESERVED INSTRUCTION
014762 001454 BEQ 75 ;R0 TERMINATES THE TABLE
014764 012204 MOV (R2+),R4 ;GET LAST RESERVED INSTRUCTION IN GROUP
014766 010317 25: MOV (R3,4PC) ;EXECUTE RESERVED INSTRUCTION
014770 000000 35: .WORD 0 ;CONTAINS RESERVED INSTRUCTION
014772 104400 HLT ;ERROR! INSTRUCTION IN R3
014774 104400 HLT ;(25) ABOVE FAILED TO CAUSE A
014776 104400 HLT ;RESERVED INSTRUCTION TRAP
015000 000405 BR 415
015002 012716 45: MOV #415,(SP) ;ADJUST RETURN PC
015006 063716 001004 ADD #FACTOR,(SP) ;TO RETURN TO 415
015012 000002 RTI ;RETURN TO 415
015014 020304 415: CMP R3,R4 ;HAS GROUP OF RESERVED INSTRUCTIONS
015016 001760 BEQ 15 ;BEEN EXECUTED
015020 005203 INC R3 ;INCREMENT THIS RESERVED INSTRUCTION
015022 000761 BR 25 ;TO NEXT ONE AND EXECUTE
;TABLE OF 11/40, 11/45 RESERVED INSTRUCTIONS (0 TERMINATES THE TABLE)
015024 000007 55: 7 ;GROUP 1
015026 000077 ; "
015030 000210 77 ; "
015032 000277 210 ;GROUP 2
015034 005000 227 ; "
015036 007777 7000 ;GROUP 3
015040 075040 7777 ; "
015042 076777 75040 ;GROUP 4
015044 106400 76777 ; "
015046 106477 106400 ;GROUP 5
015050 106700 106477 ; "
015052 107777 106700 ;GROUP 6
015054 170000 505: 107777 ; "
015056 177777 170000 ;GROUP 7 FLOATING POINT
015060 000000 0 177777 ; INSTRUCTIONS
;R0 TERMINATES THE TABLE
;TABLE OF 11/05, 11/20 RESERVED INSTRUCTIONS (0 TERMINATES THE TABLE)
015062 000006 65: 6 ;GROUP 1
015064 000077 77 ; "
015066 000210 210 ;GROUP 2
015070 000237 237 ; "
015072 006400 6400 ;GROUP 3
015074 007777 7777 ; "
015076 070000 70000 ;GROUP 4
015100 077777 77777 ; "
015102 106400 106400 ;GROUP 5
015104 107777 107777 ; "
015106 170000 170000 ;GROUP 6
015110 177777 177777 ; "
015112 000000 0 ;R0 TERMINATES THE TABLE
015114 012737 000012 000010 75: MOV #RESVEC+2,RESVEC ;RESTORE RESERVED TRAP TO HALT AT 12
015122 104000 SCOPE

```

ICHECK THAT ALL BITS IN THE PROCESSOR STATUS WORD (PSW) CAN BE SET AND

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015124 013767 177776 000152 ;CLEARED, PSMCHK1 MOV #PSW,35 ;SAVE STATUS
015132 005037 177776 CLR #PSW ;CLEAR MODE BITS IN PSW
015136 005046 CLR -(SR) ;ROUTINE TO CLEAR
015140 010746 MOV PC,-(SP) ;STATUS WORD (PSW)
015142 062716 000006 ADD #6,(SP)
015146 000002 RTI ;CLEAR PSW & EXECUTE FOLLOWING INST
015150 013746 000016 MOV #TBITVEC+2,-(SP)
015154 012704 177776 MOV #PSW,R4 ;LOAD ADDRESS OF PSW INTO R4
015160 000250 CLN ; "
015162 005714 TST (R4) ;CHECK THAT PSW WAS CLEARED
015164 001401 BEQ ,+4 ; "
015166 104400 HLT ;ERROR! PSW FAILED TO CLEAR
015170 113700 014254 MOV #OPT.CR,R0 ;GET CP TYPE
015174 016000 016566 MOV PSMBIT(0),R0 ;GET BIT MASK FOR TEST R0=THOSE BITS IN
;THE PSW WHICH CAN BE SET/CLEARED.
015200 005737 014254 TST #OPT.CR ;CHECK IF MEM MGMT IS AVAILABLE
015204 100002 BPL 105 ;BRANCH IF NOT AVAILABLE
015206 052700 170000 BIS #170000,R0 ;SET BITS 15-12 IF MEM MGMT
015212 012702 000001 105: MOV #1,R2 ;R2 = TEST BIT
015216 030200 15: BIT R2,R0 ;CHECK IF BIT CAN BE SET/CLEARED
015220 001423 BEQ 25 ; "
015222 005037 000016 CLR #TBITVEC+2
015226 030227 000020 BIT R2,#20 ;CHECK IF TEST WILL SET !1! BIT
015232 001403 BEQ 205 ; "
015234 012737 000002 000016 MOV #RTI,#TBITVEC+2 ;SET RTI INTO RETURN
015242 005014 205: CLR (R4) ;CLEAR PSW
015244 050214 BIS R2,(R4) ;SET R2 INTO PSW
015246 011403 MOV (R4),R3 ;GET BIT
015250 020203 CMP R2,R3 ;CHECK THAT BIT WAS SET IN PSW
015252 001401 BEQ ,+4 ; "
015254 104400 HLT ;ERROR! BIT IN R2 FAILED TO SET IN PSW
015256 000244 CLZ ;CLEAR Z BIT
015260 040214 BIC R2,(R4) ;CLEAR BIT IN PSW
015262 011403 MOV (R4),R3 ;GET PSW RESULT
015264 001401 BEQ 25 ;BRANCH IF BIC ABOVE CLEARED BIT IN PSW
015266 104400 HLT ;ERROR! BIT IN R2 FAILED TO CLEAR IN PSW
015270 006302 25: ASL R2 ;SHIFT TEST BIT
015272 103351 BCC 15 ;BRANCH IF ALL BITS NOT TESTED
015274 005014 CLR (R4) ;CLEAR STATUS
015276 012637 000016 MOV (SP+),#TBITVEC+2 ;RESTORE T BIT RETURN
015302 012746 35: MOV (PC+),-(SP) ;PUSH ORIGINAL STATUS ON STACK
015304 000000 .WORD 0 ;CONTAINS ORIGINAL PSW
015306 010746 MOV PC,-(SP) ;SET RETURN PC
015310 062716 000006 ADD #6,(SP)
015314 000002 RTI ;RETURN
015316 104000 45: SCOPE
015320 013704 177776 MOV #PSW,R4 ;SAVE PSW IN R4
015324 010446 R4,-(SP) ;PUSH R4 ONTO STACK
015326 112716 000300 MOV #300,(SP) ;SET PRIORITY LEVEL 6 AND
015332 010746 MOV PC,-(SP) ;CLEAR !1! BIT AND EXECUTE
015334 062716 000006 ADD #6,(SP) ;INSTRUCTION FOLLOWING RTI

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015340 000002 RTI
;CHECK THAT ALL BITS IN THE CURRENT STACK PTR CAN BE SET/CLEARED
015342 010603 CHKSP: MOV SP,R3 ;SAVE STACK PTR
015344 000257 CCC
015346 112706 000377 MOVB #377,SP ;SET STACK PTR = -1
015352 006006 1S: ROR SP ;ROTATE 0 BIT THROUGH ALL BIT
015354 103776 BCB 1S ;BIT POSITIONS
015356 005206 INC SP ;SHOULD INCREMENT SP TO 0
015360 001403 BEQ 2S
015362 010602 MOV SP,R2 ;SAVE ERROR STACK PTR
015364 010306 MOV R3,SP ;SET STACK PTR FOR TRAP
015366 104400 HLT ;ERROR!

015370 010306 2S: MOV R3,SP ;RESTORE ORIGINAL STACK PTR

;CHECK BYTE OPERATIONS USING THE STACK
015372 010600 SPCHK: MOV SP,R0 ;SAVE STACK PTR
015374 010003 MOV R0,R3
015376 005043 CLR -(R3)
015400 112746 177777 MOVB #-1,-(SP) ;(SP) = 377
015404 022713 000377 CMP #377,(R3) ;CHECK THAT ONLY EVEN BYTE WAS AFFECTED
015410 001002 BNE 1S
015412 020306 CMP R3,SP ;CHECK AUTO-DEC
015414 001401 BEQ ,+4
015416 104400 1S: HLT

015420 105226 INCB (SP)+
015422 005723 TST (R3)+ ;CHECK RESULT
015424 001002 BNE 2S
015426 020006 CMP R0,SP ;CHECK AUTO-INC
015430 001401 BEQ ,+4
015432 104400 2S: HLT

015434 005143 COM -(R3) ;(R3)=177777
015436 144613 BICB -(SP),(R3)
015440 022713 177400 CMP #177400,(R3) ;CHECK RESULT
015444 001002 BNE 3S
015446 020603 CMP SP,R3
015450 001401 BEQ ,+4
015452 104400 3S: HLT

015454 132627 000377 BITB (SP)+,#377
015460 001002 BNE 4S
015462 020600 CMP SP,R0
015464 001401 BEQ ,+4
015466 104400 4S: HLT

015470 012746 000001 MOV #1,-(SP)
015474 002706 000002 ADD #2,SP
015500 012702 177401 MOV #177401,R2
015504 120246 CMPB R2,-(SP)
015506 001004 BNE 5S
015510 122602 CMPB (SP)+,R2

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015512 001002 BNE 5S
015514 020006 CMP R0,SP
015516 001401 BEQ ,+4
015520 104400 5S: HLT
015522 010446 MOV R4,-(SP) ;RESTORE ORIGINAL PSW TO STACK
015524 010746 MOV PC,-(SP)
015526 062716 000006 ADD #6,(SP)
015532 000002 RTI
015534 104000 SCOPE

;CHECK THAT 'C' BIT SETS/CLEARs PROPERLY
015536 012727 177776 CBIT: MOV #177776,(PC)+ ;LOAD CONSTANT
015542 000000 ,NORD 0
015544 010700 1S: MOV PC,R0 ;GET CURRENT PC
015546 162700 000004 SUB #4,R0 ;POINT R0 TO 15 ABOVE
015552 005520 2S: ADC (R0)+ ;ADD 'C' BIT TO 15 ABOVE
015554 006340 ASL -(R0) ;SHIFT 15
015556 102375 BVC 2S ;UNTIL 'V' BIT SETS
015560 022767 077776 177754 CMP #077776,15 ;CHECK RESULT
015566 001401 BEQ ,+4
015570 104400 HLT ;ERROR! INCORRECT RESULT IN 15 ABOVE.
;R0=ADDRESS OF DATA

;CHECK THAT CONDITION CODES ARE SET PROPERLY WHEN A NUMBER (CURRENT PC)
;AND THAT NUMBER +1 ARE COMPARED, AND VICE VERSA.
015572 010700 CMP#: MOV PC,R0 ;GET CURRENT PC
015574 010002 MOV R0,R2 ;SAVE IN R2
015576 005202 INC R2 ;MAKE R2 = R0+1
015600 000277 SCC
015602 000251 +CLC:CLN
015604 020002 CMP R0,R2 ;CLEAR C & N BITS
015606 103003 BCC 1S ;COMPARE # WITH #+1
015610 102402 BVS 1S ;CARRY BIT SHOULD SET
015612 001401 BEQ 1S ;V BIT SHOULD CLEAR
015614 100401 BMI ,+4 ;Z BIT SHOULD CLEAR
015616 104400 1S: HLT ;N BIT SHOULD SET
;ERROR! COMPARE # WITH #+1 FAILED TO
;SET CONDITION CODES IN PSW CORRECTLY

015620 000277 SCC ;SET CONDITION CODES IN PSW
015622 120200 CMPB R2,R0 ;COMPARE #+1 WITH #
015624 103403 BCS 2S ;C BIT SHOULD CLEAR
015626 102402 BVS 2S ;V BIT SHOULD CLEAR
015630 001401 BEQ 2S ;Z BIT SHOULD CLEAR
015632 100001 BPL ,+4 ;N BIT SHOULD CLEAR
015634 104400 2S: HLT ;ERROR! COMPARE #+1 WITH # FAILED TO SET
;CONDITION CODES IN PSW CORRECTLY

```

124 NOP (240) INSTRUCTIONS FOLLOW. THESE NOPS MAY
BE CHANGED TO TEST CODE IF THE NEED ARISES, THE TEST CODE SHOULD
BE POSITION INDEPENDENT AND SHOULD RUN WHEN RELOCATED BY THE PROGRAM,

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015636 000240 NOP
015640 000240 NOP
015642 000240 NOP
015644 000240 NOP

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016276 105737 177570 TSTB #NSWR ;DELETE END OF PASS TYPE OUT IF SW7#0
016302 100020 BPL 1$ ;BRANCH IF SW7 IS DOWN
016304 016702 162470 MOV ICNT,R2 ;GET PASS COUNT
016310 004767 163014 JSR PC,SPORM0 ;GO TO FORMAT ROUTINE
016314 012702 001664 MOV #DIGITS+2,R2 ;GET ASCII VALUES
016320 012703 001702 MOV #PASSES,R3 ;AND MOVE THEM INTO MESSAGE
016324 012223 MOV (R2+),(R3)+
016326 012223 MOV (R2+),(R3)+
016330 012737 001672 016060 MOV #PASCNT,#MSG ;PASS MESSAGE ADRS TO TELETYPE SERVICE
016336 002737 000100 177564 BIS #100,#TPS ;SET IE BIT
016344 012737 000610 000024 1$ MOV #PDWN,#PFVEC ;ENABLE POWER FAIL TRAP
016352 012737 000340 000026 MOV #340,#PFVEC+2 ;PRIORITY 7 ON POWER FAIL
016360 005267 162414 INC ICNT
016364 116700 175664 MOV#B OPT,CP,R0 ;GET CP TYPE
016370 026067 016572 162402 CMP PASTAB(R0),ICNT ;CHECK IF END OF TEST
016376 001002 BNE 2$ ;BRANCH IF NOT AT END
016400 000167 000060 JMP DONE
016404 016702 162370 2$ MOV ICNT,R2 ;GET PASS COUNT
016410 006302 ASL R2
016412 046002 016562 BIC CPPASS(0),R2 ;LIMIT PASS COUNT TO 0-6
016416 005037 000016 CLR #16 ;CLEAR T BIT TRAP ADDRESS
016422 012737 000040 001122 MOV #40,#SCOPEF+2 ;SET ITERATION COUNT = 40
016430 016216 165556 MOV PSWTAB(2),(SP) ;PUSH NEXT PASS PSW ON STACK
016434 032716 000020 BIT #20,(SP) ;WILL 'T' BIT BE SET ON NEXT PASS?
016440 001406 BEQ 3$ ;BRANCH IF NOT
016442 012737 000002 001122 MOV #2,#SCOPEF+2 ;SET ITERATION COUNT = 2 FOR 'T' BIT
016450 016737 000006 000016 RTI1,#16 ;SET 'T' BIT TRAP TO RETURN VIA 16
016456 012746 002230 3$ MOV #START2,(SP) ;RESTART PROGRAM AT START2
016462 000002 RTI1: RTI ;RESTART PROGRAM AT START2 WITH NEW PSW
; (FROM TABLE BELOW) NOTE! THE RTI IS
; CHANGED TO AN RTT IF NOT AN 11/05,11/20

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ROUTINE TO SET UP MEMORY MANAGEMENT TO RELOCATE PROGRAM CODE ABOVE 28K

```

016464 032737 000100 177564 DONE: BIT #100,#TPS ;WAIT FOR TTY OUTPUT TO FINISH
016472 001374 BNE DONE
016474 105737 177564 TSTB #TPS ;WAIT FOR LAST CHARACTER TO BE PRINTED
016500 100375 BPL ,+4
016502 005027 CLR (PC)+
016504 000000 1$ ,WORD 0
016506 005267 177772 2$ INC 1$ ;DELAY WAITING FOR TELETYPE TO FINISH
016512 001375 BNE 2$ ;TYPING CHARACTER BEFORE ISSUING RESET
016514 000005 RESET
016516 105737 177570 TSTB #NSWR
016522 100003 BPL 3$
016524 004767 162502 JSR PC,PRINT ;PRINT MESSAGE BEGINING AT FOLLOWING ADRS
016530 016716 ENDMMSG
016532 013702 000042 3$ MOV #42,R2 ;CHECK DDP/ACT11 MONITOR HOOK
016536 001405 BEQ DONE1
016540 000005 RESET
016542 004712 LOGICAL:JSR PC,(R2) ;GO TO DDP/ACT11 MONITOR VIA 42
016544 000240 NOP
016546 000240 NOP
016550 000240 NOP

```

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016552 000137 002224 DONE1: JMP #START5 ;RESTART PROGRAM

;THE BELOW TABLE REPRESENTS THE 'NEW' PSW SET BY THE PROGRAM ON
;SUCCESSIVE PASSES,
;NOTE THE BELOW TABLE MAY BE MODIFIED TO CAUSE THE PROGRAM TO RUN
;UNDER USER DEFINED PARAMETERS BY PATCHING IN THE DESIRED PASS PARAMETER
;FOR EXAMPLE TO CAUSE THE PROGRAM TO RUN WITHOUT SETTING THE 'T' BIT
;IN ALL PASSES PATCH OUT THE 'T' BIT IN THE TABLE,
PSWTAB: 000000 ;ALL 11 FAMILY CP'S
016556 000000 000020 000020

;THE BELOW TABLE IS THE 'BIT MASK' USED TO DETERMINE THE INDEX VALUE
;NEEDED TO SET THE 'NEW' PSW,
016562 177774 CPPASS: 177774 ;11/05
016564 177774 177774 ;11/20

;THE BELOW TABLE REPRESENTS THOSE BITS IN THE CP WHICH CAN BE SET/CLEARED
016566 000377 PSWBIT: 000377 ;11/05
016570 000377 000377 ;11/20

;THE BELOW TABLE CONTAINS THE # OF PASSES REQUIRED TO COMPLETE TEST
016572 000002 PASTAB: ,WORD 2 ;11/05
016574 000002 ,WORD 2 ;11/20

;MESSAGES
016576 005015 047514 020127 MSG1: ,ASCII <15><12>'LOW LIMIT?'
016604 044514 044515 037524
016612 000
016613 110 043511 020110 MSG2: ,ASCII 'HIGH LIMIT?'
016620 044514 044515 037524
016626 000
016627 015 052012 044510 ILLTEST: ,ASCII <15><12>'THIS TEST INVALID FOR 11/40-11/45 PLEASE RUN DZQKC'<15><12>'
016634 020123 042524 052123
016642 044440 053116 046101
016650 042111 043040 051117
016656 030440 027461 030064
016664 030455 027461 032464
016672 050040 042514 051501
016700 020105 052522 020116
016706 041504 045921 006503
016714 000012
016716 005015 042040 050532 ENDMMSG: ,ASCII <15><12>' DZQKC DONE'
016724 041513 042040 047117
016732 000105
000001 ,END

```

ADCB2 004564	ADCB5 005374	ADCB6 006062	ADC87 006710
ADC0 002924	ADC1 003400	ADC2 004374	ADC9 005202
ADC6 005672	ADC7 006604	ADD0 007376	ADD1 007602
ADD1A 110026	ADD1B 010044	ADD2 010632	ADD3 011106
ADD6 011450	ADD7 012112	ASLB1 003942	ASLB1A 004166
ASLB3 005364	ASLB4 004670	ASLB6 006044	ASLB7 007006
ASL0 002646	ASL1 003594	ASL3 005116	ASL4 004466
ASL6 005642	ASL7 006432	ASRB1 004036	ASRB1A 004052
ASRB2 004634	ASRB2A 004692	ASRB5 005324	ASRB6 000162
ASRB7 007024	ASR0 002674	ASR1 003442	ASR2 004410
ASR3 005102	ASR6 009524	ASR7 006466	BELL 001747
BICB1 010220	BICB1A 010242	BIC0 007310	BIC1 007724
BIC2 010922	BIC3 011120	BIC7 012570	BIN07 012336
BIN1 010400	BISB1 010206	BIS0 007266	BIS0A 007344
BIS1 007712	BIS2 010460	BIS2A 010562	BIS7 012530
BITB1 010176	BITB2 010736	BITB3 011270	BIT06 011694
BIT1 007640	BIT13 = 020000	BIT14 = 040000	BIT15 = 100000
BIT2 010546	BIT6 = 000100	BIT8 = 000400	BPTVEC# 000014
C = 000001	CBIT 019536	CC0 002340	CC1 002354
CC2 002370	CC3 002402	CC4 002416	CHKSP 015342
CLR0 002442	CMPB1 010192	CMPB2 010722	CMP03 011302
CMFN 015572	CMF0 007170	CMPBA 007432	CMP1 007622
CMPIA 007742	CMF2 010490	CMF7 012046	COM01 004020
COMB1A 004200	COMB2 004546	COMB5 005302	COM0 006114
COMB7 006724	COM0 002506	COM1 003566	COM3 005160
COM4 004276	COM6 005510	COM7 006556	COM3K 014204
CPPASS 016562	DBIN07 012332	DBIN7 011746	DDATA 011312
DDATAB 011742	DECB1 003770	DECB1A 004320	DECB2 004702
DECB5 005442	DECB6A 006214	DECB7 006772	DEC0 002566
DEC1 003360	DEC1A 003630	DEC2 004444	DEC9 005134
DEC6 005656	DEC7 006490	DIGITS 001662	DIGTAB 001692
DISPLA= 177570	DONE 014464	DONE1 016592	EMTVEC# 000030
EMT1 013724	EMT1B 014004	EMT1C 014010	EMT1D 014022
END 016244	ENDMSG 016716	END1 016246	ERROR 001412
ERRPC 001707	ERRPC0 001721	ERRVEC# 000004	FACTOR 001004
FORXOR 000740	FPEVEC# 000244	FRSTAD 001010	FRSTME 001012
GSTST 003160	HLT = 104400	ICNT 001000	ILLTES 016627
INCB1 003704	INCB2 004776	INCB3 005334	INCB6 006026
INCB6A 006176	INCB7 006796	INCB 002606	INC1 003474
INC3 005170	INC4 004326	INC6 005620	INC7 006616
IOTVEC# 000020	IOT1 013624	IOT1A 013632	IOT1B 013646
JMP1 013052	JMP3 013122	JMP4 013160	JMP9 013224
JMP6 013244	JMP7 013302	JSRTST 013306	JSR1 013370
JSR1A 013372	JSR3 013456	JSR3A 013460	JSR4 013334
JSR4A 013936	JSR6A 013570	KPTR = 000600	KW11 016070
LKS = 177546	LKVEC = 000100	LOGICA 016542	LPB = 177916
LPS = 177514	LSTMEM 002140	HMVEC = 000250	MOV01 010126
MOV0 007120	MOV0A 007190	MOV1 010012	MOV7 012106
MSG 016060	MSG1 016576	MSG2 016613	N = 000010
NEGB1 003754	NEGB4 004726	NEGB6 006132	NEGB7 007040
NEG0 002626	NEG1 003620	NEG2 004350	NEG9 005146
NEG6 005544	NEG7 006524	NULL = 000000	NULLS 016064
OAERR 012616	OPT.CP 014294	DVFLW = 014332	PASCNT 001672
PASSES 001702	PASTAB 016572	PC = X000007	PDOWN 000610

PFAIL 000656	PFVEC = 000024	PIR0 = 177772	PIRVEC# 000240
PRTY4 = 002000	PRTY6 = 000300	PRTY7 = 000340	PSW = 177776
PSWBIT 016566	PSWCHK 015124	PSWTAB 016596	PSW# 000620
RECD 001752	RELOC 001132	RELR1 001006	REL0 002272
REL1 006302	REL2 012274	REL3 014306	RESTRP 014674
RESVEC# 000010	RETPC 001290	ROLB1 003716	ROLB2 004620
ROLB3 005410	ROLB6 004146	ROLB6A 006242	ROLB7 007056
ROL2 002660	ROL1 003414	ROL1A 003626	ROL3 005216
ROL4 004424	ROL6 009474	ROL7 006634	ROR01 004004
RORB1A 004074	RORB4 004602	RORB5 005392	RORB6 006100
RORB7 006742	ROR0 002544	ROR1 003344	ROR1A 003456
ROR2 004312	ROR5 005070	ROR6 005564	ROR7 006906
RTI1 016462	R0 = X000000	R1 = X000001	R10 = X000000
R1 = X000001	R12 = X000002	R13 = X000003	R14 = X000004
R15 = X000005	R2 = X000002	R3 = X000003	R4 = X000004
R5 = X000005	SBCB1 003790	SBCB3 003430	SBC0# 004714
SBCB6 006002	SBCB7 004670	SBC0 002710	SBC1 003910
SBC1A 003526	SBC5 009290	SBC6 005600	SBC7 006412
SBIN07 012322	SBIN7 011744	SCOPE = 104000	SCOPEA 001016
SCOPEB 001046	SCOPEC 001102	SCOPE# 001114	SCOPEE 001126
SCOPEF 001120	SDATA 011310	SDATAB 011740	SLR = 177774
SP = X000006	SPCHK 019372	SRO = 177572	START 002066
START1 002160	START2 002230	START3 002224	STATUS 001714
STKPTR# 000500	SUB0 007136	SUB1 007640	SUB1A 007766
SUB1B 010002	SUB2 010476	SUB2A 010616	SUB3 011032
SUB3A 011054	SUB6 011470	SUB7 012046	SWAB0 002726
SWAB1 004134	SWAB2 004340	SWAB4 004740	SWAB6 006226
SWAB7 006540	SWR = 177570	T = 000020	TBITVE# 000014
TICKS 000776	TKB = 177562	TKS = 177560	TPB = 177566
TPS = 177564	TPVEC = 000064	TRAPVE# 000034	TRAP1 014112
TRAP1C 014136	TRTVEC# 000014	YSTB1 004146	TSTB2 004736
TSTB2A 004746	TSTB6 005794	YST0 002464	TST1 003604
TST2 004266	TST6 006296	TTYCHK 015734	UBM0 006262
UBREAK# 177770	UB7 = 011794	UWM6 005450	UWM7 006332
UH7 006336	V = 000002	Z = 000004	SCRLF 001744
\$FILLS 001002	\$FORM0 001330	\$RESTR 000240	\$SAVR 000214
.PRINT 001232	. = 016734		

ERRORS DETECTED: 0/2

*,D2QKCE=D2QKCE,P11
RUN-TIME 12 22 0 SECONDS
CORE USED: 8K

digital

MAINDEC
CHANGE ORDER

ORIGINATOR Barry G. Irrgang
TEL EXT X3616 DATE 11/18/75
DISC PROJ NO. V98 05169
COST CENTER NO. 301

MC 0163

MCO NO. MD-11-DZQKC-E1/01
SHEET 1 OF 1
DATE RECEIVED 4-FEB-76
FIRST ISSUE _____
FINAL ISSUE _____

PROBLEM Arbitration logic test does not work properly with console DL11 over 600 baud and KW11-L present.

PGM TO BE CHANGED
DZQKC

DISP CODE

OPTIONS AFFECTED

CORRECTION

1. Delay test long enough to ensure KW11-L has interrupt up.
2. Raise PSW of error routing to ensure error will be reported properly if one occurs.

BREAK-IN/EFFECTIVITY

PRODUCT LINES
AFFECTED

ITEM NO.	DOCUMENT/PART NO.	OLD REV	NEW REV	DISP CODE	DESCRIPTION OF CHANGE			PDP-11
1.	MAINDEC-11- DZQKC	E	E	03	PATCH			DOCUMENTATION AFFECTED <input checked="" type="checkbox"/> DIAGNOSTICS <input type="checkbox"/> TECH MANUAL <input type="checkbox"/> TESTER <input type="checkbox"/> TEST PROG <input type="checkbox"/> ENG SPEC <input type="checkbox"/> PURCH SPEC FIELD SERVICE AFFECTED <input checked="" type="checkbox"/> YES <input type="checkbox"/> NO <input type="checkbox"/> Customer Charge <input type="checkbox"/> Product Line Charge <input type="checkbox"/> PL _____ <input type="checkbox"/> Contact Software Distribution Center for price. <input type="checkbox"/> Information Only
					<u>LOC</u>	<u>FROM</u>	<u>TO</u>	
					2266	200	340	
					14146	200	340	
					16164	5227	4737	
					16166	Ø	160	
					160	-	5227	
					162	-	0	
					164	-	1375	
					166	-	207	

History Count started AT REV. E

DISPOSITION CODES

APPROVAL SIGNATURES

- 00 - (DELETED)
- 01 - (DELETED)
- 02 - USE PRESENT STOCK UNTIL NEW STOCK AVAILABLE (PHASE IN)
- 03 - REWORK IMMEDIATELY (RETROFIT)
- 04 - (DELETED)
- 05 - (DELETED)
- 06 - DOCUMENT CORRECTION
- 07 - NEW ITEM (THIS ASSEMBLY)
- 08 - (DELETED)
- 09 - SCRAP IMMEDIATELY

DIAGNOSTIC ENGR. Barry Irrgang *[Handwritten Signature]*

MFG, ENGR. B. Kellicker *[Handwritten Signature]*

FIELD SERVICE S. Johnson *[Handwritten Signature]*

PRODUCT ENGR. D. Testa *[Handwritten Signature]*



