

IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DCKBA TO DCKBE-B-D
PRODUCT NAME: PDP11/45-11/40 BASIC CP TESTS
DATE CREATED: 15 NOV 1972
MAINTAINER: DIAGNOSTIC GROUP
AUTHOR: JOHN ADAMS

COPYRIGHT(C) 1972
DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASS

THIS GROUP OF TESTS CONSIST OF:

MAINDEC NO.	TEST FUNCTION	PROCESSOR
DCKBA-A	SXT INSTRUCTION	11/45,11/40
DCKPB-A	SOB INSTRUCTION	11/45,11/40
DCKBC-A	XOR INSTRUCTION	11/45,11/40
DCKPD-B	MARK INSTRUCTION	11/45,11/40
DCKBE-B	RTT INSTRUCTION	11/45,11/40

1.0 ABSTRACT

THIS IS THE FIRST 5 OF 15 TESTS THAT INCREMENTALLY TEST AND ISOLATE SIMPLE MALFUNCTIONS IN THE PDP 11/45, 11/40. THE TESTS SHOULD BE RUN IN THE INDICATED ALPHABETIC SEQUENCE. THERE ARE ADDITIONAL TESTS FOR MORE COMPLEX MALFUNCTIONS, ALL TESTS ARE EXECUTED IN KERNEL MODE ONLY EXCEPT FOR TEST CCKBE (11/45 ONLY).

2.0 REQUIREMENTS

2.1 EQUIPMENT

PDP11/45 OR PDP 11/40

2.2 STORAGE

PROGRAM STORAGE - THE PROGRAMS USE ALL OF A 4KW MEMORY WITH THE EXCEPTION OF 17500 TO 17776 (WHICH IS RESERVED FOR THE BOOT AND ABSOLUTE LOADER).

2.3 PRELIMINARY PROGRAMS

TESTS T0-T13 (D0AA-D0MA)

3.0 LOADING PROCEDURE

LOAD PROGRAM INTO MEMORY USING ABS LOADER.

4.0 STARTING PROCEDURE

LOAD ADDRESS 200, PRESS START, THE PROGRAM WILL LOOP, AND RING BELL ON COMPLETION. PASS COUNT MAY BE MONITORED IN THE DISPLAY REGISTER (11/45 ONLY), AND IS STORED IN ADDRESS 1000.

5.0 OPERATING PROCEDURE

5.1 OPERATIONAL SWITCH SETTINGS

SW<08>=1 LOAD PDP11/45 MICRO BREAK REGISTER WITH VALUE
IN SW<00-07>, (AT START OF TEST ONLY),

5.2 SUBROUTINE ABSTRACTS

5.2.1 SCOPE

SCOPE IS A MOVE PC, R1(010701) AND STORES THE PC+2 IN R1 (OR R11 IF THE REGISTER SET RIT IS SET IN THE PDP11/45 PROCESSOR STATUS WORD,) THUS, R1 (R11) MAY BE USED AS A TAG TO DETERMINE THE LAST TEST SUCCESSFULLY COMPLETED,

5.2.2 HLT

HLT IS A HALT INSTRUCTION AND IS EXECUTED WHENEVER A HARDWARE MALFUNCTION IS DETECTED BY THE DIAGNOSTIC, THE ADDRESS LIGHTS DISPLAY THE PC-PC+2 OF THE HALT INSTRUCTION IF AN 11/40=11/45,

5.3 PROGRAM AND/OR OPERATOR ACTION

5.3.1 PASS COUNT (ICNT)

THE NUMBER OF PROGRAM PASSES COMPLETED IS CONTAINED IN ADDRESS ICNT (1000). THIS ADDRESS MAY BE EXAMINED TO DETERMINE IN WHICH PASS THE ERROR OCCURED. NOTE; THE PASS COUNT IS DISPLAYED IN THE DISPLAY REGISTER IN THE PDP11/45;

6.0 ERRORS

6.1 TEST ERRORS WILL CAUSE A HALT

FALSE TRAP/INTERRUPT ERRORS = THE PROGRAM WILL HALT AT THE TRAP VECTOR ADDRESS +2.

6.2 ERROR RECOVERY

TEST ERRORS = PRESS CONTINUE OR LOOP TEST (SEE SEC 6.3)

TRAP ERRORS = DETERMINE WHERE ERROR OCCURRED (SEE SEC 8)

6.3 ERROR LOOPING

TO LOOP ON AN ERROR REPLACE THE HLT INSTRUCTION WITH A BRANCH BACK TO THE PREVIOUS SCOPE INSTRUCTION. NOTE THAT IF THE ERROR IS INTERMITTENT THAT THE TEST WILL DROP THROUGH THE HLT AND PROCEED TO THE NEXT TEST, THEREFORE TO LOOP THE TEST CONTINUOUSLY REPLACE THE BEQ ,+4 INSTRUCTION IMMEDIATELY

PRECEDING THE HLT WITH THE BRANCH BACK TO THE PREVIOUS SCOPE.

TO LOOP TRAP FAILURES PATCH IN THE FOLLOWING ROUTINE AT THE ADDRESS OF THE TRAP VECTOR:

```

TRAPVEC:      TRAPVEC+4
TRAPVEC+2:    0
TRAPVEC+4:    012716  ;MOVE 'SCOPE'
                ;ADDRESS TO STACK
TRAPVEC+6:    ADDRESS ;ADDRESS OF PREVIOUS
                ;SCOPE INST.
TRAPVEC+10:   000006  ;RETURN TO TEST AT 'SCOPE'

```

RESTORE ALL LOCATIONS BEFORE PROCEEDING TO NEXT TEST(S).

7.0 RESTRICTIONS

THESE PROGRAMS MUST BE LOADED IN THE LOWER 4K OF MEMORY.

7.1 STARTING RESTRICTION

ALL PROGRAMS MUST BE INITIALLY STARTED AT 200 AND MAY BE STARTED AT A SCOPE INSTRUCTION THEREAFTER.

7.2 OPERATIONAL RESTRICTION

NONE

8.0 MISCELLANEOUS

IF A HALT OCCURS IN THE TRAP OR INTERRUPT VECTOR AREA, EXAMINE REGISTER 6 (THE STACK POINTER). THE CONTENTS OF R6 CONTAINS THE ADDRESS WHERE THE PC OF THE INSTRUCTION THAT CAUSED THE TRAP IS STORED.

8.1 EXECUTION TIME

ALL TESTS TAKE APPROXIMATELY 1 MINUTE EACH ON AN 11/45. WITH CORE MEMORY.

8.2 STACK POINTER

ALL PROGRAMS INITIALLY SET THE STACK POINTER AT 500

9.0

PROGRAM DESCRIPTION

- DCKBA THIS IS A TEST OF THE SXT INSTRUCTION AND INSURES CORRECT RESULTS AND CONDITION CODE OPERATION, THE SXT INSTRUCTION IS TESTED IN ALL ADDRESS MODES IN A GENERAL REGISTER AND THE PC.
- DCKBB THIS IS A TEST OF THE SOB INSTRUCTION AND INSURES CORRECT BRANCHING AND CONDITION CODE OPERATION,
- DCKBC THIS IS A TEST OF THE XOR INSTRUCTION AND INSURES CORRECT RESULTS AND CONDITION CODE OPERATION, THE XOR INSTRUCTION IS EXECUTED USING VARIOUS OPERANDS AND IS EXECUTED USING ALL ADDRESS MODES USING A GENERAL REGISTER AND THE PC.
- DCKBD THIS IS A TEST OF THE MARK INSTRUCTION, THE TEST EXECUTES THE MARK INSTRUCTION USING ALL VALUES OF 'N' AND CHECKS RESULTS, CORRECT CONDITION CODE OPERATION IS ALSO TESTED.
- DCKBE THIS IS A TEST OF THE RTT AND RTI INSTRUCTIONS AND USES 'T' BIT TRAPS IN THE TEST, PROPER STACK OPERATION IS TESTED AND ALSO PROPER STATUS CHANGES ARE TESTED.

000042	000000	HALT
000044	000046	,+2
000046	000000	HALT
000050	000052	,+2
000052	000000	HALT
000054	000056	,+2
000056	000000	HALT
000060	000062	,+2
000062	000000	HALT
000064	000066	,+2
000066	000000	HALT
000070	000072	,+2
000072	000000	HALT
000074	000076	,+2
000076	000000	HALT
000100	000102	,+2
000102	000000	HALT
000104	000106	,+2
000106	000000	HALT
000110	000112	,+2
000112	000000	HALT
000114	000116	,+2
000116	000000	HALT
000120	000122	,+2
000122	000000	HALT
000124	000126	,+2
000126	000000	HALT
000130	000132	,+2
000132	000000	HALT
000134	000136	,+2
000136	000000	HALT
000140	000142	,+2
000142	000000	HALT
000144	000146	,+2
000146	000000	HALT
000150	000152	,+2
000152	000000	HALT
000154	000156	,+2
000156	000000	HALT
000160	000162	,+2
000162	000000	HALT
000164	000166	,+2
000166	000000	HALT
000170	000172	,+2
000172	000000	HALT
000174	000176	,+2
000176	000000	HALT
000200	000202	,+2
000202	000000	HALT
000204	000206	,+2
000206	000000	HALT
000210	000212	,+2
000212	000000	HALT
000214	000216	,+2

000216	000000	HALT
000220	000222	,+2
000222	000000	HALT
000224	000226	,+2
000226	000000	HALT
000230	000232	,+2
000232	000000	HALT
000234	000236	,+2
000236	000000	HALT
000240	000242	,+2
000242	000000	HALT
000244	000246	,+2
000246	000000	HALT
000250	000252	,+2
000252	000000	HALT
000254	000256	,+2
000256	000000	HALT
000260	000262	,+2
000262	000000	HALT
000264	000266	,+2
000266	000000	HALT
000270	000272	,+2
000272	000000	HALT
000274	000276	,+2
000276	000000	HALT
000300	000302	,+2
000302	000000	HALT
000304	000306	,+2
000306	000000	HALT
000310	000312	,+2
000312	000000	HALT
000314	000316	,+2
000316	000000	HALT
000320	000322	,+2
000322	000000	HALT
000324	000326	,+2
000326	000000	HALT
000330	000332	,+2
000332	000000	HALT
000334	000336	,+2
000336	000000	HALT
000340	000342	,+2
000342	000000	HALT
000344	000346	,+2
000346	000000	HALT
000350	000352	,+2
000352	000000	HALT
000354	000356	,+2
000356	000000	HALT
000360	000362	,+2
000362	000000	HALT
000364	000366	,+2
000366	000000	HALT
000370	000372	,+2

```

000372 000000          HALT
000374 000376          ,+2
000376 000000          HALT

000000 000000          ,#0
000000 000167 001634  JMP      SXT07
    
```

```

000000          DEST=#0
000200 000200          ,#200
000200 000167 000576  JMP      START
001000 001000          ,#1000
001000 000000          ICNT: 0          IPASS COUNT
001002 005067 177772  START: CLR      ICNT          ICLEAR PASS COUNT
001006 012706 000500  BEGIN: MOV     #STKPTR,X6    ISET STACK POINTER
001012 016737 177762 177570  MOV     ICNT,#DISPLAY     IDISPLAY PASS COUNT
001020 032737 000400 177570  BIT     #400,#SWR        ILOAD MICRO BREAK REGISTER?
001026 001403          BEQ     ,+10             IBRANCH IF NOT
001030 113737 177570 177770  MOVB   #SWR,#UBREAK     ILOAD MICRO BREAK WITH SR0=7
    
```

```

I TEST THAT WHEN N IS CLEAR SXT CLEARS THE DESTINATION
001036 010701          SCOPE
001040 012700 177777  MOV     #-1,DEST
001044 000277          SCC
001046 000250          CLN          ISET ALL CONDITION CODES
001050 006700          SXT      DEST          ICLEAR N
001052 005700          TST     DEST          IEXTEND N BIT (0) INTO DESTINATION
001054 001401          BEQ     ,+4             IDESTINATION=0?
001056 000000          HLT          IDESTINATION OTHER THAN 0
    
```

```

I TEST THAT WHEN N IS SET THAT SXT SETS ALL BITS IN THE DESTINATION
001060 010701          SCOPE
001062 005000          CLR      DEST
001064 000257          CCC          ICLEAR ALL CONDITON CODES
001066 000270          SEN          ISET N
001070 006700          SXT      DEST          IEXTEND N BIT (1) INTO DESTINATION
001072 020027 177777  CMP     DEST,#-1        IDID ALL BITS SET IN DESTINATION
001076 001401          BEQ     ,+4
001100 000000          HLT          IDESTINATION OTHER THAN 177777
    
```

```

I TEST THAT N IS CLEAR AFTER SXT EXTENDS 0'S INTO THE DESTINATION
001102 010701          SCOPE
001104 012700 177777  MOV     #-1,DEST
001110 000250          CLN
001112 006700          SXT      DEST
001114 100001          BPL     ,+4
001116 000000          HLT
    
```

```

I TEST THAT N IS SET AFTER SXT EXTENDS 1'S INTO THE DESTINATION
001120 010701          SCOPE
001122 005000          CLR      DEST
001124 000270          SEN
001126 006700          SXT      DEST
001130 100401          BMI     ,+4
    
```

001132 000000

HLT

TEST THAT SIGN EXTENDS PROPERLY AFTER A MOVE INSTRUCTION (N SET)

001134 010701

SCOPE

001136 005000

CLR

DEST

001140 012767 177777 177774

MOV

#-1, +2

001146 006700

SXT

DEST

001150 020027 177777

CMP

DEST, #-1

001154 001401

BEQ

, +4

001156 000000

HLT

TEST THAT SIGN EXTENDS PROPERLY AFTER A MOVE INSTRUCTION (N CLEAR)

001160	010701			SCOPE	
001162	012700	177777		MOV	#=1,DEST
001166	012767	000000	177774	MOV	#0,+2
001174	006700			SXT	DEST
001176	020027	000000		CMP	DEST,#0
001202	001401			BEQ	,+4
001204	000000			HLT	

TEST THAT SIGN EXTENDS PROPERLY AFTER VARIOUS BYTE INSTRUCTIONS
 (NOTE: THESE TESTS MUST BE RUN SEQUENTIALLY OR TEMP & TEMP+2 MUST
 BE MANUALLY LOADED BEFORE STARTING THE TEST, THE COMMENT AT THE BEG-
 INNING OF THE TEST SHOWS CONTENTS OF TEMP & TEMP+2,

001206	005000			CLR	DEST	1(TEMP+1),(TEMP)/(TEMP+3),(TEMP+2)
001210	005067	000226		CLR	TEMP	1?,?/??
001214	105167	000223		COMB	TEMP+1	10,0/??
001220	006700			SXT	DEST	1=1,0/??
001222	022700	177777		CMP	#=1,DEST	
001226	001401			BEQ	,+4	
001230	000000			HLT		ERROR: SIGN EXTEND FAILED (N=I)
001232	010701			SCOPE		

001234	005000			CLR	DEST	1=1,0/??
001236	105367	000200		DECB	TEMP	1=1,-1/??
001242	006700			SXT	DEST	
001244	022700	177777		CMP	#=1,DEST	
001250	001401			BEQ	,+4	
001252	000000			HLT		ERROR: SIGN EXTEND FAILED (N=I)
001254	010701			SCOPE		

001256	005000			CLR	DEST	1=1,-1/??
001260	156767	000157	000156	BISB	TEMP+1,TEMP+2	1=1,-1/??.=1
001266	006700			SXT	DEST	
001270	022700	177777		CMP	#=1,DEST	
001274	001401			BEQ	,+4	
001276	000000			HLT		ERROR: SIGN EXTEND FAILED (N=I)
001300	010701			SCOPE		

001302	005000			CLR	DEST	1=1,-1/??.=1
001304	116767	000134	000133	MOV B	TEMP+2,TEMP+3	1=1,-1?-1,-1
001312	006700			SXT	DEST	
001314	022700	177777		CMP	#=1,DEST	
001320	001401			BEQ	,+4	
001322	000000			HLT		ERROR: SIGN EXTEND FAILED (N=I)
001324	010701			SCOPE		

001326	012700	177777		MOV	#=1,DEST	1=1,-1?-1,-1
001332	146767	000105	000105	BICB	TEMP+1,TEMP+3	1=1,-1/0.=1
001340	006700			SXT	DEST	
001342	005700			TST	DEST	
001344	001401			BEQ	,+4	
001346	000000			HLT		ERROR: SIGN EXTEND FAILED (N=0)

001350	010701		SCOPE		
001352	012700	177777	MOV	#=1,DEST	;=1,-170,-1
001356	000261		SEC		;SET CARRY
001360	105567	000057	ADCB	TEMP+1	;0,-1/0,-1
001364	006700		SXT	DEST	
001366	005700		TST	DEST	
001370	001401		BEG	,+4	
001372	000000		HLT		;ERROR! SIGN EXTEND FAILED (N=0)
001374	010701		SCOPE		
001376	012700	177777	MOV	#=1,DEST	;0,-1/0,-1
001402	105267	000034	INCB	TEMP	;0,0/0,-1
001406	006700		SXT	DEST	
001410	005700		TST	DEST	
001412	001401		BEG	,+4	
001414	000000		HLT		;ERROR! SIGN EXTEND FAILED (N=0)
001416	010701		SCOPE		
001420	012700	177777	MOV	#=1,DEST	;0,0/0,-1
001424	105167	000014	COMB	TEMP+2	;0,0/0,0
001430	006700		SXT	DEST	
001432	005700		TST	DEST	
001434	001404		BEG	TZ	
001436	000000		HLT		;ERROR! SIGN EXTEN FAILED (N=0)
001440	010701		SCOPE		
001442	000000		TEMP10		
001446	001446			,0,+2	
001446	010701		;TEST THAT Z BIT IS SET AFTER SXT EXTENDS 0'S		
001450	012700	177777	TZ1	SCOPE	
001454	000250		MOV	#=1,DEST	
001456	006700		CLN		;CLEAR N
001460	001401		SXT	DEST	;EXTEND N BIT (0) INTO DESTINATION
001462	000000		BEG	,+4	;BRANCH IF Z IS SET
			HLT		;Z NOT SET AFTER 0'S WERE EXTENDED
001464	010701		;TEST THAT Z BIT IS CLEAR AFTER SXT EXTENDS 1'S		
001466	005000		SCOPE		
001470	000270		CLR	DEST	
001472	006700		SEN		
001474	001001		SXT	DEST	
001476	000000		BNE	,+4	
			HLT		
001500	010701		;TEST THAT THE CARRY BIT (C) IS UNCHANGED BY SXT (C=0,N=0)		
001502	000257		SCOPE		
001504	006700		CCC		;CLEAR ALL CONDITION CODES
001506	103001		SXT	DEST	;EXTEND SIGN
001510	000000		BCC	,+4	;BRANCH IF CARRY IS CLEAR
			HLT		;ERROR CARRY SET

;TEST THAT THE CARRY BIT IS UNCHANGED BY SXT (C=0,N=1)

001512	010701	SCOPE		
001514	000257	CCC		
001516	000270	SEN		IC=0,N=1
001520	006700	SXT	DEST	EXTEND SIGN
001522	103001	BCC	,+4	BRANCH IF CARRY IS CLEAR
001524	000000	HLT		ERROR! CARRY SET

ITEST THAT THE CARRY BIT IS UNCHANGED BY SXT (C=1,N=0)

001526	010701	SCOPE		
001530	000257	CCC		
001532	000261	SEC		IC=1,N=0
001534	006700	SXT	DEST	EXTEND SIGN
001536	103401	BCS	,+4	BRANCH IF CARRY IS SET
001540	000000	HLT		ERROR! CARRY CLEARED

ITEST THAT CARRY IS UNCHANGED BY SXT (C=1,N=1)

001542	010701	SCOPE		
001544	000277	SCC		IC=1,N=1
001546	006700	SXT	DEST	EXTEND SIGN
001550	103401	BCS	,+4	BRANCH IF CARRY SET
001552	000000	HLT		ERROR! CARRY CLEARED

ITEST THAT THE V BIT IS CLEARED BY SXT (V=0,N=0)

001554	010701	SCOPE		
001556	000257	CCC		IV=0,N=0
001560	006700	SXT	DEST	EXTEND SIGN
001562	102001	BVC	,+4	BRANCH IF V IS CLEAR
001564	000000	HLT		ERROR! V SET

ITEST THAT V IS CLEARED BY SXT (V=0,N=1)

001566	010701	SCOPE		
001570	000257	CCC		
001572	000270	SEN		IV=0,N=1
001574	006700	SXT	DEST	EXTEND SIGN
001576	102001	BVC	,+4	BRANCH IF V IS CLEAR
001600	000000	HLT		ERROR! V SET

ITEST THAT V IS CLEARED BY SXT (V=1,N=0)

001602	010701	SCOPE		
001604	000277	SCC		
001606	000250	CLN		IC=1,N=0
001610	006700	SXT	DEST	EXTEND SIGN
001612	102001	BVC	,+4	BRANCH IF V IS CLEAR
001614	000000	HLT		ERROR! V SET

```

)TEST THAT V IS CLEARED BY SXT (C=I,N=I)
001616 010701          SCOPE
001620 000277          SCC
001622 006700          SXT      DEST      !V=1,N=1
001624 102001          BVC      ,+4      !BRANCH !EXTEND SIGN
001626 000000          HLT              !V IS CLEAR
                                           !ERROR! V REMAINED SET
000007          DEST=X7
)TEST THAT SIGN EXTENDS INTO R7 (N=0).
001630 010701          SCOPE
001632 000257          CQC
001634 006707          SXT      DEST      !N=0
001636 000000          HLT              !EXTEND 0/S INTO THE PC
001640 000402          SXT071 BR      MODE67 !ERROR! PC SHOULD'VE GONE TO 0
001642          DEST=,
001644          ,N,+2
001644 000000          !DEST1 0
)TEST DESTINATION MODE 67
001646 010701          SCOPE
001650 005067          CLR      DEST      177766
001654 000270          SEN
001656 006767          SXT      DEST      !SET N
001662 026727          CMP      DEST,#-1 !EXTEND 1/S INTO DEST
001670 001401          BEQ      ,+4      !DID 1/S EXTEND
001672 000000          HLT              !1/S FAILED TO EXTEND
)TEST DESTINATION MODE 27
001674 010701          SCOPE
001676 005067          CLR      MODE27      000004
001702 000270          SEN
001704 006727          SXT      (7)+      !SET N
001706 000000          MODE271 0 !EXTEND 1/S INTO NEXT LOCATION
001710 026727          CMP      MODE27,#-1 !DID 1/S EXTEND
001716 001401          BEQ      ,+4
001720 000000          HLT
)TEST DESTINATION MODE 37
001722 010701          SCOPE
001724 012737          MOV      #-1,#DEST      177777 001642
001732 000250          CLN
001734 006737          SXT      #DEST      !CLEAR N
001740 023727          CMP      #DEST,#0 !EXTEND 0/S
001746 001401          BEQ      ,+4      !DID 0/S EXTEND
001750 000000          HLT
)TEST DESTINATION MODE 77
001752 010701          SCOPE
001754 012767          MOV      #IDEST,DEST      001644 177660
001762 012777          MOV      #-1,#DEST      177777 177652
001770 000250          CLN
001772 006777          SXT      0DEST
001776 027727          CMP      0DEST,#0
002004 001401          BEQ      ,+4

```

002006 000000

HLT

TEST DESTINATION MODE 1

002010 010701 SCOPE
 002012 012700 002320 MOV #MODE1,X0
 002016 005002 CLR X2
 002020 000261 SEC
 002022 005602 SBC X2
 002024 006710 SXT (0)
 002026 022767 177777 000264 CMP #=1,MODE1
 002034 001401 BEQ ,+4
 002036 000000 HLT

ERROR! INCORRECT RESULT

TEST DESTINATION MODE2

002040 010701 SCOPE
 002042 005046 CLR -(6)
 002044 012746 002056 MOV #MODE2,-(6)
 002050 012702 002320 MOV #MODE1,X2
 002054 000002 RTI
 002056 006722 MODE21 SXT (2)+
 002060 005767 000234 TST MODE1
 002064 001401 BEQ ,+4
 002066 000000 HLT
 002070 022702 002322 CMP #MODE1+2,X2
 002074 001401 BEQ ,+4
 002076 000000 HLT

SET UP STACK WITH /NEW/ STATUS
 AND /NEW/ PC

ERROR! INCORRECT RESULT
 CHECK AUTO-INCREMENT

ERROR! AUTO INCREMENT FAILED

TEST DESTINATION MODE 3

002100 010701 SCOPE
 002102 012767 002322 000210 MOV #MODE3,MODE1
 002110 012702 002320 MOV #MODE1,X2
 002114 000270 SEN
 002116 006732 SXT (2)+
 002120 022767 177777 000174 CMP #=1,MODE3
 002126 001401 BEQ ,+4
 002130 000000 HLT

ERROR! INCORRECT RESULT

TEST DESTINATION MODE 4

002132 010701 SCOPE
 002134 012703 002322 MOV #MODE3,X3
 002140 012767 177777 000152 MOV #=1,MODE1
 002146 005067 000150 CLR MODE3
 002152 006743 SXT -(3)
 002154 005767 000140 TST MODE1
 002160 001401 BEQ ,+4
 002162 000000 HLT
 002164 022703 002320 CMP #MODE3-2,X3
 002170 001401 BEQ ,+4
 002172 000000 HLT

ERROR! INCORRECT RESULT
 CHECK AUTO-DECREMENT

ERROR! AUTO-DECREMENT FAILED

TEST DESTINATION MODE 5

002174 010701 SCOPE
 002176 012767 002320 000116 MOV #MODE1,MODE3
 002204 012704 002324 MOV #MODE3+2,X4

002210	000270			SEN		
002212	006754			SXT	0=(4)	
002214	022767	177777	000076	CMP	#-1,MODE1	
002222	001401			BEQ	,+4	
002224	000000			HLT		!ERROR! INCORRECT RESULT
002226	022704	002322		CMP	#MODE3,X4	!CHECK AUTO-DECREMENT
002232	001401			BEQ	,+4	
002234	000000			HLT		!ERROR! AUTO-DECREMENT DEFERRED FAILED

!TEST DESTINATION MODE 6

002236	010701			SCOPE		
002240	012705	002322		MOV	#MODE3,X5	
002244	006765	177776		SXT	-2(5)	
002250	005767	000044		TST	MODE1	
002254	001401			BEQ	,+4	
002256	000000			HLT		!ERROR! INCORRECT RESULT

!TEST DESTINATION MODE 7

002260	010701			SCOPE		
002262	012767	177777	000030	MOV	#-1,MODE1	
002270	012704	177770		MOV	#-10,X4	
002274	012767	002320	000020	MOV	#MODE1,MODE3	
002302	006774	002332		SXT	#MODE3+10(4)	
002306	005767	000006		TST	MODE1	
002312	001401			BEQ	,+4	
002314	000000			HLT		!ERROR INCORRECT RESULT
002316	000402			BR	,+6	!GO TO NEXT TEST
002320	000000			MODE1:	0	
002322	000000			MODE3:	0	

!TEST CONDITION CODES WHEN THE DESTINATION MODE IS NOT 0
 !N=0,Z=1,V=0,C=0

002324	010701			SCOPE		
002326	012767	177777	177764	MOV	#-1,MODE1	
002334	000257			CCC		
002336	006767	177756		SXT	MODE1	
002342	016700	175430		MOV	PSW,X0	
002346	022700	000004		CMP	#4,X0	
002352	001401			BEQ	,+4	
002354	000000			HLT		!ERROR! INCORRECT CONDITION CODES

!N=0,Z=1,V=0,C=1

002356	010701			SCOPE		
002360	012767	177777	177732	MOV	#-1,MODE1	
002366	012702	002320		MOV	#MODE1,X2	
002372	012767	000003	175376	MOV	#3,PSW	
002400	006712			SXT	(2)	
002402	016700	175370		MOV	PSW,X0	
002406	022700	000005		CMP	#5,X0	
002412	001401			BEQ	,+4	
002414	000000			HLT		!ERROR! INCORRECT CONDITION CODES

!N=1,Z=0,V=0,C=0

002416	010701			SCOPE		
--------	--------	--	--	-------	--	--

```

002420 012702 002320          MOV    #MODE1,X2
002424 005067 177670          CLR    MODE1
002430 012767 000016 175340    MOV    #10,PSW
002436 006722                SXT
002440 016700 175332          MOV    PSW,X0
002444 022700 000010          CMP    #10,X0
002450 001401                BEQ    ,+4
002452 000000                HLT

```

;ERROR! INCORRECT CONDITION CODES

IN=1,Z=0,V=0,C=1

```

002454 010701                SCOPE
002456 012702 002322          MOV    #MODE1+2,X2
002462 005067 177632          CLR    MODE1
002466 012767 000017 175302    MOV    #17,PSW
002474 006742                SXT    -(2)
002476 016700 175274          MOV    PSW,X0
002502 022700 000011          CMP    #11,X0
002506 001401                BEQ    ,+4
002510 000000                HLT

```

;ERROR! INCORRECT CONDITION CODES

TEST THAT SXT EXTENDS 0'S INTO THE PSW

```

002512 010701                SCOPE
002514 012767 000357 175254    MOV    #357,PSW
002522 000250                CLN
002524 006767 175246          SXT    PSW
002530 016700 175242          MOV    PSW,X0
002534 001401                BEQ    ,+4
002536 000000                HLT

```

;GET & TEST PSW CONTENTS

```

002540 005267 176234          INC    ICNT          ;INCREMENT PASS COUNT
002544 026727 176230 177777    CMP    ICNT,#-1
002552 001402                BEQ    DONE          ;GO TO DONE IF 100; PASSES COMPLETED
002554 000167 176226          JMP    BEGIN
002560 012767 000007 175000  DONE1  MOV    #7,TPBUF
002566 105767 174772          TSTB  TPCSR
002572 100375                BPL    ,=4
002574 013702 000042          MOV    #42,X2
002600 001404                BEQ    DONE1
002602 004712                JSR    7,(2)
002604 000240                NOP
002606 000240                NOP
002610 000240                NOP
002612 000167 176164          DONE11 JMP    START

```

;RING BELL
;WAIT FOR THE
;BELL TO RING
;GET DECTAPE MONITOR ADDRESS
;DO NOT RETURN IF (42)=0
;RETURN TO DECTAPE MONITOR
;ACT11
;OVERLAY
;AREA
;AND REPEAT TEST

```

000001                ,END

```

BEGIN	001006	DEST	= 001642	DISPLA	= 177570	DONE	002560
DONE1	002612	HLT	= 000000	IGNY	001000	IDEST	001644
MODE1	002320	MODE2	002056	MODE27	001706	MODE3	002322
MODE67	001646	PSW	= 177776	SCOPE	= 010701	START	001002
STKPTR	= 000500	SHR	= 177570	SXT07	001640	TEMP	001442
TPBUF	= 177566	TPCSR	= 177564	TZ	001446	UBREAK	= 177770
	= 002616						

ERRORS DETECTED: 0

1

.TITLE MAINDEC-11-DCKBB-A PDP11/25-11/45 SOB INST TEST
.NLIST MC,MD,SEQ
.LIST ME
.ABS
;TEST DCKBBA- TEST OF THE SOB INSTRUCTION;
; THE SOB INSTRUCTION SUBTRACTS ONE (1) FROM THE REGISTER SPACIFIED
;AND IF THE RESULT IS NOT ZERO (0) THEN A BRANCH IS TAKEN TO THE ADDRESS
;(OR NUMBER OF WORDS) SPECIFIED. IF THE RESULT IS 0 THEN THE NEXT SEQUENTIAL
;INSTRUCTION IS EXECUTED;

;STARTING PROCEEDURE
; LOAD ADDRESS=200
; PRESS START
; STACK POINTER IS SET AT 500
; BELL WILL RING WHEN TEST IS COMPLETE

000000	000000	.#0
000002	000002	,+2
000004	000000	HALT
000006	000006	,+2
000010	000000	HALT
000012	000012	,+2
000014	000000	HALT
000016	000016	,+2
000020	000000	HALT
000022	000022	,+2
000024	000000	HALT
000026	000026	,+2
000030	000000	HALT
000032	000032	,+2
000034	000000	HALT
000036	000036	,+2
000040	000000	HALT
000042	000042	,+2
000044	000000	HALT
000046	000046	,+2
000050	000000	HALT
000052	000052	,+2
000054	000000	HALT
000056	000056	,+2
000060	000000	HALT
000062	000062	,+2
000064	000000	HALT
000066	000066	,+2
000070	000000	HALT
000072	000072	,+2
000074	000000	HALT
000076	000076	,+2
000100	000100	,+2
000102	000000	HALT
000104	000104	,+2
000106	000000	HALT
000110	000110	,+2

000112	000000	HALT
000114	000116	,+2
000116	000000	HALT
000120	000122	,+2
000122	000000	HALT
000124	000126	,+2
000126	000000	HALT
000130	000132	,+2
000132	000000	HALT
000134	000136	,+2
000136	000000	HALT
000140	000142	,+2
000142	000000	HALT
000144	000146	,+2
000146	000000	HALT
000150	000152	,+2
000152	000000	HALT
000154	000156	,+2
000156	000000	HALT
000160	000162	,+2
000162	000000	HALT
000164	000166	,+2
000166	000000	HALT
000170	000172	,+2
000172	000000	HALT
000174	000176	,+2
000176	000000	HALT
000200	000202	,+2
000202	000000	HALT
000204	000206	,+2
000206	000000	HALT
000210	000212	,+2
000212	000000	HALT
000214	000216	,+2
000216	000000	HALT
000220	000222	,+2
000222	000000	HALT
000224	000226	,+2
000226	000000	HALT
000230	000232	,+2
000232	000000	HALT
000234	000236	,+2
000236	000000	HALT
000240	000242	,+2
000242	000000	HALT
000244	000246	,+2
000246	000000	HALT
000250	000252	,+2
000252	000000	HALT
000254	000256	,+2
000256	000000	HALT
000260	000262	,+2
000262	000000	HALT
000264	000266	,+2


```

000266 000000 HALT
000270 000272 ,+2
000272 000000 HALT
000274 000276 ,+2
000276 000000 HALT
000300 000302 ,+2
000302 000000 HALT
000304 000306 ,+2
000306 000000 HALT
000310 000312 ,+2
000312 000000 HALT
000314 000316 ,+2
000316 000000 HALT
000320 000322 ,+2
000322 000000 HALT
000324 000326 ,+2
000326 000000 HALT
000330 000332 ,+2
000332 000000 HALT
000334 000336 ,+2
000336 000000 HALT
000340 000342 ,+2
000342 000000 HALT
000344 000346 ,+2
000346 000000 HALT
000350 000352 ,+2
000352 000000 HALT
000354 000356 ,+2
000356 000000 HALT
000360 000362 ,+2
000362 000000 HALT
000364 000366 ,+2
000366 000000 HALT
000370 000372 ,+2
000372 000000 HALT
000374 000376 ,+2
000376 000000 HALT
    
```

```

010701
000000
177776
177770
177564
177566
177570
177570
000500
000000
000200
000200
000200 000167 000576
    
```

```

SCOPE=010701          IMOV PC,R1
HLT=HALT
PSW=177776            IADDRESS OF PROCESSER STATUS WORD
UBREAK=177770        IADDRESS OF MICRO BREAK REGISTER
TPCSR=177564         IADDRESS OF TELEPRINTER CSR
TPBUF=177566         IADDRESS OF TELEPRINTER BUFFER
SWR=177570           IADDRESS OF CONSOLE SWITCH REGISTER
DISPLAY=177570      IADDRESS OF CONSOLE DISPLAY REGISTER
I*****INITIAL STACK POINTER=0500*****
STKPTR=0500          IINITIAL STACK SETTING
,=0
,=200
JMP START
    
```

```

001000 001000 ,*1000
001000 000000
001002 005067 177772 ICNT: 0 ;CONTAINS PASS COUNT
001006 012706 000500 START: CLR ICNT ;INITIALIZE PASS COUNT
001012 016737 177762 177570 BEGIN: MOV #STKPTR,X6 ;SET STACK POINTER
001020 032737 000400 177570 MOV ICNT,#DISPLAY ;DISPLAY PASS COUNT
001026 001403 BEQ #400,#SWR ;LOAD MICRO BREAK REGISTER?
001030 113737 177570 177770 MOVB ,+10 ;LOAD MICRO BREAK REG WITH SR0=7
;TEST THAT SOB DOES NOT BRANCH WHEN THE SPECIFIED REGISTER (R0) DECREASES TO 0.
001036 010701 T0A: SCOPE
001040 012700 000001 MOV #1,X0 ;LOAD R0=1
001044 000401 BR T0B ;GO DO SOB INSTRUCTION
001046 000402 BR T0C
001050 077002 T0B: SOB X0,-2 ;SOB SHOULD NOT BRANCH
001052 000401 BR T1A ;GO TO NEXT TEST
001054 000000 T0C: HLT ;ERROR SOB BRANCHED
;TEST THAT SOB BRANCHES WHEN R0 BECOMES NEGATIVE.
001056 010701 T1A: SCOPE
001060 005000 CLR X0
001062 000405 BR T1B ;GO DO SOB INSTRUCTION
001064 020027 177777 T1AA: CMP X0,#-1 ;WAS R0 DECREMENTED?
001070 001404 BEQ T2A
001072 000000 HLT ;ERROR! SOB FAILED TO DECREMENT R0
001074 000402 BR T2A
001076 077006 T1B: SOB X0,T1AA ;SOB SHOULD BRANCH
001100 000000 T1C: HLT ;ERROR SOB FAILED TO BRANCH
;TEST THAT SOB BRANCHES WHEN R0 DOES NOT DECREMENT TO 0
001102 010701 T2A: SCOPE
001104 012700 000002 MOV #2,X0 ;R0=2
001110 000401 BR T2B ;GO DO SOB INSTRUCTION
001112 000402 BR T3A ;GO TO NEXT TEST
001114 077002 T2B: SOB X0,-2 ;SOB SHOULD BRANCH
001116 000000 HLT ;ERROR SOB FAILED TO BRANCH
;TEST THAT SOB DECREASES R0 TO 0 PROPERLY.
001120 010701 T3A: SCOPE
001122 012700 000001 MOV #1,X0 ;LOAD R0 #1
001126 077004 T3B: SOB X0,T3A ;SOB SHOULD NOT BRANCH
001130 020027 000000 CMP X0,#0 ;DID SOB DECREMENT R0 TO 0?
001134 001401 BEQ T4A
001136 000000 HLT ;SOB DID NOT DECREMENT R0 TO = 0
;TEST THAT SOB BRANCHES WHEN R0 IS DECREMENTED AND BECOMES POSITIVE.
001140 010701 T4A: SCOPE
001142 012700 100000 MOV #100000,X0 ;LOAD R0 SUCH THAT WHEN DECREMENTED
;IT BECOMES POSITIVE (077777)
001146 000401 BR T4B ;GO DO SOB INSTRUCTION
001150 000402 BR T5A ;GO TO NEXT TEST
001152 077002 T4B: SOB X0,-2 ;SOB SHOULD BRANCH
001154 000000 HLT ;SOB FAILED TO BRANCH

```

```

)TEST THAT SOB DECREMENTS R0 PROPERLY WHEN R0=100000
T5A1  SCOPE
      MOV    #100000,X0      ;LOAD R0=100000
      BR     T5B             ;GO DO SOB INSTRUCTION
T5AA1 CMP    X0,#77777      ;DID SOB DECREMENT R0?
      BEQ    T6A            ;GO TO NEXT TEST
      HLT    T6A            ;ERROR SOB DID NOT DECREMENT R0
      BR     T6A            ;GO TO NEXT TEST
T5B1  SOB    X0,T5AA        ;SOB SHOULD BRANCH
      HLT                    ;SOB FAILED TO BRANCH

)TEST THAT SOB INSTRUCTION DECREMENTS R0 PROPERLY
T6A1  SCOPE
      MOV    #77777,X0      ;LOAD R0 & R2 WITH THE
      MOV    X0,X2          ;LARGEST POSITIVE NUMBER
      BR     T6C            ;GO DO SOB INSTRUCTION
T6B1  DEC    X2             ;DECREMENT R2
      CMP    X0,X2          ;R0=R2?
      BEQ    T6C            ;GO TO SOB INSTRUCTION
      HLT                    ;ERROR SOB DID NOT DECREMENT PROPERLY
      BR     T7A            ;GO TO NEXT TEST
T6C1  SOB    X0,T6B        ;SOB

)TEST THAT SOB DOES NOT BRANCH WHEN R0 DECREMENTS TO 0 (R0 WILL
;INITIALLY BE LOADED TO THE LARGEST POSITIVE NUMBER).
T7A1  SCOPE
      MOV    #77777,X0      ;LOAD R0 & R2 WITH THE
      MOV    X0,X2          ;LARGEST POSITIVE NUMBER
      DEC    X2             ;
T7B1  SOB    X0,-2          ;R0=R2=0?
      ADD    X0,X2          ;
      BEQ    ,+4            ;
      HLT                    ;ERROR

C=10
N=0
)TEST THAT SOB DOES NOT AFFECT THE CONDITION CODES WHEN SOB DOES NOT BRANCH
T10A1 SCOPE
      MOV    #1,X2
      MOV    #0,PSW         ;LOAD PSW
      BR     T10B          ;GO DO SOB INSTRUCTION
T10AA1 HLT
      BR     T10D          ;ERROR SOB BRANCHED
T10B1  SOB    X2,T10AA      ;SOB SHOULD NOT BRANCH
      MOV    PSW,X0         ;GET STATUS CONTENTS
      CMP    #0,X0         ;STATUS = 0
      BEQ    ,+4
      HLT                    ;ERROR STATUS WORD CHANGED
T10D1  NOP
      C=C+1
      N=N+1
    
```

001316	010701			T11A1	SCOPE		
001320	012702	000001			MOV	#1,X2	
001324	012767	000001	176444		MOV	#1,PSW	ILOAD PSW
001332	000402				BR	T11B	IGO DO SOB INSTRUCTION
001334	000000			T11AA1	HLT		IERROR SOB BRANCHED
001336	000407				BR	T11D	
001340	077203			T11B1	SOB	X2,T11AA	ISOB SHOULD NOT BRANCH
001342	016700	176430			MOV	PSW,X0	IGET STATUS CONTENTS
001346	022700	000001			CMP	#1,X0	ISTATUS = 1
001352	001401				BEG	,+4	
001354	000000				HLT		
001356	000240			T11D1	NOP		IERROR STATUS WORD CHANGED
	000012				C=C+1		
	000002				N=N+1		
001360	010701			T12A1	SCOPE		
001362	012702	000001			MOV	#1,X2	
001366	012767	000002	176402		MOV	#2,PSW	ILOAD PSW
001374	000402				BR	T12B	IGO DO SOB INSTRUCTION
001376	000000			T12AA1	HLT		IERROR SOB BRANCHED
001400	000407				BR	T12D	
001402	077203			T12B1	SOB	X2,T12AA	ISOB SHOULD NOT BRANCH
001404	016700	176366			MOV	PSW,X0	IGET STATUS CONTENTS
001410	022700	000002			CMP	#2,X0	ISTATUS = 2
001414	001401				BEG	,+4	
001416	000000				HLT		
001420	000240			T12D1	NOP		IERROR STATUS WORD CHANGED
	000013				C=C+1		
	000003				N=N+1		
001422	010701			T13A1	SCOPE		
001424	012702	000001			MOV	#1,X2	
001430	012767	000003	176340		MOV	#3,PSW	ILOAD PSW
001436	000402				BR	T13B	IGO DO SOB INSTRUCTION
001440	000000			T13AA1	HLT		IERROR SOB BRANCHED
001442	000407				BR	T13D	
001444	077203			T13B1	SOB	X2,T13AA	ISOB SHOULD NOT BRANCH
001446	016700	176324			MOV	PSW,X0	IGET STATUS CONTENTS
001452	022700	000003			CMP	#3,X0	ISTATUS = 3
001456	001401				BEG	,+4	
001460	000000				HLT		
001462	000240			T13D1	NOP		IERROR STATUS WORD CHANGED
	000014				C=C+1		
	000004				N=N+1		
001464	010701			T14A1	SCOPE		
001466	012702	000001			MOV	#1,X2	
001472	012767	000004	176276		MOV	#4,PSW	ILOAD PSW
001500	000402				BR	T14B	IGO DO SOB INSTRUCTION
001502	000000			T14AA1	HLT		IERROR SOB BRANCHED
001504	000407				BR	T14D	
001506	077203			T14B1	SOB	X2,T14AA	ISOB SHOULD NOT BRANCH
001510	016700	176262			MOV	PSW,X0	IGET STATUS CONTENTS
001514	022700	000004			CMP	#4,X0	ISTATUS = 4

001520	001401			BEQ	,+4	
001522	000000			HLT		IERROR STATUS WORD CHANGED
001524	000240		T140I	NOP		
	000015			C=C+1		
	000005			N=N+1		
001526	010701		T15AI	SCOPE		
001530	012702	000001		MOV	#1,X2	
001534	012767	000005	176234	MOV	#5,PSW	ILOAD PSW
001542	000402			BR	T15B	I GO DO SOB INSTRUCTION
001544	000000		T15AAI	HLT		IERROR SOB BRANCHED
001546	000407			BR	T15D	
001550	077203		T15BI	SOB	X2,T15AA	I SOB SHOULD NOT BRANCH
001552	016700	176220		MOV	PSW,X0	I GET STATUS CONTENTS
001556	022700	000005		CMP	#5,X0	I STATUS = 5
001562	001401			BEQ	,+4	
001564	000000			HLT		IERROR STATUS WORD CHANGED
001566	000240		T15DI	NOP		
	000016			C=C+1		
	000006			N=N+1		
001570	010701		T16AI	SCOPE		
001572	012702	000001		MOV	#1,X2	
001576	012767	000006	176172	MOV	#6,PSW	ILOAD PSW
001604	000402			BR	T16B	I GO DO SOB INSTRUCTION
001606	000000		T16AAI	HLT		IERROR SOB BRANCHED
001610	000407			BR	T16D	
001612	077203		T16BI	SOB	X2,T16AA	I SOB SHOULD NOT BRANCH
001614	016700	176156		MOV	PSW,X0	I GET STATUS CONTENTS
001620	022700	000006		CMP	#6,X0	I STATUS = 6
001624	001401			BEQ	,+4	
001626	000000			HLT		IERROR STATUS WORD CHANGED
001630	000240		T16DI	NOP		
	000017			C=C+1		
	000007			N=N+1		
001632	010701		T17AI	SCOPE		
001634	012702	000001		MOV	#1,X2	
001640	012767	000007	176130	MOV	#7,PSW	ILOAD PSW
001646	000402			BR	T17B	I GO DO SOB INSTRUCTION
001650	000000		T17AAI	HLT		IERROR SOB BRANCHED
001652	000407			BR	T17D	
001654	077203		T17BI	SOB	X2,T17AA	I SOB SHOULD NOT BRANCH
001656	016700	176114		MOV	PSW,X0	I GET STATUS CONTENTS
001662	022700	000007		CMP	#7,X0	I STATUS = 7
001666	001401			BEQ	,+4	
001670	000000			HLT		IERROR STATUS WORD CHANGED
001672	000240		T17DI	NOP		
	000020			C=C+1		
	000010			N=N+1		
001674	010701		T20AI	SCOPE		
001676	012702	000001		MOV	#1,X2	
001702	012767	000010	176066	MOV	#10,PSW	ILOAD PSW

001710	000402			BR	T20B		I GO DO SOB INSTRUCTION
001712	000000			T20AAI	HLT		I ERROR SOB BRANCHED
001714	000407			BR	T20D		
001716	077203			T20B:	SOB	X2,T20AA	I SOB SHOULD NOT BRANCH
001720	016700	176052			MOV	PSW,X0	I GET STATUS CONTENTS
001724	022700	000010			CMP	#10,X0	I STATUS = 10
001730	001401				BEQ	,+4	
001732	000000				HLT		I ERROR STATUS WORD CHANGED
001734	000240			T20DI	NOP		
	000021				C=C+1		
	000011				N=N+1		
001736	010701			T21A:	SCOPE		
001740	012702	000001			MOV	#1,X2	
001744	012767	000011	176024		MOV	#11,PSW	I LOAD PSW
001752	000402				BR	T21B	I GO DO SOB INSTRUCTION
001754	000000			T21AAI	HLT		I ERROR SOB BRANCHED
001756	000407				BR	T21D	
001760	077203			T21B:	SOB	X2,T21AA	I SOB SHOULD NOT BRANCH
001762	016700	176010			MOV	PSW,X0	I GET STATUS CONTENTS
001766	022700	000011			CMP	#11,X0	I STATUS = 11
001772	001401				BEQ	,+4	
001774	000000				HLT		I ERROR STATUS WORD CHANGED
001776	000240			T21DI	NOP		
	000022				C=C+1		
	000012				N=N+1		
002000	010701			T22A:	SCOPE		
002002	012702	000001			MOV	#1,X2	
002006	012767	000012	175762		MOV	#12,PSW	I LOAD PSW
002014	000402				BR	T22B	I GO DO SOB INSTRUCTION
002016	000000			T22AAI	HLT		I ERROR SOB BRANCHED
002020	000407				BR	T22D	
002022	077203			T22B:	SOB	X2,T22AA	I SOB SHOULD NOT BRANCH
002024	016700	175746			MOV	PSW,X0	I GET STATUS CONTENTS
002030	022700	000012			CMP	#12,X0	I STATUS = 12
002034	001401				BEQ	,+4	
002036	000000				HLT		I ERROR STATUS WORD CHANGED
002040	000240			T22DI	NOP		
	000023				C=C+1		
	000013				N=N+1		
002042	010701			T23A:	SCOPE		
002044	012702	000001			MOV	#1,X2	
002050	012767	000013	175720		MOV	#13,PSW	I LOAD PSW
002056	000402				BR	T23B	I GO DO SOB INSTRUCTION
002060	000000			T23AAI	HLT		I ERROR SOB BRANCHED
002062	000407				BR	T23D	
002064	077203			T23B:	SOB	X2,T23AA	I SOB SHOULD NOT BRANCH
002066	016700	175704			MOV	PSW,X0	I GET STATUS CONTENTS
002072	022700	000013			CMP	#13,X0	I STATUS = 13
002076	001401				BEQ	,+4	
002100	000000				HLT		I ERROR STATUS WORD CHANGED
002102	000240			T23DI	NOP		

	000024			C=C+1		
	000014			N=N+1		
002104	010701			T24A:	SCOPE	
002106	012702	000001			MOV	#1,X2
002112	012767	000014	175656		MOV	#14,PSW
002120	000402				BR	T24B
002122	000000			T24AA:	HLT	
002124	000407				BR	T24D
002126	077203			T24B:	SOB	X2,T24AA
002130	016700	175642			MOV	PSW,X0
002134	022700	000014			CMP	#14,X0
002140	001401				BEQ	,+4
002142	000000				HLT	
002144	000240			T24D:	NOP	
	000025				C=C+1	
	000015				N=N+1	
002146	010701			T25A:	SCOPE	
002150	012702	000001			MOV	#1,X2
002154	012767	000015	175614		MOV	#15,PSW
002162	000402				BR	T25B
002164	000000			T25AA:	HLT	
002166	000407				BR	T25D
002170	077203			T25B:	SOB	X2,T25AA
002172	016700	175600			MOV	PSW,X0
002176	022700	000015			CMP	#15,X0
002202	001401				BEQ	,+4
002204	000000				HLT	
002206	000240			T25D:	NOP	
	000026				C=C+1	
	000016				N=N+1	
002210	010701			T26A:	SCOPE	
002212	012702	000001			MOV	#1,X2
002216	012767	000016	175552		MOV	#16,PSW
002224	000402				BR	T26B
002226	000000			T26AA:	HLT	
002230	000407				BR	T26D
002232	077203			T26B:	SOB	X2,T26AA
002234	016700	175536			MOV	PSW,X0
002240	022700	000016			CMP	#16,X0
002244	001401				BEQ	,+4
002246	000000				HLT	
002250	000240			T26D:	NOP	
	000027				C=C+1	
	000017				N=N+1	
002252	010701			T27A:	SCOPE	
002254	012702	000001			MOV	#1,X2
002260	012767	000017	175510		MOV	#17,PSW
002266	000402				BR	T27B
002270	000000			T27AA:	HLT	
002272	000407				BR	T27D

ILOAD PSW
 IGO DO SOB INSTRUCTION
 IERROR SOB BRANCHED
 ISOB SHOULD NOT BRANCH
 IGET STATUS CONTENTS
 ISTATUS = 14
 IERROR STATUS WORD CHANGED

ILOAD PSW
 IGO DO SOB INSTRUCTION
 IERROR SOB BRANCHED
 ISOB SHOULD NOT BRANCH
 IGET STATUS CONTENTS
 ISTATUS = 15
 IERROR STATUS WORD CHANGED

ILOAD PSW
 IGO DO SOB INSTRUCTION
 IERROR SOB BRANCHED
 ISOB SHOULD NOT BRANCH
 IGET STATUS CONTENTS
 ISTATUS = 16
 IERROR STATUS WORD CHANGED

ILOAD PSW
 IGO DO SOB INSTRUCTION
 IERROR SOB BRANCHED

002274	077203		T27B1	SOB	X2,T27AA	ISOB SHOULD NOT BRANCH
002276	016700	175474		MOV	PSW,X0	I GET STATUS CONTENTS
002302	022700	000017		CMP	#17,X0	ISTATUS = 17
002306	001401			BEQ	,+4	
002310	000000			HLT		ERROR STATUS WORD CHANGED
002312	000240		T27D1	NOP		
	000030			C=C+1		
	000020			N=N+1		
	000000					
				N=0		
				TEST THAT SOB DOES NOT AFFECT THE CONDITION CODES WHEN SOB BRANCHES,		
002314	010701		T30A1	SCOPE		
002316	012702	000002		MOV	#2,X2	ISSET UP SOB TO BRANCH
002322	012767	000000		MOV	#0,PSW	ILOAD 0 INTO STATUS WORD
002330	000407	175446		BR	T30B	IGO DO SOB INSTRUCTION
002332	016700	175440	T30AA1	MOV	PSW,X0	I GET STATUS WORD CONTENTS
002336	022700	000000		CMP	#0,X0	ISTATUS = 0?
002342	001404			BEQ	T30D	
002344	000000			HLT		ERROR STATUS WORD CHANGED
002346	000402			BR	T30D	
002350	077210		T30B1	SOB	X2,T30AA	ISOB SHOULD BRANCH
002352	000000			HLT		ISOB FAILED TO BRANCH
002354	000240		T30D1	NOP		
	000031			C=C+1		
	000001			N=N+1		
002356	010701		T31A1	SCOPE		
002360	012702	000002		MOV	#2,X2	ISSET UP SOB TO BRANCH
002364	012767	000001		MOV	#1,PSW	ILOAD 1 INTO STATUS WORD
002372	000407	175404		BR	T31B	IGO DO SOB INSTRUCTION
002374	016700	175376	T31AA1	MOV	PSW,X0	I GET STATUS WORD CONTENTS
002400	022700	000001		CMP	#1,X0	ISTATUS = 1?
002404	001404			BEQ	T31D	
002406	000000			HLT		ERROR STATUS WORD CHANGED
002410	000402			BR	T31D	
002412	077210		T31B1	SOB	X2,T31AA	ISOB SHOULD BRANCH
002414	000000			HLT		ISOB FAILED TO BRANCH
002416	000240		T31D1	NOP		
	000032			C=C+1		
	000002			N=N+1		
002420	010701		T32A1	SCOPE		
002422	012702	000002		MOV	#2,X2	ISSET UP SOB TO BRANCH
002426	012767	000002		MOV	#2,PSW	ILOAD 2 INTO STATUS WORD
002434	000407	175342		BR	T32B	IGO DO SOB INSTRUCTION
002436	016700	175334	T32AA1	MOV	PSW,X0	I GET STATUS WORD CONTENTS
002442	022700	000002		CMP	#2,X0	ISTATUS = 2?
002446	001404			BEQ	T32D	
002450	000000			HLT		ERROR STATUS WORD CHANGED
002452	000402			BR	T32D	
002454	077210		T32B1	SOB	X2,T32AA	ISOB SHOULD BRANCH
002456	000000			HLT		ISOB FAILED TO BRANCH
002460	000240		T32D1	NOP		

	000033			C=C+1	
	000003			N=N+1	
002462	010701			T33A1	SCOPE
002464	012702	000002			MOV #2,X2
002470	012767	000003	175300		MOV #3,PSW
002476	000407				BR T33B
002500	016700	175272		T33AA1	MOV PSW,X0
002504	022700	000003			CMP #3,X0
002510	001404				BEQ T33D
002512	000000				HLT
002514	000402				BR T33D
002516	077210			T33B1	SOB X2,T33AA
002520	000000				HLT
002522	000240			T33D1	NOP
	000034				C=C+1
	000004				N=N+1
002524	010701			T34A1	SCOPE
002526	012702	000002			MOV #2,X2
002532	012767	000004	175236		MOV #4,PSW
002540	000407				BR T34B
002542	016700	175230		T34AA1	MOV PSW,X0
002546	022700	000004			CMP #4,X0
002552	001404				BEQ T34D
002554	000000				HLT
002556	000402				BR T34D
002560	077210			T34B1	SOB X2,T34AA
002562	000000				HLT
002564	000240			T34D1	NOP
	000035				C=C+1
	000005				N=N+1
002566	010701			T35A1	SCOPE
002570	012702	000002			MOV #2,X2
002574	012767	000005	175174		MOV #5,PSW
002602	000407				BR T35B
002604	016700	175166		T35AA1	MOV PSW,X0
002610	022700	000005			CMP #5,X0
002614	001404				BEQ T35D
002616	000000				HLT
002620	000402				BR T35D
002622	077210			T35B1	SOB X2,T35AA
002624	000000				HLT
002626	000240			T35D1	NOP
	000036				C=C+1
	000006				N=N+1
002630	010701			T36A1	SCOPE
002632	012702	000002			MOV #2,X2
002636	012767	000006	175132		MOV #6,PSW
002644	000407				BR T36B
002646	016700	175124		T36AA1	MOV PSW,X0
002652	022700	000006			CMP #6,X0
					ISTATUS = 3?
					!ERROR STATUS WORD CHANGED
					!SOB SHOULD BRANCH
					!SOB FAILED TO BRANCH
					ISTATUS = 4?
					!ERROR STATUS WORD CHANGED
					!SOB SHOULD BRANCH
					!SOB FAILED TO BRANCH
					ISTATUS = 5?
					!ERROR STATUS WORD CHANGED
					!SOB SHOULD BRANCH
					!SOB FAILED TO BRANCH
					ISTATUS = 6?

002656	001404			BEQ	T36D	
002660	000000			HLT		!ERROR STATUS WORD CHANGED
002662	000402			BR	T36D	
002664	077210		T36B:	SOB	X2,T36AA	!SOB SHOULD BRANCH
002666	000000			HLT		!SOB FAILED TO BRANCH
002670	000240		T36D:	NOP		
	000037			C=C+1		
	000007			N=N+1		
002672	010701		T37A:	SCOPE		
002674	012702	000002		MOV	#2,X2	!SET UP SOB TO BRANCH
002700	012767	000007	175070	MOV	#7,PSW	!LOAD 7 INTO STATUS WORD
002706	000407			BR	T37B	!GO DO SOB INSTRUCTION
002710	016700	175062	T37AA:	MOV	PSW,X0	!GET STATUS WORD CONTENTS
002714	022700	000007		CMP	#7,X0	!STATUS = 7?
002720	001404			BEQ	T37D	
002722	000000			HLT		!ERROR STATUS WORD CHANGED
002724	000402			BR	T37D	
002726	077210		T37B:	SOB	X2,T37AA	!SOB SHOULD BRANCH
002730	000000			HLT		!SOB FAILED TO BRANCH
002732	000240		T37D:	NOP		
	000040			C=C+1		
	000010			N=N+1		
002734	010701		T40A:	SCOPE		
002736	012702	000002		MOV	#2,X2	!SET UP SOB TO BRANCH
002742	012767	000010	175026	MOV	#10,PSW	!LOAD 10 INTO STATUS WORD
002750	000407			BR	T40B	!GO DO SOB INSTRUCTION
002752	016700	175020	T40AA:	MOV	PSW,X0	!GET STATUS WORD CONTENTS
002756	022700	000010		CMP	#10,X0	!STATUS = 10?
002762	001404			BEQ	T40D	
002764	000000			HLT		!ERROR STATUS WORD CHANGED
002766	000402			BR	T40D	
002770	077210		T40B:	SOB	X2,T40AA	!SOB SHOULD BRANCH
002772	000000			HLT		!SOB FAILED TO BRANCH
002774	000240		T40D:	NOP		
	000041			C=C+1		
	000011			N=N+1		
002776	010701		T41A:	SCOPE		
003000	012702	000002		MOV	#2,X2	!SET UP SOB TO BRANCH
003004	012767	000011	174764	MOV	#11,PSW	!LOAD 11 INTO STATUS WORD
003012	000407			BR	T41B	!GO DO SOB INSTRUCTION
003014	016700	174756	T41AA:	MOV	PSW,X0	!GET STATUS WORD CONTENTS
003020	022700	000011		CMP	#11,X0	!STATUS = 11?
003024	001404			BEQ	T41D	
003026	000000			HLT		!ERROR STATUS WORD CHANGED
003030	000402			BR	T41D	
003032	077210		T41B:	SOB	X2,T41AA	!SOB SHOULD BRANCH
003034	000000			HLT		!SOB FAILED TO BRANCH
003036	000240		T41D:	NOP		
	000042			C=C+1		
	000012			N=N+1		

003040	010701			T42A1	SCOPE		
003042	012702	000002			MOV	#2,X2	ISET UP SOB TO BRANCH
003046	012767	000012	174722		MOV	#12,PSW	ILOAD 12 INTO STATUS WORD
003054	000407				BR	T42B	IGO DO SOB INSTRUCTION
003056	016700	174714		T42AA1	MOV	PSW,X0	IGET STATUS WORD CONTENTS
003062	022700	000012			CMP	#12,X0	ISTATUS = 12?
003066	001404				BEG	T42D	
003070	000000				HLT		IERROR STATUS WORD CHANGED
003072	000402				BR	T42D	
003074	077210			T42B1	SOB	X2,T42AA	I SOB SHOULD BRANCH
003076	000000				HLT		I SOB FAILED TO BRANCH
003100	000240			T42D1	NOP		
	000043				C=C+1		
	000013				N=N+1		
003102	010701			T43A1	SCOPE		
003104	012702	000002			MOV	#2,X2	ISET UP SOB TO BRANCH
003110	012767	000013	174660		MOV	#13,PSW	ILOAD 13 INTO STATUS WORD
003116	000407				BR	T43B	IGO DO SOB INSTRUCTION
003120	016700	174652		T43AA1	MOV	PSW,X0	IGET STATUS WORD CONTENTS
003124	022700	000013			CMP	#13,X0	ISTATUS = 13?
003130	001404				BEG	T43D	
003132	000000				HLT		IERROR STATUS WORD CHANGED
003134	000402				BR	T43D	
003136	077210			T43B1	SOB	X2,T43AA	I SOB SHOULD BRANCH
003140	000000				HLT		I SOB FAILED TO BRANCH
003142	000240			T43D1	NOP		
	000044				C=C+1		
	000014				N=N+1		
003144	010701			T44A1	SCOPE		
003146	012702	000002			MOV	#2,X2	ISET UP SOB TO BRANCH
003152	012767	000014	174610		MOV	#14,PSW	ILOAD 14 INTO STATUS WORD
003160	000407				BR	T44B	IGO DO SOB INSTRUCTION
003162	016700	174610		T44AA1	MOV	PSW,X0	IGET STATUS WORD CONTENTS
003166	022700	000014			CMP	#14,X0	ISTATUS = 14?
003172	001404				BEG	T44D	
003174	000000				HLT		IERROR STATUS WORD CHANGED
003176	000402				BR	T44D	
003200	077210			T44B1	SOB	X2,T44AA	I SOB SHOULD BRANCH
003202	000000				HLT		I SOB FAILED TO BRANCH
003204	000240			T44D1	NOP		
	000045				C=C+1		
	000015				N=N+1		
003206	010701			T45A1	SCOPE		
003210	012702	000002			MOV	#2,X2	ISET UP SOB TO BRANCH
003214	012767	000015	174554		MOV	#15,PSW	ILOAD 15 INTO STATUS WORD
003222	000407				BR	T45B	IGO DO SOB INSTRUCTION
003224	016700	174546		T45AA1	MOV	PSW,X0	IGET STATUS WORD CONTENTS
003230	022700	000015			CMP	#15,X0	ISTATUS = 15?
003234	001404				BEG	T45D	
003236	000000				HLT		IERROR STATUS WORD CHANGED
003240	000402				BR	T45D	

003242	077210			T45B:	SOB	X2,T45AA		ISOB SHOULD BRANCH
003244	000000				HLT			ISOB FAILED TO BRANCH
003246	000240			T45D:	NOP			
	000046				C=C+1			
	000016				N=N+1			
003250	010701			T46A:	SCOPE			
003252	012702	000002			MOV	#2,X2		ISSET UP SOB TO BRANCH
003256	012767	000016	174512		MOV	#16,PSW		ILOAD I6 INTO STATUS WORD
003264	000407				BR	T46B		IGO DO SOB INSTRUCTION
003266	016700	174504		T46AA:	MOV	PSW,X0		IGET STATUS WORD CONTENTS
003272	022700	000016			CMP	#16,X0		ISTATUS = 16?
003276	001404				BEQ	T46D		
003300	000000				HLT			ERROR STATUS WORD CHANGED
003302	000402				BR	T46D		
003304	077210			T46B:	SOB	X2,T46AA		ISOB SHOULD BRANCH
003306	000000				HLT			ISOB FAILED TO BRANCH
003310	000240			T46D:	NOP			
	000047				C=C+1			
	000017				N=N+1			
003312	010701			T47A:	SCOPE			
003314	012702	000002			MOV	#2,X2		ISSET UP SOB TO BRANCH
003320	012767	000017	174450		MOV	#17,PSW		ILOAD I7 INTO STATUS WORD
003326	000407				BR	T47B		IGO DO SOB INSTRUCTION
003330	016700	174442		T47AA:	MOV	PSW,X0		IGET STATUS WORD CONTENTS
003334	022700	000017			CMP	#17,X0		ISTATUS = 17?
003340	001404				BEQ	T47D		
003342	000000				HLT			ERROR STATUS WORD CHANGED
003344	000402				BR	T47D		
003346	077210			T47B:	SOB	X2,T47AA		ISOB SHOULD BRANCH
003350	000000				HLT			ISOB FAILED TO BRANCH
003352	000240			T47D:	NOP			
	000050				C=C+1			
	000020				N=N+1			
003354	012702	000002			MOV	#2,X2		
003360	000476				BR	SOBS		IGO TO SOB INSTRUCTION
003362	000477			SOBEND:	BR	END		
003364	000000				0			IWITH HALTS
003366	000000				0			IWITH HALTS
003370	000000				0			IWITH HALTS
003372	000000				0			IWITH HALTS
003374	000000				0			IWITH HALTS
003376	000000				0			IWITH HALTS
003400	000000				0			IWITH HALTS
003402	000000				0			IWITH HALTS
003404	000000				0			IWITH HALTS
003406	000000				0			IWITH HALTS
003410	000000				0			IWITH HALTS
003412	000000				0			IWITH HALTS
003414	000000				0			IWITH HALTS
003416	000000				0			IWITH HALTS

I TEST THAT SOB CAN BRANCH BACK 64 WORDS

003420	000000		0			IWITH HALTS
003422	000000		0			IWITH HALTS
003424	000000		0			IWITH HALTS
003426	000000		0			IWITH HALTS
003430	000000		0			IWITH HALTS
003432	000000		0			IWITH HALTS
003434	000000		0			IWITH HALTS
003436	000000		0			IWITH HALTS
003440	000000		0			IWITH HALTS
003442	000000		0			IWITH HALTS
003444	000000		0			IWITH HALTS
003446	000000		0			IWITH HALTS
003450	000000		0			IWITH HALTS
003452	000000		0			IWITH HALTS
003454	000000		0			IWITH HALTS
003456	000000		0			IWITH HALTS
003460	000000		0			IWITH HALTS
003462	000000		0			IWITH HALTS
003464	000000		0			IWITH HALTS
003466	000000		0			IWITH HALTS
003470	000000		0			IWITH HALTS
003472	000000		0			IWITH HALTS
003474	000000		0			IWITH HALTS
003476	000000		0			IWITH HALTS
003500	000000		0			IWITH HALTS
003502	000000		0			IWITH HALTS
003504	000000		0			IWITH HALTS
003506	000000		0			IWITH HALTS
003510	000000		0			IWITH HALTS
003512	000000		0			IWITH HALTS
003514	000000		0			IWITH HALTS
003516	000000		0			IWITH HALTS
003520	000000		0			IWITH HALTS
003522	000000		0			IWITH HALTS
003524	000000		0			IWITH HALTS
003526	000000		0			IWITH HALTS
003530	000000		0			IWITH HALTS
003532	000000		0			IWITH HALTS
003534	000000		0			IWITH HALTS
003536	000000		0			IWITH HALTS
003540	000000		0			IWITH HALTS
003542	000000		0			IWITH HALTS
003544	000000		0			IWITH HALTS
003546	000000		0			IWITH HALTS
003550	000000		0			IWITH HALTS
003552	000000		0			IWITH HALTS
003554	000000		0			IWITH HALTS
003556	077277		0			IWITH HALTS
003560	000000		0			IWITH HALTS
		SOBS:	SOB	X2,SOBEND		I BRANCH BACK 64, WORDS
			HLT			I ERROR DID NOT BRANCH
003562	005267	175212	END:	INC	ICNT	I INCREMENT PASS COUNT
003566	026727	175206		CMP	ICNT,#370	
003574	001402			BEG	DONE	I GO TO DONE IF 1000 PASSES COMPLETED
003576	000167	175204		JMP	BEGIN	I RESTART TEST

003602	012767	000007	173756	DONE1	MOV	#7,TPBUF	IRING BELL
003610	105767	173750			TSTB	TPCR	
003614	100375				BPL	,=4	
003616	013702	000042			MOV	#42,X2	IGET DECTAPE MONITOR RETURN ADDRESS
003622	001404				BEQ	DONE1	IDO NOT RETURN IF (42)=0
003624	004712				JSR	7,(2)	IRETURN TO DECTAPE MONITOR
003626	000240				NOP		IAC11
003630	000240				NOP		IOverlay
003632	000240				NOP		IAREA
003634	000167	175142		DONE1	JMP	START	
	000001				,END		

BEGIN	001006	C	= 000050	DISPLA	= 177570	DONE	003602
DONE1	003634	END	003562	HLT	= 000000	ICNT	001000
N	= 000020	PSW	= 177776	SCOPE	= 010701	SOBEND	003362
SOBS	003556	START	001002	STKPTR	= 000500	SWR	= 177570
TPBUF	= 177566	TPCSR	= 177564	T0A	001036	T0B	001050
T0C	001054	T1A	001056	T1AA	001064	T1R	001076
T1C	001100	T10A	001254	T10AA	001272	T10B	001276
T10D	001314	T11A	001316	T11AA	001334	T11B	001340
T11D	001356	T12A	001360	T12AA	001376	T12B	001402
T12D	001420	T13A	001422	T13AA	001440	T13B	001444
T13D	001462	T14A	001464	T14AA	001502	T14B	001506
T14D	001524	T15A	001526	T15AA	001544	T15B	001550
T15D	001566	T16A	001570	T16AA	001606	T16B	001612
T16D	001630	T17A	001632	T17AA	001650	T17B	001654
T17D	001672	T2A	001102	T2B	001114	T20A	001674
T20AA	001712	T20B	001716	T20D	001734	T21A	001736
T21AA	001754	T21B	001760	T21D	001776	T22A	002000
T22AA	002016	T22B	002022	T22D	002040	T23A	002042
T23AA	002060	T23B	002064	T23D	002102	T24A	002104
T24AA	002122	T24B	002126	T24D	002144	T25A	002146
T25AA	002164	T25B	002170	T25D	002206	T26A	002210
T26AA	002226	T26B	002232	T26D	002250	T27A	002252
T27AA	002270	T27B	002274	T27D	002312	T3A	001120
T3B	001126	T30A	002314	T30AA	002332	T30B	002350
T30D	002354	T31A	002356	T31AA	002374	T31B	002412
T31D	002416	T32A	002420	T32AA	002436	T32B	002454
T32D	002460	T33A	002462	T33AA	002500	T33B	002516
T33D	002522	T34A	002524	T34AA	002542	T34B	002560
T34D	002564	T35A	002566	T35AA	002604	T35B	002622
T35D	002626	T36A	002630	T36AA	002646	T36B	002664
T36D	002670	T37A	002672	T37AA	002710	T37B	002726
T37D	002732	T4A	001140	T4B	001152	T40A	002734
T40AA	002752	T40B	002770	T40D	002774	T41A	002776
T41AA	003014	T41B	003032	T41D	003036	T42A	003040
T42AA	003056	T42B	003074	T42D	003100	T43A	003102
T43AA	003120	T43B	003136	T43D	003142	T44A	003144
T44AA	003162	T44B	003200	T44D	003204	T45A	003206
T45AA	003224	T45B	003242	T45D	003246	T46A	003250
T46AA	003266	T46B	003304	T46D	003310	T47A	003312
T47AA	003330	T47B	003346	T47D	003352	T5A	001156
T5AA	001166	T5B	001200	T6A	001204	T6B	001216
T6C	001230	T7A	001232	T7B	001244	UBREAK	= 177770
	= 003640						

ERRORS DETECTED: 0

1

,TITLE MAINDEC-11-DCKBC-A PDP11/25-11/45 XOR INST TEST
,NLIST MD,MC,CND,SEQ
,LIST ME
,ABS

;TEST DCKBCA- TEST OF THE XOR INSTRUCTION AND INCLUDES DATA CHECKS AND
;CONDITION CODES CHECKS

; THE XOR INSTRUCTION IS ESSENTIALLY A 'HALF ADD' INSTRUCTION, IE, IF
;THE CORRESPONDING BIT IN THE SOURCE AND DESTINATION ADDRESS ARE THE
;SAME THE RESULT IN DESTINATION ADDRESS IS A '0' IN THAT BIT POSITION
;AND, IF THE CORRESPONDING BITS ARE DIFFERENT THEN THE RESULT IS A '1'
;IN THAT BIT POSITION, THE CONDITION CODES ARE AFFECTED AS FOLLOWS:
;Z/ IS SET IF THE RESULT IS = TO 0, OTHERWISE IT IS CLEARED
;N/ IS SET IF THE RESULT IS NEGATIVE, CLEARED IF POSITIVE
;C/ IS UNCHANGED
;V/ IS CLEARED

;FUNCTIONS NOT TESTED BY THIS PROGRAM

;I: THAT XOR WILL EXPLICITLY CHANGE THE USER/EXEC BIT IN THE STATUS WORD;

;STARTING PROCEEDURE

; LOAD ADDRESS=200
; PRESS START
; STACK POINTER IS SET AT 500
; BELL WILL RING WHEN TEST IS COMPLETE

010701
000000
177776
177770
177564
177566
177570
177570

SCOPE=010701 ;MOV PC,R1
HLT=HALT
PSW=177776 ;ADDRESS OF PROCESSOR STATUS WORD
UBREAK=177770 ;ADDRESS OF MICRO BREAK REGISTER
TPCSR=177564 ;ADDRESS OF TELEPRINTER CSR
TPBUF=177566 ;ADDRESS OF TELEPRINTER BUFFER
SWR=177570 ;ADDRESS OF CONSOLE SWITCH REGISTER
DISPLAY=177570 ;ADDRESS OF CONSOLE LIGHT REGISTER

000500

*****INITIAL STACK POINTER=0500*****
STKPTR=0500 ;INITIAL STACK SETTING

000000
000000 000000
000002 000000
000004 000006
000006 000000
000010 000012
000012 000000
000014 000016
000016 000000
000020 000022
000022 000000

,=0
,+2
HALT
,+2
HALT
,+2
HALT
,+2
HALT
,+2
HALT

000024	000026	,+2
000026	000000	HALT
000030	000032	,+2
000032	000000	HALT
000034	000036	,+2
000036	000000	HALT
000040	000042	,+2
000042	000000	HALT
000044	000046	,+2
000046	000000	HALT
000050	000052	,+2
000052	000000	HALT
000054	000056	,+2
000056	000000	HALT
000060	000062	,+2
000062	000000	HALT
000064	000066	,+2
000066	000000	HALT
000070	000072	,+2
000072	000000	HALT
000074	000076	,+2
000076	000000	HALT
000100	000102	,+2
000102	000000	HALT
000104	000106	,+2
000106	000000	HALT
000110	000112	,+2
000112	000000	HALT
000114	000116	,+2
000116	000000	HALT
000120	000122	,+2
000122	000000	HALT
000124	000126	,+2
000126	000000	HALT
000130	000132	,+2
000132	000000	HALT
000134	000136	,+2
000136	000000	HALT
000140	000142	,+2
000142	000000	HALT
000144	000146	,+2
000146	000000	HALT
000150	000152	,+2
000152	000000	HALT
000154	000156	,+2
000156	000000	HALT
000160	000162	,+2
000162	000000	HALT
000164	000166	,+2
000166	000000	HALT
000170	000172	,+2
000172	000000	HALT
000174	000176	,+2
000176	000000	HALT

000200	000202	,+2
000202	000000	HALT
000204	000206	,+2
000206	000000	HALT
000210	000212	,+2
000212	000000	HALT
000214	000216	,+2
000216	000000	HALT
000220	000222	,+2
000222	000000	HALT
000224	000226	,+2
000226	000000	HALT
000230	000232	,+2
000232	000000	HALT
000234	000236	,+2
000236	000000	HALT
000240	000242	,+2
000242	000000	HALT
000244	000246	,+2
000246	000000	HALT
000250	000252	,+2
000252	000000	HALT
000254	000256	,+2
000256	000000	HALT
000260	000262	,+2
000262	000000	HALT
000264	000266	,+2
000266	000000	HALT
000270	000272	,+2
000272	000000	HALT
000274	000276	,+2
000276	000000	HALT
000300	000302	,+2
000302	000000	HALT
000304	000306	,+2
000306	000000	HALT
000310	000312	,+2
000312	000000	HALT
000314	000316	,+2
000316	000000	HALT
000320	000322	,+2
000322	000000	HALT
000324	000326	,+2
000326	000000	HALT
000330	000332	,+2
000332	000000	HALT
000334	000336	,+2
000336	000000	HALT
000340	000342	,+2
000342	000000	HALT
000344	000346	,+2
000346	000000	HALT
000350	000352	,+2
000352	000000	HALT

```

000354 000356 ,+2
000356 000000 HALT
000360 000362 ,+2
000362 000000 HALT
000364 000366 ,+2
000366 000000 HALT
000370 000372 ,+2
000372 000000 HALT
000374 000376 ,+2
000376 000000 HALT

000200 000200 ,#200
000200 000167 000576 JMP START
0001000 001000 ,#1000
001000 000000 ICNT: 0 ;CONTAINS PASS COUNT
001002 005067 177772 START: CLR ICNT ;CLEAR PASS COUNT
001006 012706 000500 BEGIN: MOV #STKPTR,%6 ;INITIALIZE STACK POINTER
001012 016737 177762 177570 MOV ICNT,#DISPLAY ;DISPLAY PASS COUNT
001020 032737 000400 177570 BIT #400,#SWR ;LOAD MICRO BREAK REGISTER?
001026 001403 BEQ ,+10
001030 113737 177570 177770 MOVB #SWR,#UBREAK ;LOAD MICRO BREAK REG WITH SR0=7
000002 SOURCE=X2
000000 DEST=X0
000000 Y=0

001036 010701 SCOPE
001040 012702 000000 MOV #0,SOURCE ;MOVE SOURCE OPERAND TO SOURCE
001044 012700 000000 MOV #0,DEST ;MOVE DESTINATION OPERAND TO DESTINATION
001050 000257 CCC
001052 074200 XOR SOURCE,DEST ;XOR SOURCE AND DESTINATION
001054 103001 BCC ,+4
001056 000000 HLT ;ERROR! CARRY CHANGED
001060 102001 BVC ,+4
001062 000000 HLT ;ERROR! V FAILED TO CLEAR
001064 020027 000000 CMP DEST,#0 ;DEST=0?
001070 001401 BEQ ,+4
001072 000000 HLT ;0 XOR 0 FAILED

000001 Y=Y+1
001074 010701 SCOPE
001076 012702 125252 MOV #125252,SOURCE ;MOVE SOURCE OPERAND TO SOURCE
001102 012700 125252 MOV #125252,DEST ;MOVE DESTINATION OPERAND TO DESTINATION
001106 000277 SCC
001110 074200 XOR SOURCE,DEST ;XOR SOURCE AND DESTINATION
001112 103401 BCS ,+4
001114 000000 HLT ;ERROR! CARRY CHANGED
001116 102001 BVC ,+4
001120 000000 HLT ;ERROR! V FAILED TO CLEAR
001122 020027 000000 CMP DEST,#0 ;DEST=0?
001126 001401 BEQ ,+4
001130 000000 HLT ;125252 XOR 125252 FAILED

000002 Y=Y+1
001132 010701 SCOPE
001134 012702 052525 MOV #052525,SOURCE ;MOVE SOURCE OPERAND TO SOURCE

```

001140	012700	052525	MOV	#052525,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
001144	000257		CCC		
001146	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
001150	103001		BCC	,+4	
001152	000000		HLT		;ERROR! CARRY CHANGED
001154	102001		BVC	,+4	
001156	000000		HLT		;ERROR! V FAILED TO CLEAR
001160	020027	000000	CMP	DEST,#0 ;DEST=0?	
001164	001401		BEQ	,+4	
001166	000000		HLT		;052525 XOR 052525 FAILED
	000003		Y=Y+1		
001170	010701		SCOPE		
001172	012702	177777	MOV	#=1,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
001176	012700	177777	MOV	#=1,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
001202	000277		SCC		
001204	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
001206	103401		BCS	,+4	
001210	000000		HLT		;ERROR! CARRY CHANGED
001212	102001		BVC	,+4	
001214	000000		HLT		;ERROR! V FAILED TO CLEAR
001216	020027	000000	CMP	DEST,#0 ;DEST=0?	
001222	001401		BEQ	,+4	
001224	000000		HLT		;=1 XOR =1 FAILED
	000004		Y=Y+1		
001226	010701		SCOPE		
001230	012702	125252	MOV	#125252,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
001234	012700	052525	MOV	#052525,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
001240	000257		CCC		
001242	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
001244	103001		BCC	,+4	
001246	000000		HLT		;ERROR! CARRY CHANGED
001250	102001		BVC	,+4	
001252	000000		HLT		;ERROR! V FAILED TO CLEAR
001254	020027	177777	CMP	DEST,#=1	;DEST=#=1?
001260	001401		BEQ	,+4	
001262	000000		HLT		;125252 XOR 052525 FAILED
	000005		Y=Y+1		
001264	010701		SCOPE		
001266	012702	052525	MOV	#052525,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
001272	012700	125252	MOV	#125252,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
001276	000277		SCC		
001300	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
001302	103401		BCS	,+4	
001304	000000		HLT		;ERROR! CARRY CHANGED
001306	102001		BVC	,+4	
001310	000000		HLT		;ERROR! V FAILED TO CLEAR
001312	020027	177777	CMP	DEST,#=1	;DEST=#=1?
001316	001401		BEQ	,+4	
001320	000000		HLT		;052525 XOR 125252 FAILED
	000006		Y=Y+1		

001322	010701		SCOPE		
001324	012702	100000	MOV	#100000,SOURCE	MOVE SOURCE OPERAND TO SOURCE
001330	012700	100001	MOV	#100001,DEST	MOVE DESTINATION OPERAND TO DESTINATION
001334	000257		CCC		
001336	074200		XOR	SOURCE,DEST	XOR SOURCE AND DESTINATION
001340	103001		BCC	,+4	
001342	000000		HLT		ERROR! CARRY CHANGED
001344	102001		BVC	,+4	
001346	000000		HLT		ERROR! V FAILED TO CLEAR
001350	020027	000001	CMP	DEST,#1 IDEST=1?	
001354	001401		BEG	,+4	
001356	000000		HLT		100000 XOR 100001 FAILED
	000007		Y=Y+1		
001360	010701		SCOPE		
001362	012702	077777	MOV	#77777,SOURCE	MOVE SOURCE OPERAND TO SOURCE
001366	012700	100000	MOV	#100000,DEST	MOVE DESTINATION OPERAND TO DESTINATION
001372	000277		SCC		
001374	074200		XOR	SOURCE,DEST	XOR SOURCE AND DESTINATION
001376	103401		BCS	,+4	
001400	000000		HLT		ERROR! CARRY CHANGED
001402	102001		BVC	,+4	
001404	000000		HLT		ERROR! V FAILED TO CLEAR
001406	020027	177777	CMP	DEST,#-1 IDEST=-1?	
001412	001401		BEG	,+4	
001414	000000		HLT		177777 XOR 100000 FAILED
	000010		Y=Y+1		
	000001	N=1	SCOPE		
001416	010701		MOV	#1,SOURCE	MOVE SOURCE OPERAND TO SOURCE
001420	012702	000001	MOV	#1,DEST	MOVE DESTINATION OPERAND TO DESTINATION
001424	012700	000001	CCC		
001430	000257		XOR	SOURCE,DEST	XOR SOURCE AND DESTINATION
001432	074200		BCC	,+4	
001434	103001		HLT		ERROR! CARRY CHANGED
001436	000000		BVC	,+4	
001440	102001		HLT		ERROR! V FAILED TO CLEAR
001442	000000		CMP	DEST,#0 IDEST=0?	
001444	020027	000000	BEG	,+4	
001450	001401		HLT		11 XOR 1 FAILED
001452	000000		Y=Y+1		
	000002		N=N+N		
001454	010701		SCOPE		
001456	012702	000002	MOV	#2,SOURCE	MOVE SOURCE OPERAND TO SOURCE
001462	012700	000002	MOV	#2,DEST	MOVE DESTINATION OPERAND TO DESTINATION
001466	000277		SCC		
001470	074200		XOR	SOURCE,DEST	XOR SOURCE AND DESTINATION
001472	103401		BCS	,+4	
001474	000000		HLT		ERROR! CARRY CHANGED
001476	102001		BVC	,+4	
001500	000000		HLT		ERROR! V FAILED TO CLEAR

001502	020027	000000	CMP	DEST,#0 ;DEST=0?
001506	001401		BEQ	,+4
001510	000000		HLT	;2 XOR 2 FAILED
	000012		Y=Y+1	
	000004		N=N+N	
001512	010701		SCOPE	
001514	012702	000004	MOV	#4,SOURCE ;MOVE SOURCE OPERAND TO SOURCE
001520	012700	000004	MOV	#4,DEST ;MOVE DESTINATION OPERAND TO DESTINATION
001524	000257		CCC	
001526	074200		XOR	SOURCE,DEST ;XOR SOURCE AND DESTINATION
001530	103001		BCC	,+4
001532	000000		HLT	;ERROR! CARRY CHANGED
001534	102001		BVC	,+4
001536	000000		HLT	;ERROR! V FAILED TO CLEAR
001540	020027	000000	CMP	DEST,#0 ;DEST=0?
001544	001401		BEQ	,+4
001546	000000		HLT	;4 XOR 4 FAILED
	000013		Y=Y+1	
	000010		N=N+N	
001550	010701		SCOPE	
001552	012702	000010	MOV	#10,SOURCE ;MOVE SOURCE OPERAND TO SOURCE
001556	012700	000010	MOV	#10,DEST ;MOVE DESTINATION OPERAND TO DESTINATION
001562	000277		SCC	
001564	074200		XOR	SOURCE,DEST ;XOR SOURCE AND DESTINATION
001566	103401		BCC	,+4
001570	000000		HLT	;ERROR! CARRY CHANGED
001572	102001		BVC	,+4
001574	000000		HLT	;ERROR! V FAILED TO CLEAR
001576	020027	000000	CMP	DEST,#0 ;DEST=0?
001602	001401		BEQ	,+4
001604	000000		HLT	;10 XOR 10 FAILED
	000014		Y=Y+1	
	000020		N=N+N	
001606	010701		SCOPE	
001610	012702	000020	MOV	#20,SOURCE ;MOVE SOURCE OPERAND TO SOURCE
001614	012700	000020	MOV	#20,DEST ;MOVE DESTINATION OPERAND TO DESTINATION
001620	000257		CCC	
001622	074200		XOR	SOURCE,DEST ;XOR SOURCE AND DESTINATION
001624	103001		BCC	,+4
001626	000000		HLT	;ERROR! CARRY CHANGED
001630	102001		BVC	,+4
001632	000000		HLT	;ERROR! V FAILED TO CLEAR
001634	020027	000000	CMP	DEST,#0 ;DEST=0?
001640	001401		BEQ	,+4
001642	000000		HLT	;20 XOR 20 FAILED
	000015		Y=Y+1	
	000040		N=N+N	
001644	010701		SCOPE	
001646	012702	000040	MOV	#40,SOURCE ;MOVE SOURCE OPERAND TO SOURCE
001652	012700	000040	MOV	#40,DEST ;MOVE DESTINATION OPERAND TO DESTINATION

001656	000277		SCC		
001660	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
001662	103401		BCS	,+4	
001664	000000		HLT		;ERROR! CARRY CHANGED
001666	102001		BVC	,+4	
001670	000000		HLT		;ERROR! V FAILED TO CLEAR
001672	020027	000000	CMP	DEST,#0 ;DEST=#?	
001676	001401		BEQ	,+4	
001700	000000		HLT		;40 XOR 40 FAILED
	000016		Y=Y+1		
	000100		N=N+N		
001702	010701		SCOPE		
001704	012702	000100	MOV	#100,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
001710	012700	000100	MOV	#100,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
001714	000257		CCC		
001716	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
001720	103001		BCC	,+4	
001722	000000		HLT		;ERROR! CARRY CHANGED
001724	102001		BVC	,+4	
001726	000000		HLT		;ERROR! V FAILED TO CLEAR
001730	020027	000000	CMP	DEST,#0 ;DEST=#?	
001734	001401		BEQ	,+4	
001736	000000		HLT		;100 XOR 100 FAILED
	000017		Y=Y+1		
	000200		N=N+N		
001740	010701		SCOPE		
001742	012702	000200	MOV	#200,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
001746	012700	000200	MOV	#200,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
001752	000277		SCC		
001754	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
001756	103401		BCS	,+4	
001760	000000		HLT		;ERROR! CARRY CHANGED
001762	102001		BVC	,+4	
001764	000000		HLT		;ERROR! V FAILED TO CLEAR
001766	020027	000000	CMP	DEST,#0 ;DEST=#?	
001772	001401		BEQ	,+4	
001774	000000		HLT		;200 XOR 200 FAILED
	000020		Y=Y+1		
	000400		N=N+N		
001776	010701		SCOPE		
002000	012702	000400	MOV	#400,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
002004	012700	000400	MOV	#400,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
002010	000257		CCC		
002012	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
002014	103001		BCC	,+4	
002016	000000		HLT		;ERROR! CARRY CHANGED
002020	102001		BVC	,+4	
002022	000000		HLT		;ERROR! V FAILED TO CLEAR
002024	020027	000000	CMP	DEST,#0 ;DEST=#?	
002030	001401		BEQ	,+4	
002032	000000		HLT		;400 XOR 400 FAILED

	000021		Y=Y+1		
	001000		N=N+N		
	010701		SCOPE		
002034	012702	001000	MOV	#1000,SOURCE	MOVE SOURCE OPERAND TO SOURCE
002036	012700	001000	MOV	#1000,DEST	MOVE DESTINATION OPERAND TO DESTINATION
002042	000277		SCC		
002046	074200		XOR	SOURCE,DEST	XOR SOURCE AND DESTINATION
002050	103401		BCC	,+4	
002052	000000		HLT		ERROR! CARRY CHANGED
002054	102001		BVC	,+4	
002056	000000		HLT		ERROR! V FAILED TO CLEAR
002060	020027	000000	CMP	DEST,#0 ;DEST=0?	
002062	001401		BEQ	,+4	
002066	000000		HLT		1000 XOR 1000 FAILED
002070					
	000022		Y=Y+1		
	002000		N=N+N		
	010701		SCOPE		
002072	012702	002000	MOV	#2000,SOURCE	MOVE SOURCE OPERAND TO SOURCE
002074	012700	002000	MOV	#2000,DEST	MOVE DESTINATION OPERAND TO DESTINATION
002100	000257		CCC		
002104	074200		XOR	SOURCE,DEST	XOR SOURCE AND DESTINATION
002106	103001		BCC	,+4	
002110	000000		HLT		ERROR! CARRY CHANGED
002112	102001		BVC	,+4	
002114	000000		HLT		ERROR! V FAILED TO CLEAR
002116	020027	000000	CMP	DEST,#0 ;DEST=0?	
002120	001401		BEQ	,+4	
002124	000000		HLT		2000 XOR 2000 FAILED
002126					
	000023		Y=Y+1		
	004000		N=N+N		
	010701		SCOPE		
002130	012702	004000	MOV	#4000,SOURCE	MOVE SOURCE OPERAND TO SOURCE
002132	012700	004000	MOV	#4000,DEST	MOVE DESTINATION OPERAND TO DESTINATION
002136	000277		SCC		
002142	074200		XOR	SOURCE,DEST	XOR SOURCE AND DESTINATION
002144	103401		BCC	,+4	
002146	000000		HLT		ERROR! CARRY CHANGED
002150	102001		BVC	,+4	
002152	000000		HLT		ERROR! V FAILED TO CLEAR
002154	020027	000000	CMP	DEST,#0 ;DEST=0?	
002156	001401		BEQ	,+4	
002162	000000		HLT		4000 XOR 4000 FAILED
002164					
	000024		Y=Y+1		
	010000		N=N+N		
	010701		SCOPE		
002166	012702	010000	MOV	#10000,SOURCE	MOVE SOURCE OPERAND TO SOURCE
002170	012700	010000	MOV	#10000,DEST	MOVE DESTINATION OPERAND TO DESTINATION
002174	000257		CCC		
002200	074200		XOR	SOURCE,DEST	XOR SOURCE AND DESTINATION
002202	103001		BCC	,+4	
002204					

002206	000000		HLT		!ERROR! CARRY CHANGED
002210	102001		BVC	,+4	
002212	000000		HLT		!ERROR! V FAILED TO CLEAR
002214	020027	000000	CMP	DEST,#0 ;DEST=#?	
002220	001401		BEG	,+4	
002222	000000		HLT		!10000 XOR 10000 FAILED
	000025		Y=Y+1		
	020000		N=N+N		
002224	010701		SCOPE		
002226	012702	020000	MOV	#20000,SOURCE	!MOVE SOURCE OPERAND TO SOURCE
002232	012700	020000	MOV	#20000,DEST	!MOVE DESTINATION OPERAND TO DESTINATION
002236	000277		SCC		
002240	074200		XOR	SOURCE,DEST	!XOR SOURCE AND DESTINATION
002242	103401		BCS	,+4	
002244	000000		HLT		!ERROR! CARRY CHANGED
002246	102001		BVC	,+4	
002250	000000		HLT		!ERROR! V FAILED TO CLEAR
002252	020027	000000	CMP	DEST,#0 ;DEST=#?	
002256	001401		BEG	,+4	
002260	000000		HLT		!20000 XOR 20000 FAILED
	000026		Y=Y+1		
	040000		N=N+N		
002262	010701		SCOPE		
002264	012702	040000	MOV	#40000,SOURCE	!MOVE SOURCE OPERAND TO SOURCE
002270	012700	040000	MOV	#40000,DEST	!MOVE DESTINATION OPERAND TO DESTINATION
002274	000257		CCC		
002276	074200		XOR	SOURCE,DEST	!XOR SOURCE AND DESTINATION
002300	103001		BCC	,+4	
002302	000000		HLT		!ERROR! CARRY CHANGED
002304	102001		BVC	,+4	
002306	000000		HLT		!ERROR! V FAILED TO CLEAR
002310	020027	000000	CMP	DEST,#0 ;DEST=#?	
002314	001401		BEG	,+4	
002316	000000		HLT		!40000 XOR 40000 FAILED
	000027		Y=Y+1		
	100000		N=N+N		
002320	010701		SCOPE		
002322	012702	100000	MOV	#100000,SOURCE	!MOVE SOURCE OPERAND TO SOURCE
002326	012700	100000	MOV	#100000,DEST	!MOVE DESTINATION OPERAND TO DESTINATION
002332	000277		SCC		
002334	074200		XOR	SOURCE,DEST	!XOR SOURCE AND DESTINATION
002336	103401		BCS	,+4	
002340	000000		HLT		!ERROR! CARRY CHANGED
002342	102001		BVC	,+4	
002344	000000		HLT		!ERROR! V FAILED TO CLEAR
002346	020027	000000	CMP	DEST,#0 ;DEST=#?	
002352	001401		BEG	,+4	
002354	000000		HLT		!100000 XOR 100000 FAILED
	000030		Y=Y+1		
	000000		N=N+N		

	000001		N=1		
002356	010701			SCOPE	
002360	012702	177777		MOV	#=1,SOURCE ;MOVE SOURCE OPERAND TO SOURCE
002364	012700	000001		MOV	#1,DEST ;MOVE DESTINATION OPERAND TO DESTINATION
002370	000257			CCC	
002372	074200			XOR	SOURCE,DEST ;XOR SOURCE AND DESTINATION
002374	103001			BCC	,+4
002376	000000			HLT	;ERROR! CARRY CHANGED
002400	102001			BVC	,+4
002402	000000			HLT	;ERROR! V FAILED TO CLEAR
002404	020027	177776		CMP	DEST,#177776 ;DEST=177776?
002410	001401			BEO	,+4
002412	000000			HLT	;=1 XOR 1 FAILED
	000031			Y=Y+1	
	000002			N=N+N	
002414	010701			SCOPE	
002416	012702	177777		MOV	#=1,SOURCE ;MOVE SOURCE OPERAND TO SOURCE
002422	012700	000002		MOV	#2,DEST ;MOVE DESTINATION OPERAND TO DESTINATION
002426	000277			SCC	
002430	074200			XOR	SOURCE,DEST ;XOR SOURCE AND DESTINATION
002432	103401			BCS	,+4
002434	000000			HLT	;ERROR! CARRY CHANGED
002436	102001			BVC	,+4
002440	000000			HLT	;ERROR! V FAILED TO CLEAR
002442	020027	177775		CMP	DEST,#177775 ;DEST=177775?
002446	001401			BEO	,+4
002450	000000			HLT	;=1 XOR 2 FAILED
	000032			Y=Y+1	
	000004			N=N+N	
002452	010701			SCOPE	
002454	012702	177777		MOV	#=1,SOURCE ;MOVE SOURCE OPERAND TO SOURCE
002460	012700	000004		MOV	#4,DEST ;MOVE DESTINATION OPERAND TO DESTINATION
002464	000257			CCC	
002466	074200			XOR	SOURCE,DEST ;XOR SOURCE AND DESTINATION
002470	103001			BCC	,+4
002472	000000			HLT	;ERROR! CARRY CHANGED
002474	102001			BVC	,+4
002476	000000			HLT	;ERROR! V FAILED TO CLEAR
002500	020027	177773		CMP	DEST,#177773 ;DEST=177773?
002504	001401			BEO	,+4
002506	000000			HLT	;=1 XOR 4 FAILED
	000033			Y=Y+1	
	000010			N=N+N	
002510	010701			SCOPE	
002512	012702	177777		MOV	#=1,SOURCE ;MOVE SOURCE OPERAND TO SOURCE
002516	012700	000010		MOV	#10,DEST ;MOVE DESTINATION OPERAND TO DESTINATION
002522	000277			SCC	
002524	074200			XOR	SOURCE,DEST ;XOR SOURCE AND DESTINATION
002526	103401			BCS	,+4
002530	000000			HLT	;ERROR! CARRY CHANGED

002532	102001		BVC	,+4	
002534	000000		HLT		!ERROR! V FAILED TO CLEAR
002536	020027	177767	CMP	DEST,#177767	!DEST=177767?
002542	001401		BEG	,+4	
002544	000000		HLT		!-1 XOR 10 FAILED
	000034		Y=Y+1		
	000020		N=N+N		
002546	010701		SCOPE		
002550	012702	177777	MOV	#-1,SOURCE	!MOVE SOURCE OPERAND TO SOURCE
002554	012700	000020	MOV	#20,DEST	!MOVE DESTINATION OPERAND TO DESTINATION
002560	000257		CCC		
002562	074200		XOR	SOURCE,DEST	!XOR SOURCE AND DESTINATION
002564	103001		BCC	,+4	
002566	000000		HLT		!ERROR! CARRY CHANGED
002570	102001		BVC	,+4	
002572	000000		HLT		!ERROR! V FAILED TO CLEAR
002574	020027	177757	CMP	DEST,#177757	!DEST=177757?
002600	001401		BEG	,+4	
002602	000000		HLT		!-1 XOR 20 FAILED
	000035		Y=Y+1		
	000040		N=N+N		
002604	010701		SCOPE		
002606	012702	177777	MOV	#-1,SOURCE	!MOVE SOURCE OPERAND TO SOURCE
002612	012700	000040	MOV	#40,DEST	!MOVE DESTINATION OPERAND TO DESTINATION
002616	000277		SCC		
002620	074200		XOR	SOURCE,DEST	!XOR SOURCE AND DESTINATION
002622	103401		BCS	,+4	
002624	000000		HLT		!ERROR! CARRY CHANGED
002626	102001		BVC	,+4	
002630	000000		HLT		!ERROR! V FAILED TO CLEAR
002632	020027	177737	CMP	DEST,#177737	!DEST=177737?
002636	001401		BEG	,+4	
002640	000000		HLT		!-1 XOR 40 FAILED
	000036		Y=Y+1		
	000100		N=N+N		
002642	010701		SCOPE		
002644	012702	177777	MOV	#-1,SOURCE	!MOVE SOURCE OPERAND TO SOURCE
002650	012700	000100	MOV	#100,DEST	!MOVE DESTINATION OPERAND TO DESTINATION
002654	000257		CCC		
002656	074200		XOR	SOURCE,DEST	!XOR SOURCE AND DESTINATION
002660	103001		BCC	,+4	
002662	000000		HLT		!ERROR! CARRY CHANGED
002664	102001		BVC	,+4	
002666	000000		HLT		!ERROR! V FAILED TO CLEAR
002670	020027	177677	CMP	DEST,#177677	!DEST=177677?
002674	001401		BEG	,+4	
002676	000000		HLT		!-1 XOR 100 FAILED
	000037		Y=Y+1		
	000200		N=N+N		
002700	010701		SCOPE		

002702	012702	177777	MOV	#=1,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
002706	012700	000200	MOV	#200,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
002712	000277		SCC		
002714	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
002716	103401		BCS	,+4	
002720	000000		HLT		;ERROR! CARRY CHANGED
002722	102001		BVC	,+4	
002724	000000		HLT		;ERROR! V FAILED TO CLEAR
002726	020027	177577	CMP	DEST,#177577	;DEST=177577?
002732	001401		BEQ	,+4	
002734	000000		HLT		;=1 XOR 200 FAILED
	000040		Y=Y+1		
	000400		N=N+N		
			SCOPE		
002736	010701		MOV	#=1,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
002740	012702	177777	MOV	#400,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
002744	012700	000400	CCC		
002750	000257		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
002752	074200		BCC	,+4	
002754	103001		HLT		;ERROR! CARRY CHANGED
002756	000000		BVC	,+4	
002760	102001		HLT		;ERROR! V FAILED TO CLEAR
002762	000000		CMP	DEST,#177377	;DEST=177377?
002764	020027	177377	BEQ	,+4	
002770	001401		HLT		;=1 XOR 400 FAILED
002772	000000				
	000041		Y=Y+1		
	001000		N=N+N		
			SCOPE		
002774	010701		MOV	#=1,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
002776	012702	177777	MOV	#1000,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
003002	012700	001000	SCC		
003006	000277		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
003010	074200		BCS	,+4	
003012	103401		HLT		;ERROR! CARRY CHANGED
003014	000000		BVC	,+4	
003016	102001		HLT		;ERROR! V FAILED TO CLEAR
003020	000000		CMP	DEST,#176777	;DEST=176777?
003022	020027	176777	BEQ	,+4	
003026	001401		HLT		;=1 XOR 1000 FAILED
003030	000000				
	000042		Y=Y+1		
	002000		N=N+N		
			SCOPE		
003032	010701		MOV	#=1,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
003034	012702	177777	MOV	#2000,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
003040	012700	002000	CCC		
003044	000257		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
003046	074200		BCC	,+4	
003050	103001		HLT		;ERROR! CARRY CHANGED
003052	000000		BVC	,+4	
003054	102001		HLT		;ERROR! V FAILED TO CLEAR
003056	000000		CMP	DEST,#175777	;DEST=175777?
003060	020027	175777			

003064	001401		BEQ	,+4	
003066	000000		HLT		;I XOR 2000 FAILED
	000043		Y=Y+1		
	004000		N=N+N		
003070	010701		SCOPE		
003072	012702	177777	MOV	#=1,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
003076	012700	004000	MOV	#4000,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
003102	000277		SCC		
003104	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
003106	103401		BCS	,+4	
003110	000000		HLT		;ERROR! CARRY CHANGED
003112	102001		BVC	,+4	
003114	000000		HLT		;ERROR! V FAILED TO CLEAR
003116	020027	173777	CMP	DEST,#173777	;DEST=173777?
003122	001401		BEQ	,+4	
003124	000000		HLT		;I XOR 4000 FAILED
	000044		Y=Y+1		
	010000		N=N+N		
003126	010701		SCOPE		
003130	012702	177777	MOV	#=1,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
003134	012700	010000	MOV	#10000,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
003140	000257		CCC		
003142	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
003144	103001		BCC	,+4	
003146	000000		HLT		;ERROR! CARRY CHANGED
003150	102001		BVC	,+4	
003152	000000		HLT		;ERROR! V FAILED TO CLEAR
003154	020027	167777	CMP	DEST,#167777	;DEST=167777?
003160	001401		BEQ	,+4	
003162	000000		HLT		;I XOR 10000 FAILED
	000045		Y=Y+1		
	020000		N=N+N		
003164	010701		SCOPE		
003166	012702	177777	MOV	#=1,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
003172	012700	020000	MOV	#20000,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
003176	000277		SCC		
003200	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
003202	103401		BCS	,+4	
003204	000000		HLT		;ERROR! CARRY CHANGED
003206	102001		BVC	,+4	
003210	000000		HLT		;ERROR! V FAILED TO CLEAR
003212	020027	157777	CMP	DEST,#157777	;DEST=157777?
003216	001401		BEQ	,+4	
003220	000000		HLT		;I XOR 20000 FAILED
	000046		Y=Y+1		
	040000		N=N+N		
003222	010701		SCOPE		
003224	012702	177777	MOV	#=1,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
003230	012700	040000	MOV	#40000,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
003234	000257		CCC		

003236	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
003240	103001		BCC	,+4	
003242	000000		HLT		;ERROR! CARRY CHANGED
003244	102001		BVC	,+4	
003246	000000		HLT		;ERROR! V FAILED TO CLEAR
003250	020027	137777	CMP	DEST,#137777	;DEST=137777?
003254	001401		BEQ	,+4	
003256	000000		HLT		;=1 XOR 40000 FAILED
	000047		Y=Y+1		
	100000		N=N+N		
003260	010701		SCOPE		
003262	012702	177777	MOV	#=1,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
003266	012700	100000	MOV	#100000,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
003272	000277		SCC		
003274	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
003276	103401		BCS	,+4	
003300	000000		HLT		;ERROR! CARRY CHANGED
003302	102001		BVC	,+4	
003304	000000		HLT		;ERROR! V FAILED TO CLEAR
003306	020027	077777	CMP	DEST,#77777	;DEST=77777?
003312	001401		BEQ	,+4	
003314	000000		HLT		;=1 XOR 100000 FAILED
	000050		Y=Y+1		
	000000		N=N+N		

	000001		N=1		
003316	010701			SCOPE	
003320	012702	000000		MOV	#0,SOURCE ;MOVE SOURCE OPERAND TO SOURCE
003324	012700	000001		MOV	#1,DEST ;MOVE DESTINATION OPERAND TO DESTINATION
003330	000257			CCC	
003332	074200			XOR	SOURCE,DEST ;XOR SOURCE AND DESTINATION
003334	103001			BCC	,+4
003336	000000			HLT	;ERROR! CARRY CHANGED
003340	102001			BVC	,+4
003342	000000			HLT	;ERROR! V FAILED TO CLEAR
003344	020027	000001		CMP	DEST,#1 ;DEST=1?
003350	001401			BEQ	,+4
003352	000000			HLT	;0 XOR 1 FAILED
	000051			Y=Y+1	
	000002			N=N+N	
003354	010701			SCOPE	
003356	012702	000000		MOV	#0,SOURCE ;MOVE SOURCE OPERAND TO SOURCE
003362	012700	000002		MOV	#2,DEST ;MOVE DESTINATION OPERAND TO DESTINATION
003366	000277			SCC	
003370	074200			XOR	SOURCE,DEST ;XOR SOURCE AND DESTINATION
003372	103401			BCS	,+4
003374	000000			HLT	;ERROR! CARRY CHANGED
003376	102001			BVC	,+4
003400	000000			HLT	;ERROR! V FAILED TO CLEAR
003402	020027	000002		CMP	DEST,#2 ;DEST=2?
003406	001401			BEQ	,+4
003410	000000			HLT	;0 XOR 2 FAILED
	000052			Y=Y+1	
	000004			N=N+N	
003412	010701			SCOPE	
003414	012702	000000		MOV	#0,SOURCE ;MOVE SOURCE OPERAND TO SOURCE
003420	012700	000004		MOV	#4,DEST ;MOVE DESTINATION OPERAND TO DESTINATION
003424	000257			CCC	
003426	074200			XOR	SOURCE,DEST ;XOR SOURCE AND DESTINATION
003430	103001			BCC	,+4
003432	000000			HLT	;ERROR! CARRY CHANGED
003434	102001			BVC	,+4
003436	000000			HLT	;ERROR! V FAILED TO CLEAR
003440	020027	000004		CMP	DEST,#4 ;DEST=4?
003444	001401			BEQ	,+4
003446	000000			HLT	;0 XOR 4 FAILED
	000053			Y=Y+1	
	000010			N=N+N	
003450	010701			SCOPE	
003452	012702	000000		MOV	#0,SOURCE ;MOVE SOURCE OPERAND TO SOURCE
003456	012700	000010		MOV	#10,DEST ;MOVE DESTINATION OPERAND TO DESTINATION
003462	000277			SCC	
003464	074200			XOR	SOURCE,DEST ;XOR SOURCE AND DESTINATION
003466	103401			BCS	,+4
003470	000000			HLT	;ERROR! CARRY CHANGED

003472	102001		BVC	,+4	
003474	000000		HLT		;ERROR! V FAILED TO CLEAR
003476	020027	000010	CMP	DEST,#10	;DEST=10?
003502	001401		BEQ	,+4	
003504	000000		HLT		;0 XOR 10 FAILED
	000054		Y=Y+1		
	000020		N=N+N		
003506	010701		SCOPE		
003510	012702	000000	MOV	#0,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
003514	012700	000020	MOV	#20,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
003520	000257		CCC		
003522	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
003524	103001		BCC	,+4	
003526	000000		HLT		;ERROR! CARRY CHANGED
003530	102001		BVC	,+4	
003532	000000		HLT		;ERROR! V FAILED TO CLEAR
003534	020027	000020	CMP	DEST,#20	;DEST=20?
003540	001401		BEQ	,+4	
003542	000000		HLT		;0 XOR 20 FAILED
	000055		Y=Y+1		
	000040		N=N+N		
003544	010701		SCOPE		
003546	012702	000000	MOV	#0,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
003552	012700	000040	MOV	#40,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
003556	000277		SCC		
003560	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
003562	103401		BCS	,+4	
003564	000000		HLT		;ERROR! CARRY CHANGED
003566	102001		BVC	,+4	
003570	000000		HLT		;ERROR! V FAILED TO CLEAR
003572	020027	000040	CMP	DEST,#40	;DEST=40?
003576	001401		BEQ	,+4	
003600	000000		HLT		;0 XOR 40 FAILED
	000056		Y=Y+1		
	000100		N=N+N		
003602	010701		SCOPE		
003604	012702	000000	MOV	#0,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
003610	012700	000100	MOV	#100,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
003614	000257		CCC		
003616	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
003620	103001		BCC	,+4	
003622	000000		HLT		;ERROR! CARRY CHANGED
003624	102001		BVC	,+4	
003626	000000		HLT		;ERROR! V FAILED TO CLEAR
003630	020027	000100	CMP	DEST,#100	;DEST=100?
003634	001401		BEQ	,+4	
003636	000000		HLT		;0 XOR 100 FAILED
	000057		Y=Y+1		
	000200		N=N+N		
003640	010701		SCOPE		

003642	012702	000000	MOV	#0,SOURCE	MOVE SOURCE OPERAND TO SOURCE
003646	012700	000200	MOV	#200,DEST	MOVE DESTINATION OPERAND TO DESTINATION
003652	000277		SCC		
003654	074200		XOR	SOURCE,DEST	XOR SOURCE AND DESTINATION
003656	103401		BCS	,+4	
003660	000000		HLT		ERROR! CARRY CHANGED
003662	102001		BVC	,+4	
003664	000000		HLT		ERROR! V FAILED TO CLEAR
003666	020027	000200	CMP	DEST,#200	DEST=200?
003672	001401		BEG	,+4	
003674	000000		HLT		XOR 200 FAILED
	000060		Y=Y+1		
	000400		N=N+N		
003676	010701		SCOPE		
003700	012702	000000	MOV	#0,SOURCE	MOVE SOURCE OPERAND TO SOURCE
003704	012700	000400	MOV	#400,DEST	MOVE DESTINATION OPERAND TO DESTINATION
003710	000257		CCC		
003712	074200		XOR	SOURCE,DEST	XOR SOURCE AND DESTINATION
003714	103001		BCC	,+4	
003716	000000		HLT		ERROR! CARRY CHANGED
003720	102001		BVC	,+4	
003722	000000		HLT		ERROR! V FAILED TO CLEAR
003724	020027	000400	CMP	DEST,#400	DEST=400?
003730	001401		BEG	,+4	
003732	000000		HLT		XOR 400 FAILED
	000061		Y=Y+1		
	001000		N=N+N		
003734	010701		SCOPE		
003736	012702	000000	MOV	#0,SOURCE	MOVE SOURCE OPERAND TO SOURCE
003742	012700	001000	MOV	#1000,DEST	MOVE DESTINATION OPERAND TO DESTINATION
003746	000277		SCC		
003750	074200		XOR	SOURCE,DEST	XOR SOURCE AND DESTINATION
003752	103401		BCS	,+4	
003754	000000		HLT		ERROR! CARRY CHANGED
003756	102001		BVC	,+4	
003760	000000		HLT		ERROR! V FAILED TO CLEAR
003762	020027	001000	CMP	DEST,#1000	DEST=1000?
003766	001401		BEG	,+4	
003770	000000		HLT		XOR 1000 FAILED
	000062		Y=Y+1		
	002000		N=N+N		
003772	010701		SCOPE		
003774	012702	000000	MOV	#0,SOURCE	MOVE SOURCE OPERAND TO SOURCE
004000	012700	002000	MOV	#2000,DEST	MOVE DESTINATION OPERAND TO DESTINATION
004004	000257		CCC		
004006	074200		XOR	SOURCE,DEST	XOR SOURCE AND DESTINATION
004010	103001		BCC	,+4	
004012	000000		HLT		ERROR! CARRY CHANGED
004014	102001		BVC	,+4	
004016	000000		HLT		ERROR! V FAILED TO CLEAR
004020	020027	002000	CMP	DEST,#2000	DEST=2000?

004024	001401		BEQ	,+4	
004026	000000		HLT		;0 XOR 2000 FAILED
	000063		Y=Y+1		
	004000		N=N+N		
004030	010701		SCOPE		
004032	012702	000000	MOV	#0,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
004036	012700	004000	MOV	#4000,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
004042	000277		SCC		
004044	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
004046	103401		BCS	,+4	
004050	000000		HLT		;ERROR! CARRY CHANGED
004052	102001		BVC	,+4	
004054	000000		HLT		;ERROR! V FAILED TO CLEAR
004056	020027	004000	CMP	DEST,#4000	;DEST=4000?
004062	001401		BEQ	,+4	
004064	000000		HLT		;0 XOR 4000 FAILED
	000064		Y=Y+1		
	010000		N=N+N		
004066	010701		SCOPE		
004070	012702	000000	MOV	#0,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
004074	012700	010000	MOV	#10000,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
004100	000257		CCC		
004102	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
004104	103001		BCC	,+4	
004106	000000		HLT		;ERROR! CARRY CHANGED
004110	102001		BVC	,+4	
004112	000000		HLT		;ERROR! V FAILED TO CLEAR
004114	020027	010000	CMP	DEST,#10000	;DEST=10000?
004120	001401		BEQ	,+4	
004122	000000		HLT		;0 XOR 10000 FAILED
	000065		Y=Y+1		
	020000		N=N+N		
004124	010701		SCOPE		
004126	012702	000000	MOV	#0,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
004132	012700	020000	MOV	#20000,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
004136	000277		SCC		
004140	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
004142	103401		BCS	,+4	
004144	000000		HLT		;ERROR! CARRY CHANGED
004146	102001		BVC	,+4	
004150	000000		HLT		;ERROR! V FAILED TO CLEAR
004152	020027	020000	CMP	DEST,#20000	;DEST=20000?
004156	001401		BEQ	,+4	
004160	000000		HLT		;0 XOR 20000 FAILED
	000066		Y=Y+1		
	040000		N=N+N		
004162	010701		SCOPE		
004164	012702	000000	MOV	#0,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
004170	012700	040000	MOV	#40000,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
004174	000257		CCC		

004176	074200		XOR	SOURCE,DEST	!XOR SOURCE AND DESTINATION
004200	103001		BCC	,+4	
004202	000000		HLT		!ERROR! CARRY CHANGED
004204	102001		BVC	,+4	
004206	000000		HLT		!ERROR! V FAILED TO CLEAR
004210	020027	040000	CMP	DEST,#40000	!DEST=40000?
004214	001401		BEQ	,+4	
004216	000000		HLT		!0 XOR 40000 FAILED
	000067		Y=Y+1		
	100000		N=N+N		
004220	010701		SCOPE		
004222	012702	000000	MOV	#0,SOURCE	!MOVE SOURCE OPERAND TO SOURCE
004226	012700	100000	MOV	#100000,DEST	!MOVE DESTINATION OPERAND TO DESTINATION
004232	000277		SCC		
004234	074200		XOR	SOURCE,DEST	!XOR SOURCE AND DESTINATION
004236	103401		BCS	,+4	
004240	000000		HLT		!ERROR! CARRY CHANGED
004242	102001		BVC	,+4	
004244	000000		HLT		!ERROR! V FAILED TO CLEAR
004246	020027	100000	CMP	DEST,#100000	!DEST=100000?
004252	001401		BEQ	,+4	
004254	000000		HLT		!0 XOR 100000 FAILED
	000070		Y=Y+1		
	000000		N=N+N		
	000001	N=1	SCOPE		
004256	010701		MOV	#1,SOURCE	!MOVE SOURCE OPERAND TO SOURCE
004260	012702	000001	MOV	#177776,DEST	!MOVE DESTINATION OPERAND TO DESTINATION
004264	012700	177776	CCC		
004270	000277		XOR	SOURCE,DEST	!XOR SOURCE AND DESTINATION
004272	074200		BCC	,+4	
004274	103001		HLT		!ERROR! CARRY CHANGED
004276	000000		BVC	,+4	
004300	102001		HLT		!ERROR! V FAILED TO CLEAR
004302	000000		CMP	DEST,#177777	!DEST=177777?
004304	020027	177777	BEQ	,+4	
004310	001401		HLT		!1 XOR 177776 FAILED
004312	000000				
	000071		Y=Y+1		
	000002		N=N+N		
004314	010701		SCOPE		
004316	012702	000002	MOV	#2,SOURCE	!MOVE SOURCE OPERAND TO SOURCE
004322	012700	177775	MOV	#177775,DEST	!MOVE DESTINATION OPERAND TO DESTINATION
004326	000277		SCC		
004330	074200		XOR	SOURCE,DEST	!XOR SOURCE AND DESTINATION
004332	103401		BCS	,+4	
004334	000000		HLT		!ERROR! CARRY CHANGED
004336	102001		BVC	,+4	
004340	000000		HLT		!ERROR! V FAILED TO CLEAR
004342	020027	177777	CMP	DEST,#177777	!DEST=177777?
004346	001401		BEQ	,+4	

004350	000000		HLT		;2 XOR 177775 FAILED
	000072		Y=Y+1		
	000004		N=N+N		
004352	010701		SCOPE		
004354	012702	000004	MOV	#4,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
004360	012700	177773	MOV	#177773,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
004364	000257		CCC		
004366	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
004370	103001		BCC	,+4	
004372	000000		HLT		;ERROR! CARRY CHANGED
004374	102001		BVC	,+4	
004376	000000		HLT		;ERROR! V FAILED TO CLEAR
004400	020027	177777	CMP	DEST,#177777	;DEST=177777?
004404	001401		BEQ	,+4	
004406	000000		HLT		;4 XOR 177773 FAILED
	000073		Y=Y+1		
	000010		N=N+N		
004410	010701		SCOPE		
004412	012702	000010	MOV	#10,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
004416	012700	177767	MOV	#177767,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
004422	000277		SCC		
004424	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
004426	103401		BCS	,+4	
004430	000000		HLT		;ERROR! CARRY CHANGED
004432	102001		BVC	,+4	
004434	000000		HLT		;ERROR! V FAILED TO CLEAR
004436	020027	177777	CMP	DEST,#177777	;DEST=177777?
004442	001401		BEQ	,+4	
004444	000000		HLT		;10 XOR 177767 FAILED
	000074		Y=Y+1		
	000020		N=N+N		
004446	010701		SCOPE		
004450	012702	000020	MOV	#20,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
004454	012700	177757	MOV	#177757,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
004460	000257		CCC		
004462	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
004464	103001		BCC	,+4	
004466	000000		HLT		;ERROR! CARRY CHANGED
004470	102001		BVC	,+4	
004472	000000		HLT		;ERROR! V FAILED TO CLEAR
004474	020027	177777	CMP	DEST,#177777	;DEST=177777?
004500	001401		BEQ	,+4	
004502	000000		HLT		;20 XOR 177757 FAILED
	000075		Y=Y+1		
	000040		N=N+N		
004504	010701		SCOPE		
004506	012702	000040	MOV	#40,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
004512	012700	177737	MOV	#177737,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
004516	000277		SCC		
004520	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION

004522	103401		BCS	,+4	
004524	000000		HLT		!ERROR! CARRY CHANGED
004526	102001		BVC	,+4	
004530	000000		HLT		!ERROR! V FAILED TO CLEAR
004532	020027	177777	CMP	DEST,#177777	!DEST=177777?
004536	001401		BEG	,+4	
004540	000000		HLT		!40 XOR 177737 FAILED
	000076		Y=Y+1		
	000100		N=N+N		
	010701		SCOPE		
004542	010701		MOV	#100,SOURCE	!MOVE SOURCE OPERAND TO SOURCE
004544	012702	000100	MOV	#177677,DEST	!MOVE DESTINATION OPERAND TO DESTINATION
004550	012700	177677	CCC		
004554	000257		XOR	SOURCE,DEST	!XOR SOURCE AND DESTINATION
004556	074200		BCC	,+4	
004560	103001		HLT		!ERROR! CARRY CHANGED
004562	000000		BVC	,+4	
004564	102001		HLT		!ERROR! V FAILED TO CLEAR
004566	000000		CMP	DEST,#177777	!DEST=177777?
004570	020027	177777	BEG	,+4	
004574	001401		HLT		!100 XOR 177677 FAILED
004576	000000				
	000077		Y=Y+1		
	000200		N=N+N		
	010701		SCOPE		
004600	010701		MOV	#200,SOURCE	!MOVE SOURCE OPERAND TO SOURCE
004602	012702	000200	MOV	#177577,DEST	!MOVE DESTINATION OPERAND TO DESTINATION
004606	012700	177577	SCC		
004612	000277		XOR	SOURCE,DEST	!XOR SOURCE AND DESTINATION
004614	074200		BCS	,+4	
004616	103401		HLT		!ERROR! CARRY CHANGED
004620	000000		BVC	,+4	
004622	102001		HLT		!ERROR! V FAILED TO CLEAR
004624	000000		CMP	DEST,#177777	!DEST=177777?
004626	020027	177777	BEG	,+4	
004632	001401		HLT		!200 XOR 177577 FAILED
004634	000000				
	000100		Y=Y+1		
	000400		N=N+N		
	010701		SCOPE		
004636	010701		MOV	#400,SOURCE	!MOVE SOURCE OPERAND TO SOURCE
004640	012702	000400	MOV	#177377,DEST	!MOVE DESTINATION OPERAND TO DESTINATION
004644	012700	177377	CCC		
004650	000257		XOR	SOURCE,DEST	!XOR SOURCE AND DESTINATION
004652	074200		BCC	,+4	
004654	103001		HLT		!ERROR! CARRY CHANGED
004656	000000		BVC	,+4	
004660	102001		HLT		!ERROR! V FAILED TO CLEAR
004662	000000		CMP	DEST,#177777	!DEST=177777?
004664	020027	177777	BEG	,+4	
004670	001401		HLT		!400 XOR 177377 FAILED
004672	000000				
	000101		Y=Y+1		

	001000		N=N+N		
004674	010701		SCOPE		
004676	012702	001000	MOV	#1000,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
004702	012700	176777	MOV	#176777,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
004706	000277		SCC		
004710	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
004712	103401		BCC	,+4	
004714	000000		HLT		;ERROR! CARRY CHANGED
004716	102001		BVC	,+4	
004720	000000		HLT		;ERROR! V FAILED TO CLEAR
004722	020027	177777	CMP	DEST,#177777	;DEST=177777?
004726	001401		BEG	,+4	
004730	000000		HLT		;1000 XOR 176777 FAILED
	000102		Y=Y+1		
	002000		N=N+N		
004732	010701		SCOPE		
004734	012702	002000	MOV	#2000,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
004740	012700	175777	MOV	#175777,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
004744	000257		CCC		
004746	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
004750	103001		BCC	,+4	
004752	000000		HLT		;ERROR! CARRY CHANGED
004754	102001		BVC	,+4	
004756	000000		HLT		;ERROR! V FAILED TO CLEAR
004760	020027	177777	CMP	DEST,#177777	;DEST=177777?
004764	001401		BEG	,+4	
004766	000000		HLT		;2000 XOR 175777 FAILED
	000103		Y=Y+1		
	004000		N=N+N		
004770	010701		SCOPE		
004772	012702	004000	MOV	#4000,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
004776	012700	173777	MOV	#173777,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
005002	000277		SCC		
005004	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
005006	103401		BCC	,+4	
005010	000000		HLT		;ERROR! CARRY CHANGED
005012	102001		BVC	,+4	
005014	000000		HLT		;ERROR! V FAILED TO CLEAR
005016	020027	177777	CMP	DEST,#177777	;DEST=177777?
005022	001401		BEG	,+4	
005024	000000		HLT		;4000 XOR 173777 FAILED
	000104		Y=Y+1		
	010000		N=N+N		
005026	010701		SCOPE		
005030	012702	010000	MOV	#10000,SOURCE	;MOVE SOURCE OPERAND TO SOURCE
005034	012700	167777	MOV	#167777,DEST	;MOVE DESTINATION OPERAND TO DESTINATION
005040	000257		CCC		
005042	074200		XOR	SOURCE,DEST	;XOR SOURCE AND DESTINATION
005044	103001		BCC	,+4	
005046	000000		HLT		;ERROR! CARRY CHANGED
005050	102001		BVC	,+4	

005052	000000		HLT		ERROR! V FAILED TO CLEAR
005054	020027	177777	CMP	DEST,#177777	IDEST=177777?
005060	001401		BEG	,+4	
005062	000000		HLT		!10000 XOR 167777 FAILED
	000105		Y=Y+1		
	020000		N=N+N		
005064	010701		SCOPE		
005066	012702	020000	MOV	#20000,SOURCE	!MOVE SOURCE OPERAND TO SOURCE
005072	012700	157777	MOV	#157777,DEST	!MOVE DESTINATION OPERAND TO DESTINATION
005076	000277		SCC		
005100	074200		XOR	SOURCE,DEST	!XOR SOURCE AND DESTINATION
005102	103401		BCS	,+4	
005104	000000		HLT		ERROR! CARRY CHANGED
005106	102001		BVC	,+4	
005110	000000		HLT		ERROR! V FAILED TO CLEAR
005112	020027	177777	CMP	DEST,#177777	IDEST=177777?
005116	001401		BEG	,+4	
005120	000000		HLT		!20000 XOR 157777 FAILED
	000106		Y=Y+1		
	040000		N=N+N		
005122	010701		SCOPE		
005124	012702	040000	MOV	#40000,SOURCE	!MOVE SOURCE OPERAND TO SOURCE
005130	012700	137777	MOV	#137777,DEST	!MOVE DESTINATION OPERAND TO DESTINATION
005134	000257		CCC		
005136	074200		XOR	SOURCE,DEST	!XOR SOURCE AND DESTINATION
005140	103001		BCC	,+4	
005142	000000		HLT		ERROR! CARRY CHANGED
005144	102001		BVC	,+4	
005146	000000		HLT		ERROR! V FAILED TO CLEAR
005150	020027	177777	CMP	DEST,#177777	IDEST=177777?
005154	001401		BEG	,+4	
005156	000000		HLT		!40000 XOR 137777 FAILED
	000107		Y=Y+1		
	100000		N=N+N		
005160	010701		SCOPE		
005162	012702	100000	MOV	#100000,SOURCE	!MOVE SOURCE OPERAND TO SOURCE
005166	012700	077777	MOV	#77777,DEST	!MOVE DESTINATION OPERAND TO DESTINATION
005172	000277		SCC		
005174	074200		XOR	SOURCE,DEST	!XOR SOURCE AND DESTINATION
005176	103401		BCS	,+4	
005200	000000		HLT		ERROR! CARRY CHANGED
005202	102001		BVC	,+4	
005204	000000		HLT		ERROR! V FAILED TO CLEAR
005206	020027	177777	CMP	DEST,#177777	IDEST=177777?
005212	001401		BEG	,+4	
005214	000000		HLT		!100000 XOR 77777 FAILED
	000110		Y=Y+1		
	000000		N=N+N		
	000000		SOURCE=%0		

000002			DEST=X2	
000002			RSLT=X2	
005216	010701		SCOPE	
005220	012700	031463	MOV #031463, SOURCE	;MOVE SOURCE OPERAND TO SOURCE
005224	012702	146314	MOV #146314, DEST	;MOVE DESTINATION OPERAND TO DESTINATION
005230	000257		CCC	
005232	074002		XOR SOURCE, DEST	;XOR SOURCE AND DESTINATION
005234	103001		BCC ,+4	
005236	000000		HLT	;ERROR! CARRY CHANGED
005240	102001		BVC ,+4	
005242	000000		HLT	;ERROR! V FAILED TO CLEAR
005244	020227	177777	CMP DEST, #-1	;DEST=-1?
005250	001401		BEQ ,+4	
005252	000000		HLT	;031463 XOR 146314 FAILED
000111			Y=Y+1	
000002			SOURCE=X2	
000000			DEST=X0	
000002			RSLT=X2	
005254	010701		SCOPE	
005256	012702	146314	MOV #146314, SOURCE	;MOVE SOURCE OPERAND TO SOURCE
005262	012700	031463	MOV #031463, DEST	;MOVE DESTINATION OPERAND TO DESTINATION
005266	000277		SCC	
005270	074200		XOR SOURCE, DEST	;XOR SOURCE AND DESTINATION
005272	103401		BCS ,+4	
005274	000000		HLT	;ERROR! CARRY CHANGED
005276	102001		BVC ,+4	
005300	000000		HLT	;ERROR! V FAILED TO CLEAR
005302	020027	177777	CMP DEST, #-1	;DEST=-1?
005306	001401		BEQ ,+4	
005310	000000		HLT	;146314 XOR 031463 FAILED
000112			Y=Y+1	
000003			SOURCE=X3	
005312	010701		SCOPE	
005314	012703	016161	MOV #016161, SOURCE	;MOVE SOURCE OPERAND TO SOURCE
005320	012700	161616	MOV #161616, DEST	;MOVE DESTINATION OPERAND TO DESTINATION
005324	000257		CCC	
005326	074300		XOR SOURCE, DEST	;XOR SOURCE AND DESTINATION
005330	103001		BCC ,+4	
005332	000000		HLT	;ERROR! CARRY CHANGED
005334	102001		BVC ,+4	
005336	000000		HLT	;ERROR! V FAILED TO CLEAR
005340	020027	177777	CMP DEST, #-1	;DEST=-1?
005344	001401		BEQ ,+4	
005346	000000		HLT	;016161 XOR 161616 FAILED
000113			Y=Y+1	
000005			DEST=X5	

005350	010701		SCOPE		
005352	012703	161616	MOV	#161616, SOURCE	MOVE SOURCE OPERAND TO SOURCE
005356	012705	016161	MOV	#016161, DEST	MOVE DESTINATION OPERAND TO DESTINATION
005362	000277		SCC		
005364	074305		XOR	SOURCE, DEST	IXOR SOURCE AND DESTINATION
005366	103401		BQS	,+4	
005370	000000		HLT		ERROR! CARRY CHANGED
005372	102001		BVC	,+4	
005374	000000		HLT		ERROR! V FAILED TO CLEAR
005376	020527	177777	CMF	DEST, #-1	IDEST=-1?
005402	001401		BEQ	,+4	
005404	000000		HLT		161616 XOR 016161 FAILED
	000114			Y=Y+1	
	000000			DEST=X0	

TEST THAT CONDITION CODES ARE OPERATED ON CORRECTLY
 CARRY REMAINS UNCHANGED (CARRY SET)

005406	010701		SCOPE		
005410	012702	177777	MOV	#=1, X2	
005414	012700	177777	MOV	#=1, X0	
005420	000261		SEC		SET CARRY
005422	074200		XOR	X2, X0	IXOR
005424	103401		BQS	,+4	BRANCH IF CARRY IS SET
005426	000000		HLT		

TEST /N/

005430	010701		SCOPE		
005432	012700	100000	MOV	#100000, X0	
005436	005002		CLR	X2	
005440	074200		XOR	X2, X0	
005442	100401		BMI	,+4	
005444	000000		HLT		ERROR! /N/ FAILED TO SET

SCOPE

005446	010701		SCOPE		
005450	012702	100000	MOV	#100000, X2	
005454	012700	177777	MOV	#=1, X0	
005460	000250		CLN		
005462	074200		XOR	X2, X0	
005464	100001		BPL	,+4	
005466	000000		HLT		ERROR! /N/ FAILED TO CLEAR

CARRY REMAINS UNCHANGED (CARRY CLEAR)

005470	010701		SCOPE		
005472	012702	177777	MOV	#=1, X2	
005476	012700	177777	MOV	#=1, X0	
005502	000241		CLC		CLEAR CARRY
005504	074200		XOR	X2, X0	IXOR
005506	103001		BCC	,+4	BRANCH IF CARRY IS CLEAR
005510	000000		HLT		

N=1

TEST THAT Z BIT IS CLEAR IF RESULT IS NOT ZERO

005512	010701		SCOPE		
005514	012700	000001	MOV	#1,DEST	
005520	000264		SEZ		;SET Z BIT
005522	074300		XOR	SOURCE,DEST	;XOR
005524	001001		BNE	,+4	
005526	000000		HLT		!0 XOR 1 FAILED TO CLEAR Z
	000002		N=N+N		
005530	010701		SCOPE		
005532	012700	000002	MOV	#2,DEST	
005536	000264		SEZ		;SET Z BIT
005540	074300		XOR	SOURCE,DEST	;XOR
005542	001001		BNE	,+4	
005544	000000		HLT		!0 XOR 2 FAILED TO CLEAR Z
	000004		N=N+N		
005546	010701		SCOPE		
005550	012700	000004	MOV	#4,DEST	
005554	000264		SEZ		;SET Z BIT
005556	074300		XOR	SOURCE,DEST	;XOR
005560	001001		BNE	,+4	
005562	000000		HLT		!0 XOR 4 FAILED TO CLEAR Z
	000010		N=N+N		
005564	010701		SCOPE		
005566	012700	000010	MOV	#10,DEST	
005572	000264		SEZ		;SET Z BIT
005574	074300		XOR	SOURCE,DEST	;XOR
005576	001001		BNE	,+4	
005600	000000		HLT		!0 XOR 10 FAILED TO CLEAR Z
	000020		N=N+N		
005602	010701		SCOPE		
005604	012700	000020	MOV	#20,DEST	
005610	000264		SEZ		;SET Z BIT
005612	074300		XOR	SOURCE,DEST	;XOR
005614	001001		BNE	,+4	
005616	000000		HLT		!0 XOR 20 FAILED TO CLEAR Z
	000040		N=N+N		
005620	010701		SCOPE		
005622	012700	000040	MOV	#40,DEST	
005626	000264		SEZ		;SET Z BIT
005630	074300		XOR	SOURCE,DEST	;XOR
005632	001001		BNE	,+4	
005634	000000		HLT		!0 XOR 40 FAILED TO CLEAR Z
	000100		N=N+N		
005636	010701		SCOPE		
005640	012700	000100	MOV	#100,DEST	
005644	000264		SEZ		;SET Z BIT
005646	074300		XOR	SOURCE,DEST	;XOR
005650	001001		BNE	,+4	

005652	000000		HLT		!0 XOR 100 FAILED TO CLEAR Z
	000200		N=N+N		
005654	010701		SCOPE		
005656	012700	000200	MOV	#200,DEST	
005662	000264		SEZ		!SET Z BIT
005664	074300		XOR	SOURCE,DEST	!XOR
005666	001001		BNE	,+4	
005670	000000		HLT		!0 XOR 200 FAILED TO CLEAR Z
	000400		N=N+N		
005672	010701		SCOPE		
005674	012700	000400	MOV	#400,DEST	
005700	000264		SEZ		!SET Z BIT
005702	074300		XOR	SOURCE,DEST	!XOR
005704	001001		BNE	,+4	
005706	000000		HLT		!0 XOR 400 FAILED TO CLEAR Z
	001000		N=N+N		
005710	010701		SCOPE		
005712	012700	001000	MOV	#1000,DEST	
005716	000264		SEZ		!SET Z BIT
005720	074300		XOR	SOURCE,DEST	!XOR
005722	001001		BNE	,+4	
005724	000000		HLT		!0 XOR 1000 FAILED TO CLEAR Z
	002000		N=N+N		
005726	010701		SCOPE		
005730	012700	002000	MOV	#2000,DEST	
005734	000264		SEZ		!SET Z BIT
005736	074300		XOR	SOURCE,DEST	!XOR
005740	001001		BNE	,+4	
005742	000000		HLT		!0 XOR 2000 FAILED TO CLEAR Z
	004000		N=N+N		
005744	010701		SCOPE		
005746	012700	004000	MOV	#4000,DEST	
005752	000264		SEZ		!SET Z BIT
005754	074300		XOR	SOURCE,DEST	!XOR
005756	001001		BNE	,+4	
005760	000000		HLT		!0 XOR 4000 FAILED TO CLEAR Z
	010000		N=N+N		
005762	010701		SCOPE		
005764	012700	010000	MOV	#10000,DEST	
005770	000264		SEZ		!SET Z BIT
005772	074300		XOR	SOURCE,DEST	!XOR
005774	001001		BNE	,+4	
005776	000000		HLT		!0 XOR 10000 FAILED TO CLEAR Z
	020000		N=N+N		
006000	010701		SCOPE		
006002	012700	020000	MOV	#20000,DEST	
006006	000264		SEZ		!SET Z BIT

006010	074300		XOR	SOURCE,DEST		
006012	001001		BNE	,+4		
006014	000000		HLT			!0 XOR 20000 FAILED TO CLEAR Z
	040000		N=N+N			
006016	010701		SCOPE			
006020	012700	040000	MOV	#40000,DEST		
006024	000264		SEZ			!SET Z BIT
006026	074300		XOR	SOURCE,DEST		!XOR
006030	001001		BNE	,+4		
006032	000000		HLT			!0 XOR 40000 FAILED TO CLEAR Z
	100000		N=N+N			
006034	010701		SCOPE			
006036	012700	100000	MOV	#100000,DEST		
006042	000264		SEZ			!SET Z BIT
006044	074300		XOR	SOURCE,DEST		!XOR
006046	001001		BNE	,+4		
006050	000000		HLT			!0 XOR 100000 FAILED TO CLEAR Z
	000000		N=N+N			
	000001					
			N=1			
			!Z BIT IS SET IF RESULT=0			
006052	010701		SCOPE			
006054	012702	125252	MOV	#125252,%2		
006060	012700	125252	MOV	#125252,%0		
006064	000244		CLZ			!CLEAR Z
006066	074200		XOR	%2,%0		!XOR
006070	001401		BEQ	,+4		!BRANCH IF Z IS SET
006072	000000		HLT			
			!V IS CLEARED			
006074	010701		SCOPE			
006076	012702	125252	MOV	#125252,%2		
006102	012700	025252	MOV	#025252,%0		
006106	000262		SEV			
006110	074200		XOR	%2,%0	!XOR	
006112	102001		BVC	,+4		!BRANCH IF V IS CLEARED
006114	000000		HLT			
006116	010701		SCOPE			
006120	012702	052525	MOV	#052525,%2		
006124	012700	125252	MOV	#125252,%0		
006130	000262		SEV			!SET V
006132	074200		XOR	%2,%0	!XOR	
006134	102001		BVC	,+4		!BRANCH IF V IS CLEARED
006136	000000		HLT			
006140	010701		SCOPE			
006142	005002		CLR	%2		
006144	005000		CLR	%0		
006146	000262		SEV			
006150	074200		XOR	%2,%0		
006152	102001		BVC	,+4		

006154	000000			HLT		ERROR: V FAILED TO CLEAR
006156	010701			SCOPE		
006160	012702	177777		MOV	#=1,X2	
006164	012700	177777		MOV	#=1,X0	
006170	000262			SEV		
006172	074200			XOR	X2,X0	
006174	102003			BVC	,+10	
006176	000000			HLT		ERROR: V FAILED TO CLEAR
	000002			SOURCE=X2		
	006200			DEST=,		
	006202			,=,+2		
006202	000000			IDEST: 0		
				ITEST DESTINATION MODE=67		
006204	010701			SCOPE		
006206	012702	177777		MOV	#=1,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
006212	012767	177777	177760	MOV	#=1,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
006220	000257			CCC		
006222	074267	177752		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
006226	103001			BCC	,+4	
006230	000000			HLT		ERROR: CARRY CHANGED
006232	102001			BVC	,+4	
006234	000000			HLT		ERROR: V FAILED TO CLEAR
006236	026727	177736	000000	CMP	DEST,#0 IDEST=0?	
006244	001401			BEQ	,+4	
006246	000000			HLT		I=1 XOR -1 FAILED
	000115			Y=Y+1		
				ITEST DESTINATION MODE=27		
006250	010701			SCOPE		
006252	012702	177777		MOV	#=1,X2	
006256	012767	177777	000002	MOV	#=1,MODE27	
006264	074227			XOR	X2,(7)+	
006266	000000			MODE27: 0		
006270	001401			BEQ	,+4	
006272	000000			HLT		
				ITEST DESTINATION MODE 37		
006274	010701			SCOPE		
006276	012702	177777		MOV	#=1,X2	
006302	012737	177777	006200	MOV	#=1,@#DEST	
006310	074237	006200		XOR	X2,@#DEST	
006314	001401			BEQ	,+4	
006316	000000			HLT		
				ITEST DESTINATION MODE 77		
006320	010701			SCOPE		
006322	012767	006202	177650	MOV	#IDEST,DEST	
006330	012702	177777		MOV	#=1,X2	
006334	012777	177777	177636	MOV	#=1,@DEST	
006342	074277	177632		XOR	X2,@DEST	
006346	001401			BEQ	,+4	

006350 000000

HLT

TEST DESTINATION MODE 1

006352	010701			SCOPE		
006354	012700	006750		MOV	#MODE1,X0	
006360	012702	177777		MOV	#-1,X2	
006364	012710	177776		MOV	#-2,(0)	
006370	074210			XOR	X2,(0)	
006372	022767	000001	000350	CMP	#1,MODE1	!CHECK RESULT
006400	001401			BEQ	,+4	
006402	000000			HLT		!ERROR! INCORRECT RESULT
006404	022700	006750		CMP	#MODE1,X0	!CHECK REGISTER
006410	001401			BEQ	,+4	
006412	000000			HLT		!ERROR! R0 CHANGED
006414	022702	177777		CMP	#-1,X2	!CHECK REGISTER
006420	001401			BEQ	,+4	
006422	000000			HLT		!ERROR! R2 CHANGED

TEST DESTINATION MODE 2

006424	010701			SCOPE		
006426	012702	006750		MOV	#MODE1,X2	
006432	012703	000002		MOV	#2,X3	
006436	005012			CLR	(2)	
006440	074322			XOR	X3,(2)+	
006442	022767	000002	000300	CMP	#2,MODE1	!CHECK RESULT
006450	001401			BEQ	,+4	
006452	000000			HLT		!ERROR! INCORRECT RESULT
006454	022702	006752		CMP	#MODE1+2,X2	!TEST AUTO-INCREMENT
006460	001401			BEQ	,+4	
006462	000000			HLT		!ERROR! AUTO-INCREMENT FAILED

TEST DESTINATION MODE 3

006464	010701			SCOPE		
006466	012767	006752	000254	MOV	#MODE3,MODE1	
006474	012704	006750		MOV	#MODE1,X4	
006500	012703	000003		MOV	#3,X3	
006504	005067	000242		CLR	MODE3	
006510	074334			XOR	X3,#(4)+	
006512	022767	000003	000232	CMP	#3,MODE3	
006520	001401			BEQ	,+4	
006522	000000			HLT		!ERROR! INCORRECT RESULT
006524	022704	006752		CMP	#MODE1+2,X4	!TEST AUTO-INCREMENT
006530	001401			BEQ	,+4	
006532	000000			HLT		!ERROR! ATO-INCREMENT DEFERRED FAILED

TEST DESTINATION MODE 4

006534	010701			SCOPE		
006536	012726	000004		MOV	#4,(6)+	
006542	005005			CLR	X5	
006544	074546			XOR	X5,-(6)	
006546	022716	000004		CMP	#4,(6)	
006552	001401			BEQ	,+4	
006554	000000			HLT		!ERROR! INCORRECT RESULT

```

006556 012706 000500          MOV      #STKPTR,%6      ;RESTORE STACK PTR

                                ;TEST DESTINATION MODE 5
006562 010701          SCOPE
006564 012767 006750 000160      MOV      #MODE1,MODE3
006572 012702 006754          MOV      #MODE3+2,%2
006576 012767 000005 000144      MOV      #5,MODE1
006604 005000          CLR      %0
006606 074052          XOR      %0,%-(2)
006610 022767 000005 000132      CMP      #5,MODE1
006616 001401          BEQ      ,+4
006620 000000          HLT
006622 022702 006752          CMP      #MODE3,%2      ;ERROR! INCORRECT RESULT
006626 001401          BEQ      ,+4          ;CHECK REGISTER
006630 000000          HLT          ;ERROR! REGISTER CONTENTS CHANGED
    
```

```

                                ;TEST DESTINATION MODE 6
006632 010701          SCOPE
006634 005067 000110          CLR      MODE1
006640 012702 000006          MOV      #6,%2
006644 012703 006752          MOV      #MODE3,%3
006650 074263 177776          XOR      %2,-2(3)
006654 022767 000006 000066      CMP      #6,MODE1
006662 001401          BEQ      ,+4
006664 000000          HLT
006666 022703 006752          CMP      #MODE3,%3      ;ERROR! INCORRECT RESULT
006672 001401          BEQ      ,+4          ;CHECK REGISTER
006674 000000          HLT          ;ERROR! REGISTER CONTENTS CHANGED
    
```

```

                                ;TEST DESTINATION MODE 7
006676 010701          SCOPE
006700 012767 006752 000042      MOV      #MODE3,MODE1
006706 012705 177770          MOV      #-10,%5
006712 012767 177777 000032      MOV      #-1,MODE3
006720 074575 006760          XOR      %5,%MODE1+10(5)
006724 022767 000007 000020      CMP      #7,MODE3
006732 001401          BEQ      ,+4
006734 000000          HLT
006736 022705 177770          CMP      #-10,%5      ;ERROR! INCORRECT RESULT
006742 001401          BEQ      ,+4
006744 000000          HLT          ;ERROR! REGISTER CONTENTS CHANGED
006746 000402          BR      ,+6          ;GO TO NEXT TEST
006750 000000      MODE1: 0
006752 000000      MODE3: 0
    
```

```

                                ;TEST CONDITION CODES WHEN DESTINATION MODE IS NOT = 0
                                ;N=0,Z=0,V=0,C=0
006754 010701          SCOPE
006756 012700 000001          MOV      #1,%0
006762 012767 000002 177760      MOV      #2,MODE1
006770 012702 006750          MOV      #MODE1,%2
006774 000277          SCC
006776 000241          CLC
    
```

```

007000 074022          XOR    %0,(2)+
007002 016700 170770  MOV    PSW,%0
007006 022700 000000  CMP    #0,%0
007012 001401          BEQ    ,+4
007014 000000          HLT

```

IN=0,Z=0,V=0,C=1

```

007016 010701          SCOPE
007020 012700 000002  MOV    #2,%0
007024 012767 000004 177716  MOV    #4,MODE1
007032 012703 006752  MOV    #MODE3,%3
007036 000277          SCC
007040 074043          XOR    %0,-(3)
007042 016700 170730  MOV    PSW,%0
007046 022700 000001  CMP    #1,%0
007052 001401          BEQ    ,+4
007054 000000          HLT

```

IN=0,Z=1,V=0,C=0

```

007056 010701          SCOPE
007060 012767 177777 177662  MOV    #-1,MODE1
007066 012702 177777          MOV    #-1,%2
007072 012767 000012 170676  MOV    #12,PSW
007100 074267 177644          XOR    %2,MODE1
007104 016700 170666          MOV    PSW,%0
007110 022700 000004          CMP    #4,%0
007114 001401          BEQ    ,+4
007116 000000          HLT

```

!ERROR! INCORRECT CONDITION CODES

IN=0,Z=1,V=0,C=1

```

007120 010701          SCOPE
007122 012767 177777 177620  MOV    #-1,MODE1
007130 012704 177777          MOV    #-1,%4
007134 012702 006750  MOV    #MODE1,%2
007140 000277          SCC
007142 000244          CLZ
007144 074412          XOR    %4,(2)
007146 016700 170624  MOV    PSW,%0
007152 022700 000005          CMP    #5,%0
007156 001401          BEQ    ,+4
007160 000000          HLT

```

!ERROR! INCORRECT CONDITION CODES

IN=1,Z=0,V=0,C=0

```

007162 010701          SCOPE
007164 012700 177777          MOV    #-1,%0
007170 012767 077777 177552  MOV    #77777,MODE1
007176 012767 000006 170572  MOV    #6,PSW
007204 074067 177540          XOR    %0,MODE1
007210 016700 170562          MOV    PSW,%0
007214 022700 000010          CMP    #10,%0
007220 001401          BEQ    ,+4
007222 000000          HLT

```

!ERROR! INCORRECT CONDITION CODES


```

                                IN=1,Z=0,V=0,C=1
007224 010701                                SCOPE
007226 012700 077777                                MOV      #77777,X0
007232 012767 177777 177510                       MOV      #-1,MODE1
007240 012767 000007 170530                       MOV      #7,PSW
007246 074067 177476                                XOR      X0,MODE1
007252 016700 170520                                MOV      PSW,X0
007256 022700 000011                                CMP      #11,X0
007262 001401                                BEQ     ,+4
007264 000000                                HLT
                                ;ERROR! INCORRECT CONDITION CODES

                                N=0
                                SOURCE=X2
                                ;TEST THAT XOR OPERATES PROPERLY WHEN THE DESTINATION IS THE PSW,
007266 010701                                SCOPE
007270 012767 007322 170510                       MOV      #TBITSET,14 ;SET UP TRACE TRAP VECTOR
007276 005067 170514                               CLR      16           ;TO TRAP TO 'TBITSET'
007302 012702 000377                                MOV      #377,X2
007306 005067 170464                               CLR      PSW         ;CLEAR THE PSW
007312 074267 170460                               XOR      X2,PSW      ;XOR THE #377 INTO A CLEAR PSW
007316 000240                                NOP
007320 000401                                BR       ,+4
007322 000000                                TBITSET;HLT
007324 022767 000357 170444                       CMP      #357,PSW    ;T BIT SET
007332 001401                                BEQ     ,+4          ;DID ALL BUT THE T BIT SET?
007334 000000                                HLT
                                ;PSW IS NOT CLEAR

007336 010701                                SCOPE
007340 012702 000357                                MOV      #357,X2
007344 012767 000357 170424                       MOV      #357,PSW   ;PRE SET THE STATUS WORD
007352 074267 170420                               XOR      X2,PSW
007356 016700 170414                               MOV      PSW,X0     ;GET RESULT
007362 001401                                BEQ     ,+4
007364 000000                                HLT
007366 005267 171406                                ENDI  INC      ICNT   ;ERROR XOR #357,#357 FAILED TO CLEAR PSW
007372 026727 171402 100000                       CMP      ICNT,#100000 ;INCREMENT THE PASS COUNT
007400 001402                                BEQ     DONE        ;GO TO DONE IF 1000 PASSES COMPLETED
007402 000167 171400                                JMP     BEGIN       ;RESTART PROGRAM
007406 012767 000007 170152                                DONE1 MOV      #7,TPBUF  ;RING BELL
007414 105767 170144                                TSTB   TPCSR
007420 100375                                BPL    ,+4
007422 013702 000042                                MOV      #42,X2     ;GET DECTAPE MONITOR RETURN ADDRESS
007426 001404                                BEQ     DONE1      ;DO NOT RETURN IF (42)=0
007430 004712                                JSR     7,(2)       ;RETURN TO DECTAPE MONITOR
007432 000240                                NOP
007434 000240                                NOP
007436 000240                                NOP
007440 000167 171336                                DONE1 JMP     START
                                ;END
000001

```

BEGIN	001006	DEST	= 006200	DISPLA	= 177570	DONE	007406
DONE1	007440	END	007366	HLT	= 000000	ICNT	001000
IDEST	006202	MODE1	006750	MODE27	006266	MODE3	006752
N	000000	PSW	= 177776	RLT	=%000002	SCOPE	= 010701
SOURCE	%000002	START	001002	STKPTR	= 000500	SWR	= 177570
TBITSE	007322	TPBUF	= 177566	TPCSR	= 177564	UBREAK	= 177770
Y	000115	.	= 007444				

ERRORS DETECTED: 0

1

.TITLE MAINDEC-11-DCKBD-B 11/45,11/40 MARK TEST
.NLIST MC;MD,SEQ
.LIST ME
.ABS

TEST DCKBD- TEST OF THE MARK INSTRUCTION
OPERATIONS

TEMP ← PC+2*N
PC ← R5
R6 ← TEMP
R5 ← (TEMP) TEMP HOLDS A MEMORY ADDRESS
R6 ← TEMP+2

STARTING PROCEEDURE

LOAD ADDRESS=200
PRESS START
STACK POINTER IS SET AT 500
BELL WILL RING WHEN TEST IS COMPLETE

EQUATE STATEMENTS:

000000
000001
000002
000003
000004
000005
000006
000007

R0=X0
R1=X1
R2=X2
R3=X3
R4=X4
R5=X5
SP=X6
PC=X7

STACK POINTER
PROGRAM COUNTER

010701
000000
177776
177770
177564
177566
177570
177570

SCOPE=010701
HLT=HALT
PSW=177776
UBREAK=177770
TPCSR=177564
TPBUF=177566
SWR=177570
DISPLAY=177570

MOVE PC TO R1
ERROR HALT
PROCESSOR STATUS WORD ADDRESS
ADDRESS OF PDP11/45 MICRO BREAK REGISTER
ADDRESS OF TELEPRINTER CSR
ADDRESS OF CONSOLE SWITCH REGISTER
ADDRESS OF CONSOLE DISPLAY REGISTER

000500

*****INITIAL STACK POINTER=0500*****
STKPTR=0500 INITIAL STACK SETTING

006400
006401
006402

MARK EQUATES

MARK0=MARK+0
MARK1=MARK+1
MARK2=MARK+2


```

000200 000200      ,=200
000200 000167 000576 JMP      START
001000 001000      ,=1000
001002 005067 177772 ICNT:    0          ;CONTAINS PASS COUNT
001006 012706 000500 START:  CLR      ICNT      ;CLEAR PASS COUNT
001012 016737 177762 177570 BEGIN:  MOV      #STKPTR,SP ;SET STACK POINTER
001020 032737 000400 177570 MOV      ICNT,#DISPLAY ;DISPLAY PASS COUNT
001026 001403 BEQ      #400,#SWR      ;LOAD PDP11/45 MICRO BREAK REGISTER
001030 113737 177570 177770 MOVB     @#SWR,@#UBREAK ;LOAD MICRO BREAK REG WITH SR0=7

;TEST THAT THE OPERATION PC=R5 IS PERFORMED
001036 010701      SCOPE
001040 012706 000500 MOV      #STKPTR,SP ;INITIALIZE THE STACK POINTER
001044 012705 001054 MOV      #T0A,R5    ;LOAD R5
001050 006400 MARK    0          ;MARK N=0
001052 000000 HALT
001054 000240 T0A:   NOP          ;PC=R5 FAILED
                                ;PC=R5 OK

;TEST THAT THE STACK POINTER (R6) CONTAINS THE PROPER VALUE
001056 010701      SCOPE
001060 012706 000500 MOV      #STKPTR,SP ;INITIALIZE THE STACK POINTER
001064 012705 001072 MOV      #T1A,R5    ;LOAD R5
001070 006400 MARK    0          ;MARK N=0

;FUNCTION RESULTS N=0
ITEMP = PC*(2*N)      TEMP = #T1A+(2*N) = #T1A
IPC = R5              PC = #T1A
ISP = TEMP            SP = #T1A+(2*N) = #T1A
IR5 = (TEMP)         R5 = (#T1A+(2*N)) = (T1A) NOT TESTED
ISP = TEMP*2         SP = #T1A+(2*N)*2 = #T1A*2 TESTED

001072 010600      T1A:  MOV      SP,R0          ;GET RESULTANT SP
001074 022706 001074 CMP      #2*0+2*T1A,SP ;SP = #T1A*(2*0)+2? NOTE:
001100 001401 BEQ      ,+4          ;#2*0+2*T1A = #T1A*(2*0)+2
001102 000000 HLT          ;ERROR INCORRECT RESULTANT SP

;TEST THAT R5 IS PROPERLY LOADED
001104 010701      SCOPE
001106 012706 000500 MOV      #STKPTR,SP
001112 012705 001120 MOV      #T2A,R5
001116 006400 MARK    0

;FUNCTION RESULTS N=0
ITEMP = PC*(2*N)      TEMP = #T2A+(2*N) = #T2A
IPC = R5              PC = #T2A
ISP = TEMP            SP = #T2A+(2*N) = #T2A
IR5 = (TEMP)         R5 = (#T2A+(2*N)) = (T2A) = 240 (NOP)
ISP = TEMP*2         SP = #T2A+(2*N)*2 = #T2A*2

001120 000240      T2A:  NOP
001122 010500 MOV      R5,R0          ;GET CONTENTS OF R5
001124 022705 000240 CMP      #NOP,R5       ;R5 = (#T2A*(2*N)) = (T2A)?

```

001130 001401
001132 000000
000003
000000

REQ .+4
HLT
C=3
M=0

ITEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
IINSTRUCTION FOR ALL VALUES OF N (0-77)

001134 010701
001136 012706 000500
001142 012705 001150
001146 006400

SCOPE
MOV #STKPTR,SP IINITIALIZE THE STACK POINTER
MOV #T3A,R5 ILOAD R5
MARK 0 IMARK N=0

IFUNCTION RESULTS N=0
ITEMP = PC*(2*N) TEMP = #T3A+(2*0)
IPC = R5 PC = T3A
ISP = TEMP SP = #T3A+(2*0)
IR5 = (TEMP) R5 = (#T3A+(2*0))
ISP = TEMP+2 SP = #T3A+(2*0)+2

001150 010600
001152 022706 001152
001156 001401
001160 000000

000004
000001

T3A: MOV SP,R0 IGET RESULTANT SP
CMP #2*0+T3A+2,SP ISP = #T3A+(2*0)+2?
BEQ .+4
HLT IERROR INCORRECT RESULTANT SP

C=C+1
M=M+1
ITEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
IINSTRUCTION FOR ALL VALUES OF N (0-77)

001162 010701
001164 012706 000500
001170 012705 001176
001174 006401

SCOPE
MOV #STKPTR,SP IINITIALIZE THE STACK POINTER
MOV #T4A,R5 ILOAD R5
MARK 1 IMARK N=1

IFUNCTION RESULTS N=1
ITEMP = PC*(2*N) TEMP = #T4A+(2*1)
IPC = R5 PC = T4A
ISP = TEMP SP = #T4A+(2*1)
IR5 = (TEMP) R5 = (#T4A+(2*1))
ISP = TEMP+2 SP = #T4A+(2*1)+2

001176 010600
001200 022706 001202
001204 001401
001206 000000

000005
000002

T4A: MOV SP,R0 IGET RESULTANT SP
CMP #2*1+T4A+2,SP ISP = #T4A+(2*1)+2?
BEQ .+4
HLT IERROR INCORRECT RESULTANT SP

C=C+1
M=M+1
ITEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
IINSTRUCTION FOR ALL VALUES OF N (0-77)

001210 010701
001212 012706 000500

SCOPE
MOV #STKPTR,SP IINITIALIZE THE STACK POINTER

001216 012705 001224
001222 006402

MOV #T5A,R5 ILOAD R5
MARK 2 IMARK N=2

```

*****
)FUNCTION RESULTS N=2
)TEMP = PC*(2*N)          TEMP = #T5A+(2*2)
)PC = R5                  PC = T5A
)SP = TEMP                SP = #T5A+(2*2)
)R5 = (TEMP)              R5 = (#T5A+(2*2))
)SP = TEMP*2              SP = #T5A+(2*2)*2
*****
    
```

001224 010600
001226 022706 001232
001232 001401
001234 000000

T5A: MOV SP,R0 IGET RESULTANT SP
CMP #2*2+T5A+2,SP ISP = #T5A+(2*2)+2?
BEQ ,+4
HLT IERROR INCORRECT RESULTANT SP

000006
000003

C=C+1
M=M+1

)TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
)INSTRUCTION FOR ALL VALUES OF N (0-77)

001236 010701
001240 012706 000500
001244 012705 001252
001250 006403

SCOPE
MOV #STKPTR,SP IINITIALIZE THE STACK POINTER
MOV #T6A,R5 ILOAD R5
MARK 3 IMARK N=3

```

*****
)FUNCTION RESULTS N=3
)TEMP = PC*(2*N)          TEMP = #T6A+(2*3)
)PC = R5                  PC = T6A
)SP = TEMP                SP = #T6A+(2*3)
)R5 = (TEMP)              R5 = (#T6A+(2*3))
)SP = TEMP*2              SP = #T6A+(2*3)*2
*****
    
```

001252 010600
001254 022706 001262
001260 001401
001262 000000

T6A: MOV SP,R0 IGET RESULTANT SP
CMP #2*3+T6A+2,SP ISP = #T6A+(2*3)+2?
BEQ ,+4
HLT IERROR INCORRECT RESULTANT SP

000007
000004

C=C+1
M=M+1

)TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
)INSTRUCTION FOR ALL VALUES OF N (0-77)

001264 010701
001266 012706 000500
001272 012705 001300
001276 006404

SCOPE
MOV #STKPTR,SP IINITIALIZE THE STACK POINTER
MOV #T7A,R5 ILOAD R5
MARK 4 IMARK N=4

```

*****
)FUNCTION RESULTS N=4
)TEMP = PC*(2*N)          TEMP = #T7A+(2*4)
)PC = R5                  PC = T7A
)SP = TEMP                SP = #T7A+(2*4)
*****
    
```

```

;R5 = (TEMP)          R5 = (#T7A+(2*4))
;SP = TEMP+2          SP = #T7A+(2*4)+2
;*****

001300 010600          T7A:  MOV     SP,R0          ;GET RESULTANT SP
001302 022706 001312  CMP     #2*4+T7A+2,SP  ;SP = #T7A+(2*4)+2?
001306 001401          BEQ     ,+4
001310 000000          HLT                    ;ERROR INCORRECT RESULTANT SP

          C=C+1
          M=M+1
;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)
          SCOPE
001312 010701          MOV     #STKPTR,SP      ;INITIALIZE THE STACK POINTER
001314 012706 000500  MOV     #T10A,R5        ;LOAD R5
001320 012705 001326  MARK    5              ;MARK N=5
001324 006405

;*****
;FUNCTION RESULTS N=5
;TEMP = PC+(2*N)      TEMP = #T10A+(2*5)
;PC = R5              PC = T10A
;SP = TEMP            SP = #T10A+(2*5)
;R5 = (TEMP)         R5 = (#T10A+(2*5))
;SP = TEMP+2         SP = #T10A+(2*5)+2
;*****

001326 010600          T10A: MOV    SP,R0          ;GET RESULTANT SP
001330 022706 001342  CMP     #2*5+T10A+2,SP  ;SP = #T10A+(2*5)+2?
001334 001401          BEQ     ,+4
001336 000000          HLT                    ;ERROR INCORRECT RESULTANT SP

          C=C+1
          M=M+1
;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)
          SCOPE
001340 010701          MOV     #STKPTR,SP      ;INITIALIZE THE STACK POINTER
001342 012706 000500  MOV     #T11A,R5        ;LOAD R5
001346 012705 001354  MARK    6              ;MARK N=6
001352 006406

;*****
;FUNCTION RESULTS N=6
;TEMP = PC+(2*N)      TEMP = #T11A+(2*6)
;PC = R5              PC = T11A
;SP = TEMP            SP = #T11A+(2*6)
;R5 = (TEMP)         R5 = (#T11A+(2*6))
;SP = TEMP+2         SP = #T11A+(2*6)+2
;*****

001354 010600          T11A: MOV    SP,R0          ;GET RESULTANT SP
001356 022706 001372  CMP     #2*6+T11A+2,SP  ;SP = #T11A+(2*6)+2?
001362 001401          BEQ     ,+4
001364 000000          HLT                    ;ERROR INCORRECT RESULTANT SP

```

000012
000007

001366 010701
001370 012706 000500
001374 012705 001402
001400 006407

C=C+1
M=M+1
;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T12A,R5 ;LOAD R5
MARK 7 ;MARK N=7

;FUNCTION RESULTS N=7
;TEMP = PC+(2*N) TEMP= #T12A+(2*7)
;PC = R5 PC = T12A
;SP = TEMP SP = #T12A+(2*7)
;R5 = (TEMP) R5 = (#T12A+(2*7))
;SP = TEMP+2 SP = #T12A+(2*7)+2

001402 010600
001404 022706 001422
001410 001401
001412 000000

T12A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*7+T12A+2,SP ;SP = #T12A+(2*7)+2?
BEQ ,+4
HLT ;ERROR INCORRECT RESULTANT SP

000013
000010

001414 010701
001416 012706 000500
001422 012705 001430
001426 006410

C=C+1
M=M+1
;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T13A,R5 ;LOAD R5
MARK 10 ;MARK N=10

;FUNCTION RESULTS N=10
;TEMP = PC+(2*N) TEMP= #T13A+(2*10)
;PC = R5 PC = T13A
;SP = TEMP SP = #T13A+(2*10)
;R5 = (TEMP) R5 = (#T13A+(2*10))
;SP = TEMP+2 SP = #T13A+(2*10)+2

001430 010600
001432 022706 001452
001436 001401
001440 000000

T13A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*10+T13A+2,SP ;SP = #T13A+(2*10)+2?
BEQ ,+4
HLT ;ERROR INCORRECT RESULTANT SP

000014
000011

001442 010701
001444 012706 000500
001450 012705 001456

C=C+1
M=M+1
;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T14A,R5 ;LOAD R5

001454 006411

MARK 11

MARK N=11

```
*****  
FUNCTION RESULTS N=11  
TEMP = PC*(2*N)      TEMP = #T14A*(2*11)  
IPC = R5              PC = T14A  
ISP = TEMP            SP = #T14A*(2*11)  
R5 = (TEMP)          R5 = (#T14A*(2*11))  
ISP = TEMP+2         SP = #T14A*(2*11)+2  
*****
```

001456 010600
001460 022706 001502
001464 001401
001466 000000

```
T14A:  MOV    SP,R0      IGET RESULTANT SP  
      CMP    #2*11+T14A+2,SP  ISP = #T14A*(2*11)+2?  
      BEQ    ,+4  
      HLT  
      IERROR INCORRECT RESULTANT SP
```

000015
000012

```
C=C+1  
M=M+1  
ITEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)
```

001470 010701
001472 012706 000500
001476 012705 001504
001502 006412

```
SCOPE  
MOV    #STKPTR,SP      IINITIALIZE THE STACK POINTER  
MOV    #T15A,R5        ILOAD R5  
MARK   12              IMARK N=12
```

```
*****  
FUNCTION RESULTS N=12  
TEMP = PC*(2*N)      TEMP = #T15A*(2*12)  
IPC = R5              PC = T15A  
ISP = TEMP            SP = #T15A*(2*12)  
R5 = (TEMP)          R5 = (#T15A*(2*12))  
ISP = TEMP+2         SP = #T15A*(2*12)+2  
*****
```

001504 010600
001506 022706 001532
001512 001401
001514 000000

```
T15A:  MOV    SP,R0      IGET RESULTANT SP  
      CMP    #2*12+T15A+2,SP  ISP = #T15A*(2*12)+2?  
      BEQ    ,+4  
      HLT  
      IERROR INCORRECT RESULTANT SP
```

000016
000013

```
C=C+1  
M=M+1  
ITEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)
```

001516 010701
001520 012706 000500
001524 012705 001532
001530 006413

```
SCOPE  
MOV    #STKPTR,SP      IINITIALIZE THE STACK POINTER  
MOV    #T16A,R5        ILOAD R5  
MARK   13              IMARK N=13
```

```
*****  
FUNCTION RESULTS N=13  
TEMP = PC*(2*N)      TEMP = #T16A*(2*13)  
IPC = R5              PC = T16A  
ISP = TEMP            SP = #T16A*(2*13)  
R5 = (TEMP)          R5 = (#T16A*(2*13))  
*****
```

```

)SP * TEMP+2          SP = #T16A+(2*13)+2
)*****

001532 010600          T16A: MOV SP,R0          )GET RESULTANT SP
001534 022706 001562  CMP #2*13+T16A+2,SP )SP = #T16A+(2*13)+2?
001540 001401          BEQ ,+4
001542 000000          HLT          )ERROR INCORRECT RESULTANT SP

          C=C+1
          M=M+1

)TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
)INSTRUCTION FOR ALL VALUES OF N (0-77)
          SCOPE
001544 010701          MOV #STKPTR,SP      )INITIALIZE THE STACK POINTER
001546 012706 000500  MOV #T17A,R5        )LOAD R5
001552 012705 001560  MARK 14              )MARK N=14
001556 006414

)*****
)FUNCTION RESULTS N=14
)TEMP * PC*(2*N)      TEMP= #T17A+(2*14)
)PC * R5              PC = T17A
)SP * TEMP            SP = #T17A+(2*14)
)R5 * (TEMP)          R5 = (#T17A*(2*14))
)SP * TEMP+2          SP = #T17A+(2*14)+2
)*****

001560 010600          T17A: MOV SP,R0          )GET RESULTANT SP
001562 022706 001612  CMP #2*14+T17A+2,SP )SP = #T17A+(2*14)+2?
001566 001401          BEQ ,+4
001570 000000          HLT          )ERROR INCORRECT RESULTANT SP

          C=C+1
          M=M+1

)TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
)INSTRUCTION FOR ALL VALUES OF N (0-77)
          SCOPE
001572 010701          MOV #STKPTR,SP      )INITIALIZE THE STACK POINTER
001574 012706 000500  MOV #T20A,R5        )LOAD R5
001600 012705 001606  MARK 15              )MARK N=15
001604 006415

)*****
)FUNCTION RESULTS N=15
)TEMP * PC*(2*N)      TEMP= #T20A+(2*15)
)PC * R5              PC = T20A
)SP * TEMP            SP = #T20A+(2*15)
)R5 * (TEMP)          R5 = (#T20A*(2*15))
)SP * TEMP+2          SP = #T20A+(2*15)+2
)*****

001606 010600          T20A: MOV SP,R0          )GET RESULTANT SP
001610 022706 001642  CMP #2*15+T20A+2,SP )SP = #T20A+(2*15)+2?
001614 001401          BEQ ,+4
001616 000000          HLT          )ERROR INCORRECT RESULTANT SP

```

```

000021          C=C+1
000016          M=M+1
)TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
)INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV      #STKPTR,SP      )INITIALIZE THE STACK POINTER
MOV      #T21A,R5        )LOAD R5
MARK     16              )MARK N=16

)*****
)FUNCTION RESULTS  N=16
)TEMP = PC+(2*N)      TEMP= #T21A+(2*16)
)PC = R5              PC = T21A
)SP = TEMP            SP = #T21A+(2*16)
)R5 = (TEMP)          R5 = (#T21A+(2*16))
)SP = TEMP+2          SP = #T21A+(2*16)+2
)*****

001634  010600          T21A:  MOV      SP,R0      )GET RESULTANT SP
001636  022706          )CMP      #2*16+T21A+2,SP )SP = #T21A+(2*16)+27
001642  001401          )BEQ     ,+4
001644  000000          )HLT
)ERROR INCORRECT RESULTANT SP

000022          C=C+1
000017          M=M+1
)TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
)INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV      #STKPTR,SP      )INITIALIZE THE STACK POINTER
MOV      #T22A,R5        )LOAD R5
MARK     17              )MARK N=17

)*****
)FUNCTION RESULTS  N=17
)TEMP = PC+(2*N)      TEMP= #T22A+(2*17)
)PC = R5              PC = T22A
)SP = TEMP            SP = #T22A+(2*17)
)R5 = (TEMP)          R5 = (#T22A+(2*17))
)SP = TEMP+2          SP = #T22A+(2*17)+2
)*****

001662  010600          T22A:  MOV      SP,R0      )GET RESULTANT SP
001664  022706          )CMP      #2*17+T22A+2,SP )SP = #T22A+(2*17)+27
001670  001401          )BEQ     ,+4
001672  000000          )HLT
)ERROR INCORRECT RESULTANT SP

000023          C=C+1
000020          M=M+1
)TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
)INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV      #STKPTR,SP      )INITIALIZE THE STACK POINTER
MOV      #T23A,R5        )LOAD R5
MARK     20              )MARK N=20

```

```
*****  
;FUNCTION RESULTS N=20  
;TEMP ← PC*(2*N)          TEMP= #T23A+(2*20)  
;PC ← R5                  PC = T23A  
;SP ← TEMP                SP = #T23A+(2*20)  
;R5 ← (TEMP)              R5 = (#T23A+(2*20))  
;SP ← TEMP+2              SP = #T23A+(2*20)+2  
*****
```

001710 010600
001712 022706 001752
001716 001401
001720 000000

000024
000021

```
T23A:  MOV    SP,R0          ;GET RESULTANT SP  
      CMP    #2*20+T23A+2,SP  ;SP = #T23A+(2*20)+2?  
      BEQ    .+4  
      HLT                    ;ERROR INCORRECT RESULTANT SP
```

C=C+1
M=M+1

;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)

001722 010701
001724 012706 000500
001730 012705 001736
001734 006421

```
SCOPE  
MOV    #STKPTR,SP          ;INITIALIZE THE STACK POINTER  
MOV    #T24A,R5           ;LOAD R5  
MARK   21                  ;MARK N=21
```

```
*****  
;FUNCTION RESULTS N=21  
;TEMP ← PC*(2*N)          TEMP= #T24A+(2*21)  
;PC ← R5                  PC = T24A  
;SP ← TEMP                SP = #T24A+(2*21)  
;R5 ← (TEMP)              R5 = (#T24A+(2*21))  
;SP ← TEMP+2              SP = #T24A+(2*21)+2  
*****
```

001736 010600
001740 022706 002002
001744 001401
001746 000000

000025
000022

```
T24A:  MOV    SP,R0          ;GET RESULTANT SP  
      CMP    #2*21+T24A+2,SP  ;SP = #T24A+(2*21)+2?  
      BEQ    .+4  
      HLT                    ;ERROR INCORRECT RESULTANT SP
```

C=C+1
M=M+1

;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)

001750 010701
001752 012706 000500
001756 012705 001764
001762 006422

```
SCOPE  
MOV    #STKPTR,SP          ;INITIALIZE THE STACK POINTER  
MOV    #T25A,R5           ;LOAD R5  
MARK   22                  ;MARK N=22
```

```
*****  
;FUNCTION RESULTS N=22  
;TEMP ← PC*(2*N)          TEMP= #T25A+(2*22)  
;PC ← R5                  PC = T25A  
;SP ← TEMP                SP = #T25A+(2*22)  
;R5 ← (TEMP)              R5 = (#T25A+(2*22))  
;SP ← TEMP+2              SP = #T25A+(2*22)+2  
*****
```

```

;*****
001764 010600      T25A:  MOV    SP,R0      ;GET RESULTANT SP
001766 022706 002032  CMP    #2*22+T25A+2,SP ;SP = #T25A+(2*22)*2?
001772 001401      BEQ    ,+4
001774 000000      HLT                    ;ERROR INCORRECT RESULTANT SP

      C=C+1
      M=M+1
  
```

;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
 ;INSTRUCTION FOR ALL VALUES OF N (0-77)

```

001776 010701      SCOPE
002000 012706 000500  MOV    #STKPTR,SP      ;INITIALIZE THE STACK POINTER
002004 012705 002012  MOV    #T26A,R5        ;LOAD R5
002010 006423      MARK    23            ;MARK N=23
  
```

```

;*****
;FUNCTION RESULTS N=23
;TEMP ← PC+(2*N)      TEMP = #T26A+(2*23)
;PC ← R5              PC = T26A
;SP ← TEMP            SP = #T26A+(2*23)
;R5 ← (TEMP)          R5 = (#T26A+(2*23))
;SP ← TEMP+2          SP = #T26A+(2*23)+2
;*****
  
```

```

002012 010600      T26A:  MOV    SP,R0      ;GET RESULTANT SP
002014 022706 002062  CMP    #2*23+T26A+2,SP ;SP = #T26A+(2*23)*2?
002020 001401      BEQ    ,+4
002022 000000      HLT                    ;ERROR INCORRECT RESULTANT SP

      C=C+1
      M=M+1
  
```

;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
 ;INSTRUCTION FOR ALL VALUES OF N (0-77)

```

002024 010701      SCOPE
002026 012706 000500  MOV    #STKPTR,SP      ;INITIALIZE THE STACK POINTER
002032 012705 002040  MOV    #T27A,R5        ;LOAD R5
002036 006424      MARK    24            ;MARK N=24
  
```

```

;*****
;FUNCTION RESULTS N=24
;TEMP ← PC+(2*N)      TEMP = #T27A+(2*24)
;PC ← R5              PC = T27A
;SP ← TEMP            SP = #T27A+(2*24)
;R5 ← (TEMP)          R5 = (#T27A+(2*24))
;SP ← TEMP+2          SP = #T27A+(2*24)+2
;*****
  
```

```

002040 010600      T27A:  MOV    SP,R0      ;GET RESULTANT SP
002042 022706 002112  CMP    #2*24+T27A+2,SP ;SP = #T27A+(2*24)*2?
002046 001401      BEQ    ,+4
002050 000000      HLT                    ;ERROR INCORRECT RESULTANT SP

      C=C+1
  
```



```

000025          M=M+1
                )TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
                )INSTRUCTION FOR ALL VALUES OF N (0-77)
                SCOPE
002052 010701   MOV      #STKPTR,SP      )INITIALIZE THE STACK POINTER
002054 012706   MOV      #T30A,R5        )LOAD R5
002060 012705   MARK     25          )MARK N=25
002064 006425

)*****
)FUNCTION RESULTS N=25
)TEMP = PC+(2*N)      TEMP= #T30A+(2*25)
)PC = R5              PC = T30A
)SP = TEMP            SP = #T30A+(2*25)
)R5 = (TEMP)          R5 = (#T30A+(2*25))
)SP = TEMP+2          SP = #T30A+(2*25)+2
)*****

002066 010600   T30A1  MOV      SP,R0          )GET RESULTANT SP
002070 022706   CMP      #2*25+T30A+2,SP     )SP = #T30A+(2*25)+2?
002074 001401   BEQ     ,+4
002076 000000   HLT
                                )ERROR INCORRECT RESULTANT SP

                C=C+1
                M=M+1
                )TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
                )INSTRUCTION FOR ALL VALUES OF N (0-77)
                SCOPE
002100 010701   MOV      #STKPTR,SP      )INITIALIZE THE STACK POINTER
002102 012706   MOV      #T31A,R5        )LOAD R5
002106 012705   MARK     26          )MARK N=26
002112 006426

)*****
)FUNCTION RESULTS N=26
)TEMP = PC+(2*N)      TEMP= #T31A+(2*26)
)PC = R5              PC = T31A
)SP = TEMP            SP = #T31A+(2*26)
)R5 = (TEMP)          R5 = (#T31A+(2*26))
)SP = TEMP+2          SP = #T31A+(2*26)+2
)*****

002114 010600   T31A1  MOV      SP,R0          )GET RESULTANT SP
002116 022706   CMP      #2*26+T31A+2,SP     )SP = #T31A+(2*26)+2?
002122 001401   BEQ     ,+4
002124 000000   HLT
                                )ERROR INCORRECT RESULTANT SP

                C=C+1
                M=M+1
                )TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
                )INSTRUCTION FOR ALL VALUES OF N (0-77)
                SCOPE
002126 010701   MOV      #STKPTR,SP      )INITIALIZE THE STACK POINTER
002130 012706   MOV      #T32A,R5        )LOAD R5
002134 012705   MARK     27          )MARK N=27
002140 006427

```

```

*****
;FUNCTION RESULTS N=27
;TEMP = PC*(2*N)          TEMP= #T32A*(2*27)
;PC = R5                  PC = T32A
;SP = TEMP                SP = #T32A+(2*27)
;R5 = (TEMP)              R5 = (#T32A+(2*27))
;SP = TEMP+2              SP = #T32A+(2*27)+2
*****

```

002142 010600
002144 022706 002222
002150 001401
002152 000000

```

T32A:  MOV    SP,R0          ;GET RESULTANT SP
        CMP    #2*27+T32A+2,SP  ;SP = #T32A+(2*27)+2?
        BEQ    ,+4
        HLT                    ;ERROR INCORRECT RESULTANT SP

```

000033
000030

```

        C=C+1
        M=M+1
;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)

```

002154 010701
002156 012706 000500
002162 012705 002170
002166 006430

```

        SCOPE
        MOV    #STKPTR,SP      ;INITIALIZE THE STACK POINTER
        MOV    #T33A,R5       ;LOAD R5
        MARK   30             ;MARK N=30

```

```

*****
;FUNCTION RESULTS N=30
;TEMP = PC*(2*N)          TEMP= #T33A*(2*30)
;PC = R5                  PC = T33A
;SP = TEMP                SP = #T33A+(2*30)
;R5 = (TEMP)              R5 = (#T33A+(2*30))
;SP = TEMP+2              SP = #T33A+(2*30)+2
*****

```

002170 010600
002172 022706 002252
002176 001401
002200 000000

```

T33A:  MOV    SP,R0          ;GET RESULTANT SP
        CMP    #2*30+T33A+2,SP  ;SP = #T33A+(2*30)+2?
        BEQ    ,+4
        HLT                    ;ERROR INCORRECT RESULTANT SP

```

000034
000031

```

        C=C+1
        M=M+1
;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)

```

002202 010701
002204 012706 000500
002210 012705 002216
002214 006431

```

        SCOPE
        MOV    #STKPTR,SP      ;INITIALIZE THE STACK POINTER
        MOV    #T34A,R5       ;LOAD R5
        MARK   31             ;MARK N=31

```

```

*****
;FUNCTION RESULTS N=31
;TEMP = PC*(2*N)          TEMP= #T34A*(2*31)
;PC = R5                  PC = T34A
;SP = TEMP                SP = #T34A+(2*31)
;R5 = (TEMP)              R5 = (#T34A+(2*31))
;SP = TEMP+2              SP = #T34A+(2*31)+2
*****

```

002216 010600
002220 022706 002302
002224 001401
002226 000000

000035
000032

T34A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*31+T34A+2,SP ;SP = #T34A+(2*31)+2?
BEQ ,+4
HLT ;ERROR INCORRECT RESULTANT SP

C=C+1
M=M+1

TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)

002230 010701
002232 012706 000500
002236 012705 002244
002242 006432

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T35A,R5 ;LOAD R5
MARK 32 ;MARK N=32

FUNCTION RESULTS N=32
TEMP = PC*(2*N) TEMP = #T35A+(2*32)
PC = R5 PC = T35A
SP = TEMP SP = #T35A+(2*32)
R5 = (TEMP) R5 = (#T35A+(2*32))
SP = TEMP+2 SP = #T35A+(2*32)+2

002244 010600
002246 022706 002332
002252 001401
002254 000000

000036
000033

T35A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*32+T35A+2,SP ;SP = #T35A+(2*32)+2?
BEQ ,+4
HLT ;ERROR INCORRECT RESULTANT SP

C=C+1
M=M+1

TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)

002256 010701
002260 012706 000500
002264 012705 002272
002270 006432

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T36A,R5 ;LOAD R5
MARK 33 ;MARK N=33

FUNCTION RESULTS N=33
TEMP = PC*(2*N) TEMP = #T36A+(2*33)
PC = R5 PC = T36A
SP = TEMP SP = #T36A+(2*33)
R5 = (TEMP) R5 = (#T36A+(2*33))
SP = TEMP+2 SP = #T36A+(2*33)+2

002272 010600
002274 022706 002362
002300 001401
002302 000000

000037
000034

T36A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*33+T36A+2,SP ;SP = #T36A+(2*33)+2?
BEQ ,+4
HLT ;ERROR INCORRECT RESULTANT SP

C=C+1
M=M+1

TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)

002304 010701
002306 012706 000500
002312 012705 002320
002316 006434

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T37A,R5 ;LOAD R5
MARK 34 ;MARK N=34

FUNCTION RESULTS N=34
TEMP = PC*(2*N) TEMP = #T37A*(2*34)
IPC = R5 PC = T37A
JSP = TEMP SP = #T37A*(2*34)
JR5 = (TEMP) R5 = (#T37A*(2*34))
JSP = TEMP+2 SP = #T37A*(2*34)+2

002320 010600
002322 022706 002412
002326 001401
002330 000000

T37A: MOV SP,R0 ;GET RESULTANT SP
 CMP #2*34+T37A+2,SP ;SP = #T37A*(2*34)+2?
 BEQ ,+4
 HLT ;ERROR INCORRECT RESULTANT SP

000040
000035

C=C+1
M=M+1

TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)

002332 010701
002334 012706 000500
002340 012705 002346
002344 006435

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T40A,R5 ;LOAD R5
MARK 35 ;MARK N=35

FUNCTION RESULTS N=35
TEMP = PC*(2*N) TEMP = #T40A*(2*35)
IPC = R5 PC = T40A
JSP = TEMP SP = #T40A*(2*35)
JR5 = (TEMP) R5 = (#T40A*(2*35))
JSP = TEMP+2 SP = #T40A*(2*35)+2

002346 010600
002350 022706 002442
002354 001401
002356 000000

T40A: MOV SP,R0 ;GET RESULTANT SP
 CMP #2*35+T40A+2,SP ;SP = #T40A*(2*35)+2?
 BEQ ,+4
 HLT ;ERROR INCORRECT RESULTANT SP

000041
000036

C=C+1
M=M+1

TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)

002360 010701
002362 012706 000500
002366 012705 002374
002372 006436

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T41A,R5 ;LOAD R5
MARK 36 ;MARK N=36

```

)FUNCTION RESULTS N=36
)TEMP ← PC*(2*N)          TEMP= #T41A+(2*36)
)PC ← R5                  PC = T41A
)SP ← TEMP                SP = #T41A+(2*36)
)R5 ← (TEMP)              R5 = (#T41A+(2*36))
)SP ← TEMP+2              SP = #T41A+(2*36)+2
)*****

002374 010600
002376 022706 002472
002402 001401
002404 000000

T41A: MOV SP,R0           )GET RESULTANT SP
      CMP #2*36+T41A+2,SP )SP = #T41A+(2*36)+2?
      BEQ ,+4
      HLT                )ERROR INCORRECT RESULTANT SP

      C=C+1
      M=M+1

)TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
)INSTRUCTION FOR ALL VALUES OF N (0-77)
      SCOPE
      MOV #STKPTR,SP     )INITIALIZE THE STACK POINTER
      MOV #T42A,R5       )LOAD R5
      MARK 37            )MARK N=37

)*****
)FUNCTION RESULTS N=37
)TEMP ← PC*(2*N)          TEMP= #T42A+(2*37)
)PC ← R5                  PC = T42A
)SP ← TEMP                SP = #T42A+(2*37)
)R5 ← (TEMP)              R5 = (#T42A+(2*37))
)SP ← TEMP+2              SP = #T42A+(2*37)+2
)*****

002422 010600
002424 022706 002522
002430 001401
002432 000000

T42A: MOV SP,R0           )GET RESULTANT SP
      CMP #2*37+T42A+2,SP )SP = #T42A+(2*37)+2?
      BEQ ,+4
      HLT                )ERROR INCORRECT RESULTANT SP

      C=C+1
      M=M+1

)TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
)INSTRUCTION FOR ALL VALUES OF N (0-77)
      SCOPE
      MOV #STKPTR,SP     )INITIALIZE THE STACK POINTER
      MOV #T43A,R5       )LOAD R5
      MARK 40            )MARK N=40

)*****
)FUNCTION RESULTS N=40
)TEMP ← PC*(2*N)          TEMP= #T43A+(2*40)
)PC ← R5                  PC = T43A
)SP ← TEMP                SP = #T43A+(2*40)
)R5 ← (TEMP)              R5 = (#T43A+(2*40))
)SP ← TEMP+2              SP = #T43A+(2*40)+2
)*****

```

002450 010600
002452 022706 002552
002456 001401
002460 000000

T43A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*40+T43A+2,SP ;ISP = #T43A+(2*40)+2?
BEQ .+4
HLT ;ERROR INCORRECT RESULTANT SP

000044
000041

C=C+1
M=M+1
;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)

002462 010701
002464 012706 000500
002470 012705 002476
002474 006441

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T44A,R5 ;LOAD R5
MARK 41 ;MARK N=41

```

|*****
|FUNCTION RESULTS N=41
|TEMP = PC*(2*N)          TEMP = #T44A+(2*41)
|PC = R5                  PC = T44A
|ISP = TEMP               SP = #T44A+(2*41)
|R5 = (TEMP)              R5 = (#T44A+(2*41))
|ISP = TEMP+2            SP = #T44A+(2*41)+2
|*****
    
```

002476 010600
002500 022706 002602
002504 001401
002506 000000

T44A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*41+T44A+2,SP ;ISP = #T44A+(2*41)+2?
BEQ .+4
HLT ;ERROR INCORRECT RESULTANT SP

000045
000042

C=C+1
M=M+1
;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)

002510 010701
002512 012706 000500
002516 012705 002524
002522 006442

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T45A,R5 ;LOAD R5
MARK 42 ;MARK N=42

```

|*****
|FUNCTION RESULTS N=42
|TEMP = PC*(2*N)          TEMP = #T45A+(2*42)
|PC = R5                  PC = T45A
|ISP = TEMP               SP = #T45A+(2*42)
|R5 = (TEMP)              R5 = (#T45A+(2*42))
|ISP = TEMP+2            SP = #T45A+(2*42)+2
|*****
    
```

002524 010600
002526 022706 002632
002532 001401
002534 000000

T45A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*42+T45A+2,SP ;ISP = #T45A+(2*42)+2?
BEQ .+4
HLT ;ERROR INCORRECT RESULTANT SP

000046
000043

C=C+1
M=M+1
;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK

```
002536 010701
002540 012706 000500
002544 012705 002552
002550 006443

;INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T46A,R5 ;LOAD R5
MARK 43 ;MARK N=43

;*****
;FUNCTION RESULTS N=43
;TEMP = PC+(2*N) TEMP= #T46A+(2*43)
;PC = R5 PC = T46A
;SP = TEMP SP = #T46A+(2*43)
;R5 = (TEMP) R5 = (#T46A+(2*43))
;SP = TEMP+2 SP = #T46A+(2*43)+2
;*****

002552 010600
002554 022706 002662
002560 001401
002562 000000

T46A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*43+T46A+2,SP ;SP = #T46A+(2*43)+2?
BEQ ;+4
HLT ;ERROR INCORRECT RESULTANT SP

C=C+1
M=M+1

;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T47A,R5 ;LOAD R5
MARK 44 ;MARK N=44

;*****
;FUNCTION RESULTS N=44
;TEMP = PC+(2*N) TEMP= #T47A+(2*44)
;PC = R5 PC = T47A
;SP = TEMP SP = #T47A+(2*44)
;R5 = (TEMP) R5 = (#T47A+(2*44))
;SP = TEMP+2 SP = #T47A+(2*44)+2
;*****

002600 010600
002602 022706 002712
002606 001401
002610 000000

T47A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*44+T47A+2,SP ;SP = #T47A+(2*44)+2?
BEQ ;+4
HLT ;ERROR INCORRECT RESULTANT SP

C=C+1
M=M+1

;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T50A,R5 ;LOAD R5
MARK 45 ;MARK N=45

;*****
;FUNCTION RESULTS N=45
```

```

;TEMP ← PC*(2*N)          TEMP= #T50A+(2*45)      *
;PC ← R5                  PC = T50A              *
;SP ← TEMP                SP = #T50A+(2*45)      *
;R5 ← (TEMP)              R5 = (#T50A+(2*45))    *
;SP ← TEMP+2              SP = #T50A+(2*45)+2    *
;*****

002626 010600             T50A:  MOV     SP,R0          ;GET RESULTANT SP
002630 022706 002742     CMP     #2*45+T50A+2,SP ;SP = #T50A+(2*45)+2?
002634 001401             BEQ     ,+4
002636 000000             HLT
                                ;ERROR INCORRECT RESULTANT SP

                                C=C+1
                                M=M+1

;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV     #STKPTR,SP          ;INITIALIZE THE STACK POINTER
MOV     #T51A,R5           ;LOAD R5
MARK    46                 ;MARK N=46

;*****
;FUNCTION RESULTS N=46
;TEMP ← PC*(2*N)          TEMP= #T51A+(2*46)      *
;PC ← R5                  PC = T51A              *
;SP ← TEMP                SP = #T51A+(2*46)      *
;R5 ← (TEMP)              R5 = (#T51A+(2*46))    *
;SP ← TEMP+2              SP = #T51A+(2*46)+2    *
;*****

002654 010600             T51A:  MOV     SP,R0          ;GET RESULTANT SP
002656 022706 002772     CMP     #2*46+T51A+2,SP ;SP = #T51A+(2*46)+2?
002662 001401             BEQ     ,+4
002664 000000             HLT
                                ;ERROR INCORRECT RESULTANT SP

                                C=C+1
                                M=M+1

;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV     #STKPTR,SP          ;INITIALIZE THE STACK POINTER
MOV     #T52A,R5           ;LOAD R5
MARK    47                 ;MARK N=47

;*****
;FUNCTION RESULTS N=47
;TEMP ← PC*(2*N)          TEMP= #T52A+(2*47)      *
;PC ← R5                  PC = T52A              *
;SP ← TEMP                SP = #T52A+(2*47)      *
;R5 ← (TEMP)              R5 = (#T52A+(2*47))    *
;SP ← TEMP+2              SP = #T52A+(2*47)+2    *
;*****

002702 010600             T52A:  MOV     SP,R0          ;GET RESULTANT SP

```


002704 022706 003022
002710 001401
002712 000000

000053
000050

CMP #2*47+T52A+2,SP JSP = #T52A+(2*47)+2?
BEQ .+4
HLT JERROR INCORRECT RESULTANT SP

C=C+1
M=M+1

TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)

002714 010701
002716 012706 000500
002722 012705 002730
002726 006450

SCOPE
MOV #STKPTR,SP JINITIALIZE THE STACK POINTER
MOV #T53A,R5 JLOAD R5
MARK 50 JMARK N=50

IFUNCTION RESULTS N=50 *
ITEMP = PC*(2*N) TEMP = #T53A+(2*50) *
IPC = R5 PC = T53A *
JSP = TEMP SP = #T53A+(2*50) *
JR5 = (TEMP) R5 = (#T53A+(2*50)) *
JSP = TEMP+2 SP = #T53A+(2*50)+2 *

002730 010600
002732 022706 003052
002736 001401
002740 000000

000054
000051

T53A: MOV SP,R0 JGET RESULTANT SP
CMP #2*50+T53A+2,SP JSP = #T53A+(2*50)+2?
BEQ .+4
HLT JERROR INCORRECT RESULTANT SP

C=C+1
M=M+1

TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)

002742 010701
002744 012706 000500
002750 012705 002756
002754 006451

SCOPE
MOV #STKPTR,SP JINITIALIZE THE STACK POINTER
MOV #T54A,R5 JLOAD R5
MARK 51 JMARK N=51

IFUNCTION RESULTS N=51 *
ITEMP = PC*(2*N) TEMP = #T54A+(2*51) *
IPC = R5 PC = T54A *
JSP = TEMP SP = #T54A+(2*51) *
JR5 = (TEMP) R5 = (#T54A+(2*51)) *
JSP = TEMP+2 SP = #T54A+(2*51)+2 *

002756 010600
002760 022706 003102
002764 001401
002766 000000

000055
000052

T54A: MOV SP,R0 JGET RESULTANT SP
CMP #2*51+T54A+2,SP JSP = #T54A+(2*51)+2?
BEQ .+4
HLT JERROR INCORRECT RESULTANT SP

C=C+1
M=M+1

TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)

002770 010701
002772 012706 000500
002776 012705 003004
003002 006452

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T55A,R5 ;LOAD R5
MARK 52 ;MARK N=52

;FUNCTION RESULTS N=52
;TEMP = PC*(2*N) TEMP= #T55A*(2*52)
;PC = R5 PC = T55A
;SP = TEMP SP = #T55A*(2*52)
;R5 = (TEMP) R5 = (#T55A*(2*52))
;SP = TEMP+2 SP = #T55A*(2*52)+2

003004 010600
003006 022706 003132
003012 001401
003014 000000

T55A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*52+T55A+2,SP ;SP = #T55A*(2*52)+2?
BEQ .+4
HLT ;ERROR INCORRECT RESULTANT SP

000056
000053

C=C+1
M=M+1
;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)

003016 010701
003020 012706 000500
003024 012705 003032
003030 006453

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T56A,R5 ;LOAD R5
MARK 53 ;MARK N=53

;FUNCTION RESULTS N=53
;TEMP = PC*(2*N) TEMP= #T56A*(2*53)
;PC = R5 PC = T56A
;SP = TEMP SP = #T56A*(2*53)
;R5 = (TEMP) R5 = (#T56A*(2*53))
;SP = TEMP+2 SP = #T56A*(2*53)+2

003032 010600
003034 022706 003162
003040 001401
003042 000000

T56A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*53+T56A+2,SP ;SP = #T56A*(2*53)+2?
BEQ .+4
HLT ;ERROR INCORRECT RESULTANT SP

000057
000054

C=C+1
M=M+1
;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)

003044 010701
003046 012706 000500
003052 012705 003060
003056 006454

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T57A,R5 ;LOAD R5
MARK 54 ;MARK N=54

;FUNCTION RESULTS N=54
;TEMP = PC*(2*N) TEMP= #T57A*(2*54)

```

IPC  * R5          PC  = T57A
ISP  * TEMP       SP  = #T57A+(2*54)
IR5  * (TEMP)     R5  = (#T57A+(2*54))
ISP  * TEMP+2     SP  = #T57A+(2*54)+2
|*****|

```

003060 010600
003062 022706 003212
003066 001401
003070 000000

```

T57A:  MOV    SP,R0          IGET RESULTANT SP
        CMP    #2*54+T57A+2,SP  ISP = #T57A+(2*54)+2?
        BEQ    ,+4
        HLT                    IERROR INCORRECT RESULTANT SP

```

000060
000055

```

C=C+1
M=M+1
I TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
I INSTRUCTION FOR ALL VALUES OF N (0-77)

```

003072 010701
003074 012706 000500
003100 012705 003106
003104 006455

```

SCOPE
MOV    #STKPTR,SP  I INITIALIZE THE STACK POINTER
MOV    #T60A,R5    I LOAD R5
MARK   55          I MARK N=55

```

```

|*****|
I FUNCTION RESULTS N=55
I TEMP = PC+(2*N)      TEMP = #T60A+(2*55)
IPC  * R5          PC  = T60A
ISP  * TEMP       SP  = #T60A+(2*55)
IR5  * (TEMP)     R5  = (#T60A+(2*55))
ISP  * TEMP+2     SP  = #T60A+(2*55)+2
|*****|

```

003106 010600
003110 022706 003242
003114 001401
003116 000000

```

T60A:  MOV    SP,R0          IGET RESULTANT SP
        CMP    #2*55+T60A+2,SP  ISP = #T60A+(2*55)+2?
        BEQ    ,+4
        HLT                    IERROR INCORRECT RESULTANT SP

```

000061
000056

```

C=C+1
M=M+1
I TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
I INSTRUCTION FOR ALL VALUES OF N (0-77)

```

003120 010701
003122 012706 000500
003126 012705 003134
003132 006456

```

SCOPE
MOV    #STKPTR,SP  I INITIALIZE THE STACK POINTER
MOV    #T61A,R5    I LOAD R5
MARK   56          I MARK N=56

```

```

|*****|
I FUNCTION RESULTS N=56
I TEMP = PC+(2*N)      TEMP = #T61A+(2*56)
IPC  * R5          PC  = T61A
ISP  * TEMP       SP  = #T61A+(2*56)
IR5  * (TEMP)     R5  = (#T61A+(2*56))
ISP  * TEMP+2     SP  = #T61A+(2*56)+2
|*****|

```

003134 010600
003136 022706 003272

```

T61A:  MOV    SP,R0          IGET RESULTANT SP
        CMP    #2*56+T61A+2,SP  ISP = #T61A+(2*56)+2?

```

003142 001401
003144 000000

000062
000057

BEQ ,+4
HLT ;ERROR INCORRECT RESULTANT SP

C=C+1
M=M+1
;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)

003146 010701
003150 012706 000500
003154 012705 003162
003160 006457

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T62A,R5 ;LOAD R5
MARK 57 ;MARK N=57

;FUNCTION RESULTS N=57
;TEMP = PC+(2*N) TEMP= #T62A+(2*57)
;PC = R5 PC = T62A
;SP = TEMP SP = #T62A+(2*57)
;R5 = (TEMP) R5 = (#T62A+(2*57))
;SP = TEMP+2 SP = #T62A+(2*57)+2

003162 010600
003164 022706 003322
003170 001401
003172 000000

T62A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*57+T62A+2,SP ;SP = #T62A+(2*57)+2?
BEQ ,+4
HLT ;ERROR INCORRECT RESULTANT SP

000063
000060

C=C+1
M=M+1
;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)

003174 010701
003176 012706 000500
003202 012705 003210
003206 006460

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T63A,R5 ;LOAD R5
MARK 60 ;MARK N=60

;FUNCTION RESULTS N=60
;TEMP = PC+(2*N) TEMP= #T63A+(2*60)
;PC = R5 PC = T63A
;SP = TEMP SP = #T63A+(2*60)
;R5 = (TEMP) R5 = (#T63A+(2*60))
;SP = TEMP+2 SP = #T63A+(2*60)+2

003210 010600
003212 022706 003352
003216 001401
003220 000000

T63A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*60+T63A+2,SP ;SP = #T63A+(2*60)+2?
BEQ ,+4
HLT ;ERROR INCORRECT RESULTANT SP

000064
000061

C=C+1
M=M+1
;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)

003222 010701

SCOPE

003224 012706 000500
003230 012705 003236
003234 006461

MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T64A,R5 ;LOAD R5
MARK 61 ;MARK N=61

FUNCTION RESULTS N=61
TEMP = PC+(2*N) TEMP = #T64A+(2*61)
PC = R5 PC = T64A
SP = TEMP SP = #T64A+(2*61)
R5 = (TEMP) R5 = (#T64A+(2*61))
SP = TEMP+2 SP = #T64A+(2*61)+2

003236 010600
003240 022706 003402
003244 001401
003246 000000

T64A1 MOV SP,R0 ;GET RESULTANT SP
CMP #2*61+T64A+2,SP ;SP = #T64A+(2*61)+2?
BEQ ,+4
HLT ;ERROR INCORRECT RESULTANT SP

000065
000062

C=C+1
M=M+1

TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)

003250 010701
003252 012706 000500
003256 012705 003264
003262 006462

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T65A,R5 ;LOAD R5
MARK 62 ;MARK N=62

FUNCTION RESULTS N=62
TEMP = PC+(2*N) TEMP = #T65A+(2*62)
PC = R5 PC = T65A
SP = TEMP SP = #T65A+(2*62)
R5 = (TEMP) R5 = (#T65A+(2*62))
SP = TEMP+2 SP = #T65A+(2*62)+2

003264 010600
003266 022706 003432
003272 001401
003274 000000

T65A1 MOV SP,R0 ;GET RESULTANT SP
CMP #2*62+T65A+2,SP ;SP = #T65A+(2*62)+2?
BEQ ,+4
HLT ;ERROR INCORRECT RESULTANT SP

000066
000063

C=C+1
M=M+1

TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)

003276 010701
003300 012706 000500
003304 012705 003312
003310 006463

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T66A,R5 ;LOAD R5
MARK 63 ;MARK N=63

FUNCTION RESULTS N=63
TEMP = PC+(2*N) TEMP = #T66A+(2*63)
PC = R5 PC = T66A

```

;SP * TEMP          SP = #T66A+(2*63)
;R5 * (TEMP)       R5 = (#T66A+(2*63))
;SP * TEMP+2       SP = #T66A+(2*63)+2
;*****

003312 010600      T66A:  MOV     SP,R0          ;GET RESULTANT SP
003314 022706 003462  CMP     #2*63+T66A+2,SP ;SP = #T66A+(2*63)+2?
003320 001401      BEQ     ,+4
003322 000000      HLT

                C=C+1
                M=M+1

;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)
                SCOPE
003324 010701      MOV     #STKPTR,SP      ;INITIALIZE THE STACK POINTER
003326 012706 000500  MOV     #T67A,R5       ;LOAD R5
003332 012705 003340  MARK   64              ;MARK N=64
003336 006464

;*****
;FUNCTION RESULTS N=64
;TEMP * PC*(2*N)   TEMP= #T67A+(2*64)
;PC * R5          PC = T67A
;SP * TEMP        SP = #T67A+(2*64)
;R5 * (TEMP)      R5 = (#T67A+(2*64))
;SP * TEMP+2      SP = #T67A+(2*64)+2
;*****

003340 010600      T67A:  MOV     SP,R0          ;GET RESULTANT SP
003342 022706 003512  CMP     #2*64+T67A+2,SP ;SP = #T67A+(2*64)+2?
003346 001401      BEQ     ,+4
003350 000000      HLT

                C=C+1
                M=M+1

;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)
                SCOPE
003352 010701      MOV     #STKPTR,SP      ;INITIALIZE THE STACK POINTER
003354 012706 000500  MOV     #T70A,R5       ;LOAD R5
003360 012705 003366  MARK   65              ;MARK N=65
003364 006465

;*****
;FUNCTION RESULTS N=65
;TEMP * PC*(2*N)   TEMP= #T70A+(2*65)
;PC * R5          PC = T70A
;SP * TEMP        SP = #T70A+(2*65)
;R5 * (TEMP)      R5 = (#T70A+(2*65))
;SP * TEMP+2      SP = #T70A+(2*65)+2
;*****

003366 010600      T70A:  MOV     SP,R0          ;GET RESULTANT SP
003370 022706 003542  CMP     #2*65+T70A+2,SP ;SP = #T70A+(2*65)+2?
003374 001401      BEQ     ,+4
```

003376 000000
000071
000066

HLT ;ERROR INCORRECT RESULTANT SP
C=C+1
M=M+1

;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)

003400 010701
003402 012706 000500
003406 012705 003414
003412 006466

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T71A,R5 ;LOAD R5
MARK 66 ;MARK N=66

```

;*****
;FUNCTION RESULTS N=66
;TEMP = PC*(2*N)          TEMP= #T71A+(2*66)
;PC = R5                  PC = T71A
;SP = TEMP                SP = #T71A+(2*66)
;R5 = (TEMP)              R5 = (#T71A+(2*66))
;ISP = TEMP+2             SP = #T71A+(2*66)+2
;*****
    
```

003414 010600
003416 022706 003572
003422 001401
003424 000000
000072
000067

T71A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*66+T71A+2,SP ;ISP = #T71A+(2*66)+2?
BEQ ,+4
HLT ;ERROR INCORRECT RESULTANT SP
C=C+1
M=M+1

;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)

003426 010701
003430 012706 000500
003434 012705 003442
003440 006467

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T72A,R5 ;LOAD R5
MARK 67 ;MARK N=67

```

;*****
;FUNCTION RESULTS N=67
;TEMP = PC*(2*N)          TEMP= #T72A+(2*67)
;PC = R5                  PC = T72A
;SP = TEMP                SP = #T72A+(2*67)
;R5 = (TEMP)              R5 = (#T72A+(2*67))
;ISP = TEMP+2             SP = #T72A+(2*67)+2
;*****
    
```

003442 010600
003444 022706 003622
003450 001401
003452 000000
000073
000070

T72A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*67+T72A+2,SP ;ISP = #T72A+(2*67)+2?
BEQ ,+4
HLT ;ERROR INCORRECT RESULTANT SP
C=C+1
M=M+1

;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)

003454 010701
003456 012706 000500

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER

003462 012705 003470
003466 006470

MOV #T73A,R5 ;LOAD R5
MARK 70 ;MARK N=70

```
*****  
;FUNCTION RESULTS N=70  
;TEMP = PC*(2*N) TEMP = #T73A+(2*70)  
;PC = R5 PC = T73A  
;SP = TEMP SP = #T73A+(2*70)  
;R5 = (TEMP) R5 = (#T73A+(2*70))  
;SP = TEMP+2 SP = #T73A+(2*70)+2  
*****
```

003470 010600
003472 022706 003652
003476 001401
003500 000000

T73A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*70+T73A+2,SP ;SP = #T73A+(2*70)+2?
BEQ ,+4
HLT ;ERROR INCORRECT RESULTANT SP

000074
000071

C=C+1
M=M+1
;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)

003502 010701
003504 012706 000500
003510 012705 003516
003514 006471

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T74A,R5 ;LOAD R5
MARK 71 ;MARK N=71

```
*****  
;FUNCTION RESULTS N=71  
;TEMP = PC*(2*N) TEMP = #T74A+(2*71)  
;PC = R5 PC = T74A  
;SP = TEMP SP = #T74A+(2*71)  
;R5 = (TEMP) R5 = (#T74A+(2*71))  
;SP = TEMP+2 SP = #T74A+(2*71)+2  
*****
```

003516 010600
003520 022706 003702
003524 001401
003526 000000

T74A: MOV SP,R0 ;GET RESULTANT SP
CMP #2*71+T74A+2,SP ;SP = #T74A+(2*71)+2?
BEQ ,+4
HLT ;ERROR INCORRECT RESULTANT SP

000075
000072

C=C+1
M=M+1
;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)

003530 010701
003532 012706 000500
003536 012705 003544
003542 006472

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T75A,R5 ;LOAD R5
MARK 72 ;MARK N=72

```
*****  
;FUNCTION RESULTS N=72  
;TEMP = PC*(2*N) TEMP = #T75A+(2*72)  
;PC = R5 PC = T75A  
;SP = TEMP SP = #T75A+(2*72)  
*****
```



```

R5 ← (TEMP)          R5 = (#T75A+(2*72))
ISP ← TEMP+2         ISP = #T75A+(2*72)+2
|*****|

003544 010600          T75A: MOV SP,R0           ;GET RESULTANT SP
003546 022706 003732  CMP #2*72+T75A+2,SP ;ISP = #T75A+(2*72)+2?
003552 001401          BEQ ,+4
003554 000000          HLT           ;ERROR INCORRECT RESULTANT SP

000076          C=C+1
000073          M=M+1

;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV #STKPTR,SP      ;INITIALIZE THE STACK POINTER
MOV #T76A,R5        ;LOAD R5
MARK 73             ;MARK N=73

|*****|
;FUNCTION RESULTS N=73
;TEMP ← PC*(2*N)      TEMP = #T76A+(2*73)
IPC ← R5              PC = T76A
ISP ← TEMP            SP = #T76A+(2*73)
R5 ← (TEMP)           R5 = (#T76A+(2*73))
ISP ← TEMP+2          SP = #T76A+(2*73)+2
|*****|

003572 010600          T76A: MOV SP,R0           ;GET RESULTANT SP
003574 022706 003762  CMP #2*73+T76A+2,SP ;ISP = #T76A+(2*73)+2?
003600 001401          BEQ ,+4
003602 000000          HLT           ;ERROR INCORRECT RESULTANT SP

000077          C=C+1
000074          M=M+1

;TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
;INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV #STKPTR,SP      ;INITIALIZE THE STACK POINTER
MOV #T77A,R5        ;LOAD R5
MARK 74             ;MARK N=74

|*****|
;FUNCTION RESULTS N=74
;TEMP ← PC*(2*N)      TEMP = #T77A+(2*74)
IPC ← R5              PC = T77A
ISP ← TEMP            SP = #T77A+(2*74)
R5 ← (TEMP)           R5 = (#T77A+(2*74))
ISP ← TEMP+2          SP = #T77A+(2*74)+2
|*****|

003620 010600          T77A: MOV SP,R0           ;GET RESULTANT SP
003622 022706 004012  CMP #2*74+T77A+2,SP ;ISP = #T77A+(2*74)+2?
003626 001401          BEQ ,+4
003630 000000          HLT           ;ERROR INCORRECT RESULTANT SP

```

```

000100          C=C+1
000075          M=M+1
)TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
)INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
003632 010701          MOV      #STKPTR,SP      )INITIALIZE THE STACK POINTER
003634 012706 000500    MOV      #T100A,R5      )LOAD R5
003640 012705 003646    MARK     75          )MARK N=75
003644 006475

)*****
)FUNCTION RESULTS N=75
)TEMP = PC+(2*N)      TEMP= #T100A+(2*75)
)PC = R5              PC = T100A
)SP = TEMP            SP = #T100A+(2*75)
)R5 = (TEMP)          R5 = (#T100A+(2*75))
)SP = TEMP+2          SP = #T100A+(2*75)+2
)*****

003646 010600          T100A: MOV     SP,R0      )GET RESULTANT SP
003650 022706 004042    CMP     #2*75+T100A+2,SP )SP = #T100A+(2*75)+2?
003654 001401          BEQ     ,+4
003656 000000          HLT

)*****
)FUNCTION RESULTS N=76
)TEMP = PC+(2*N)      TEMP= #T101A+(2*76)
)PC = R5              PC = T101A
)SP = TEMP            SP = #T101A+(2*76)
)R5 = (TEMP)          R5 = (#T101A+(2*76))
)SP = TEMP+2          SP = #T101A+(2*76)+2
)*****

003674 010600          T101A: MOV     SP,R0      )GET RESULTANT SP
003676 022706 004072    CMP     #2*76+T101A+2,SP )SP = #T101A+(2*76)+2?
003702 001401          BEQ     ,+4
003704 000000          HLT

)*****
)FUNCTION RESULTS N=77
)TEMP = PC+(2*N)      TEMP= #T102A+(2*77)
)PC = R5              PC = T102A
)SP = TEMP            SP = #T102A+(2*77)
)R5 = (TEMP)          R5 = (#T102A+(2*77))
)SP = TEMP+2          SP = #T102A+(2*77)+2
)*****

003706 010701          MOV      #STKPTR,SP      )INITIALIZE THE STACK POINTER
003710 012706 000500    MOV      #T102A,R5      )LOAD R5
003714 012705 003722    MARK     77          )MARK N=77

```

003720 006477

MARK 77

;MARK N=77

```

*****
)FUNCTION RESULTS N=77
)TEMP * PC*(2*N)          TEMP = #T102A+(2*77)
)PC * R5                  PC = T102A
)SP * TEMP                SP = #T102A+(2*77)
)R5 * (TEMP)              R5 = (#T102A+(2*77))
)SP * TEMP+2              SP = #T102A+(2*77)+2
*****

```

003722 010600
 003724 022706 004122
 003730 001401
 003732 000000

```

T102A: MOV     SP,R0          )GET RESULTANT SP
        CMP     #2*77+T102A+2,9P  )SP = #T102A+(2*77)+2
        BEQ     ,+4
        HLT
        )ERROR INCORRECT RESULTANT SP

```

000103
 000100
 000000

C=C+1
 M=M+1

M=0
)TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
)OF N (0-77)

003734 010701
 003736 012706 000500
 003742 012705 003750
 003746 006400

```

        SCOPE
        MOV     #STKPTR,SP      )INITIALIZE THE STACK POINTER
        MOV     #T103A,R5      )LOAD R5
        MARK   0               )MARK N=0

```

```

*****
)FUNCTION RESULTS N=0
)TEMP * PC*(2*0)          TEMP = #T103A+(2*0)
)PC * R5                  PC = #T103A
)SP * TEMP                SP = #T103A+(2*0)
)R5 * (TEMP)              R5 = (#T103A+(2*0))
)SP * TEMP+2              SP = #T103A+(2*0)+2
*****

```

003750 000240
 003752 010500
 003754 023705 003750
 003760 001401
 003762 000000

```

T103A: NOP
        MOV     R5,R0          )GET CONTENTS OF R5
        CMP     #2*0+T103A,R5  )R5 = (#T103A+(2*0))
        BEQ     ,+4
        HLT
        )R5 IMPROPERLY LOADED

```

000104
 000001

C=C+1
 M=M+1

)TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
)OF N (0-77)

003764 010701
 003766 012706 000500
 003772 012705 004000
 003776 006401

```

        SCOPE
        MOV     #STKPTR,SP      )INITIALIZE THE STACK POINTER
        MOV     #T104A,R5      )LOAD R5
        MARK   1               )MARK N=1

```

```

*****
)FUNCTION RESULTS N=1
)TEMP * PC*(2*1)          TEMP = #T104A+(2*1)
)PC * R5                  PC = #T104A
*****

```

```

;SP * TEMP          SP = #T104A+(2*1)          *
;R5 * (TEMP)        R5 = (#T104A+(2*1))        *
;SP * TEMP+2        SP = #T104A+(2*1)+2        *
;*****

```

```

004000 000240
004002 010500
004004 023705 004002
004010 001401
004012 000000

```

```

T104A: NOP
      MOV     R5,R0          ;GET CONTENTS OF R5
      CMP     @#2*1+T104A,R5 ;R5 = (#T104A+(2*1))?
      BEQ     ,+4
      HLT
      ;R5 IMPROPERLY LOADED

```

```

000105
000002

```

```

      C=C+1
      M=M+1
;TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0-77)

```

```

004014 010701
004016 012706 000500
004022 012705 004030
004026 006402

```

```

      SCOPE
      MOV     #STKPTR,SP     ;INITIALIZE THE STACK POINTER
      MOV     #T105A,R5     ;LOAD R5
      MARK   2              ;MARK N=2

```

```

;*****
;FUNCTION RESULTS N=2
;TEMP * PC*(2*2)    TEMP = #T105A+(2*2)
;PC * R5            PC = #T105A
;SP * TEMP          SP = #T105A+(2*2)
;R5 * (TEMP)        R5 = (#T105A+(2*2))
;SP * TEMP+2        SP = #T105A+(2*2)+2
;*****

```

```

004030 000240
004032 010500
004034 023705 004034
004040 001401
004042 000000

```

```

T105A: NOP
      MOV     R5,R0          ;GET CONTENTS OF R5
      CMP     @#2*2+T105A,R5 ;R5 = (#T105A*(2*2))?
      BEQ     ,+4
      HLT
      ;R5 IMPROPERLY LOADED

```

```

000106
000003

```

```

      C=C+1
      M=M+1
;TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0-77)

```

```

004044 010701
004046 012706 000500
004052 012705 004060
004056 006403

```

```

      SCOPE
      MOV     #STKPTR,SP     ;INITIALIZE THE STACK POINTER
      MOV     #T106A,R5     ;LOAD R5
      MARK   3              ;MARK N=3

```

```

;*****
;FUNCTION RESULTS N=3
;TEMP * PC*(2*3)    TEMP = #T106A*(2*3)
;PC * R5            PC = #T106A
;SP * TEMP          SP = #T106A+(2*3)
;R5 * (TEMP)        R5 = (#T106A+(2*3))
;SP * TEMP+2        SP = #T106A+(2*3)+2
;*****

```

```

004060 000240

```

```

T106A: NOP

```

004062 010500
004064 023705 004066
004070 001401
004072 000000

MOV R5,R0 ;GET CONTENTS OF R5
CMP @#2*3+T106A,R5 ;R5 = (#T106A+(2*3))?
BEQ ,+4
HLT ;R5 IMPROPERLY LOADED

000107
000004

C=C+1
M=M+1

!TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
!OF N (0-77)

004074 010701
004076 012706 000500
004102 012705 004110
004106 006404

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T107A,R5 ;LOAD R5
MARK 4 ;MARK N=4

!*****
!FUNCTION RESULTS N=4
!TEMP * PC*(2*4) TEMP = #T107A*(2*4)
!PC * R5 PC = #T107A
!SP * TEMP SP = #T107A*(2*4)
!R5 * (TEMP) R5 = (#T107A*(2*4))
!SP * TEMP*2 SP = #T107A*(2*4)+2
!*****

004110 000240
004112 010500
004114 023705 004120
004120 001401
004122 000000

T107A: NOP
MOV R5,R0 ;GET CONTENTS OF R5
CMP @#2*4+T107A,R5 ;R5 = (#T107A*(2*4))?
BEQ ,+4
HLT ;R5 IMPROPERLY LOADED

000110
000005

C=C+1
M=M+1

!TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
!OF N (0-77)

004124 010701
004126 012706 000500
004132 012705 004140
004136 006405

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T110A,R5 ;LOAD R5
MARK 5 ;MARK N=5

!*****
!FUNCTION RESULTS N=5
!TEMP * PC*(2*5) TEMP = #T110A*(2*5)
!PC * R5 PC = #T110A
!SP * TEMP SP = #T110A*(2*5)
!R5 * (TEMP) R5 = (#T110A*(2*5))
!SP * TEMP*2 SP = #T110A*(2*5)+2
!*****

004140 000240
004142 010500
004144 023705 004152
004150 001401
004152 000000

T110A: NOP
MOV R5,R0 ;GET CONTENTS OF R5
CMP @#2*5+T110A,R5 ;R5 = (#T110A*(2*5))?
BEQ ,+4
HLT ;R5 IMPROPERLY LOADED

000111

C=C+1

```

000006          M=M+1
                )TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
                IOF N (0=77)
                SCOPE
004154 010701   MOV      #STKPTR,SP      )INITIALIZE THE STACK POINTER
004156 012706   MOV      #T111A,R5      )LOAD R5
004162 012705   MARK     6              )MARK N=6
004166 006406

;*****
;FUNCTION RESULTS N=6
;TEMP = PC*(2*6)      TEMP = #T111A*(2*6)
;PC = R5              PC = #T111A
;SP = TEMP            SP = #T111A*(2*6)
;R5 = (TEMP)          R5 = (#T111A*(2*6))
;SP = TEMP+2          SP = #T111A*(2*6)+2
;*****

004170 000240   T111A: NOP
004172 010500   MOV      R5,R0      )GET CONTENTS OF R5
004174 023705   CMP      @#2*6+T111A,R5  )R5 = (#T111A*(2*6))?
004200 001401   BEQ     ,+4
004202 000000   HLT
                )R5 IMPROPERLY LOADED

                C=C+1
                M=M+1
                )TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
                IOF N (0=77)
                SCOPE
004204 010701   MOV      #STKPTR,SP      )INITIALIZE THE STACK POINTER
004206 012706   MOV      #T112A,R5      )LOAD R5
004212 012705   MARK     7              )MARK N=7
004216 006407

;*****
;FUNCTION RESULTS N=7
;TEMP = PC*(2*7)      TEMP = #T112A*(2*7)
;PC = R5              PC = #T112A
;SP = TEMP            SP = #T112A*(2*7)
;R5 = (TEMP)          R5 = (#T112A*(2*7))
;SP = TEMP+2          SP = #T112A*(2*7)+2
;*****

004220 000240   T112A: NOP
004222 010500   MOV      R5,R0      )GET CONTENTS OF R5
004224 023705   CMP      @#2*7+T112A,R5  )R5 = (#T112A*(2*7))?
004230 001401   BEQ     ,+4
004232 000000   HLT
                )R5 IMPROPERLY LOADED

                C=C+1
                M=M+1
                )TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
                IOF N (0=77)
                SCOPE
004234 010701   MOV      #STKPTR,SP      )INITIALIZE THE STACK POINTER
004236 012706   MOV      #T113A,R5      )LOAD R5
004242 012705   MARK     5

```

004246 006410

MARK 10 ;MARK N=10

```

*****
)FUNCTION RESULTS N=10
)TEMP * PC*(2*10)      TEMP = #T113A*(2*10)
)PC * R5                PC = #T113A
)SP * TEMP              SP = #T113A*(2*10)
)R5 * (TEMP)           R5 = (#T113A*(2*10))
)SP * TEMP+2           SP = #T113A*(2*10)+2
*****

```

004250 000240
 004252 010500
 004254 023705 004270
 004260 001401
 004262 000000

 000114
 000011

```

T113A: NOP
      MOV      R5,R0      ;GET CONTENTS OF R5
      CMP      @#2*10*T113A,R5 ;R5 = (#T113A*(2*10))?
      BEQ      ,*4
      HLT
      ;R5 IMPROPERLY LOADED

```

```

      C=C+1
      M=M+1
)TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
)OF N (0-77)

```

004264 010701
 004266 012706 000500
 004272 012705 004300
 004276 006411

```

      SCOPE
      MOV      #STKPTR,SP  ;INITIALIZE THE STACK POINTER
      MOV      #T114A,R5  ;LOAD R5
      MARK    11          ;MARK N=11

```

```

*****
)FUNCTION RESULTS N=11
)TEMP * PC*(2*11)      TEMP = #T114A*(2*11)
)PC * R5                PC = #T114A
)SP * TEMP              SP = #T114A*(2*11)
)R5 * (TEMP)           R5 = (#T114A*(2*11))
)SP * TEMP+2           SP = #T114A*(2*11)+2
*****

```

004300 000240
 004302 010500
 004304 023705 004322
 004310 001401
 004312 000000

 000115
 000012

```

T114A: NOP
      MOV      R5,R0      ;GET CONTENTS OF R5
      CMP      @#2*11*T114A,R5 ;R5 = (#T114A*(2*11))?
      BEQ      ,*4
      HLT
      ;R5 IMPROPERLY LOADED

```

```

      C=C+1
      M=M+1
)TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
)OF N (0-77)

```

004314 010701
 004316 012706 000500
 004322 012705 004330
 004326 006412

```

      SCOPE
      MOV      #STKPTR,SP  ;INITIALIZE THE STACK POINTER
      MOV      #T115A,R5  ;LOAD R5
      MARK    12          ;MARK N=12

```

```

*****
)FUNCTION RESULTS N=12
)TEMP * PC*(2*12)      TEMP = #T115A*(2*12)
)PC * R5                PC = #T115A

```

```
ISP ← TEMP          SP = #T115A+(2*12)
IR5 ← (TEMP)        R5 = (#T115A+(2*12))
ISP ← TEMP+2        SP = #T115A+(2*12)+2
J*****
```

004330 000240
004332 010500
004334 023705 004354
004340 001401
004342 000000

```
T115A: NOP
      MOV R5,R0          ;GET CONTENTS OF R5
      CMP @#2*12+T115A,R5 ;R5 = (#T115A+(2*12))?
      BEQ ,+4
      HLT                ;R5 IMPROPERLY LOADED
```

000116
000013

```
C=C+1
M=M+1
;TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0-77)
```

004344 010701
004346 012706 000500
004352 012705 004360
004356 006413

```
      SCOPE
      MOV #STKPTR,SP    ;INITIALIZE THE STACK POINTER
      MOV #T116A,R5     ;LOAD R5
      MARK 13           ;MARK N=13
```

```
J*****
;FUNCTION RESULTS N=13
;TEMP ← PC*(2*13)    TEMP = #T116A+(2*13)
;PC ← R5             PC = #T116A
;SP ← TEMP           SP = #T116A+(2*13)
;R5 ← (TEMP)        R5 = (#T116A+(2*13))
;SP ← TEMP+2        SP = #T116A+(2*13)+2
J*****
```

004360 000240
004362 010500
004364 023705 004406
004370 001401
004372 000000

```
T116A: NOP
      MOV R5,R0          ;GET CONTENTS OF R5
      CMP @#2*13+T116A,R5 ;R5 = (#T116A+(2*13))?
      BEQ ,+4
      HLT                ;R5 IMPROPERLY LOADED
```

000117
000014

```
C=C+1
M=M+1
;TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0-77)
```

004374 010701
004376 012706 000500
004402 012705 004410
004406 006414

```
      SCOPE
      MOV #STKPTR,SP    ;INITIALIZE THE STACK POINTER
      MOV #T117A,R5     ;LOAD R5
      MARK 14           ;MARK N=14
```

```
J*****
;FUNCTION RESULTS N=14
;TEMP ← PC*(2*14)    TEMP = #T117A+(2*14)
;PC ← R5             PC = #T117A
;SP ← TEMP           SP = #T117A+(2*14)
;R5 ← (TEMP)        R5 = (#T117A+(2*14))
;SP ← TEMP+2        SP = #T117A+(2*14)+2
J*****
```

004410 000240

```
T117A: NOP
```


004412 010500
004414 023705 004440
004420 001401
004422 000000

MOV R5,R0 ;GET CONTENTS OF R5
CMP @#2*14+T117A,R5 ;R5 = (#T117A+(2*14))?
BEQ ;+4
HLT ;R5 IMPROPERLY LOADED

000120
000015

C=C+1
M=M+1

);TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0=77)

004424 010701
004426 012706 000500
004432 012705 004440
004436 006415

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T120A,R5 ;LOAD R5
MARK 15 ;MARK N=15

;FUNCTION RESULTS N=15
;TEMP * PC*(2*15) TEMP = #T120A*(2*15)
;PC * R5 PC = #T120A
;SP * TEMP SP = #T120A*(2*15)
;R5 * (TEMP) R5 = (#T120A*(2*15))
;SP * TEMP+2 SP = #T120A*(2*15)+2

004440 000240
004442 010500
004444 023705 004472
004450 001401
004452 000000

T120A: NOP
MOV R5,R0 ;GET CONTENTS OF R5
CMP @#2*15+T120A,R5 ;R5 = (#T120A+(2*15))?
BEQ ;+4
HLT ;R5 IMPROPERLY LOADED

000121
000016

C=C+1
M=M+1

);TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0=77)

004454 010701
004456 012706 000500
004462 012705 004470
004466 006415

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T121A,R5 ;LOAD R5
MARK 16 ;MARK N=16

;FUNCTION RESULTS N=16
;TEMP * PC*(2*16) TEMP = #T121A*(2*16)
;PC * R5 PC = #T121A
;SP * TEMP SP = #T121A*(2*16)
;R5 * (TEMP) R5 = (#T121A*(2*16))
;SP * TEMP+2 SP = #T121A*(2*16)+2

004470 000240
004472 010500
004474 023705 004524
004500 001401
004502 000000

T121A: NOP
MOV R5,R0 ;GET CONTENTS OF R5
CMP @#2*16+T121A,R5 ;R5 = (#T121A+(2*16))?
BEQ ;+4
HLT ;R5 IMPROPERLY LOADED

000122

C=C+1

```

000017          M=M+1
                ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
                IOF N (0=77)
                SCOPE
004504 010701    MOV      #STKPTR,SP      ;INITIALIZE THE STACK POINTER
004506 012706    MOV      #T122A,R5          ;LOAD R5
004512 012705    MARK     17              ;MARK N=17
004516 006417

;*****
;FUNCTION RESULTS  N=17
;TEMP = PC*(2*17)      TEMP = #T122A*(2*17)
;PC = R5              PC = #T122A
;SP = TEMP            SP = #T122A*(2*17)
;R5 = (TEMP)          R5 = (#T122A*(2*17))
;SP = TEMP+2          SP = #T122A*(2*17)+2
;*****

004520 000240    T122A:  NOP
004522 010500    MOV      R5,R0          ;GET CONTENTS OF R5
004524 023705    CMP      @#2*17*T122A,R5 ;R5 = (#T122A*(2*17))?
004530 001401    BEQ     .+4
004532 000000    HLT                    ;R5 IMPROPERLY LOADED

                C=C+1
                M=M+1
                ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
                IOF N (0=77)
                SCOPE
004534 010701    MOV      #STKPTR,SP      ;INITIALIZE THE STACK POINTER
004536 012706    MOV      #T123A,R5          ;LOAD R5
004542 012705    MARK     20              ;MARK N=20
004546 006420

;*****
;FUNCTION RESULTS  N=20
;TEMP = PC*(2*20)      TEMP = #T123A*(2*20)
;PC = R5              PC = #T123A
;SP = TEMP            SP = #T123A*(2*20)
;R5 = (TEMP)          R5 = (#T123A*(2*20))
;SP = TEMP+2          SP = #T123A*(2*20)+2
;*****

004550 000240    T123A:  NOP
004552 010500    MOV      R5,R0          ;GET CONTENTS OF R5
004554 023705    CMP      @#2*20*T123A,R5 ;R5 = (#T123A*(2*20))?
004560 001401    BEQ     .+4
004562 000000    HLT                    ;R5 IMPROPERLY LOADED

                C=C+1
                M=M+1
                ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
                IOF N (0=77)
                SCOPE
004564 010701    MOV      #STKPTR,SP      ;INITIALIZE THE STACK POINTER
004566 012706    MOV      #T124A,R5          ;LOAD R5
004572 012705    MARK     004600
```

004576 006421

MARK 21

;MARK N=21

```
*****  
IFUNCTION RESULTS N=21  
JTEMP * PC*(2*21)      TEMP = #T124A*(2*21)  
IPC   * R5              PC    = #T124A  
JSP   * TEMP            SP    = #T124A*(2*21)  
JR5   * (TEMP)          R5    = (#T124A*(2*21))  
JSP   * TEMP*2          SP    = #T124A*(2*21)+2  
*****
```

004600 000240
004602 010500
004604 023705 004642
004610 001401
004612 000000

```
T124A:  NOP  
        MOV     R5;R0      JGET CONTENTS OF R5  
        CMP     @#2*21+T124A,R5 JR5 = (#T124A*(2*21))?  
        BEQ     ,+4  
        HLT  
        JR5 IMPROPERLY LOADED
```

000125
000022

C=C+1
M=M+1

```
JTEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
JOF N (0=77)
```

004614 010701
004616 012706 000500
004622 012705 004630
004626 006422

```
        SCOPE  
        MOV     #STKPTR,SP  JINITIALIZE THE STACK POINTER  
        MOV     #T125A,R5  JLOAD R5  
        MARK    22        JMARK N=22
```

```
*****  
IFUNCTION RESULTS N=22  
JTEMP * PC*(2*22)      TEMP = #T125A*(2*22)  
IPC   * R5              PC    = #T125A  
JSP   * TEMP            SP    = #T125A*(2*22)  
JR5   * (TEMP)          R5    = (#T125A*(2*22))  
JSP   * TEMP*2          SP    = #T125A*(2*22)+2  
*****
```

004630 000240
004632 010500
004634 023705 004674
004640 001401
004642 000000

```
T125A:  NOP  
        MOV     R5;R0      JGET CONTENTS OF R5  
        CMP     @#2*22+T125A,R5 JR5 = (#T125A*(2*22))?  
        BEQ     ,+4  
        HLT  
        JR5 IMPROPERLY LOADED
```

000126
000023

C=C+1
M=M+1

```
JTEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
JOF N (0=77)
```

004644 010701
004646 012706 000500
004652 012705 004660
004656 006423

```
        SCOPE  
        MOV     #STKPTR,SP  JINITIALIZE THE STACK POINTER  
        MOV     #T126A,R5  JLOAD R5  
        MARK    23        JMARK N=23
```

```
*****  
IFUNCTION RESULTS N=23  
JTEMP * PC*(2*23)      TEMP = #T126A*(2*23)  
IPC   * R5              PC    = #T126A  
*****
```

```
ISP * TEMP          SP = #T126A+(2*23) *
IR5 * (TEMP)        R5 = (#T126A+(2*23)) *
ISP * TEMP+2         SP = #T126A+(2*23)+2 *
|*****|
```

004660 000240
004662 010500
004664 023705 004726
004670 001401
004672 000000

```
T126A: NOP
      MOV      R5,R0          IGET CONTENTS OF R5
      CMP      @#2*23+T126A,R5 IR5 = (#T126A+(2*23))?
      BEQ      .+4
      HLT
      IR5 IMPROPERLY LOADED
```

000127
000024

```
C=C+1
M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)
```

004674 010701
004676 012706 000500
004702 012705 004710
004706 006424

```
SCOPE
MOV      #STKPTR,SP          IINITIALIZE THE STACK POINTER
MOV      #T127A,R5          ILOAD R5
MARK     24                  IMARK N=24
```

```
|*****|
IFUNCTION RESULTS N=24
ITEMP * PC+(2*24)      TEMP = #T127A+(2*24)
IPC * R5               PC = #T127A
ISP * TEMP             SP = #T127A+(2*24)
IR5 * (TEMP)           R5 = (#T127A+(2*24))
ISP * TEMP+2           SP = #T127A+(2*24)+2
|*****|
```

004710 000240
004712 010500
004714 023705 004760
004720 001401
004722 000000

```
T127A: NOP
      MOV      R5,R0          IGET CONTENTS OF R5
      CMP      @#2*24+T127A,R5 IR5 = (#T127A+(2*24))?
      BEQ      .+4
      HLT
      IR5 IMPROPERLY LOADED
```

000130
000025

```
C=C+1
M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)
```

004724 010701
004726 012706 000500
004732 012705 004740
004736 006425

```
SCOPE
MOV      #STKPTR,SP          IINITIALIZE THE STACK POINTER
MOV      #T130A,R5          ILOAD R5
MARK     25                  IMARK N=25
```

```
|*****|
IFUNCTION RESULTS N=25
ITEMP * PC+(2*25)      TEMP = #T130A+(2*25)
IPC * R5               PC = #T130A
ISP * TEMP             SP = #T130A+(2*25)
IR5 * (TEMP)           R5 = (#T130A+(2*25))
ISP * TEMP+2           SP = #T130A+(2*25)+2
|*****|
```

004740 000240

```
T130A: NOP
```

004742 010500
 004744 023705 005012
 004750 001401
 004752 000000

MOV R5,R0 ;GET CONTENTS OF R5
 CMP @#2*25+T130A,R5 ;R5 = (#T130A+(2*25))?
 BEQ .+4
 HLT ;R5 IMPROPERLY LOADED

000131
 000026

C=C+1
 M=M+1

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
 IOF N (0=77)

004754 010701
 004756 012706 000500
 004762 012705 004770
 004766 006426

SCOPE
 MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
 MOV #T131A,R5 ;LOAD R5
 MARK 26 ;MARK N=26

 IFUNCTION RESULTS N=26
 ITEMP = PC+(2*26) TEMP = #T131A+(2*26)
 IPC = R5 PC = #T131A
 ISP = TEMP SP = #T131A+(2*26)
 IR5 = (TEMP) R5 = (#T131A+(2*26))
 ISP = TEMP+2 SP = #T131A+(2*26)+2

004770 000240
 004772 010500
 004774 023705 005044
 005000 001401
 005002 000000

T131A: NOP
 MOV R5,R0 ;GET CONTENTS OF R5
 CMP @#2*26+T131A,R5 ;R5 = (#T131A+(2*26))?
 BEQ .+4
 HLT ;R5 IMPROPERLY LOADED

000132
 000027

C=C+1
 M=M+1

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
 IOF N (0=77)

005004 010701
 005006 012706 000500
 005012 012705 005020
 005016 006427

SCOPE
 MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
 MOV #T132A,R5 ;LOAD R5
 MARK 27 ;MARK N=27

 IFUNCTION RESULTS N=27
 ITEMP = PC+(2*27) TEMP = #T132A+(2*27)
 IPC = R5 PC = #T132A
 ISP = TEMP SP = #T132A+(2*27)
 IR5 = (TEMP) R5 = (#T132A+(2*27))
 ISP = TEMP+2 SP = #T132A+(2*27)+2

005020 000240
 005022 010500
 005024 023705 005076
 005030 001401
 005032 000000

T132A: NOP
 MOV R5,R0 ;GET CONTENTS OF R5
 CMP @#2*27+T132A,R5 ;R5 = (#T132A+(2*27))?
 BEQ .+4
 HLT ;R5 IMPROPERLY LOADED

000133

C=C+1

```

000030          M=M+1
                )TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
                )OF N (0-77)
005034 010701          SCOPE
005036 012706 000500          MOV #STKPTR,SP          )INITIALIZE THE STACK POINTER
005042 012705 005050          MOV #T133A,R5          )LOAD R5
005046 006430          MARK 30          )MARK N=30

)*****
)FUNCTION RESULTS N=30
)TEMP * PC*(2*30)          TEMP = #T133A*(2*30)
)PC * R5                    PC = #T133A
)SP * TEMP                  SP = #T133A*(2*30)
)R5 * (TEMP)                R5 = (#T133A*(2*30))
)SP * TEMP+2                SP = #T133A*(2*30)+2
)*****

005050 000240          T133A: NOP
005052 010500          MOV R5,R0          )GET CONTENTS OF R5
005054 023705 005130          CMP #2*30+T133A,R5 )R5 = (#T133A*(2*30))?
005060 001401          BEQ ,+4
005062 000000          HLT          )R5 IMPROPERLY LOADED

                C=C+1
                M=M+1
                )TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
                )OF N (0-77)
005064 010701          SCOPE
005066 012706 000500          MOV #STKPTR,SP          )INITIALIZE THE STACK POINTER
005072 012705 005100          MOV #T134A,R5          )LOAD R5
005076 006431          MARK 31          )MARK N=31

)*****
)FUNCTION RESULTS N=31
)TEMP * PC*(2*31)          TEMP = #T134A*(2*31)
)PC * R5                    PC = #T134A
)SP * TEMP                  SP = #T134A*(2*31)
)R5 * (TEMP)                R5 = (#T134A*(2*31))
)SP * TEMP+2                SP = #T134A*(2*31)+2
)*****

005100 000240          T134A: NOP
005102 010500          MOV R5,R0          )GET CONTENTS OF R5
005104 023705 005162          CMP #2*31+T134A,R5 )R5 = (#T134A*(2*31))?
005110 001401          BEQ ,+4
005112 000000          HLT          )R5 IMPROPERLY LOADED

                C=C+1
                M=M+1
                )TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
                )OF N (0-77)
005114 010701          SCOPE
005116 012706 000500          MOV #STKPTR,SP          )INITIALIZE THE STACK POINTER
005122 012705 005130          MOV #T135A,R5          )LOAD R5

```

005126 006432

MARK 32

MARK N=32

```
*****  
IFUNCTION RESULTS N=32  
TEMP * PC*(2*32)      TEMP = #T135A*(2*32)  
IPC * R5              PC = #T135A  
ISP * TEMP            SP = #T135A*(2*32)  
IR5 * (TEMP)         R5 = (#T135A*(2*32))  
ISP * TEMP+2         SP = #T135A*(2*32)+2  
*****
```

005130 000240
005132 010500
005134 023705 005214
005140 001401
005142 000000

```
T135A: NOP  
      MOV R5,R0      ;GET CONTENTS OF R5  
      CMP #2*32+T135A,R5 ;R5 = (#T135A*(2*32))?  
      BEQ ,+4  
      HLT           ;R5 IMPROPERLY LOADED
```

000136
000033

```
C=C+1  
M=M+1
```

```
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)
```

005144 010701
005146 012706 000500
005152 012705 005160
005156 006433

```
SCOPE  
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER  
MOV #T136A,R5 ;LOAD R5  
MARK 33 ;MARK N=33
```

```
*****  
IFUNCTION RESULTS N=33  
TEMP * PC*(2*33)      TEMP = #T136A*(2*33)  
IPC * R5              PC = #T136A  
ISP * TEMP            SP = #T136A*(2*33)  
IR5 * (TEMP)         R5 = (#T136A*(2*33))  
ISP * TEMP+2         SP = #T136A*(2*33)+2  
*****
```

005160 000240
005162 010500
005164 023705 005246
005170 001401
005172 000000

```
T136A: NOP  
      MOV R5,R0      ;GET CONTENTS OF R5  
      CMP #2*33+T136A,R5 ;R5 = (#T136A*(2*33))?  
      BEQ ,+4  
      HLT           ;R5 IMPROPERLY LOADED
```

000137
000034

```
C=C+1  
M=M+1
```

```
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)
```

005174 010701
005176 012706 000500
005202 012705 005210
005206 006434

```
SCOPE  
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER  
MOV #T137A,R5 ;LOAD R5  
MARK 34 ;MARK N=34
```

```
*****  
IFUNCTION RESULTS N=34  
TEMP * PC*(2*34)      TEMP = #T137A*(2*34)  
IPC * R5              PC = #T137A  
*****
```

```

JSP = TEMP          SP = #T137A+(2*34)          *
JR5 = (TEMP)        R5 = (#T137A+(2*34))        *
JSP = TEMP+2        SP = #T137A+(2*34)+2        *
|*****|

005210 000240      T137A: NOP
005212 010500      MOV      R5,R0          ;GET CONTENTS OF R5
005214 023705 005300 CMP      @#2*34+T137A,R5 ;R5 = (#T137A+(2*34))?
005220 001401      BEQ      ,+4
005222 000000      HLT
                                ;R5 IMPROPERLY LOADED

                                C=C+1
                                M=M+1
;TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0-77)
                                SCOPE
005224 010701      MOV      #STKPTR,SP      ;INITIALIZE THE STACK POINTER
005226 012706 000500 MOV      #T140A,R5      ;LOAD R5
005232 012705 005240 MARK     35              ;MARK N=35
005236 006435

|*****|
;FUNCTION RESULTS N=35
;TEMP = PC+(2*35)      TEMP = #T140A+(2*35)
;PC = R5              PC = #T140A
;JSP = TEMP          SP = #T140A+(2*35)
;JR5 = (TEMP)        R5 = (#T140A+(2*35))
;JSP = TEMP+2        SP = #T140A+(2*35)+2
|*****|

005240 000240      T140A: NOP
005242 010500      MOV      R5,R0          ;GET CONTENTS OF R5
005244 023705 005332 CMP      @#2*35+T140A,R5 ;R5 = (#T140A+(2*35))?
005250 001401      BEQ      ,+4
005252 000000      HLT
                                ;R5 IMPROPERLY LOADED

                                C=C+1
                                M=M+1
;TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0-77)
                                SCOPE
005254 010701      MOV      #STKPTR,SP      ;INITIALIZE THE STACK POINTER
005256 012706 000500 MOV      #T141A,R5      ;LOAD R5
005262 012705 005270 MARK     36              ;MARK N=36
005266 006436

|*****|
;FUNCTION RESULTS N=36
;TEMP = PC+(2*36)      TEMP = #T141A+(2*36)
;PC = R5              PC = #T141A
;JSP = TEMP          SP = #T141A+(2*36)
;JR5 = (TEMP)        R5 = (#T141A+(2*36))
;JSP = TEMP+2        SP = #T141A+(2*36)+2
|*****|

005270 000240      T141A: NOP

```


005272 010500
005274 023705 005364
005300 001401
005302 000000

MOV R5,R0 ;GET CONTENTS OF R5
CMP @#2*36+T141A,R5 ;R5 = (#T141A*(2*36))?
BEQ ,+4
HLT ;R5 IMPROPERLY LOADED

000142
000037

C=C+1
M=M+1

);TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0-77)

005304 010701
005306 012706 000500
005312 012705 005320
005316 006437

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T142A,R5 ;LOAD R5
MARK 37 ;MARK N=37

);*****
);FUNCTION RESULTS N=37
);TEMP * PC*(2*37) TEMP = #T142A*(2*37)
);PC * R5 PC = #T142A
);SP * TEMP SP = #T142A*(2*37)
);R5 * (TEMP) R5 = (#T142A*(2*37))
);SP * TEMP+2 SP = #T142A*(2*37)+2
);*****

005320 000240
005322 010500
005324 023705 005416
005330 001401
005332 000000

T142A: NOP
MOV R5,R0 ;GET CONTENTS OF R5
CMP @#2*37+T142A,R5 ;R5 = (#T142A*(2*37))?
BEQ ,+4
HLT ;R5 IMPROPERLY LOADED

000143
000040

C=C+1
M=M+1

);TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0-77)

005334 010701
005336 012706 000500
005342 012705 005350
005346 006440

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T143A,R5 ;LOAD R5
MARK 40 ;MARK N=40

);*****
);FUNCTION RESULTS N=40
);TEMP * PC*(2*40) TEMP = #T143A*(2*40)
);PC * R5 PC = #T143A
);SP * TEMP SP = #T143A*(2*40)
);R5 * (TEMP) R5 = (#T143A*(2*40))
);SP * TEMP+2 SP = #T143A*(2*40)+2
);*****

005350 000240
005352 010500
005354 023705 005450
005360 001401
005362 000000

T143A: NOP
MOV R5,R0 ;GET CONTENTS OF R5
CMP @#2*40+T143A,R5 ;R5 = (#T143A*(2*40))?
BEQ ,+4
HLT ;R5 IMPROPERLY LOADED

000144

C=C+1

```

000041
M=M+1
;TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0-77)
SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T144A,R5 ;LOAD R5
MARK 41 ;MARK N=41

;*****
;FUNCTION RESULTS N=41
;TEMP = PC*(2*41) TEMP = #T144A*(2*41)
;PC = R5 PC = #T144A
;SP = TEMP SP = #T144A*(2*41)
;R5 = (TEMP) R5 = (#T144A*(2*41))
;SP = TEMP+2 SP = #T144A*(2*41)+2
;*****

005400 000240 T144A: NOP
005402 010500 MOV R5,R0 ;GET CONTENTS OF R5
005404 023705 005502 CMP #2*41+T144A,R5 ;R5 = (#T144A*(2*41))?
005410 001401 BEQ ,+4
005412 000000 HLT ;R5 IMPROPERLY LOADED

000145
000042
C=C+1
M=M+1
;TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0-77)
SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T145A,R5 ;LOAD R5
MARK 42 ;MARK N=42

;*****
;FUNCTION RESULTS N=42
;TEMP = PC*(2*42) TEMP = #T145A*(2*42)
;PC = R5 PC = #T145A
;SP = TEMP SP = #T145A*(2*42)
;R5 = (TEMP) R5 = (#T145A*(2*42))
;SP = TEMP+2 SP = #T145A*(2*42)+2
;*****

005430 000240 T145A: NOP
005432 010500 MOV R5,R0 ;GET CONTENTS OF R5
005434 023705 005534 CMP #2*42+T145A,R5 ;R5 = (#T145A*(2*42))?
005440 001401 BEQ ,+4
005442 000000 HLT ;R5 IMPROPERLY LOADED

000146
000043
C=C+1
M=M+1
;TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0-77)
SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T146A,R5 ;LOAD R5

```

005456 006443

MARK 43

MARK N=43

```

*****
IFUNCTION RESULTS N=43
ITEMP = PC+(2*43)      TEMP = #T146A+(2*43)
IPC = R5                PC = #T146A
ISP = TEMP              SP = #T146A+(2*43)
IR5 = (TEMP)           R5 = (#T146A+(2*43))
ISP = TEMP+2           SP = #T146A+(2*43)+2
*****

```

005460 000240
 005462 010500
 005464 023705 005566
 005470 001401
 005472 000000

```

T146A:  NOP
        MOV      R5,R0      IGET CONTENTS OF R5
        CMP      @#2*43+T146A,R5  IR5 = (#T146A+(2*43))?
        BEQ      .+4
        HLT
        IR5 IMPROPERLY LOADED

```

000147
 000044

```

C=C+1
M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

005474 010701
 005476 012706 000500
 005502 012705 005510
 005506 006444

```

SCOPE
MOV      #STKPTR,SP      IINITIALIZE THE STACK POINTER
MOV      #T147A,R5      ILOAD R5
MARK     44              IMARK N=44

```

```

*****
IFUNCTION RESULTS N=44
ITEMP = PC+(2*44)      TEMP = #T147A+(2*44)
IPC = R5                PC = #T147A
ISP = TEMP              SP = #T147A+(2*44)
IR5 = (TEMP)           R5 = (#T147A+(2*44))
ISP = TEMP+2           SP = #T147A+(2*44)+2
*****

```

005510 000240
 005512 010500
 005514 023705 005620
 005520 001401
 005522 000000

```

T147A:  NOP
        MOV      R5,R0      IGET CONTENTS OF R5
        CMP      @#2*44+T147A,R5  IR5 = (#T147A+(2*44))?
        BEQ      .+4
        HLT
        IR5 IMPROPERLY LOADED

```

000150
 000045

```

C=C+1
M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

005524 010701
 005526 012706 000500
 005532 012705 005540
 005536 006445

```

SCOPE
MOV      #STKPTR,SP      IINITIALIZE THE STACK POINTER
MOV      #T150A,R5      ILOAD R5
MARK     45              IMARK N=45

```

```

*****
IFUNCTION RESULTS N=45
ITEMP = PC+(2*45)      TEMP = #T150A+(2*45)
IPC = R5                PC = #T150A
*****

```

```

;SP = TEMP          SP = #T150A+(2*45)
;R5 = (TEMP)        R5 = (#T150A+(2*45))
;SP = TEMP+2        SP = #T150A+(2*45)+2
;*****

005540 000240      T150A: NOP
005542 010500      MOV     R5,R0          ;GET CONTENTS OF R5
005544 023705 005652  CMP     @#2*45+T150A,R5 ;R5 = (#T150A+(2*45))?
005550 001401      BEQ     ,+4
005552 000000      HLT                    ;R5 IMPROPERLY LOADED

                C=C+1
                M=M+1
;TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0-77)
                SCOPE
005554 010701      MOV     #STKPTR,SP      ;INITIALIZE THE STACK POINTER
005556 012706 000500  MOV     #T151A,R5      ;LOAD R5
005562 012705 005570  MARK   46              ;MARK N=46
005566 006446

;*****
;FUNCTION RESULTS N=46
;TEMP = PC*(2*46)    TEMP = #T151A+(2*46)
;PC = R5             PC = #T151A
;SP = TEMP           SP = #T151A+(2*46)
;R5 = (TEMP)         R5 = (#T151A+(2*46))
;SP = TEMP+2        SP = #T151A+(2*46)+2
;*****

005570 000240      T151A: NOP
005572 010500      MOV     R5,R0          ;GET CONTENTS OF R5
005574 023705 005704  CMP     @#2*46+T151A,R5 ;R5 = (#T151A+(2*46))?
005600 001401      BEQ     ,+4
005602 000000      HLT                    ;R5 IMPROPERLY LOADED

                C=C+1
                M=M+1
;TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0-77)
                SCOPE
005604 010701      MOV     #STKPTR,SP      ;INITIALIZE THE STACK POINTER
005606 012706 000500  MOV     #T152A,R5      ;LOAD R5
005612 012705 005620  MARK   47              ;MARK N=47
005616 006447

;*****
;FUNCTION RESULTS N=47
;TEMP = PC*(2*47)    TEMP = #T152A+(2*47)
;PC = R5             PC = #T152A
;SP = TEMP           SP = #T152A+(2*47)
;R5 = (TEMP)         R5 = (#T152A+(2*47))
;SP = TEMP+2        SP = #T152A+(2*47)+2
;*****

005620 000240      T152A: NOP

```

005622 010500
005624 023705 005736
005630 001401
005632 000000

MOV R5,R0 ;GET CONTENTS OF R5
CMP #2*47+T152A,R5 ;R5 = (#T152A+(2*47))?
BEQ ,+4
HLT ;R5 IMPROPERLY LOADED

000153
000050

C=C+1
M=M+1

!TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
!OF N (0-77)

005634 010701
005636 012706 000500
005642 012705 005650
005646 006450

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T153A,R5 ;LOAD R5
MARK 50 ;MARK N=50

!*****
!FUNCTION RESULTS N=50
!TEMP = PC+(2*50) TEMP = #T153A+(2*50)
!PC = R5 PC = #T153A
!SP = TEMP SP = #T153A+(2*50)
!R5 = (TEMP) R5 = (#T153A+(2*50))
!SP = TEMP+2 SP = #T153A+(2*50)+2
!*****

005650 000240
005652 010500
005654 023705 005770
005660 001401
005662 000000

T153A: NOP
MOV R5,R0 ;GET CONTENTS OF R5
CMP #2*50+T153A,R5 ;R5 = (#T153A+(2*50))?
BEQ ,+4
HLT ;R5 IMPROPERLY LOADED

000154
000051

C=C+1
M=M+1

!TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
!OF N (0-77)

005664 010701
005666 012706 000500
005672 012705 005700
005676 006451

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T154A,R5 ;LOAD R5
MARK 51 ;MARK N=51

!*****
!FUNCTION RESULTS N=51
!TEMP = PC+(2*51) TEMP = #T154A+(2*51)
!PC = R5 PC = #T154A
!SP = TEMP SP = #T154A+(2*51)
!R5 = (TEMP) R5 = (#T154A+(2*51))
!SP = TEMP+2 SP = #T154A+(2*51)+2
!*****

005700 000240
005702 010500
005704 023705 006022
005710 001401
005712 000000

T154A: NOP
MOV R5,R0 ;GET CONTENTS OF R5
CMP #2*51+T154A,R5 ;R5 = (#T154A+(2*51))?
BEQ ,+4
HLT ;R5 IMPROPERLY LOADED

000155

C=C+1

000052 M=M+1
;TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OP N (0=77)
005714 010701 005500 SCOPE
005716 012706 005730 MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
005722 012705 005730 MOV #T155A,R5 ;LOAD R5
005726 006452 MARK 52 ;MARK N=52

;FUNCTION RESULTS N=52
;TEMP = PC+(2*52) TEMP = #T155A*(2*52)
;PC = R5 PC = #T155A
;SP = TEMP SP = #T155A+(2*52)
;R5 = (TEMP) R5 = (#T155A+(2*52))
;SP = TEMP+2 SP = #T155A*(2*52)+2

005730 000240 T155A: NOP
005732 010500 MOV R5,R0 ;GET CONTENTS OF R5
005734 023705 006054 CMP @#2*52+T155A,R5 ;R5 = (#T155A+(2*52))?
005740 001401 BEQ ,+4
005742 000000 HLT ;R5 IMPROPERLY LOADED

000156 C=C+1
000053 M=M+1
;TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OP N (0=77)
005744 010701 005500 SCOPE
005746 012706 005760 MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
005752 012705 005760 MOV #T156A,R5 ;LOAD R5
005756 006453 MARK 53 ;MARK N=53

;FUNCTION RESULTS N=53
;TEMP = PC+(2*53) TEMP = #T156A*(2*53)
;PC = R5 PC = #T156A
;SP = TEMP SP = #T156A+(2*53)
;R5 = (TEMP) R5 = (#T156A+(2*53))
;SP = TEMP+2 SP = #T156A*(2*53)+2

005760 000240 T156A: NOP
005762 010500 MOV R5,R0 ;GET CONTENTS OF R5
005764 023705 006106 CMP @#2*53+T156A,R5 ;R5 = (#T156A+(2*53))?
005770 001401 BEQ ,+4
005772 000000 HLT ;R5 IMPROPERLY LOADED

000157 C=C+1
000054 M=M+1
;TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OP N (0=77)
005774 010701 005500 SCOPE
005776 012706 006002 MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
006002 012705 006010 MOV #T157A,R5 ;LOAD R5

006006 006454

MARK 54

MARK N=54

```
*****  
FUNCTION RESULTS N=54  
TEMP = PC+(2*54)      TEMP = #T157A+(2*54)  
PC = R5                PC = #T157A  
SP = TEMP              SP = #T157A+(2*54)  
R5 = (TEMP)            R5 = (#T157A+(2*54))  
SP = TEMP+2            SP = #T157A+(2*54)+2  
*****
```

006010 000240
006012 010500
006014 023705 006140
006020 001401
006022 000000

```
T157A:  NOP  
        MOV      R5,R0      ;GET CONTENTS OF R5  
        CMP      @#2*54+T157A,R5 ;R5 = (#T157A+(2*54))?  
        BEQ      ,+4  
        HLT  
        ;R5 IMPROPERLY LOADED
```

000160
000055

C=C+1
M=M+1

TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
OF N (0-77)

006024 010701
006026 012706 000500
006032 012705 006040
006036 006455

```
SCOPE  
MOV     #STKPTR,SP  ;INITIALIZE THE STACK POINTER  
MOV     #T160A,R5   ;LOAD R5  
MARK    55          ;MARK N=55
```

```
*****  
FUNCTION RESULTS N=55  
TEMP = PC+(2*55)      TEMP = #T160A+(2*55)  
PC = R5                PC = #T160A  
SP = TEMP              SP = #T160A+(2*55)  
R5 = (TEMP)            R5 = (#T160A+(2*55))  
SP = TEMP+2            SP = #T160A+(2*55)+2  
*****
```

006040 000240
006042 010500
006044 023705 006172
006050 001401
006052 000000

```
T160A:  NOP  
        MOV      R5,R0      ;GET CONTENTS OF R5  
        CMP      @#2*55+T160A,R5 ;R5 = (#T160A+(2*55))?  
        BEQ      ,+4  
        HLT  
        ;R5 IMPROPERLY LOADED
```

000161
000056

C=C+1
M=M+1

TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
OF N (0-77)

006054 010701
006056 012706 000500
006062 012705 006070
006066 006456

```
SCOPE  
MOV     #STKPTR,SP  ;INITIALIZE THE STACK POINTER  
MOV     #T161A,R5   ;LOAD R5  
MARK    56          ;MARK N=56
```

```
*****  
FUNCTION RESULTS N=56  
TEMP = PC+(2*56)      TEMP = #T161A+(2*56)  
PC = R5                PC = #T161A  
*****
```

```

)SP = TEMP          SP = #T161A+(2*56)
)R5 = (TEMP)        R5 = (#T161A+(2*56))
)SP = TEMP+2        SP = #T161A+(2*56)+2
)*****

006070 000240      T161A: NOP
006072 010500      MOV      R5,R0          )GET CONTENTS OF R5
006074 023705      CMP      #2*56+T161A,R5 )R5 = (#T161A+(2*56))?
006100 001401      BEQ      ,+4
006102 000000      HLT              )R5 IMPROPERLY LOADED

          C=C+1
          M=M+1
)TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
)OF N (0-77)
)SCOPE
)MOV      #STKPTR,SP    )INITIALIZE THE STACK POINTER
)MOV      #T162A,R5     )LOAD R5
)MARK     57           )MARK N=57

)*****
)FUNCTION RESULTS N=57
)TEMP = PC+(2*57)      TEMP = #T162A+(2*57)
)PC = R5              PC = #T162A
)SP = TEMP            SP = #T162A+(2*57)
)R5 = (TEMP)          R5 = (#T162A+(2*57))
)SP = TEMP+2          SP = #T162A+(2*57)+2
)*****

006120 000240      T162A: NOP
006122 010500      MOV      R5,R0          )GET CONTENTS OF R5
006124 023705      CMP      #2*57+T162A,R5 )R5 = (#T162A+(2*57))?
006130 001401      BEQ      ,+4
006132 000000      HLT              )R5 IMPROPERLY LOADED

          C=C+1
          M=M+1
)TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
)OF N (0-77)
)SCOPE
)MOV      #STKPTR,SP    )INITIALIZE THE STACK POINTER
)MOV      #T163A,R5     )LOAD R5
)MARK     60           )MARK N=60

)*****
)FUNCTION RESULTS N=60
)TEMP = PC+(2*60)      TEMP = #T163A+(2*60)
)PC = R5              PC = #T163A
)SP = TEMP            SP = #T163A+(2*60)
)R5 = (TEMP)          R5 = (#T163A+(2*60))
)SP = TEMP+2          SP = #T163A+(2*60)+2
)*****

006150 000240      T163A: NOP

```


006152 010500
006154 023705 006310
006160 001401
006162 000000

MOV R5,R0 ;GET CONTENTS OF R5
CMP @#2*60+T163A,R5 ;R5 = (#T163A+(2*60))?
BEQ ,+4
HLT ;R5 IMPROPERLY LOADED

000164
000061

C=C+1
M=M+1

!TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
!OF N (0=77)

006164 010701
006166 012706 000500
006172 012705 006200
006176 006461

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T164A,R5 ;LOAD R5
MARK 61 ;MARK N=61

!*****!
!FUNCTION RESULTS N=61 !
!TEMP = PC*(2*61) TEMP = #T164A+(2*61) !
!PC = R5 PC = #T164A !
!SP = TEMP SP = #T164A+(2*61) !
!R5 = (TEMP) R5 = (#T164A+(2*61)) !
!SP = TEMP+2 SP = #T164A+(2*61)+2 !
!*****!

006200 000240
006202 010500
006204 023705 006342
006210 001401
006212 000000

T164A: NOP
MOV R5,R0 ;GET CONTENTS OF R5
CMP @#2*61+T164A,R5 ;R5 = (#T164A+(2*61))?
BEQ ,+4
HLT ;R5 IMPROPERLY LOADED

000165
000062

C=C+1
M=M+1

!TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
!OF N (0=77)

006214 010701
006216 012706 000500
006222 012705 006230
006226 006462

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T165A,R5 ;LOAD R5
MARK 62 ;MARK N=62

!*****!
!FUNCTION RESULTS N=62 !
!TEMP = PC*(2*62) TEMP = #T165A+(2*62) !
!PC = R5 PC = #T165A !
!SP = TEMP SP = #T165A+(2*62) !
!R5 = (TEMP) R5 = (#T165A+(2*62)) !
!SP = TEMP+2 SP = #T165A+(2*62)+2 !
!*****!

006230 000240
006232 010500
006234 023705 006374
006240 001401
006242 000000

T165A: NOP
MOV R5,R0 ;GET CONTENTS OF R5
CMP @#2*62+T165A,R5 ;R5 = (#T165A+(2*62))?
BEQ ,+4
HLT ;R5 IMPROPERLY LOADED

000166

C=C+1

```

000063          M=M+1
                )TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
                )OF N (0-77)
000244 010701          SCOPE
000246 012706 000500    MOV      #STKPTR,SP      )INITIALIZE THE STACK POINTER
000252 012705 000260    MOV      #T166A,R5      )LOAD R5
000256 006463          MARK      63          )MARK N=63
    
```

```

)*****
)FUNCTION RESULTS  N=63
)TEMP = PC+(2*63)      TEMP = #T166A+(2*63)
)PC = R5                PC = #T166A
)SP = TEMP              SP = #T166A+(2*63)
)R5 = (TEMP)            R5 = (#T166A+(2*63))
)SP = TEMP+2            SP = #T166A+(2*63)+2
)*****
    
```

```

000260 000240          T166A:  NOP
000262 010500          MOV      R5,R0          )GET CONTENTS OF R5
000264 023705 006426    CMP      @#2*63+T166A,R5 )R5 = (#T166A+(2*63))?
000270 001401          BEQ      ,+4
000272 000000          HLT
                                )R5 IMPROPERLY LOADED
    
```

000167
000064

```

                C=C+1
                M=M+1
                )TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
                )OF N (0-77)
    
```

```

000274 010701          SCOPE
000276 012706 000500    MOV      #STKPTR,SP      )INITIALIZE THE STACK POINTER
000302 012705 006310    MOV      #T167A,R5      )LOAD R5
000306 006464          MARK      64          )MARK N=64
    
```

```

)*****
)FUNCTION RESULTS  N=64
)TEMP = PC+(2*64)      TEMP = #T167A+(2*64)
)PC = R5                PC = #T167A
)SP = TEMP              SP = #T167A+(2*64)
)R5 = (TEMP)            R5 = (#T167A+(2*64))
)SP = TEMP+2            SP = #T167A+(2*64)+2
)*****
    
```

```

000310 000240          T167A:  NOP
000312 010500          MOV      R5,R0          )GET CONTENTS OF R5
000314 023705 006460    CMP      @#2*64+T167A,R5 )R5 = (#T167A+(2*64))?
000320 001401          BEQ      ,+4
000322 000000          HLT
                                )R5 IMPROPERLY LOADED
    
```

000170
000065

```

                C=C+1
                M=M+1
                )TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
                )OF N (0-77)
    
```

```

000324 010701          SCOPE
000326 012706 000500    MOV      #STKPTR,SP      )INITIALIZE THE STACK POINTER
000332 012705 006340    MOV      #T170A,R5      )LOAD R5
    
```

006336 006465

MARK 65

MARK N=65

```
*****  
FUNCTION RESULTS N=65  
TEMP ← PC+(2*65)      TEMP = #T170A+(2*65)  
PC ← R5                PC = #T170A  
SP ← TEMP              SP = #T170A+(2*65)  
R5 ← (TEMP)           R5 = (#T170A+(2*65))  
SP ← TEMP+2           SP = #T170A+(2*65)+2  
*****
```

006340 000240
006342 010500
006344 023705 006512
006350 001401
006352 000000

```
T170A:  NOP  
        MOV      R5,R0      ;GET CONTENTS OF R5  
        CMP      @#2*65+T170A,R5 ;R5 = (#T170A+(2*65))?  
        BEQ      ,+4  
        HLT  
        ;R5 IMPROPERLY LOADED
```

000171
000066

C=C+1
M=M+1

TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
JOF N (0-77)

006354 010701
006356 012706 000500
006362 012705 006370
006366 006466

```
SCOPE  
MOV     #STKPTR,SP  ;INITIALIZE THE STACK POINTER  
MOV     #T171A,R5  ;LOAD R5  
MARK    66         ;MARK N=66
```

```
*****  
FUNCTION RESULTS N=66  
TEMP ← PC+(2*66)      TEMP = #T171A+(2*66)  
PC ← R5                PC = #T171A  
SP ← TEMP              SP = #T171A+(2*66)  
R5 ← (TEMP)           R5 = (#T171A+(2*66))  
SP ← TEMP+2           SP = #T171A+(2*66)+2  
*****
```

006370 000240
006372 010500
006374 023705 006544
006400 001401
006402 000000

```
T171A:  NOP  
        MOV      R5,R0      ;GET CONTENTS OF R5  
        CMP      @#2*66+T171A,R5 ;R5 = (#T171A+(2*66))?  
        BEQ      ,+4  
        HLT  
        ;R5 IMPROPERLY LOADED
```

000172
000067

C=C+1
M=M+1

TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
JOF N (0-77)

006404 010701
006406 012706 000500
006412 012705 006420
006416 006467

```
SCOPE  
MOV     #STKPTR,SP  ;INITIALIZE THE STACK POINTER  
MOV     #T172A,R5  ;LOAD R5  
MARK    67         ;MARK N=67
```

```
*****  
FUNCTION RESULTS N=67  
TEMP ← PC+(2*67)      TEMP = #T172A+(2*67)  
PC ← R5                PC = #T172A  
*****
```

```
ISP * TEMP          SP = #T172A+(2*67)
IR5 * (TEMP)        R5 = (#T172A+(2*67))
ISP * TEMP+2        SP = #T172A+(2*67)+2
*****
```

006420 000240
006422 010500
006424 023705 006576
006430 001401
006432 000000

```
T172A: NOP
      MOV R5,R0          ;GET CONTENTS OF R5
      CMP @#2*67+T172A,R5 ;R5 = (#T172A+(2*67))?
      BEQ ,+4
      HLT                ;R5 IMPROPERLY LOADED
```

000173
000070

```
C=C+1
M=M+1
;TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0=77)
```

006434 010701
006436 012706 000500
006442 012705 006450
006446 006470

```
SCOPE
MOV #STKPTR,SP          ;INITIALIZE THE STACK POINTER
MOV #T173A,R5          ;LOAD R5
MARK 70                 ;MARK N=70
```

```
*****
;FUNCTION RESULTS N=70
;TEMP * PC*(2*70)     TEMP = #T173A+(2*70)
;PC * R5              PC = #T173A
;SP * TEMP            SP = #T173A+(2*70)
;R5 * (TEMP)         R5 = (#T173A+(2*70))
;SP * TEMP+2        SP = #T173A+(2*70)+2
*****
```

006450 000240
006452 010500
006454 023705 006630
006460 001401
006462 000000

```
T173A: NOP
      MOV R5,R0          ;GET CONTENTS OF R5
      CMP @#2*70+T173A,R5 ;R5 = (#T173A+(2*70))?
      BEQ ,+4
      HLT                ;R5 IMPROPERLY LOADED
```

000174
000071

```
C=C+1
M=M+1
;TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0=77)
```

006464 010701
006466 012706 000500
006472 012705 006500
006476 006471

```
SCOPE
MOV #STKPTR,SP          ;INITIALIZE THE STACK POINTER
MOV #T174A,R5          ;LOAD R5
MARK 71                 ;MARK N=71
```

```
*****
;FUNCTION RESULTS N=71
;TEMP * PC*(2*71)     TEMP = #T174A+(2*71)
;PC * R5              PC = #T174A
;SP * TEMP            SP = #T174A+(2*71)
;R5 * (TEMP)         R5 = (#T174A+(2*71))
;SP * TEMP+2        SP = #T174A+(2*71)+2
*****
```

006500 000240

T174A: NOP

006502 010500
006504 023705 006662
006510 001401
006512 000000

MOV R5,R0 ;GET CONTENTS OF R5
CMP #2*71+T174A,R5 ;R5 = (#T174A+(2*71))?
BEQ ,+4
HLT ;R5 IMPROPERLY LOADED

000175
000072

C=C+1
M=M+1

TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
OF N (0-77)

006514 010701
006516 012706 000500
006522 012705 006530
006526 006472

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T175A,R5 ;LOAD R5
MARK 72 ;MARK N=72

FUNCTION RESULTS N=72
TEMP = PC+(2*72) TEMP = #T175A+(2*72)
PC = R5 PC = #T175A
SP = TEMP SP = #T175A+(2*72)
R5 = (TEMP) R5 = (#T175A+(2*72))
SP = TEMP+2 SP = #T175A+(2*72)+2

006530 000240
006532 010500
006534 023705 006714
006540 001401
006542 000000

T175A: NOP
MOV R5,R0 ;GET CONTENTS OF R5
CMP #2*72+T175A,R5 ;R5 = (#T175A+(2*72))?
BEQ ,+4
HLT ;R5 IMPROPERLY LOADED

000176
000073

C=C+1
M=M+1

TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
OF N (0-77)

006544 010701
006546 012706 000500
006552 012705 006560
006556 006473

SCOPE
MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
MOV #T176A,R5 ;LOAD R5
MARK 73 ;MARK N=73

FUNCTION RESULTS N=73
TEMP = PC+(2*73) TEMP = #T176A+(2*73)
PC = R5 PC = #T176A
SP = TEMP SP = #T176A+(2*73)
R5 = (TEMP) R5 = (#T176A+(2*73))
SP = TEMP+2 SP = #T176A+(2*73)+2

006560 000240
006562 010500
006564 023705 006746
006570 001401
006572 000000

T176A: NOP
MOV R5,R0 ;GET CONTENTS OF R5
CMP #2*73+T176A,R5 ;R5 = (#T176A+(2*73))?
BEQ ,+4
HLT ;R5 IMPROPERLY LOADED

000177

C=C+1

000074 M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)
SCOPE
006574 010701 MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
006576 012706 000500 MOV #T177A,R5 ;LOAD R5
006602 012705 006610 MARK 74 ;MARK N=74
006606 006474

IFUNCTION RESULTS N=74
ITEMP ← PC*(2*74) TEMP = #T177A*(2*74)
IPC ← R5 PC = #T177A
ISP ← TEMP SP = #T177A*(2*74)
IR5 ← (TEMP) R5 = (#T177A*(2*74))
ISP ← TEMP+2 SP = #T177A*(2*74)+2

006610 000240 T177A: NOP
006612 010500 MOV R5,R0 ;GET CONTENTS OF R5
006614 023705 007000 CMP #2*74+T177A,R5 ;R5 = (#T177A*(2*74))?
006620 001401 BEQ ,+4
006622 000000 HLT ;R5 IMPROPERLY LOADED

000200 C=C+1
000075 M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)
SCOPE
006624 010701 MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
006626 012706 000500 MOV #T200A,R5 ;LOAD R5
006632 012705 006640 MARK 75 ;MARK N=75
006636 006475

IFUNCTION RESULTS N=75
ITEMP ← PC*(2*75) TEMP = #T200A*(2*75)
IPC ← R5 PC = #T200A
ISP ← TEMP SP = #T200A*(2*75)
IR5 ← (TEMP) R5 = (#T200A*(2*75))
ISP ← TEMP+2 SP = #T200A*(2*75)+2

006640 000240 T200A: NOP
006642 010500 MOV R5,R0 ;GET CONTENTS OF R5
006644 023705 007032 CMP #2*75+T200A,R5 ;R5 = (#T200A*(2*75))?
006650 001401 BEQ ,+4
006652 000000 HLT ;R5 IMPROPERLY LOADED

000201 C=C+1
000076 M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)
SCOPE
006654 010701 MOV #STKPTR,SP ;INITIALIZE THE STACK POINTER
006656 012706 000500 MOV #T201A,R5 ;LOAD R5
006662 012705 006670 MARK 76 ;MARK N=76

006666 006476

MARK 76

MARK N=76

```

*****
IFUNCTION RESULTS N=76
ITEMP = PC+(2*76)      TEMP = #T201A+(2*76)
IPC = R5                PC = #T201A
ISP = TEMP              SP = #T201A+(2*76)
IR5 = (TEMP)            R5 = (#T201A*(2*76))
ISP = TEMP+2            SP = #T201A+(2*76)+2
*****

```

006670 000240
 006672 010500
 006674 023705 007064
 006700 001401
 006702 000000

```

T201A: NOP
        MOV      R5,R0          ;GET CONTENTS OF R5
        CMP      @#2*76+T201A,R5 ;R5 = (#T201A*(2*76))
        BEQ      ,+4
        HLT
        ;R5 IMPROPERLY LOADED

```

000202
 000077

```

C=C+1
M=M+1
;TEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
;OF N (0-77)

```

006704 010701
 006706 012706 000500
 006712 012705 006720
 006716 006477

```

SCOPE
MOV      #STKPTR,SP          ;INITIALIZE THE STACK POINTER
MOV      #T202A,R5          ;LOAD R5
MARK     77                  ;MARK N=77

```

```

*****
IFUNCTION RESULTS N=77
ITEMP = PC+(2*77)      TEMP = #T202A+(2*77)
IPC = R5                PC = #T202A
ISP = TEMP              SP = #T202A+(2*77)
IR5 = (TEMP)            R5 = (#T202A*(2*77))
ISP = TEMP+2            SP = #T202A+(2*77)+2
*****

```

006720 000240
 006722 010500
 006724 023705 007116
 006730 001401
 006732 000000

```

T202A: NOP
        MOV      R5,R0          ;GET CONTENTS OF R5
        CMP      @#2*77+T202A,R5 ;R5 = (#T202A*(2*77))
        BEQ      ,+4
        HLT
        ;R5 IMPROPERLY LOADED

```

000203
 000100

```

C=C+1
M=M+1
;TEST THAT MARK RETURNS FROM SUBROUTINE PROPERLY

```

006734 010701
 006736 012706 000500
 006742 012705 000007
 006746 010546
 006750 012746 006400
 006754 010605
 006756 004767 000002
 006762 000401
 006764 000205

```

T203:  SCOPE
        MOV      #STKPTR,SP          ;INITIALIZE THE STACK POINTER
        MOV      #7,R5              ;LOAD R5
        MOV      R5,@(SP)           ;SAVE CURRENT R5
        MOV      #MARK0,-(SP)       ;PUSH MARK ON THE STACK (N=0)
        MOV      SP,R5              ;SET UP PARAMETER POINTER
        JSR      7,T203A            ;GO TO SUBROUTINE
        BR       T204                ;EXIT TEST
T203A: RTS      5                    ;EXIT SUBROUTINE

```

```

;TEST THAT SUBROUTINE LEAVES STACK POINTER POSITIONED PROPERLY
T204:  SCOPE
      MOV    #STKPTR,SP    ;INITIALIZE THE STACK POINTER
      MOV    #7,R5         ;LOAD R5
      MOV    R5,*(SP)      ;SAVE CURRENT R5
      MOV    #MARK0,-(SP)  ;PUSH MARK ON THE STACK (N=0)
      MOV    SP,R5         ;SET UP PARAMETER POINTER
      JSR    7,T204A       ;GO TO SUBROUTINE
      BR     T204R
T204A: RTS    5            ;EXIT SUBROUTINE
T204B:  MOV    SP,R0        ;GET STACK POINTER
      CMP    #STKPTR,R0    ;STACK POINTER AT PROPER POSITION?
      BEQ    T205
      HLT
      ;ERROR INCORRECT STACK POINTER

;TEST THAT SUBROUTINE LEAVES R5 UNDISTURBED
T205:  SCOPE
      MOV    #STKPTR,SP    ;INITIALIZE THE STACK POINTER
      MOV    #7,R5         ;LOAD R5
      MOV    R5,*(SP)      ;SAVE CURRENT R5
      MOV    #MARK0,-(SP)  ;PUSH MARK ON THE STACK (N=0)
      MOV    SP,R5         ;SET UP PARAMETER POINTER
      JSR    7,T205A       ;GO TO SUBROUTINE
      BR     T205B
T205A:  RTS    5            ;EXIT SUBROUTINE
T205B:  MOV    R5,R0        ;GET R5
      CMP    #7,R0         ;WAS R5 = TO #7?
      BEQ    T206
      HLT
      ;ERROR CONTENTS OF R5 ARE INCORRECT

;TEST THAT SUBROUTINE CAN 'PICK' A PARAMETER
T206:  SCOPE
      CLR    R0
      MOV    #7,R5         ;LOAD R5
      MOV    R5,*(SP)      ;SAVE CURRENT R5
      MOV    #1,*(SP)      ;PUSH A PARAMETER ON THE STACK
      MOV    #MARK1,-(SP)  ;PUSH MARK ON THE STACK (N=1)
      MOV    SP,R5         ;SET UP PARAMETER POINTER
      JSR    7,T206A       ;GO TO SUBROUTINE
      BR     T206R
T206A:  MOV    2(5),R0      ;GET THE PARAMETER
      RTS    5            ;EXIT SUBROUTINE
T206B:  CMP    #1,R0        ;DID SUBROUTINE GET THE PARAMETER
      BEQ    T207
      HLT

;TEST THAT MARK DOES NOT CHANGE THE PSW
T207:  SCOPE
      MOV    #STKPTR,SP    ;INITIALIZE THE STACK POINTER
      MOV    #T207A,R5
      CLR    PSW
      MARK   0
T207A:  MOV    PSW,R0      ;GET PSW

```


007172 001401
 007174 000000

REQ T210
 HLT

TEST THAT MARK DOES NOT CHANGE THE PSW

007176 010701
 007200 012706 000500
 007204 012705 007220
 007210 012767 000357 170560
 007216 006400
 007220 013700 177776
 007224 012706 000500
 007230 022700 000357
 007234 001401
 007236 000000

T210: SCOPE
 MOV #STKPTR,SP ;INITIALIZE TH ESTACK POINTER
 MOV #T210A,R5
 MOV #357,PSW
 MARK 0
 T210A: MOV @#PSW,R0 ;GET PSW RESULTS
 MOV #STKPTR,SP ;RESET STACK PTR
 CMP #357,R0 ;DID PSW CHANGE
 BEQ .+4
 HLT

007240 005267 171534
 007244 026727 171530 070000
 007252 001402
 007254 000167 171526
 007260 112767 000007 170300
 007266 105767 170272
 007272 100375
 007274 013702 000042
 007300 001404
 007302 004712
 007304 000240
 007306 000240
 007310 000240
 007312 000167 171464
 000001

END: INC ICNT ;INCREMENT PASS COUNT
 CMP ICNT,#70000
 BEQ DONE ;GO TO DONE IF 1000 PASSES COMPLETED
 JMP BEGIN ;RESTART TEST
 DONE: MOV #7,TPBUF ;RING BELL
 TSTB TPCSR
 BPL .-4
 MOV @#42,%2 ;GET DECTAPE MONITOR RETURN ADDRESS
 BEQ DONE1 ;DO NOT RETURN IF (42)=0
 JSR 7,(2) ;RETURN TO DECTAPE MONITOR
 NOP
 NOP
 NOP
 DONE1: JMP START
 .END

BEGIN	= 001006	C	= 000203	DISPLA	= 177570	DONE	007260
DONE1	007312	END	007240	HLT	= 000000	ICNT	001000
M	= 000100	MARK0	= 006400	MARK1	= 006401	MARK2	= 006402
PC	=X000007	PSW	= 177776	R0	=X000000	R1	=X000001
R2	=X000002	R3	=X000003	R4	=X000004	R5	=X000005
SCOPE	= 010701	SP	=X000006	START	001002	STKPTR	= 000500
SWR	= 177570	TPBUF	= 177566	TPCSR	= 177564	T0A	001054
T1A	001072	T10A	001326	T100A	003646	T101A	003674
T102A	003722	T103A	003750	T104A	004000	T105A	004030
T106A	004060	T107A	004110	T11A	001354	T110A	004140
T111A	004170	T112A	004220	T113A	004250	T114A	004300
T115A	004330	T116A	004360	T117A	004410	T12A	001402
T120A	004440	T121A	004470	T122A	004520	T123A	004550
T124A	004600	T125A	004630	T126A	004660	T127A	004710
T13A	001430	T130A	004740	T131A	004770	T132A	005020
T133A	005050	T134A	005100	T135A	005130	T136A	005160
T137A	005210	T14A	001456	T140A	005240	T141A	005270
T142A	005320	T143A	005350	T144A	005400	T145A	005430
T146A	005460	T147A	005510	T15A	001504	T150A	005540
T151A	005570	T152A	005620	T153A	005650	T154A	005700
T155A	005730	T156A	005760	T157A	006010	T16A	001532
T160A	006040	T161A	006070	T162A	006120	T163A	006150
T164A	006200	T165A	006230	T166A	006260	T167A	006310
T17A	001560	T170A	006340	T171A	006370	T172A	006420
T173A	006450	T174A	006500	T175A	006530	T176A	006560
T177A	006610	T2A	001120	T20A	001606	T200A	006640
T201A	006670	T202A	006720	T203	006734	T203A	006764
T204	006766	T204A	007016	T204B	007020	T205	007032
T205A	007062	T205B	007064	T206	007076	T206A	007130
T206B	007136	T207	007146	T207A	007166	T21A	001634
T210	007176	T210A	007220	T22A	001662	T23A	001710
T24A	001736	T25A	001764	T20A	002012	T27A	002040
T3A	001150	T30A	002066	T31A	002114	T32A	002142
T33A	002170	T34A	002216	T35A	002244	T36A	002272
T37A	002320	T4A	001176	T40A	002346	T41A	002374
T42A	002422	T43A	002450	T44A	002476	T45A	002524
T46A	002552	T47A	002600	T5A	001224	T50A	002626
T51A	002654	T52A	002702	T53A	002730	T54A	002756
T55A	003004	T56A	003032	T57A	003060	T6A	001252
T60A	003106	T61A	003134	T62A	003162	T63A	003210
T64A	003236	T65A	003264	T66A	003312	T67A	003340
T7A	001300	T70A	003366	T71A	003414	T72A	003442
T73A	003470	T74A	003516	T75A	003544	T76A	003572
T77A	003620	UBREAK	= 177770		= 007316		

ERRORS DETECTED: 0

1
2

.TITLE MAINDEC-11-DCKBE-B PDP11/45 RTI/T TEST
.LIST ME
.NLIST MC,MD,SEQ
.ABS

!TEST DCKBE- THIS IS A TEST OF THE RTT & RTI INSTRUCTIONS.
!THE RTI INSTRUCTION ALLOWS A 'T' BIT TRAP IMMEDIATLY FOLLOWING THE
!RTI, WHEREAS THE RTT WILL EXECUTE THE INSTRUCTION FOLLOWING THE RTT
!BEFORE THE 'T' BIT TRAP IS ALLOWED. (LIKE 11/20 RTI)
!NOTE: THIS TEST EXECUTES 'T' BIT TRAPS.
!THE TEST TESTS RTT/RTI IN ALL THREE STATES (KERNEL,SUPERVISOR,AND USER).

!STARTING PROCEEDURE
! LOAD ADDRESS=200
! PRESS START
! STACK POINTER IS AT 500
! BELL WILL RING WHEN TEST IS COMPLETE

000000
010701
177570
177570
177776
177770
177564
177566

HLT=HALT
SCOPE=010701
SWR=177570
DISPLAY=177570
PSW=177776
UBREAK=177770
TPCSR=177564
TPBUF=177566
!MOVE PC TO R1
!ADDRESS OF CONSOL SWITCH REGISTER
!ADDRESS OF CONSOL DISPLAY REGISTER
!ADDRESS OF PROCESSOR STATUS WORD
!ADDRESS OF PDP11/45 MICRO BREAK REGISTER

000500

!*****INITIAL STACK POINTER=0500*****
STKPTR=0500 !INITIAL STACK SETTING

!GENERAL REGISTERS

000000
000001
000002
000003
000004
000005
000006
000006
000006
000006
000007
000020
000004
000010
000014

R0=%0
R1=%1
R2=%2
R3=%3
R4=%4
R5=%5
SP=%6
KSP=%6 !KERNEL'S STACK POINTER
SSP=%6 !SUPERVISORY STACK POINTER
USP=%6 !USER'S STACK POINTER
PC=%7
TBIT=20
ERRVEC=4 !ADDRESS OF ERROR TRAP VECTOR
RESVEC=10 !ADDRESS OF RESERVED INST. TRAP VECTOR
TBITVEC=14 !ADDRESS OF 'T' BIT TRAP VECTOR

!PROCESSOR STATUS BITS

000000
040000
140000
000000
010000
030000
004000
000200

KM=0 !KERNEL MODE
SM=40000 !SUPERVISORY MODE
UM=140000 !USER MODE
PKM=0 !PREVIOUS KERNEL MODE
PSM=10000 !PREVIOUS SUPERVISORY MODE
PUM=30000 !PREVIOUS USER MODE
REG=4000 !REGISTER SET BIT
PRTY4=200 !PRIORITY LEVEL 4

000340		PRTY7=340	IPRIORITY LEVEL 7
000000		PRTY0=0	IPRIORITY LEVEL 0
		ISTACK POINTERS	
000770		KPTR=770	IKERNELS INITIAL STACK POINTER
000750		SPTR=750	ISUPERVISORS INITIAL STACK POINTER
000730		UPTR=730	IUSER INITIAL STACK SETTING
000000		=0	
		.NLIST MC;MD,SEQ	
000000	000002	,+2	
000002	000000	HALT	
000004	000006	,+2	
000006	000000	HALT	
000010	000012	,+2	
000012	000000	HALT	
000014	000016	,+2	
000016	000000	HALT	
000020	000022	,+2	
000022	000000	HALT	
000024	000026	,+2	
000026	000000	HALT	
000030	000032	,+2	
000032	000000	HALT	
000034	000036	,+2	
000036	000000	HALT	
000040	000042	,+2	
000042	000000	HALT	
000044	000046	,+2	
000046	000000	HALT	
000050	000052	,+2	
000052	000000	HALT	
000054	000056	,+2	
000056	000000	HALT	
000060	000062	,+2	
000062	000000	HALT	
000064	000066	,+2	
000066	000000	HALT	
000070	000072	,+2	
000072	000000	HALT	
000074	000076	,+2	
000076	000000	HALT	
000100	000102	,+2	
000102	000000	HALT	
000104	000106	,+2	
000106	000000	HALT	
000110	000112	,+2	
000112	000000	HALT	
000114	000116	,+2	
000116	000000	HALT	
000120	000122	,+2	
000122	000000	HALT	
000124	000126	,+2	
000126	000000	HALT	
000130	000132	,+2	

000132	000000	HALT
000134	000136	,+2
000136	000000	HALT
000140	000142	,+2
000142	000000	HALT
000144	000146	,+2
000146	000000	HALT
000150	000152	,+2
000152	000000	HALT
000154	000156	,+2
000156	000000	HALT
000160	000162	,+2
000162	000000	HALT
000164	000166	,+2
000166	000000	HALT
000170	000172	,+2
000172	000000	HALT
000174	000176	,+2
000176	000000	HALT
000200	000202	,+2
000202	000000	HALT
000204	000206	,+2
000206	000000	HALT
000210	000212	,+2
000212	000000	HALT
000214	000216	,+2
000216	000000	HALT
000220	000222	,+2
000222	000000	HALT
000224	000226	,+2
000226	000000	HALT
000230	000232	,+2
000232	000000	HALT
000234	000236	,+2
000236	000000	HALT
000240	000242	,+2
000242	000000	HALT
000244	000246	,+2
000246	000000	HALT
000250	000252	,+2
000252	000000	HALT
000254	000256	,+2
000256	000000	HALT
000260	000262	,+2
000262	000000	HALT
000264	000266	,+2
000266	000000	HALT
000270	000272	,+2
000272	000000	HALT
000274	000276	,+2
000276	000000	HALT
000300	000302	,+2
000302	000000	HALT
000304	000306	,+2

001062	012706	000500	MOV	#STKPTR,SP	INITIALIZE THE STACK POINTER
001066	005046		CLR	-(SP)	
001070	012746	001076	MOV	#T1A, -(SP)	
001074	000006		RTT		

```

001076 010600          T1A:  MOV    SP,R0          IGET THE STACK POINTER
001100 022700 000500    CMP    #STKPTR,R0       IDID RTT POP TWO WORDS
001104 001401          BEQ    T2
001106 000000          HLT                    IERROR! RTT DID NOT POP TWO WORDS

I TEST THAT RTT POPS THE TWO WORDS OFF THE STACK AND INTO THE
IPC AND PSW RESPECTIVELY.
T2:  SCOPE
001110 010701          MOV    #STKPTR,SP       ISET STACK PTR
001112 012706 000500    CLR    -(SP)           IPUSH STATUS ON THE STACK
001116 005046          MOV    #T2A,-(SP)      IPUSH PC ON THE STACK
001120 012746 001134    MOV    #357,PSW        IPRESET STATUS
001124 012767 000357 176644 RTT
001132 000006          T2A:  MOV    PSW,R0       IGET STATUS AFTER RTT
001134 016700 176636    TST    R0              ICHECK THAT RTT LOADED
001140 005700          BEQ    T3              INEW STATUS FROM STACK
001142 001401          HLT                    IERROR! RTT FAILED TO LOAD STATUS
001144 000000

T3:  SCOPE
001146 010701          MOV    #STKPTR,SP       ISET STACK PTR
001150 012706 000500    MOV    #357,-(SP)      IPUSH STATUS ON THE STACK
001154 012746 000357    MOV    #T3A,-(SP)      IPUSH PC ON THE STACK
001160 012746 001172    CLR    PSW             IPRE SET STATUS
001164 005067 176606    RTT
001170 000006          T3A:  MOV    PSW,R0       IGET STATUS AFTER RTT
001172 016700 176600    CMP    #357,R0         ICHECK STATUS AFTER RTT
001176 022700 000357    BEQ    T4
001202 001411          HLT                    IERROR! INCORRECT STATUS AFTER RTT
001204 000000

ICHECK THAT CP CAN TIME OUT TRAP
001206 012737 001224 000004 MOV    #TORET,#ERRVEC  ILOAD TIMEOUT TRAP VECTOR
001214 005037 173000    CLR    #173000        IADDRESS 173000 ALWAYS TIMES OUT ON
                                IDATIP/DATO BUS CYCLE
001220 000000          HLT                    IERROR! FAILED TO TIME OUT TRAP
001222 000672          BR     BEGIN          ILOOP TEST
001224 022626    TORET: CMP    (6)+,(6)+ IRESTORE THE STACK

I TEST THAT THE CP DOES THE INSTRUCTION FOLLOWING THE RTT IF THE TBIT
IGETS SET (BY THE RTT)
T4:  SCOPE
001226 010701          MOV    #STKPTR,SP       ISET STACK PTR
001230 012706 000500    MOV    #T4B,TBITVEC    ILOAD 'T' BIT TRAP VECTOR AND
001234 012767 001266 176552 CLR    TBITVEC+2       IITS STATUS
001242 005067 176550    MOV    #TBIT,-(SP)     ISET UP STACK SUCH THAT RTT CAUSES
001246 012746 000020          I'T' BIT TO SET

001252 012746 001260          MOV    #T4A,-(SP)
001256 000006          RTT
001260 000240          T4A:  NOP              ICP SHOULD DO THIS INSTRUCTION AND
                                ITHEN TRAP
001262 000000          T4AA: HLT              IPROCESSER DID NOT ACKNOWLEDGE 'T'
                                IBIT TRAP
001264 000405          BR     T5
    
```



```

001266 011600          T4B:  MOV      (SP),R0      IGET PC WHEN 'T' TRAP OCCURRED
001270 022700 001262    CMP      #T4AA,R0      IDID CP DO THE NOP
001274 001401          BEQ      T5
001276 000000          HLT
                                IERROR! 'T' BIT DID NOT TRAP AFTER
                                ITHE NOP
ITEST THAT THE CP ACKNOWLEDGES THE 'T' BIT IF THE INSTRUCTION FOLLOWING
ITHE RTT (WHICH SETS THE 'T' BIT) IS A THREE WORD (MOV PSW,2) INSTRUCTION.
T5:  SCOPE
001300 010701          MOV      #STKPTR,SP    ISET STACK PTR
001302 012706 000500    MOV      #T5B,TBITVEC ILOAD 'T' BIT TRAP VECTOR
001306 012767 001344 176500  MOV      #TBIT, -(SP)  ISETUP TO SET 'T' BIT
001314 012746 000020    MOV      #T5A, -(SP)
001320 012746 001332    CLR      TEMP
001324 005067 177452    RTT
                                IRTT SETS THE 'T' BIT
001330 000006          T5A:  MOV      PSW,TEMP    IMOVE STATUS TO MEM. LOCATION 2
001332 016767 176440 177442  HLT
                                I'T' BIT DID NOT CAUSE A TRAP
001340 000000          BR      T6
001342 000407          T5B:  MOV      TEMP,R0    IGET STATUS THAT RTT LOADED
001344 016700 177432    CMP      #TBIT,R0      IDID RTT LOAD 'T' BIT INTO STATUS
001350 022700 000020    BEQ      T6
001354 001402          HLT
                                IERROR! EITHER RTT DID NOT LOAD
001356 000000          HLT
                                I'T' BIT INTO STATUS OR 'T' BIT TRAPPED
                                IBEFORE PSW WAS SAVED.
ITEST THAT THE CP ACKNOWLEDGES THE 'T' BIT BEING SET BEFORE EXECUTING
ITHE INSTRUCTION FOLLOWING AN RTI
T6:  SCOPE
001362 010701          MOV      #STKPTR,SP    ISET STACK PTR
001364 012706 000500    MOV      #T6B,TBITVEC ILOAD 'T' BIT TRAP VECTOR
001370 012767 001412 176416  MOV      #TBIT, -(SP)  ISET UP TO SET 'T' BIT
001376 012746 000020    MOV      #T6A, -(SP)
001402 012746 001410    RTI
                                IRTT SETS THE 'T' BIT
001406 000002          T6A:  HLT
                                IERROR! CP SHOULD HAVE TRAPPED
001410 000000          IBEFORE THIS INSTRUCTION WAS EXECUTED.

001412 000240          T6B:  NOP
001414 000400          BR      T7
                                IGO TO NEXT TEST

ITEST THAT THE CP DOES NOT ACKNOWLEDGE THE 'T' BIT IF THE INSTRUCTION
IFOLLOWING THE RTT CLEARS THE 'T' BIT,

```

```

001416 010701          T7:  SCOPE
001420 012706 000500    MOV      #STKPTR,SP      ;THIS IS WHAT THE STACK LOCKS
001424 012767 001462 176362  MOV      #T7C,TBITVEC  ;LIKE BEFORE THE RTT
001432 005046          CLR      -(SP)         ;670 * T7A * SP=670
001434 012746 001456    MOV      #T7B,-(SP)    ;672 * 20 *
001440 012746 000020    MOV      #TBIT,-(SP)   ;674 * T7B *
001444 012746 001454    MOV      #T7A,-(SP)    ;676 * 0 *
001450 000240          NOP                    ;700 * ? * UNKNOWN
                                ;STACK AFTER THE RTT
001452 000006          RTT                    ;674 * T7B * SP=674
                                ;676 * 0 * AND 'T' BIT
                                ;700 * ? * IS SET
                                ;STACK AFTER THE RTI
001454 000002          T7A:  RTI                    ;700 * ? * SP=700
                                ; AND 'T' BIT
                                ; IS CLEAR

001456 000240          T7B:  NOP
001460 000401          BR      EL45
001462 000000          T7C:  HLT                    ;CP ACKNOWLEDGED THE 'T' BIT

;TEST IF THE PROCESSER IS AN 11/45
001464 012737 002164 000010 EL45:  MOV      #END,#10    ;SET RESERVED INST TRAP VECTOR
001472 000230          SPL      0            ;WILL TRAP TO END IF AN 11/40

;TEST THAT AN RTI LOADS THE STATUS WORD PROPERLY IN KERNEL MODE.
001474 010701          T10:  SCOPE
001476 005067 176274    CLR      PSW
001502 012706 000770    MOV      #KPTR,KSP     ;SET KERNEL STACK POINTER
001506 012716 054340    MOV      #SM+PSM+REG+PRTY7,(KSP)
001512 012746 001526    MOV      #T10A,-(KSP)
001516 005067 177260    CLR      TEMP
001522 000002          RTI                    ;POP 2 TOP WORDS OF KERNEL STACK INTO
                                ;STATUS AND PC
001524 000000          HLT
001526 016767 176244 177246 T10A:  MOV      PSW,TEMP     ;ERROR! RTI FAILED
001534 005067 176236    CLR      PSW           ;GET STATUS WORD
001540 022767 054340 177234  CMP      #SM+PSM+REG+PRTY7,TEMP
001546 001401          BEQ      ,+4
001550 000000          HLT                    ;ERROR! INCORRECT STATUS

;THIS TEST SAME AS ABOVE EXCEPT THAT RTT IS FROM KERNEL TO USER MODE.
001552 010701          T11:  SCOPE
001554 012767 000340 176214  MOV      #PRTY7,PSW
001562 012706 000770    MOV      #KPTR,KSP
001566 012716 174000    MOV      #UM+PUM+REG,(KSP)
001572 012746 001606    MOV      #T11A,-(KSP)
001576 005067 177200    CLR      TEMP
001602 000006          RTT
001604 000000          HLT                    ;ERROR! RTT FAILED

001606 016767 176164 177166 T11A:  MOV      PSW,TEMP     ;GET STATUS WORD
001614 005067 176156    CLR      PSW
001620 022767 174000 177154  CMP      #UM+PUM+REG,TEMP

```

```

001626 001401          BEQ      ,+4
001630 000000          HLT              ;ERROR! INCORRECT STATUS WORD

;TEST RTT LOADS THE STATUS WORD CORRECTLY IN SUPERVISORY MODE
001632 010701          T12:  SCOPE
001634 012767 040000 176134  MOV      #SM,PSW          ;SUPERVISORY MODE!!!
001642 012706 000750          MOV      #SPTR,SSP        ;SET SUPERVISORY STACK POINTER
001646 012716 174340          MOV      #UM+PUM+REG+PRTY7,(SSP)
001652 012746 001660          MOV      #T12A,-(SSP)
001656 000006          RTT
001660 016767 176112 177114 T12A: MOV      PSW,TEMP        ;GET STATUS WORD
001666 005067 176104          CLR      PSW             ;KERNEL MODE!!!
001672 022767 174000 177102  CMP      #UM+PUM+REG,TEMP
001700 001401          BEQ      ,+4
001702 000000          HLT              ;ERROR! INCORRECT STATUS

;TEST THAT RTI DOES NOT CLEAR BITS15-11 IN SUPERVISORY MODE.
001704 010701          T13:  SCOPE
001706 012767 074200 176062  MOV      #SM+PUM+REG+PRTY4,PSW
001714 012706 000750          MOV      #SPTR,SSP
001720 012716 000340          MOV      #PRTY7,(SSP)    ;LOAD RETURN STATUS ON SUPER STACK
001724 012746 001736          MOV      #T13A,-(SSP)
001730 005067 177046          CLR      TEMP
001734 000006          RTT
001736 016767 176034 177036 T13A: MOV      PSW,TEMP
001744 005067 176026          CLR      PSW
001750 022767 074200 177024  CMP      #SM+PUM+REG+PRTY4,TEMP
001756 001401          BEQ      ,+4
001760 000000          HLT              ;ERROR! INCORRECT STATUS AFTER RTT

;TEST THAT RTI LOADS STATUS PROPERLY IN USER MODE
001762 010701          T14:  SCOPE
001764 012767 140000 176004  MOV      #UM,PSW          ;USER MODE!!!
001772 012706 000730          MOV      #UPTR,USP        ;SET USER'S STACK POINTER
001776 012716 174340          MOV      #UM+PUM+REG+PRTY7,(USP)
002002 012746 002014          MOV      #T14A,-(USP)
002006 005067 176770          CLR      TEMP
002012 000002          RTI
002014 016767 175756 176760 T14A: MOV      PSW,TEMP        ;GET STATUS WORD
002022 005067 175750          CLR      PSW             ;KERNEL MODE!!!
002026 022767 174000 176746  CMP      #UM+PUM+REG,TEMP
002034 001401          BEQ      ,+4
002036 000000          HLT              ;ERROR! INCORRECT STATUS AFTER RTI

;TEST THAT RTT DOES NOT CLEAR STATUS BITS 15-11 IN USER MODE.
002040 010701          T15:  SCOPE
002042 012767 174340 175726  MOV      #UM+PUM+REG+PRTY7,PSW    ;USER MODE!!!
002050 012706 000730          MOV      #UPTR,USP
002054 005046          CLR      -(USP)
002056 012746 002070          MOV      #T15A,-(USP)
002062 005067 176714          CLR      TEMP
002066 000006          RTT
    
```

```

002070 016767 175702 176704 T15A: MOV PSW,TEMP
002076 005067 175674 CLR PSW ;KERNEL MODE!!!
002102 022767 174340 176672 CMP #UM+PUM+REG+PRTY7,TEMP
002110 001401 BEQ ,+4
002112 000000 HLT

;TEST THAT BITS 13-11 CAN BE CLEARED BY RTT IN KERNEL MODE,
002114 010701 T16: SCOPE
002116 012767 034000 175652 MOV #PUM+REG,PSW ;KERNEL MODE!!!
002124 012706 000770 MOV #KPTR,KSP ;SET KERNEL STACK
002130 005016 CLR (KSP)
002132 012746 002146 MOV #T16A,*(KSP)
002136 012767 177777 176636 MOV #-1,TEMP ;PRESET TEMP
002144 000006 RTT
002146 016767 175624 176626 T16A: MOV PSW,TEMP ;GET STATUS AFTER RTT
002154 005767 176622 TST TEMP ;CHECK STATUS AFTER RTT
002160 001401 BEQ ,+4
002162 000000 HLT ;ERROR! INCORRECT STATUS AFTER RTT

002164 012737 000012 000010 END: MOV #12,0#10 ;SET RESERVED INST TO HALT AT 12
002172 005267 176602 INC ICNT
002176 005767 176576 TST ICNT
002202 001402 BEQ DONE
002204 000167 176600 JMP BEGIN
002210 012767 000007 175350 DONE: MOV #7,TPBUF
002216 005767 175342 TSTB TPCSR
002222 000375 RPL ,=4
002224 013702 000042 MOV 0#42,%2 ;GET DECTAPE MONITOR RETURN ADDRESS
002230 001404 BEQ DONE1 ;DO NOT RETURN IF (42)=0
002232 004712 JSR 7,(2) ;RETURN TO DECTAPE MONITOR
002234 000240 NOP ;ACT11
002236 000240 NOP ;OVERLAY
002240 000240 NOP ;AREA
002242 000167 176536 DONE1: JMP START ;RESTART TEST

000001 ;END

```

BEGIN	001010	DISPLA	= 177570	DONE	002210	DONE1	002242
EL45	001464	END	002164	ERRVEC	= 000004	HLT	= 000000
ICNT	001000	KH	= 000000	KPTR	= 000770	KSP	=X000006
PC	=X000007	PKM	= 000000	PRTY0	= 000000	PRTY4	= 000200
PRTY7	= 000340	PSM	= 010000	PSW	= 177776	PUM	= 030000
REG	= 004000	RESVEC	= 000010	R0	=X000000	R1	=X000001
R2	=X000002	R3	=X000003	R4	=X000004	R5	=X000005
SCOPE	= 010701	SM	= 040000	SP	=X000006	SPTR	= 000750
SSP	=X000006	START	001004	STKPTR	= 000500	SWR	= 177570
TBIT	= 000020	TBITVE	= 000014	TEMP	001002	TORET	001224
TPBUF	= 177566	TPCSR	= 177564	T0	001040	T0A	001056
T1	001060	T1A	001076	T10	001474	T10A	001526
T11	001552	T11A	001606	T12	001632	T12A	001660
T13	001704	T13A	001736	T14	001762	T14A	002014
T15	002040	T15A	002070	T16	002114	T16A	002146
T2	001110	T2A	001134	T3	001146	T3A	001172
T4	001226	T4A	001260	T4AA	001262	T4B	001266
T5	001300	T5A	001332	T5B	001344	T6	001362
T6A	001410	T6B	001412	T7	001416	T7A	001454
T7B	001456	T7C	001462	UBREAK	= 177770	UM	= 140000
UPTR	= 000730	USP	=X000006	,	= 002246		

ERRORS DETECTED: 0

