

IDENTIFICATION

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PRODUCT CODE: MAINDEC-11-DBKEB-A-D
PRODUCT NAME: KE11F (PDP-11 FIS) EXERCISER
DATE CREATED: 1-AUG-72
MAINTAINER: DIAGNOSTIC GROUP
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1. ABSTRACT

This program exercises the KE11F floating point instructions (FADD, FSUB, FMUL, FDIV) with random number patterns. The answers are checked against results obtained using the corresponding FORTRAN software routines. About 200 passes should be run to establish credibility.
2. REQUIREMENTS
 - 2.1 Equipment

PDP-11 (KD11A) standard computer with KE11F option
 - 2.2 Storage

The routines use memory locations 0 - 17500. The map at the end of the listings shows the absolute locations of the FORTRAN math routines which were assembled separately and linked to the main program via LNKX11 on a DECsystem-10.
 - 2.3 Preliminary programs

MAINDEC-11-DBKEA-A KE11F Instruction Tests.
3. LOADING PROCEDURE

Use standard procedure for ABS tapes.
4. STARTING PROCEDURE
 - 4.1 Control switch settings

See 5.1.1 (all down for worst case testing)
 - 4.2 Starting address

The program should always be started at 200.
 - 4.3 Program and/or operator action
 - 1) Load program into memory using ABS loader.
 - 2) Load address 200.
 - 3) Set switches (see 5.1.1) All down for worst case.

- 4) Press start.
- 5) The program will loop and bell will ring once every pass.

5. OPERATING PROCEDURE

5.1 Operational switch settings

SW<15> = 1 HALT ON ERROR
SW<14> = 1 SCOPE LOOP
SW<13> = 1 INHIBIT PRINTOUT
SW<12> = 1 INHIBIT TRACE TRAPPING
SW<11> = 1 INHIBIT ITERATIONS OF SURTEST
SW<10> = 1 BELL ON ERROR
 0 BELL ON PASS COMPLETE
SW<09> = 1 LOOP ON ERROR
SW<08> = 1 LOOP ON TEST IN SW<610>
SW<07> = 1 INPUT DATA FROM THE TELETYPE

Caution: SW<810> are also used for ROM word match with KM11 maintenance card.

5.2 Subroutine Abstracts

5.2.1 TYPIN

If SW<7> is on a 0, the program calculates a pseudo-random number to be used as input data. If SW<7> is on a 1, the program will ask for input data from the teletype at the beginning of each pass. The same data is used with all instructions (FADD, FSUB, FMUL, FDIV) for the entire pass. If SW<7> is put down after entering the data entry routine, that data is used as the starting numbers for the random number generator.

The input format is:

Type input data:
A1: NNNNNN
A2: NNNNNN
B1: NNNNNN
B2: NNNNNN

Where:

A1 = left word of first argument
A2 = right word of first argument
B1 = left word of second argument
B2 = right word of second argument

I.e. A1,A2(+,=,*,/)B1,B2 = answer

NNNNN = data typed by the operator

A1, A2, B1, and B2 must be 16 bit left justified octal numbers,

E.G.

42 = 000042
200002 = not accepted (17 bits)
4812 = not accepted (8 is not octal)

They are assumed to be in floating point format, I.E. bit 15 of A1 and B1 are the sign bits, bits 7-14 of A1 and B1 are the exponents (excess 128 format) and the rest (bits 0-6 of A1 and B1 and all of A2 and B2) form the mantissa (normalized) less the hidden bit. For more information read the maintenance manual. A1, A2, B1, and B2 are put into RAND.A, RAND.B, RAND.C, and RAND.D respectively.

5.2.2 FORTAN

This routine make use of "polish mode" to link the FORTRAN MATH PACKAGE ROUTINES TO CALCULATE THE EXPECTED RESULT,

LOCATIONS \$ADD1, \$ADD2 contain addition answer,
Locations \$SUB1, \$SUB2 contain subtract answer,
Locations \$MUL1, \$MUL2 contain multiply answer,
Locations \$DIV1, \$DIV2 contain divide answer,

If a floating error occurs (overflow, underflow, or divide by zero), these answers are meaningless. The locations \$ADDPS, \$SUBPS, \$MULPS, or \$DIVPS contains 340 and \$ADDER, \$SUBER, \$MULER, or \$DIVER, contain the conditions codes of the error.

5.2.3 SCOPE

This subroutine call is placed between each subtest in the test section. It records the starting address of each subtest as it is being entered in location "LADS". If a scope loop is requested, the current subtest will be looped upon. SW<11> on a 1 inhibits iteration of subtests. The contents of LADS may be used to determine the last subtest successfully completed.

5.2.4 HLT

This routine prints out an error message (See 6.1). To inhibit typeouts, put SW<13> on a 1.

5.2.5 TRTRAP

If SW<12> is on a 0, the T-bit will be set on alternate passes. When the T-bit is set, the processor traps after each instruction. The first instruction executed upon trapping is an "RTT" which returns to the interrupted sequence of instructions. This sequence is continued until the end of the program is reached.

5.2.6 TRAPCATCHER

A "+2" = "HALT" sequence is repeated from 0 = 776 to catch any unexpected traps. Thus any unexpected traps or interrupts will HALT at the vector + 2.

5.2.7 FLOATING POINT TRAP (to 244)

All tests set the floating point trap vector (244) to point to the instruction following the floating point instruction. Thus, whether or not a trap occurs is only detected if the data or the stack pointer(s) are wrong.

6. ERRORS

6.1 Error printout

There are two formats for error typeout; one for normal numbers and one for floating errors (overflow, underflow and divide by zero).

6.1.1 The normal format (when no floating point error is indicated) is as follows:

```
AAAAAA MMMMMM,MMMMMM S MMMMMM,MMMMMM
      PSW  SP    ANSWER
EXPECT:  NNN  NNN  NNNNNN,NNNNNN
GOT:    NNN  NNN  NNNNNN,NNNNNN
```

Where:

AAAAAA ==> PC of HLT instruction
MMMMMM ==> input data (RAND,A, RAND,B, RAND,C, RAND,D)
S ==> type of operation being tested (+, -, *, or /)

NNNNNN ==> results
PSW = processor status word
SP = stack pointer (not necessarily R6)
ANSWER = resulting answer off the stack

6.1.2 When a floating point error is indicated (overflow, underflow, or divide by zero) the format is as follows:

```
AAAAAA MMMMM,MMMMM S MMMMM,MMMMM
      PSW SP ANS1 ANS2 ANS3 ANS4 ANS5 ANS6
EXPECT: NNN NNN NNNNNN NNNNNN NNNNNN NNNNNN NNNNNN NNNNNN
GOT:    NNN NNN NNNNNN NNNNNN NNNNNN NNNNNN NNNNNN NNNNNN
```

Where:

AAAAAA ==> PC of HLT instruction
MMMMMM ==> input data (RAND,A, RAND,B, RAND,C, RAND,D)
S ==> type of operation being tested (+,=,*, or /)
NNNNNN ==> results
PSW = processor status word
SP = stack pointer (not necessarily R6)
ANS1 = PC of interrupted instruction (should be FIS)
ANS2 = PSW at interrupt time
ANS3 = input data (RAND,C)
ANS4 = " " (RAND,D)
ANS5 = " " (RAND,A)
ANS6 = " " (RAND,B)

To find the failing test, look at the listing above the address typed.

6.2 Error recovery

Restart at 200

6.3 Error count

An error count is kept in "ERRORS" (LOC 1002). It is cleared by restarting at 200.

7. RESTRICTIONS

None

8. MISCELLANEOUS

8.1 Execution time

A bell will ring within 5 seconds with all switches down. More than 200 passes should be run to insure a wide variety of number patterns.

8.2 Stack Pointer

Stack is initially set to 604

8.3 Pass counter

A 32 bit (2 words) pass count is kept in "PCNT" (LOC 1004,1006). It is cleared by restarting at 200.

8.4 Power Fail

Each test can be power failed with no errors. To use, start the test as usual and power down then up at any time. The program should type "POWER" and continue to run from where power fail interrupted with no other typeouts.

9. PROGRAM DESCRIPTION

This program tests all the FIS instructions on the KE11F using all registers except 7 for the "stack pointer". The program has many subtests (the code between 2 SCOPE statements) which are run 256 times before continuing to the next. SW<11> on a 1 causes each subtest to be run only once. The address ICNT (LOC 1000) contains the iteration count in the left byte and the test number in the right byte. All the subtests should be run sequentially by starting at 200 not by starting at the beginning of the subtest. To loop on a particular subtest, put the test number (see listing) in SW<6:0> of the switch register and SW<8> on a 1. This test will be looped upon until SW<8> is put on a 0 or the right byte is changed. If the test is non-existent, the program will be run as usual.

The FORTRAN math routines, which are used to calculate the correct answers, were taken unmodified from the PDP-11 FORTRAN package and assembled as separate modules. They were linked to the main programs via LNKX11 on a DECsystem-10 which produces a binary tape in the normal absolute format. Thus, the program loads and runs just like any other diagnostic program.

1	SWITCH SETTINGS AND ERROR TYPEOUT FORMAT
55	EQUALITIES
104	VECTOR AND ANSWER AREA
162	RANDOM NUMBER GENERATOR, TRACE TSR, AND FIS TSR
164	FOUR WORD RANDOM NUMBER GENERATOR
183	SETUP AREA
277	TEST 1: EXERCISE FADD R0
363	TEST 2: EXERCISE FSUB R1
449	TEST 3: EXERCISE FMUL R2
535	TEST 4: EXERCISE FDIV R3
621	TEST 5: EXERCISE FADD R4
693	TEST 6: EXERCISE FSUB R5
765	TEST 7: EXERCISE FMUL SP
838	TEST 10: EXERCISE FDIV R0
910	TEST 11: EXERCISE FADD R1
982	TEST 12: EXERCISE FSUB R2
1054	TEST 13: EXERCISE FMUL R3
1126	TEST 14: EXERCISE FDIV R4
1198	TEST 15: EXERCISE FADD R5
1270	TEST 16: EXERCISE FSUB SP
1343	TEST 17: EXERCISE FMUL R0
1415	TEST 20: EXERCISE FDIV R1
1487	TEST 21: EXERCISE FADD R2
1559	TEST 22: EXERCISE FSUB R3
1631	TEST 23: EXERCISE FMUL R4
1703	TEST 24: EXERCISE FDIV R5
1775	TEST 25: EXERCISE FADD SP
1848	TEST 26: EXERCISE FSUB R0
1920	TEST 27: EXERCISE FMUL R1
1992	TEST 30: EXERCISE FDIV R2
2064	TEST 31: EXERCISE FADD R3
2136	TEST 32: EXERCISE FSUB R4
2208	TEST 33: EXERCISE FMUL R5
2280	TEST 34: EXERCISE FDIV SP
2353	END ROUTINE
2381	READ OCTAL INPUT ROUTINE (READIN)
2407	POLISH MODE ROUTINES TO ACCESS FORTRAN ROUTINES
2523	PUSH DATA ROUTINE (PUSHR)
2534	SCOPE ROUTINE
2566	HLT ROUTINE (ERROR TYPEOUT)
2721	TTY INPUT ROUTINE
2746	TYPE ROUTINE
2775	OCTAL DUMP OF A WORD & 18 BIT ADDRESS TYPED
2810	POWER DOWN AND UP ROUTINES

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,TITLE MAINDEC-11-DBKEB-A KE11F (PDP-11 FIS) EXERCISER,
 ,ASECT
 ,GLOBL \$ADR,\$SBR,\$MLR,\$DVR,\$ERR,\$ERRA

;COPYRIGHT 1972, DIGITAL EQUIPMENT CORP., MAYNARD, MASS
 ;PROGRAM BY KEN CHAPMAN
 ,REMI

SWITCH	USE
7	TTY DATA INPUT
8	LOOP ON TEST IN SWC610
9	LOOP ON ERROR
10	0=BELL ON PASS COMPLETED 1=BELL ON ERROR
11	INHIBIT ITERATIONS
12	INHIBIT TRACE TRAP
13	INHIBIT ERROR TYPEOUTS
14	LOOP ON TEST
15	HALT ON ERROR

ERROR MESSAGE FORMATS:

1, WHEN NO FLOATING POINT ERROR IS INDICATED

AAAAAA MMMMMM,MMMMM S MMMMM,MMMMMM
 PSW SP ANSWER
 EXPECT: NNN NNN NNNNNN,NNNNNN
 GOT: NNN NNN NNNNNN,NNNNNN

WHERE:

AAAAAA ==> PC OF HLT INSTRUCTION
 MMMMMM ==> INPUT DATA (RAND,A, RAND,B, RAND,C, RAND,D)
 S ==> TYPE OF OPERATION BEING TESTED (+,=,*, OR /)
 NNN ==> RESULTS
 PSW = PROCESSOR STATUS WORD
 SP = STACK POINTER (NOT NECESSARILY R6)
 ANSWER = RESULTING ANSWER OFF THE STACK

2, WHEN A FLOATING POINT ERROR IS INDICATED (OVERFLOW, UNDERFLOW,
 OR DIVIDE BY ZERO):

AAAAAA MMMMMM,MMMMM S MMMMM,MMMMMM
 PSW SP ANS1 ANS2 ANS3 ANS4 ANS5 ANS6
 EXPECT: NNN NNN NNNNNN NNNNNN NNNNNN NNNNNN NNNNNN NNNNNN
 GOT: NNN NNN NNNNNN NNNNNN NNNNNN NNNNNN NNNNNN NNNNNN

WHERE:

AAAAAA, MMMMMM, S, NNN, PSW, AND SP ARE THE SAME AS ABOVE,
 ANS1 = PC OF INTERRUPTED INSTRUCTION (SHOULD BE FIS)
 ANS2 = PSW AT INTERRUPT TIME
 ANS3 = INPUT DATA (RAND,C)
 ANS4 = " " (RAND,D)
 ANS5 = " " (RAND,A)
 ANS6 = " " (RAND,B):

54	104400	SCOPE=	TRAP
55	104000	HLT=	EMT
56	000004	TYPE=	IOT
57	177776	PS=	177776
58	177570	SWR=	177570
59	177570	DISPLAY=	SWR
60	000007	BELL=	7
61	000000	R0=	X0
62	000001	R1=	X1
63	000002	R2=	X2
64	000003	R3=	X3
65	000004	R4=	X4
66	000005	R5=	X5
67	000005	TTY=	X5
68	000006	SP=	X6
69	000007	PC=	X7
70	100000	SW15=	100000
71	040000	SW14=	40000
72	020000	SW13=	20000
73	010000	SW12=	10000
74	004000	SW11=	4000
75	002000	SW10=	2000
76	001000	SW09=	1000
77	000400	SW08=	400
78	000001	BIT0 =	000001
79	000002	BIT1 =	000002
80	000004	BIT2 =	000004
81	000010	BIT3 =	000010
82	000020	BIT4 =	000020
83	000040	BIT5 =	000040
84	000100	BIT6 =	000100
85	000200	BIT7 =	000200
86	000400	BIT8 =	000400
87	001000	BIT9 =	001000
88	002000	BIT10 =	002000
89	004000	BIT11 =	004000
90	010000	BIT12 =	010000
91	020000	BIT13 =	020000
92	040000	BIT14 =	040000
93	100000	BIT15 =	100000
94	000000	LEVEL0 =	000
95	000040	LEVEL1 =	040
96	000100	LEVEL2 =	100
97	000140	LEVEL3 =	140
98	000200	LEVEL4 =	200
99	000240	LEVEL5 =	240
100	000300	LEVEL6 =	300
101	000340	LEVEL7 =	340

102					
103		000000	,	0	TRAP CATCHER FROM 0 = 776
104					
105		000200	,	200	
106					
107	000200	000167	000604	JMP	BEGIN I JUMP TO STARTING ADDRESS OF PROGRAM
108					
109		000204	,	204	
110	000204	000167	000736	JMP	START I RESTART ADDRESS
111					
112		000600	,	600	

THE FOLLOWING LOCATIONS ARE USED FOR THE STACKS; R6 IS INITIALLY SET TO 604 (STACK0), AS ARE THE OTHER REGISTERS (R0 THRU R5) WHEN THEY ARE TO BE USED AS THE FLOATING POINT STACK POINTER, THE DATA IS PUT DIRECTLY ONTO THE STACK, NOT BY PUSHES; IF NO ERROR OCCURES THE STACK POINTER (ANY REGISTER) IS POINTING TO 610 (ANS1), IF AN ERROR OCCURES, R6 IS POINTING TO 604, SO THE TRAP PUTS THE RETURN ADDRESS AND PS IN 600 (STK1) AND 602 (STK2) RESPECTIVELY,

123	000600	000000	STK1:	0
124	000602	000000	STK2:	0
125	000604	000000	STK3:	STACK0: 0
126	000606	000000	STK4:	STACK2: 0
127	000610	000000	STK5:	STACK4: ANS1: 0
128	000612	000000	STK6:	STACK6: ANS2: 0
129	000614	000000	SPSW:	0
130	000616	000000	SSP:	0
131				
132	000620	000000	RAND.A:	0
133	000622	000000	RAND.B:	0
134	000624	000000	RAND.C:	0
135	000626	000000	RAND.D:	0
136				
137	000630	000000	\$ADDPS:	0
138	000632	000000	\$ADD1:	0
139	000634	000000	\$ADD2:	0
140	000636	000000	\$ADDER:	0
141				
142	000640	000000	\$SUBPS:	0
143	000642	000000	\$SUB1:	0
144	000644	000000	\$SUB2:	0
145	000646	000000	\$SUBER:	0
146				
147	000650	000000	\$MULPS:	0
148	000652	000000	\$MUL1:	0
149	000654	000000	\$MUL2:	0
150	000656	000000	\$MULER:	0
151				
152	000660	000000	\$DIVPS:	0
153	000662	000000	\$DIV1:	0
154	000664	000000	\$DIV2:	0
155	000666	000000	\$DIVER:	0


```

178
179          001000          ,*      1000
180
181 001000 000000          ICNT: 0          ;ITERATION COUNT (HI BYTE); TEST # (LO BYTE)
182 001002 000000          ERRORS: 0        ;ERROR COUNT LOCATION
183 001004 000000 000000  PCNT: 0,0        ;PASS COUNT LOCATION
184
185 001010 012706 000604          BEGIN: MOV #STACK0,SP          ;SET UP STACK
186 001014 012737 000752 000014  MOV #YESRT,0#14        ;SET UP TRACE TRAP
187 001022 012700 000020          MOV #20,R0
188 001026 012720 015256          MOV #,IOT,(R0)+        ;SET UP IOT VECTOR
189 001032 012720 000340          MOV #340,(R0)+
190 001036 012720 015536          MOV #PDOWNS,(R0)+     ;SET UP POWER FAIL VECTOR
191 001042 012720 000340          MOV #340,(R0)+
192 001046 012720 014020          MOV #HLTS,(R0)+      ;SET EMT VECTOR
193 001052 012720 000340          MOV #340,(R0)+
194 001056 012720 013644          MOV #SCOPES,(R0)+    ;SET TRAP VECTOR
195 001062 012720 000340          MOV #340,(R0)+
196 001066 012737 000754 000244  MOV #FISTRP,0#244     ;SET UP FIS VECTOR
197 001074 012737 000340 000246  MOV #340,0#246
198 001102 012767 123456 177510  MOV #123456,RAND,A    ;PRIME THE RANDOM NUMBER GENERATOR
199 001110 012767 107654 177504  MOV #107654,RAND,B
200 001116 012767 070707 177500  MOV #070707,RAND,C
201 001124 012767 125252 177476  MOV #125252,RAND,D
202 001132 005067 177644          CLR ERRORS           ;CLEAR ERROR COUNTER
203 001136 005067 177642          CLR PCNT             ;CLEAR PASS COUNTER
204 001142 005067 177640          CLR PCNT+2
205 001146 012706 000604          START: MOV #STACK0,SP  ;SET UP STACK
206 001152 012737 000140 177776  MOV #140,0#PS        ;SET UP PROCESSOR STATUS
207 001160 005067 177614          CLR ICNT
208 001164 005067 012622          CLR LADS
209 001170 005067 177476          CLR RNDFLG          ;CLEAR THE ROUNDING FLAGS
210 001174 105737 177570          TSTB #SWR           ;CHECK FOR TTY INPUT
211 001200 100403          BMI TYPIN
212 001202 004767 177466          JSR PC,RAND48
213 001206 000464          BR FORTAN           ;BRANCH TO ROUTINE TO CALCULATE ANSWERS
214
215          ;THE FOLLOWING ROUTINE ACCEPTS DATA FROM THE TELETYPE;
216          ; THE FORMAT IS FIXED: A1,A2 (0,1,2,3,4,5,6,7,8,9) B1,B2;
217          ; THE PROGRAM ASKES FOR ONE ARGUMENT AT A TIME, AND RE-ASKES
218          ; WHEN INVALID DATA IS ENTERED.
219
220 001210 000004 001214          TYPIN: TYPE, ,+2
221 001214 005015 054524 042520  ,ASCIZ <15><12>"TYPE INPUT DATA!"<15><12>
222 001222 044440 050116 052125
223 001230 042040 052101 035101
224 001236 005015 000
225          ,EVEN
226 001242 000004 001246          1$: TYPE, ,+2
227 001246 030501 020072 000040  ,ASCIZ "A1: "
228 001254 004567 011502          JSR R5, READIN      ;ACCEPT FIRST ARGUMENT FROM THE TTY
229 001260 000620
230 001262 103752          RAND,A
231 001264 000004 001270          BCS TYPIN
          TYPE, ,+2

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232	001270	031101	020072	000040		,ASCIZ	"A2; "		
233	001276	004567	011460			JSR	R5,	READIN	IACCEPT SECOND ARGUEMENT FROM THE TTY
234	001302	000622				RAND,B			
235	001304	103767				BCS	2\$		
236	001306	001340				BNE	TYPIN		
237	001310	000004	001314		3\$:	TYPE,	,+2		
238	001314	030502	020072	000040		,ASCIZ	"B1; "		
239	001322	004567	011434			JSR	R5,	READIN	IACCEPT THIRD ARGUEMENT FROM THE TTY
240	001326	000624				RAND,C			
241	001330	103767				BCS	3\$		
242	001332	001326				BNE	TYPIN		
243	001334	000004	001340		4\$:	TYPE,	,+2		
244	001340	031102	020072	000040		,ASCIZ	"B2; "		
245	001346	004567	011410			JSR	R5,	READIN	IACCEPT FOURTH ARGUEMENT FROM THE TTY
246	001352	000626				RAND,D			
247	001354	103767				BCS	4\$		
248	001356	001314				BNE	TYPIN		
249									
250	001360	005067	177244		FORTAN:	CLR	\$ADDP		I CLEAR ALL THE PS SAVE LOCATIONS
251	001364	005067	177250			CLR	\$SUBPS		
252	001370	005067	177254			CLR	\$MULPS		
253	001374	005067	177260			CLR	\$DIVPS		
254									
255	001400	004467	011460			JSR	X4,	SPOLSH	I ENTER POLISH MODE
256	001404	013044				\$PUSH			I PUSH THE DATA ONTO THE STACK
257	001406	000000G				\$ADR			I FORTAN ADD ROUTINE
258	001410	013066				\$POPAD			I SAVE THE ADD ANSWERS
259	001412	013044				\$PUSH			I PUSH THE DATA ONTO THE STACK
260	001414	000000G				\$SBR			I FORTAN SUBTRACT ROUTINE
261	001416	013144				\$POPSB			I SAVE THE SUBTRACT ANSWERS
262	001420	013044				\$PUSH			I PUSH THE DATA ONTO THE STACK
263	001422	000000G				\$MLR			I FORTAN MULTIPLY ROUTINE
264	001424	013222				\$POPML			I SAVE THE MULTIPLY ANSWERS
265	001426	013044				\$PUSH			I PUSH THE DATA ONTO THE STACK
266	001430	000000G				\$DVR			I FORTAN DIVIDE ROUTINE
267	001432	013300				\$POPDV			I SAVE THE DIVIDE ANSWERS
268	001434	013412				SEXIT			I EXIT POLISH MODE
269									
270	001436	104400				SCOPE			


```

271
272
273 |*****
|TEST 1: EXERCISE FADD (PDP-11 FLOATING ADD INSTRUCTION)
| RAND,A,RAND,B + RAND,C,RAND,D = ANS1,ANS2
| STACK POINTER = R0
|*****
274
275
276
277
278 201440 012700 000604 TST1: MOV #STACK0,R0 ;SET UP THE STACK POINTER
279 201444 004767 012130 JSR PC, PUSHR ;PUT THE DATA ON THE STACK
280
281 201450 000240 NOP
282 201452 075000 FADD+ R0 ;FLOATING ADD ON THE R0 STACK
283
284 201454 013767 177776 177132 1S: MOV #NPS, SPSW ;SAVE PROCESSOR STATUS
285 201462 010067 177130 MOV R0, SSP ;SAVE THE STACK POINTER
286 201466 026767 177136 177120 6S: CMP SADDPS, SPSW ;CHECK THE PROCESSOR STATUS
287 201474 001023 BNE 4S ;GO CHECK FOR ROUNDING ERROR
288
289 201476 105767 177112 TSTB SPSW ;CHECK FOR ERROR
290 201502 100464 BMI 2S ;BRANCH IF ERROR
291
292 201504 012767 000610 177156 MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
293 201512 026767 177152 177076 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
294 201520 001401 BEQ ,+4 ;BRANCH IF OK
295 201522 104000 HLT ;STACK POINTER NOT EQUAL TO #STACK4
296
297 201524 026767 177102 177056 CMP SADD1, ANS1 ;CHECK THE ANSWER
298 201532 001004 BNE 4S
299 201534 026767 177074 177050 CMP SADD2, ANS2 ;CHECK THE ANSWER
300 201542 001515 BEQ 3S
301 201544 032767 000002 177120 4S: BIT #BIT1, RNDFLG ;CHECK THE ROUNDING FLAG
302 201552 001022 BNE 5S
303 201554 052767 000002 177110 BIS #BIT1, RNDFLG ;SET ROUNDING FLAG
304 201562 062767 000001 177044 ADD #1, SADD2 ;INCREMENT FORTRAN ANSWER
305 201570 005567 177036 ADC SADD1 ;ADD CARRY
306 201574 102334 BVC 6S ;BRANCH IF NO OVERFLOW
307 201576 000257 CCC ;CLEAR ALL CONDITION CODES
308 201600 000262 SEV ;SET VBIT
309 201602 013767 177776 177026 MOV #NPS, SADDR ;SET PSW FOR OVERFLOW
310 201610 012767 000340 177012 MOV #340, SADDPS ;SET STACK PSW
311 201616 000723 BR 6S ;TRY AGAIN
312
313 201620 132767 000002 177045 5S: BITB #BIT1, RNDFLG+1 ;CHECK "DEROUNDING" FLAG
314 201626 001010 BNE 7S ;BRANCH IF SET
315 201630 152767 000002 177035 BISB #BIT1, RNDFLG+1 ;SET "DEROUNDING" FLAG
316 201636 162767 000001 176770 SUB #1, SADD2 ;RESTORE ORIGINAL ANSWER
317 201644 005667 176762 SBC SADD1 ;SUBTRACT CARRY
318 201650 104000 7S: HLT ;WRONG PSW OR ANSWER
319
320 201652 000451 BR 3S
321
322 201654 012767 000604 177006 2S: MOV #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
323 201662 026767 177002 176726 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
324 201670 001401 BEQ ,+4 ;BRANCH IF OK

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363 002000 012701 000604
364 002004 004767 011570
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366 002010 000240
367 002012 075011
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369 002014 013767 177776 176572 1S:
370 002022 010167 176570
371 002026 026767 176606 176560 6S:
372 002034 001023
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374 002036 105767 176552
375 002042 100464
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377 002044 012767 000610 176616
378 002052 026767 176612 176536
379 002060 001401
380 002062 104000
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382 002064 026767 176552 176516
383 002072 001004
384 002074 026767 176544 176510
385 002102 001515
386 002104 032767 000004 176560 4S:
387 002112 001022
388 002114 052767 000004 176550
389 002122 062767 000001 176514
390 002130 005567 176506
391 002134 102334
392 002136 000257
393 002140 000262
394 002142 013767 177776 176476
395 002150 012767 000340 176462
396 002156 000723
397
398 002160 132767 000004 176505 5S:
399 002166 001010
400 002170 152767 000004 176475
401 002176 162767 000001 176440
402 002204 005667 176432
403 002210 104000 7S:
404
405 002212 000451
406
407 002214 012767 000604 176446 2S:
408 002222 026767 176442 176366
409 002230 001401

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;*****
;TEST 2: EXERCISE FSUB (PDP-11 FLOATING SUBTRACT INSTRUCTION)
; RAND,A,RAND,B = RAND,C,RAND,D = ANS1,ANS2
; STACK POINTER = R1
;*****

TST2: MOV #STACK0,R1 ;SET UP THE STACK POINTER
JSR PC, PUSHR ;PUT THE DATA ON THE STACK

NOP
FSUB+ R1 ;FLOATING SUBTRACT ON THE R1 STACK

MOV @#PS, SPSW ;SAVE PROCESSOR STATUS
MOV R1, SSP ;SAVE THE STACK POINTER
CMP \$\$SUBPS, SPSW ;CHECK THE PROCESSOR STATUS
BNE 4\$;GO CHECK FOR ROUNDING ERROR

TSTB SPSW ;CHECK FOR ERROR
BMI 2\$;BRANCH IF ERROR

MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
CMP SAVSTK, SSP ;CHECK THE STACK POINTER
BEQ ,+4 ;BRANCH IF OK
HLT ;STACK POINTER NOT EQUAL TO #STACK4

CMP \$\$SUB1, ANS1 ;CHECK THE ANSWER
BNE 4\$
CMP \$\$SUB2, ANS2 ;CHECK THE ANSWER
BEQ 3\$
BIT #BIT2, RNDFLG ;CHECK THE ROUNDING FLAG
BNE 5\$
BIS #BIT2, RNDFLG ;SET ROUNDING FLAG
ADD #1, \$\$SUB2 ;INCREMENT FORTRAN ANSWER
ADC \$\$SUB1 ;ADD CARRY
BVC 6\$;BRANCH IF NO OVERFLOW
CCC ;CLEAR ALL CONDITION CODES
SEV ;SET V-BIT
MOV @#PS, \$\$SUBR ;SET UP PSW FOR OVERFLOW
MOV #340, \$\$SUBPS ;SET UP TRAP PSW
BR 6\$;TRY IT AGAIN

BITB #BIT2, RNDFLG+1 ;CHECK "DEROUNDING" FLAG
BNE 7\$;BRANCH IF SET
BISB #BIT2, RNDFLG+1 ;SET "DEROUNDING" FLAG
SUB #1, \$\$SUB2 ;RESTORE ORIGINAL ANSWER
SBC \$\$SUB1 ;SUBTRACT CARRY
HLT ;WRONG PSW OR ANSWER

BR 3\$

MOV #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
CMP SAVSTK, SSP ;CHECK THE STACK POINTER
BEQ ,+4 ;BRANCH IF OK


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443 ;*****
444 ;TEST 3: EXERCISE FMUL (PDP-11 FLOATING MULTIPLY INSTRUCTION)
445 ; RAND,A,RAND,B * RAND,C,RAND,D = AN$1,ANS2
446 ; STACK POINTER = R2
447 ;*****
448 002340 012702 000604 TST3: MOV #STACK0,R2 ;SET UP THE STACK POINTER
449 002344 004767 011230 JSR PC, PUSHR ;PUT THE DATA ON THE STACK
450
451 002350 000240 NOP
452 002352 075022 FMUL+ R2 ;FLOATING MULTIPLY ON THE R2 STACK
453
454 002354 013767 177776 176232 1$: MOV #PS, SPSW ;SAVE PROCESSOR STATUS
455 002362 010267 176230 MOV R2, SSP ;SAVE THE STACK POINTER
456 002366 026767 176256 176220 6$: CMP SMULPS, SPSW ;CHECK THE PROCESSOR STATUS
457 002374 001023 BNE 4$ ;GO CHECK FOR ROUNDING ERROR
458
459 002376 105767 176212 TSTB SPSW ;CHECK FOR ERROR
460 002402 100464 BMI 2$ ;BRANCH IF ERROR
461
462 002404 012767 000610 176256 MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
463 002412 026767 176252 176176 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
464 002420 001401 BEQ ,+4 ;BRANCH IF OK
465 002422 104000 HLT ;STACK POINTER NOT EQUAL TO #STACK4
466
467 002424 026767 176222 176156 CMP SMUL1, AN$1 ;CHECK THE ANSWER
468 002432 001004 BNE 4$
469 002434 026767 176214 176150 CMP SMUL2, AN$2 ;CHECK THE ANSWER
470 002442 001515 BEQ 3$
471 002444 032767 000010 176220 4$: BIT #BIT3, RNDFLG ;CHECK THE ROUNDING FLAG
472 002452 001022 BNE 5$
473 002454 052767 000010 176210 BIS #BIT3, RNDFLG ;SET ROUNDING FLAG
474 002462 062767 000001 176164 ADD #1, SMUL2 ;INCREMENT FORTRAN ANSWER
475 002470 005567 176156 ADC SMUL1 ;ADD CARRY
476 002474 102334 BVC 6$ ;BRANCH IF NO OVERFLOW
477 002476 000257 CCC ;CLEAR ALL CONDITION CODES
478 002500 000262 SEV ;SET V-BIT
479 002502 013767 177776 176146 MOV #PS, SMULR ;SET UP PSW FOR OVERFLOW
480 002510 012767 000340 176132 MOV #340, SMULPS ;SET UP TRAP PSW
481 002516 000723 BR 6$ ;TRY IT AGAIN
482
483 002520 132767 000010 176145 5$: BITB #BIT3, RNDFLG+1 ;CHECK "DEROUNDING" FLAG
484 002526 001010 BNE 7$ ;BRANCH IF SET
485 002530 152767 000010 176135 BISB #BIT3, RNDFLG+1 ;SET "DEROUNDING" FLAG
486 002536 162767 000001 176110 SUB #1, SMUL2 ;RESTORE ORIGINAL ANSWER
487 002544 005667 176102 SBC SMUL1 ;SUBTRACT CARRY
488 002550 104000 7$: HLT ;WRONG PSW OR ANSWER
489
490 002552 000451 BR 3$
491
492 002554 012767 000604 176106 2$: MOV #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
493 002562 026767 176102 176026 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
494 002570 001401 BEQ ,+4 ;BRANCH IF OK

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495	022572	104000			HLT			;STACK POINTER FOULED UP
496								
497	022574	022767	002354	175776	CMP	#15,	STK1	;CHECK THE RTI ADDRESS ON THE STACK
498	022622	001401			BEG	,+4		;BRANCH IF OK
499	022624	104000			HLT			;RTI ADDRESS NOT EQUAL TO #15
500								
501	022626	026767	176044	175766	CMP	SMULR,	STK2	;CHECK THE PSW ON THE STACK
502	022614	001401			BEG	,+4		;BRANCH IF OK
503	022616	104000			HLT			;RTI PSW NOT EQUAL TO 200
504								
505	022620	026767	176000	175756	CMP	RAND,C,	STK3	;CHECK THE DATA ON THE STACK
506	022626	001401			BEG	,+4		;BRANCH IF OK
507	022630	104000			HLT			;STK3 NOT EQUAL TO RAND,C
508								
509	022632	026767	175770	175746	CMP	RAND,D,	STK4	;CHECK THE DATA ON THE STACK
510	022640	001401			BEG	,+4		;BRANCH IF OK
511	022642	104000			HLT			;STK4 NOT EQUAL TO RAND,D
512								
513	022644	026767	175750	175736	CMP	RAND,A,	STK5	;CHECK THE DATA ON THE STACK
514	022652	001401			BEG	,+4		;BRANCH IF OK
515	022654	104000			HLT			;STK5 NOT EQUAL TO RAND,A
516								
517	022656	026767	175740	175726	CMP	RAND,B,	STK6	;CHECK THE DATA ON THE STACK
518	022664	001401			BEG	,+4		;BRANCH IF OK
519	022666	104000			HLT			;STK6 NOT EQUAL TO RAND,B
520								
521	022670	012716	002676		MOV	#35,	(SP)	;RESET THE STACK
522	022674	000002			RTI			;RESTORE THE STATUS (T=BIT)
523								
524	022676	104400						
525								

35: SCOPE

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002700 012703 000604
002704 004767 010670

002710 000240
002712 075033

002714 013767 177776 175672 1S:
002722 010367 175670
002726 026767 175726 175660 6S:
002734 001023

002736 105767 175652
002742 100464

002744 012767 000610 175716
002752 026767 175712 175636
002760 001401
002762 104000

002764 026767 175672 175616
002772 001004
002774 026767 175664 175610
003002 001515
003004 032767 000020 175660 4S:
003012 001022
003014 052767 000020 175650
003022 062767 000001 175634
003030 005567 175626
003034 102334
003036 000257
003040 000262
003042 013767 177776 175616
003050 012767 000340 175602
003056 000723

003060 132767 000020 175605 5S:
003066 001010
003070 152767 000020 175575
003076 162767 000001 175560
003104 005667 175552
003110 104000 7S:

003112 000451

003114 012767 000604 175546 2S:
003122 026767 175542 175466
003130 001401

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;*****
;TEST 4:      EXERCISE FDIV (PDP-11 FLOATING DIVIDE INSTRUCTION)
;      RAND,A,RAND,B / RAND,C,RAND,D = ANS1,ANS2
;      STACK POINTER = R3
;*****
TST4:  MOV      #STACK0,R3      ;SET UP THE STACK POINTER
      JSR      PC,      PUSHR   ;PUT THE DATA ON THE STACK

      NOP
      FDIV+   R3              ;FLOATING DIVIDE ON THE R3 STACK

      MOV      @#PS,  $PSW     ;SAVE PROCESSOR STATUS
      MOV      R3,    $SSP     ;SAVE THE STACK POINTER
      CMP      $DIVPS,$PSW    ;CHECK THE PROCESSOR STATUS
      BNE     4$             ;GO CHECK FOR ROUNDING ERROR

      TSTB    $PSW           ;CHECK FOR ERROR
      BMI     2$             ;BRANCH IF ERROR

      MOV      #STACK4,SAVSTK  ;SAVE PROPER STACK ADDRESS FOR TYPING
      CMP      SAVSTK,$SSP    ;CHECK THE STACK POINTER
      BEQ     ,+4            ;BRANCH IF OK
      HLT     ;STACK POINTER NOT EQUAL TO #STACK4

      CMP      $DIV1,  ANS1    ;CHECK THE ANSWER
      BNE     4$
      CMP      $DIV2,  ANS2    ;CHECK THE ANSWER
      BEQ     3$
      BIT      #BIT4,  RNDFLG  ;CHECK THE ROUNDING FLAG
      BNE     5$
      BIS      #BIT4,  RNDFLG  ;SET ROUNDING FLAG
      ADD     #1,    $DIV2     ;INCREMENT FORTRAN ANSWER
      ADC     $DIV1
      BVC     6$             ;BRANCH IF NO OVERFLOW
      CCC     ;CLEAR ALL CONDITION CODES
      SEV     ;SET V-BIT
      MOV     @#PS,  $DIVER    ;SET UP PSW FOR OVERFLOW
      MOV     #340,  $DIVPS   ;SET UP TRAP PSW
      BR     6$             ;TRY IT AGAIN

      BITB   #BIT4,  RNDFLG+1 ;CHECK "DEROUNDING" FLAG
      BNE     7$             ;BRANCH IF SET
      BISB  #BIT4,  RNDFLG+1 ;SET "DEROUNDING" FLAG
      SUB     #1,    $DIV2     ;RESTORE ORIGINAL ANSWER
      SBC     $DIV1
      HLT     ;SUBTRACT CARRY
      ;WRONG PSW OR ANSWER

      BR     3$

      MOV     #STACK0,SAVSTK  ;SAVE STACK ADDRESS FOR TYPING
      CMP     SAVSTK,$SSP    ;CHECK THE STACK POINTER
      BEQ     ,+4            ;BRANCH IF OK

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DBKEBA.P11 TEST 4: EXERCISE PDIV R3

580	003132	104000			HLT				;STACK POINTER FOULED UP
581									
582	003134	022767	002714	175436	CMP	#15,	STK1		;CHECK THE RTI ADDRESS ON THE STACK
583	003142	001401			BEQ	,+4			;BRANCH IF OK
584	003144	104000			HLT				;RTI ADDRESS NOT EQUAL TO #15
585									
586	003146	026767	175514	175426	CMP	\$DIVER,	STK2		;CHECK THE PSW ON THE STACK
587	003154	001401			BEQ	,+4			;BRANCH IF OK
588	003156	104000			HLT				;RTI PSW NOT EQUAL TO 200
589									
590	003160	026767	175440	175416	CMP	RAND,C,	STK3		;CHECK THE DATA ON THE STACK
591	003166	001401			BEQ	,+4			;BRANCH IF OK
592	003170	104000			HLT				;STK3 NOT EQUAL TO RAND,C
593									
594	003172	026767	175430	175406	CMP	RAND,D,	STK4		;CHECK THE DATA ON THE STACK
595	003200	001401			BEQ	,+4			;BRANCH IF OK
596	003202	104000			HLT				;STK4 NOT EQUAL TO RAND,D
597									
598	003204	026767	175410	175376	CMP	RAND,A,	STK5		;CHECK THE DATA ON THE STACK
599	003212	001401			BEQ	,+4			;BRANCH IF OK
600	003214	104000			HLT				;STK5 NOT EQUAL TO RAND,A
601									
602	003216	026767	175400	175366	CMP	RAND,B,	STK6		;CHECK THE DATA ON THE STACK
603	003224	001401			BEQ	,+4			;BRANCH IF OK
604	003226	104000			HLT				;STK6 NOT EQUAL TO RAND,B
605									
606	003230	012716	003236		MOV	#35,	(SP)		;RESET THE STACK
607	003234	000002			RTI				;RESTORE THE STATUS (T=BIT)
608									
609	003236	104400							
610									

35: SCOPE


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618 003240 012704 000604
619 003244 004767 010330
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621 003250 000240
622 003252 075004
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624 003254 013767 177776 175332 1S:
625 003262 010467 175330
626 003266 026767 175336 175320
627 003274 001401
628 003276 104000
629
630 003300 105767 175310
631 003304 100423
632
633 003306 012767 000610 175354
634 003314 026767 175350 175274
635 003322 001401
636 003324 104000
637
638 003326 026767 175300 175254
639 003334 001401
640 003336 104000
641
642 003340 026767 175270 175244
643 003346 001401
644 003350 104000
645
646 003352 000451
647
648 003354 012767 000604 175306 2S:
649 003362 026767 175302 175226
650 003370 001401
651 003372 104000
652
653 003374 022767 003254 175176
654 003402 001401
655 003404 104000
656
657 003406 026767 175224 175166
658 003414 001401
659 003416 104000
660
661 003420 026767 175200 175156
662 003426 001401
663 003430 104000
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;*****
;TEST 5: EXERCISE FADD (PDP-11 FLOATING ADD INSTRUCTION)
; RAND,A,RAND,B + RAND,C,RAND,D = ANS1,ANS2
; STACK POINTER = R4
;*****
TST5: MOV #STACK0,R4 ;SET UP THE STACK POINTER
JSR PC, PUSHR ;PUT THE DATA ON THE STACK
NOP
FADD+ R4 ;FLOATING ADD ON THE R4 STACK
1S: MOV @#PS, SPSW ;SAVE PROCESSOR STATUS
MOV R4, SSP ;SAVE THE STACK POINTER
CMP $ADDPS, SPSW ;CHECK THE PROCESSOR STATUS
BEQ ,+4 ;BRANCH IF OK
HLT ;PSW NOT EQUAL TO $ADDPS
TSTB SPSW ;CHECK FOR ERROR
BMI 2S ;BRANCH IF ERROR
MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
CMP SAVSTK, SSP ;CHECK THE STACK POINTER
BEQ ,+4 ;BRANCH IF OK
HLT ;STACK POINTER NOT EQUAL TO #STACK4
CMP $ADD1, ANS1 ;CHECK THE ANSWER
BEQ ,+4 ;BRANCH IF OK
HLT ;LEFT HALF OF ANSWER WRONG
CMP $ADD2, ANS2 ;CHECK THE ANSWER
BEQ ,+4 ;BRANCH IF OK
HLT ;RIGHT HALF OF ANSWER WRONG
BR 3S
MOV #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
CMP SAVSTK, SSP ;CHECK THE STACK POINTER
BEQ ,+4 ;BRANCH IF OK
HLT ;STACK POINTER FOULED UP
CMP #1S, STK1 ;CHECK THE RTI ADDRESS ON THE STACK
BEQ ,+4 ;BRANCH IF OK
HLT ;RTI ADDRESS NOT EQUAL TO #1S
CMP $ADDER, STK2 ;CHECK THE PSW ON THE STACK
BEQ ,+4 ;BRANCH IF OK
HLT ;RTI PSW NOT EQUAL TO 200
CMP RAND,C, STK3 ;CHECK THE DATA ON THE STACK
BEQ ,+4 ;BRANCH IF OK
HLT ;STK3 NOT EQUAL TO RAND,C

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665 003432 026767 175170 175146      CMP      RAND,D, STK4      ;CHECK THE DATA ON THE STACK
666 003440 001401                    BEQ      ,+4            ;BRANCH IF OK
667 003442 104000                    HLT                                     ;STK4 NOT EQUAL TO RAND,D
668
669 003444 026767 175150 175136      CMP      RAND,A, STK5      ;CHECK THE DATA ON THE STACK
670 003452 001401                    BEQ      ,+4            ;BRANCH IF OK
671 003454 104000                    HLT                                     ;STK5 NOT EQUAL TO RAND,A
672
673 003456 026767 175140 175126      CMP      RAND,B, STK6      ;CHECK THE DATA ON THE STACK
674 003464 001401                    BEQ      ,+4            ;BRANCH IF OK
675 003466 104000                    HLT                                     ;STK6 NOT EQUAL TO RAND,B
676
677 003470 012716 003476              MOV      #3$, (SP)       ;RESET THE STACK
678 003474 000002                    RTI      ;RESTORE THE STATUS (Y=BIT)
679
680 003476 104400                    3$:     SCOPE

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;*****
;TEST 6:      EXERCISE FSUB (POP-11 FLOATING SUBTRACT INSTRUCTION)
;      RAND,A,RAND,B = RAND,C,RAND,D = ANS1,ANS2
;      STACK POINTER = R5
;*****

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689 003500 012705 000604              TST6:   MOV      #STACK0,R5      ;SET UP THE STACK POINTER
690 003504 004767 010070              JSR      PC,      PUSHR      ;PUT THE DATA ON THE STACK
691
692 003510 000240                    NOP
693 003512 075015                    F$UB+   R5                ;FLOATING SUBTRACT ON THE R5 STACK
694
695 003514 013767 177776 175072      1$:    MOV      #PS,   $PSW      ;SAVE PROCESSOR STATUS
696 003522 010567 175070              MOV      R5,     $SP        ;SAVE THE STACK POINTER
697 003526 026767 175106 175060      CMP      $$UBPS, $PSW      ;CHECK THE PROCESSOR STATUS
698 003534 001401                    BEQ      ,+4            ;BRANCH IF OK
699 003536 104000                    HLT                                     ;PSW NOT EQUAL TO $$UBPS
700
701 003540 105767 175050              TSTB    $PSW            ;CHECK FOR ERROR
702 003544 100423                    BMI     2$              ;BRANCH IF ERROR
703
704 003546 012767 000610 175114      MOV      #STACK4,SAVSTK    ;SAVE PROPER STACK ADDRESS FOR TYPING
705 003554 026767 175110 175034      CMP      SAVSTK, $SP      ;CHECK THE STACK POINTER
706 003562 001401                    BEQ      ,+4            ;BRANCH IF OK
707 003564 104000                    HLT                                     ;STACK POINTER NOT EQUAL TO #STACK4
708
709 003566 026767 175050 175014      CMP      $$SUB1, ANS1      ;CHECK THE ANSWER
710 003574 001401                    BEQ      ,+4            ;BRANCH IF OK
711 003576 104000                    HLT                                     ;LEFT HALF OF ANSWER WRONG
712
713 003600 026767 175040 175004      CMP      $$SUB2, ANS2      ;CHECK THE ANSWER
714 003606 001401                    BEQ      ,+4            ;BRANCH IF OK
715 003610 104000                    HLT                                     ;RIGHT HALF OF ANSWER WRONG
716
717 003612 000451                    BR      3$
718

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719	003614	012767	000604	175046	2S:	MOV	#STACK0, SAVSTK	ISAVE STACK ADDRESS FOR TYPING
720	003622	026767	175042	174766		CMP	SAVSTK, SSP	ICHECK THE STACK POINTER
721	003630	001401				BEQ	,+4	IBRANCH IF OK
722	003632	104000				HLT		ISTACK POINTER FOULED UP
723								
724	003634	022767	003514	174736		CMP	#1S, STK1	ICHECK THE RTI ADDRESS ON THE STACK
725	003642	001401				BEQ	,+4	IBRANCH IF OK
726	003644	104000				HLT		IRTI ADDRESS NOT EQUAL TO #1S
727								
728	003646	026767	174774	174726		CMP	SSUBER, STK2	ICHECK THE PSW ON THE STACK
729	003654	001401				BEQ	,+4	IBRANCH IF OK
730	003656	104000				HLT		IRTI PSW NOT EQUAL TO 200
731								
732	003660	026767	174740	174716		CMP	RAND,C, STK3	ICHECK THE DATA ON THE STACK
733	003666	001401				BEQ	,+4	IBRANCH IF OK
734	003670	104000				HLT		Istk3 NOT EQUAL TO RAND,C
735								
736	003672	026767	174730	174706		CMP	RAND,D, STK4	ICHECK THE DATA ON THE STACK
737	003700	001401				BEQ	,+4	IBRANCH IF OK
738	003702	104000				HLT		Istk4 NOT EQUAL TO RAND,D
739								
740	003704	026767	174710	174676		CMP	RAND,A, STK5	ICHECK THE DATA ON THE STACK
741	003712	001401				BEQ	,+4	IBRANCH IF OK
742	003714	104000				HLT		Istk5 NOT EQUAL TO RAND,A
743								
744	003716	026767	174700	174666		CMP	RAND,B, STK6	ICHECK THE DATA ON THE STACK
745	003724	001401				BEQ	,+4	IBRANCH IF OK
746	003726	104000				HLT		Istk6 NOT EQUAL TO RAND,B
747								
748	003730	012716	003736			MOV	#3S, (SP)	IRESET THE STACK
749	003734	000002				RTI		IRESTORE THE STATUS (T=BIT)
750								
751	003736	104400			3S:	SCOPE		
752								
753								
754								
755								
756								
757								
758								
759								
760	003740	012706	000604		TST7:	MOV	#STACK0, SP	ISET UP THE STACK POINTER
761	003744	004767	007630			JSR	PC, PUSHR	IPUT THE DATA ON THE STACK
762								
763	003750	000240				NOP		
764	003752	075026				FMUL+	SP	IFLOATING MULTIPLY ON THE SP STACK
765								
766	003754	013767	177776	174632	1S:	MOV	#PS, SPSW	ISAVE PROCESSOR STATUS
767	003762	010667	174630			MOV	SP, SSP	ISAVE THE STACK POINTER
768	003766	026767	174656	174620		CMP	\$MULPS, SPSW	ICHECK THE PROCESSOR STATUS
769	003774	001401				BEQ	,+4	IBRANCH IF OK
770	003776	104000				HLT		IPSW NOT EQUAL TO \$MULPS
771								
772	004000	105767	174610			TSTB	SPSW	ICHECK FOR ERROR

 ITEST 7: EXERCISE FMUL (PDP-11 FLOATING MULTIPLY INSTRUCTION)
 I RAND,A,RAND,B * RAND,C,RAND,D = ANS1,ANS2
 I STACK POINTER = SP

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825
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830
831
832 004202 012700 000604 TST101 MOV #STACK0,R0 ;SET UP THE STACK POINTER
833 004206 004767 007366 JSR PC, PUSHR ;PUT THE DATA ON THE STACK
834
835 004212 000240 NOP
836 004214 075030 FDIV+ R0 ;FLOATING DIVIDE ON THE R0 STACK
837
838 004216 013767 177776 174370 1S: MOV #PS, SPSW ;SAVE PROCESSOR STATUS
839 004224 010067 174366 MOV R0, SSP ;SAVE THE STACK POINTER
840 004230 026767 174424 174356 CMP SDIVPS, SPSW ;CHECK THE PROCESSOR STATUS
841 004236 001401 BEQ ,+4 ;BRANCH IF OK
842 004240 104000 HLT ;PSW NOT EQUAL TO SDIVPS
843
844 004242 105767 174346 TSTB SPSW ;CHECK FOR ERROR
845 004246 100423 BMI 2S ;BRANCH IF ERROR
846
847 004250 012767 000610 174412 MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
848 004256 026767 174406 174332 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
849 004264 001401 BEQ ,+4 ;BRANCH IF OK
850 004266 104000 HLT ;STACK POINTER NOT EQUAL TO #STACK4
851
852 004270 026767 174366 174312 CMP SDIV1, ANS1 ;CHECK THE ANSWER
853 004276 001401 BEQ ,+4 ;BRANCH IF OK
854 004300 104000 HLT ;LEFT HALF OF ANSWER WRONG
855
856 004302 026767 174356 174302 CMP SDIV2, ANS2 ;CHECK THE ANSWER
857 004310 001401 BEQ ,+4 ;BRANCH IF OK
858 004312 104000 HLT ;RIGHT HALF OF ANSWER WRONG
859
860 004314 000451 BR 3S
861
862 004316 012767 000604 174344 2S: MOV #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
863 004324 026767 174340 174264 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
864 004332 001401 BEQ ,+4 ;BRANCH IF OK
865 004334 104000 HLT ;STACK POINTER FOULED UP
866
867 004336 022767 004216 174234 CMP #1S, STK1 ;CHECK THE RTI ADDRESS ON THE STACK
868 004344 001401 BEQ ,+4 ;BRANCH IF OK
869 004346 104000 HLT ;RTI ADDRESS NOT EQUAL TO #1S
870
871 004350 026767 174312 174224 CMP SDIVER, STK2 ;CHECK THE PSW ON THE STACK
872 004356 001401 BEQ ,+4 ;BRANCH IF OK
873 004360 104000 HLT ;RTI PSW NOT EQUAL TO 200
874
875 004362 026767 174236 174214 CMP RAND,C, STK3 ;CHECK THE DATA ON THE STACK
876 004370 001401 BEQ ,+4 ;BRANCH IF OK
877 004372 104000 HLT ;STK3 NOT EQUAL TO RAND,C
878

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879	004374	026767	174226	174204	CMP	RAND,D, STK4	;	CHECK THE DATA ON THE STACK
880	004402	001401			BEQ	,+4	;	BRANCH IF OK
881	004404	104000			HLT		;	STK4 NOT EQUAL TO RAND,D
882								
883	004406	026767	174206	174174	CMP	RAND,A, STK5	;	CHECK THE DATA ON THE STACK
884	004414	001401			BEQ	,+4	;	BRANCH IF OK
885	004416	104000			HLT		;	STK5 NOT EQUAL TO RAND,A
886								
887	004420	026767	174176	174164	CMP	RAND,B, STK6	;	CHECK THE DATA ON THE STACK
888	004426	001401			BEQ	,+4	;	BRANCH IF OK
889	004430	104000			HLT		;	STK6 NOT EQUAL TO RAND,B
890								
891	004432	012716	004440		MOV	#3\$, (SP)	;	RESET THE STACK
892	004436	000002			RTI		;	RESTORE THE STATUS (T=BIT)
893								
894	004440	104400			3\$:	SCOPE		
895								
896								
897								
898								
899								
900								
901								
902								
903	004442	012701	000604		TST11:	MOV #STACK0,R1	;	SET UP THE STACK POINTER
904	004446	004767	007126			JSR PC, PUSHR	;	PUT THE DATA ON THE STACK
905								
906	004452	000240			NOP			
907	004454	075001			FADD+	R1	;	FLOATING ADD ON THE R1 STACK
908								
909	004456	013767	177776	174130	1\$:	MOV #SP\$, SPSW	;	SAVE PROCESSOR STATUS
910	004464	010167	174126			MOV R1, SSP	;	SAVE THE STACK POINTER
911	004470	026767	174134	174116		CMP SADDPS, SPSW	;	CHECK THE PROCESSOR STATUS
912	004476	001401			BEQ	,+4	;	BRANCH IF OK
913	004500	104000			HLT		;	PSW NOT EQUAL TO SADDPS
914								
915	004502	105767	174106		TSTB	SPSW	;	CHECK FOR ERROR
916	004506	100423			BMI	2\$;	BRANCH IF ERROR
917								
918	004510	012767	000610	174152	MOV	#STACK4, SAVSTK	;	SAVE PROPER STACK ADDRESS FOR TYPING
919	004516	026767	174146	174072	CMP	SAVSTK, SSP	;	CHECK THE STACK POINTER
920	004524	001401			BEQ	,+4	;	BRANCH IF OK
921	004526	104000			HLT		;	STACK POINTER NOT EQUAL TO #STACK4
922								
923	004530	026767	174076	174052	CMP	SADD1, ANS1	;	CHECK THE ANSWER
924	004536	001401			BEQ	,+4	;	BRANCH IF OK
925	004540	104000			HLT		;	LEFT HALF OF ANSWER WRONG
926								
927	004542	026767	174066	174042	CMP	SADD2, ANS2	;	CHECK THE ANSWER
928	004550	001401			BEQ	,+4	;	BRANCH IF OK
929	004552	104000			HLT		;	RIGHT HALF OF ANSWER WRONG
930								
931	004554	000451			BR	3\$		
932								

3\$: SCOPE

;TEST 11: EXERCISE FADD (PDP-11 FLOATING ADD INSTRUCTION)
; RAND,A,RAND,B + RAND,C,RAND,D * AN\$1,ANS2
; STACK POINTER = R1

TST11: MOV #STACK0,R1 ;SET UP THE STACK POINTER
JSR PC, PUSHR ;PUT THE DATA ON THE STACK

NOP ;
FADD+ R1 ;FLOATING ADD ON THE R1 STACK

1\$: MOV #SP\$, SPSW ;SAVE PROCESSOR STATUS
MOV R1, SSP ;SAVE THE STACK POINTER
CMP SADDPS, SPSW ;CHECK THE PROCESSOR STATUS
BEQ ,+4 ;BRANCH IF OK
HLT ;PSW NOT EQUAL TO SADDPS

TSTB SPSW ;CHECK FOR ERROR
BMI 2\$;BRANCH IF ERROR

MOV #STACK4, SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
CMP SAVSTK, SSP ;CHECK THE STACK POINTER
BEQ ,+4 ;BRANCH IF OK
HLT ;STACK POINTER NOT EQUAL TO #STACK4

CMP SADD1, ANS1 ;CHECK THE ANSWER
BEQ ,+4 ;BRANCH IF OK
HLT ;LEFT HALF OF ANSWER WRONG

CMP SADD2, ANS2 ;CHECK THE ANSWER
BEQ ,+4 ;BRANCH IF OK
HLT ;RIGHT HALF OF ANSWER WRONG

BR 3\$

933	004556	012767	000604	174104	2S:	MOV	#STACK0, SAVSTK	ISAVE STACK ADDRESS FOR TYPING
934	004564	026767	174100	174024		CMP	SAVSTK, \$SP	ICHECK THE STACK POINTER
935	004572	001401				BEQ	,+4	IBRANCH IF OK
936	004574	104000				HLT		ISTACK POINTER FOULED UP
937								
938	004576	022767	004456	173774		CMP	#15, STK1	ICHECK THE RTI ADDRESS ON THE STACK
939	004604	001401				BEQ	,+4	IBRANCH IF OK
940	004606	104000				HLT		IRTI ADDRESS NOT EQUAL TO #15
941								
942	004610	026767	174022	173764		CMP	\$ADDER, STK2	ICHECK THE PSW ON THE STACK
943	004616	001401				BEQ	,+4	IBRANCH IF OK
944	004620	104000				HLT		IRTI PSW NOT EQUAL TO 200
945								
946	004622	026767	173776	173754		CMP	RAND,C, STK3	ICHECK THE DATA ON THE STACK
947	004630	001401				BEQ	,+4	IBRANCH IF OK
948	004632	104000				HLT		Istk3 NOT EQUAL TO RAND,C
949								
950	004634	026767	173766	173744		CMP	RAND,D, STK4	ICHECK THE DATA ON THE STACK
951	004642	001401				BEQ	,+4	IBRANCH IF OK
952	004644	104000				HLT		Istk4 NOT EQUAL TO RAND,D
953								
954	004646	026767	173746	173734		CMP	RAND,A, STK5	ICHECK THE DATA ON THE STACK
955	004654	001401				BEQ	,+4	IBRANCH IF OK
956	004656	104000				HLT		Istk5 NOT EQUAL TO RAND,A
957								
958	004660	026767	173736	173724		CMP	RAND,B, STK6	ICHECK THE DATA ON THE STACK
959	004666	001401				BEQ	,+4	IBRANCH IF OK
960	004670	104000				HLT		Istk6 NOT EQUAL TO RAND,B
961								
962	004672	012716	004700			MOV	#35, (SP)	IRESET THE STACK
963	004676	000002				RTI		IRESTORE THE STATUS (T=BIT)
964								
965	004700	104400			3S:	SCOPE		
966								
967								
968								
969								
970								
971								
972								
973								
974	004702	012702	000604		TST121	MOV	#STACK0, R2	ISSET UP THE STACK POINTER
975	004706	004767	006666			JSR	PC, PUSHR	IPUT THE DATA ON THE STACK
976								
977	004712	000240				NOP		
978	004714	075012				FSUB+	R2	IFLOATING SUBTRACT ON THE R2 STACK
979								
980	004716	013767	177776	173670	1S:	MOV	@#PS, \$PSW	ISAVE PROCESSOR STATUS
981	004724	010267	173666			MOV	R2, \$SP	ISAVE THE STACK POINTER
982	004730	026767	173704	173656		CMP	\$SUBPS, \$PSW	ICHECK THE PROCESSOR STATUS
983	004736	001401				BEQ	,+4	IBRANCH IF OK
984	004740	104000				HLT		IPSW NOT EQUAL TO \$SUBPS
985								
986	004742	105767	173646			TSTB	\$PSW	ICHECK FOR ERROR

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*****
I TEST 121      EXERCISE FSUB (PDP-11 FLOATING SUBTRACT INSTRUCTION)
I      RAND,A,RAND,B = RAND,C,RAND,D = ANS1,ANS2
I      STACK POINTER = R2
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1044
1045 005142 012703 000604 TST131 MOV #STACK0,R3 ;SET UP THE STACK POINTER
1046 005146 004767 006426 JSR PC, PUSHR ;PUT THE DATA ON THE STACK
1047
1048 005152 000240 NOP
1049 005154 075023 FMUL+ R3 ;FLOATING MULTIPLY ON THE R3 STACK
1050
1051 005156 013767 177776 173430 1S: MOV #PS, SPSW ;SAVE PROCESSOR STATUS
1052 005164 010367 173426 MOV R3, SSP ;SAVE THE STACK POINTER
1053 005170 026767 173454 173416 CMP SMULPS, SPSW ;CHECK THE PROCESSOR STATUS
1054 005176 001401 BEQ ,+4 ;BRANCH IF OK
1055 005200 104000 HLT ;PSW NOT EQUAL TO SMULPS
1056
1057 005202 105767 173406 TSTB SPSW ;CHECK FOR ERROR
1058 005206 100423 BMI 2S ;BRANCH IF ERROR
1059
1060 005210 012767 000610 173452 MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
1061 005216 026767 173446 173372 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
1062 005224 001401 BEQ ,+4 ;BRANCH IF OK
1063 005226 104000 HLT ;STACK POINTER NOT EQUAL TO #STACK4
1064
1065 005230 026767 173416 173352 CMP SMUL1, ANS1 ;CHECK THE ANSWER
1066 005236 001401 BEQ ,+4 ;BRANCH IF OK
1067 005240 104000 HLT ;LEFT HALF OF ANSWER WRONG
1068
1069 005242 026767 173406 173342 CMP SMUL2, ANS2 ;CHECK THE ANSWER
1070 005250 001401 BEQ ,+4 ;BRANCH IF OK
1071 005252 104000 HLT ;RIGHT HALF OF ANSWER WRONG
1072
1073 005254 000451 BR 3S
1074
1075 005256 012767 000604 173404 2S: MOV #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
1076 005264 026767 173400 173324 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
1077 005272 001401 BEQ ,+4 ;BRANCH IF OK
1078 005274 104000 HLT ;STACK POINTER FOULED UP
1079
1080 005276 022767 005156 173274 CMP #1S, STK1 ;CHECK THE RTI ADDRESS ON THE STACK
1081 005304 001401 BEQ ,+4 ;BRANCH IF OK
1082 005306 104000 HLT ;RTI ADDRESS NOT EQUAL TO #1S
1083
1084 005310 026767 173342 173264 CMP SMULER, STK2 ;CHECK THE PSW ON THE STACK
1085 005316 001401 BEQ ,+4 ;BRANCH IF OK
1086 005320 104000 HLT ;RTI PSW NOT EQUAL TO 200
1087
1088 005322 026767 173276 173254 CMP RAND,C, STK3 ;CHECK THE DATA ON THE STACK
1089 005330 001401 BEQ ,+4 ;BRANCH IF OK
1090 005332 104000 HLT ;STK3 NOT EQUAL TO RAND.C
1091

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1092	005334	026767	173266	173244	CMP	RAND,D, STK4	ICHECK THE DATA ON THE STACK
1093	005342	001401			BEQ	,+4	IBRANCH IF OK
1094	005344	104000			HLT		ISTK4 NOT EQUAL TO RAND,D
1095							
1096	005346	026767	173246	173234	CMP	RAND,A, STK5	ICHECK THE DATA ON THE STACK
1097	005354	001401			BEQ	,+4	IBRANCH IF OK
1098	005356	104000			HLT		ISTK5 NOT EQUAL TO RAND,A
1099							
1100	005360	026767	173236	173224	CMP	RAND,B, STK6	ICHECK THE DATA ON THE STACK
1101	005366	001401			BEQ	,+4	IBRANCH IF OK
1102	005370	104000			HLT		ISTK6 NOT EQUAL TO RAND,B
1103							
1104	005372	012716	005400		MOV	#35, (SP)	IRESET THE STACK
1105	005376	000002			RTI		IRESTORE THE STATUS (T=BIT)
1106							
1107	005400	104400			3S:	SCOPE	
1108							
1109							
1110							
1111							
1112							
1113							
1114							
1115							
1116	005402	012704	000604		TST141	MOV #STACK0,R4	ISET UP THE STACK POINTER
1117	005406	004767	006166			JSR PC, PUSHR	IPUT THE DATA ON THE STACK
1118							
1119	005412	000240				NOF	
1120	005414	075034				FDIV+ R4	IFLOATING DIVIDE ON THE R4 STACK
1121							
1122	005416	013767	177776	173170	1S:	MOV #PS, SPSW	ISAVE PROCESSOR STATUS
1123	005424	010467	173166			MOV R4, SSP	ISAVE THE STACK POINTER
1124	005430	026767	173224	173156		CMP SDIVPS, SPSW	ICHECK THE PROCESSOR STATUS
1125	005436	001401				BEQ ,+4	IBRANCH IF OK
1126	005440	104000				HLT	IPSW NOT EQUAL TO SDIVPS
1127							
1128	005442	105767	173146			TSTB SPSW	ICHECK FOR ERROR
1129	005446	100423				BMI 25	IBRANCH IF ERROR
1130							
1131	005450	012767	000610	173212		MOV #STACK4, SAVSTK	ISAVE PROPER STACK ADDRESS FOR TYPING
1132	005456	026767	173206	173132		CMP SAVSTK, SSP	ICHECK THE STACK POINTER
1133	005464	001401				BEQ ,+4	IBRANCH IF OK
1134	005466	104000				HLT	ISTACK POINTER NOT EQUAL TO #STACK4
1135							
1136	005470	026767	173166	173112		CMP SDIV1, ANS1	ICHECK THE ANSWER
1137	005476	001401				BEQ ,+4	IBRANCH IF OK
1138	005500	104000				HLT	ILEFT HALF OF ANSWER WRONG
1139							
1140	005502	026767	173156	173102		CMP SDIV2, ANS2	ICHECK THE ANSWER
1141	005510	001401				BEQ ,+4	IBRANCH IF OK
1142	005512	104000				HLT	IRIGHT HALF OF ANSWER WRONG
1143							
1144	005514	000451				BR 35	
1145							

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*****
TEST 141 EXERCISE FDIV (PDP-11 FLOATING DIVIDE INSTRUCTION)
RAND,A,RAND,B / RAND,C,RAND,D = ANS1,ANS2
STACK POINTER = R4
*****

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1146	005516	012767	000604	173144	2S:	MOV	#STACK0,SAVSTK	ISAVE STACK ADDRESS FOR TYPING
1147	005524	026767	173140	173064		CMP	SAVSTK, SSP	ICHECK THE STACK POINTER
1148	005532	001401				BEQ	,+4	IBRANCH IF OK
1149	005534	104000				HLT		ISTACK POINTER FOULED UP
1150								
1151	005536	022767	005416	173034		CMP	#15, STK1	ICHECK THE RTI ADDRESS ON THE STACK
1152	005544	001401				BEQ	,+4	IBRANCH IF OK
1153	005546	104000				HLT		IRTI ADDRESS NOT EQUAL TO #15
1154								
1155	005550	026767	173112	173024		CMP	\$DIVER, STK2	ICHECK THE PSW ON THE STACK
1156	005556	001401				BEQ	,+4	IBRANCH IF OK
1157	005560	104000				HLT		IRTI PSW NOT EQUAL TO 200
1158								
1159	005562	026767	173036	173014		CMP	RAND,C, STK3	ICHECK THE DATA ON THE STACK
1160	005570	001401				BEQ	,+4	IBRANCH IF OK
1161	005572	104000				HLT		Istk3 NOT EQUAL TO RAND,C
1162								
1163	005574	026767	173026	173004		CMP	RAND,D, STK4	ICHECK THE DATA ON THE STACK
1164	005602	001401				BEQ	,+4	IBRANCH IF OK
1165	005604	104000				HLT		Istk4 NOT EQUAL TO RAND,D
1166								
1167	005606	026767	173006	172774		CMP	RAND,A, STK5	ICHECK THE DATA ON THE STACK
1168	005614	001401				BEQ	,+4	IBRANCH IF OK
1169	005616	104000				HLT		Istk5 NOT EQUAL TO RAND,A
1170								
1171	005620	026767	172776	172764		CMP	RAND,B, STK6	ICHECK THE DATA ON THE STACK
1172	005626	001401				BEQ	,+4	IBRANCH IF OK
1173	005630	104000				HLT		Istk6 NOT EQUAL TO RAND,B
1174								
1175	005632	012716	005640			MOV	#35, (SP)	IRESET THE STACK
1176	005636	000002				RTI		IRESTORE THE STATUS (T=BIT)
1177								
1178	005640	104400			3S:	SCOPE		
1179								
1180								
1181								
1182								
1183								
1184								
1185								
1186								
1187	005642	012705	000604		TST15I	MOV	#STACK0,R5	ISSET OF THE STACK POINTER
1188	005646	004767	005726			JSR	PC, PUSHR	IPUT THE DATA ON THE STACK
1189								
1190	005652	000240				NOP		
1191	005654	075005				FADD+	R5	IFLOATING ADD ON THE R5 STACK
1192								
1193	005656	013767	177776	172730	1S:	MOV	@#PS, SPSW	ISAVE PROCESSOR STATUS
1194	005664	010567	172726			MOV	R5, SSP	ISAVE THE STACK POINTER
1195	005670	026767	172734	172716		CMP	\$ADDPS, SPSW	ICHECK THE PROCESSOR STATUS
1196	005676	001401				BEQ	,+4	IBRANCH IF OK
1197	005700	104000				HLT		IPSW NOT EQUAL TO \$ADDPS
1198								
1199	005702	105767	172706			TSTB	SPSW	ICHECK FOR ERROR

```

*****
;TEST 15I      EXERCISE FADD (PDP-11 FLOATING ADD INSTRUCTION)
;      RAND,A,RAND,B + RAND,C,RAND,D = AN$1,ANS2
;      STACK POINTER = R5
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006102 012706 000604
006106 004767 005466

006112 000240
006114 075016

006116 013767 177776 172470
006124 010667 172466
006130 026767 172504 172456
006136 001401
006140 104000

006142 105767 172446
006146 100424

006150 012767 000610 172512
006156 026767 172506 172432
006164 001401
006166 104000

006170 026767 172446 172412
006176 001401
006200 104000

006202 026767 172436 172402
006210 001401
006212 104000

006214 024646
006216 000451

006220 012767 000600 172442
006226 026767 172436 172362
006234 001401
006236 104000

006240 022767 006116 172332
006246 001401
006250 104000

006252 026767 172370 172322
006260 001401
006262 104000

006264 026767 172334 172312
006272 001401
006274 104000

```
*****  
;TEST 16: EXERCISE FSUB (PDP-11 FLOATING SUBTRACT INSTRUCTION)  
; RAND,A,RAND,B = RAND,C,RAND,D = ANS1,ANS2  
; STACK POINTER = SP  
*****  
TST16: MOV #STACK0,SP ;SET UP THE STACK POINTER  
JSR PC, PUSHR ;PUT THE DATA ON THE STACK  
  
NOP  
FSUB+ SP ;FLOATING SUBTRACT ON THE SP STACK  
  
1S: MOV @#PS, SPSW ;SAVE PROCESSOR STATUS  
MOV SP, SSP ;SAVE THE STACK POINTER  
CMP $$SUBPS, SPSW ;CHECK THE PROCESSOR STATUS  
BEQ ,+4 ;BRANCH IF OK  
HLT ;PSW NOT EQUAL TO $$SUBPS  
  
TSTB $PSW ;CHECK FOR ERROR  
BMI 2S ;BRANCH IF ERROR  
  
MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING  
CMP SAVSTK, SSP ;CHECK THE STACK POINTER  
BEQ ,+4 ;BRANCH IF OK  
HLT ;STACK POINTER NOT EQUAL TO #STACK4  
  
CMP $$SUB1, ANS1 ;CHECK THE ANSWER  
BEQ ,+4 ;BRANCH IF OK  
HLT ;LEFT HALF OF ANSWER WRONG  
  
CMP $$SUB2, ANS2 ;CHECK THE ANSWER  
BEQ ,+4 ;BRANCH IF OK  
HLT ;RIGHT HALF OF ANSWER WRONG  
  
CMP =(SP), =(SP) ;RESTORE THE STACK  
BR 3S  
  
2S: MOV #STK1, SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING  
CMP SAVSTK, SSP ;CHECK THE STACK POINTER  
BEQ ,+4 ;BRANCH IF OK  
HLT ;STACK POINTER FOULED UP  
  
CMP #1S, STK1 ;CHECK THE RTI ADDRESS ON THE STACK  
BEQ ,+4 ;BRANCH IF OK  
HLT ;RTI ADDRESS NOT EQUAL TO #1S  
  
CMP $$SUBR, STK2 ;CHECK THE PSW ON THE STACK  
BEQ ,+4 ;BRANCH IF OK  
HLT ;RTI PSW NOT EQUAL TO 200  
  
CMP RAND,C, STK3 ;CHECK THE DATA ON THE STACK  
BEQ ,+4 ;BRANCH IF OK  
HLT ;STK3 NOT EQUAL TO RAND,C
```

```

1305
1306 006276 026767 172324 172302      CMP      RAND,D, STK4      ;CHECK THE DATA ON THE STACK
1307 006304 001401                    BEQ      ,+4            ;BRANCH IF OK
1308 006306 104000                    HLT                               ;STK4 NOT EQUAL TO RAND,D
1309
1310 006310 026767 172304 172272      CMP      RAND,A, STK5      ;CHECK THE DATA ON THE STACK
1311 006316 001401                    BEQ      ,+4            ;BRANCH IF OK
1312 006320 104000                    HLT                               ;STK5 NOT EQUAL TO RAND,A
1313
1314 006322 026767 172274 172262      CMP      RAND,B, STK6      ;CHECK THE DATA ON THE STACK
1315 006330 001401                    BEQ      ,+4            ;BRANCH IF OK
1316 006332 104000                    HLT                               ;STK6 NOT EQUAL TO RAND,B
1317
1318 006334 012716 006342              MOV      #3$, (SP)        ;RESET THE STACK
1319 006340 000002                    RTI                           ;RESTORE THE STATUS (T=BIT)
1320
1321 006342 104400                      3$:      SCOPE
1322
1323
1324 ;*****
1325 ;TEST 17: EXERCISE FMUL (PDP-11 FLOATING MULTIPLY INSTRUCTION)
1326 ;      RAND,A,RAND,B * RAND,C,RAND,D = ANS1,ANS2
1327 ;      STACK POINTER = R0
1328 ;*****
1329
1330 006344 012700 000604      TST17:  MOV      #STACK0,R0      ;SET UP THE STACK POINTER
1331 006350 004767 005224      JSR      PC,      PUSHR        ;PUT THE DATA ON THE STACK
1332
1333 006354 000240              NOP
1334 006356 075020              FMUL+   R0                    ;FLOATING MULTIPLY ON THE R0 STACK
1335
1336 006360 013767 177776 172226      1$:    MOV      #SPS, SPSW        ;SAVE PROCESSOR STATUS
1337 006366 010067 172224      MOV      R0,      SSP         ;SAVE THE STACK POINTER
1338 006372 026767 172252 172214      CMP      $MULPS, SPSW        ;CHECK THE PROCESSOR STATUS
1339 006400 001401                    BEQ      ,+4            ;BRANCH IF OK
1340 006402 104000                    HLT                               ;PSW NOT EQUAL TO SMULPS
1341
1342 006404 105767 172204      TSTB    SPSW                ;CHECK FOR ERROR
1343 006410 100423      BMI     2$                    ;BRANCH IF ERROR
1344
1345 006412 012767 000610 172250      MOV      #STACK4,SAVSTK      ;SAVE PROPER STACK ADDRESS FOR TYPING
1346 006420 026767 172244 172170      CMP      SAVSTK, SSP         ;CHECK THE STACK POINTER
1347 006426 001401                    BEQ      ,+4            ;BRANCH IF OK
1348 006430 104000                    HLT                               ;STACK POINTER NOT EQUAL TO #STACK4
1349
1350 006432 026767 172214 172150      CMP      $MUL1, ANS1        ;CHECK THE ANSWER
1351 006440 001401                    BEQ      ,+4            ;BRANCH IF OK
1352 006442 104000                    HLT                               ;LEFT HALF OF ANSWER WRONG
1353
1354 006444 026767 172204 172140      CMP      $MUL2, ANS2        ;CHECK THE ANSWER
1355 006452 001401                    BEQ      ,+4            ;BRANCH IF OK
1356 006454 104000                    HLT                               ;RIGHT HALF OF ANSWER WRONG
1357
1358 006456 000451              BR      3$

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```

1359
1360 006460 012767 000604 172202 2S:  MOV  #STACK0,SAVSTK  ;SAVE STACK ADDRESS FOR TYPING
1361 006466 026767 172176 172122  CMP  SAVSTK, $SP    ;CHECK THE STACK POINTER
1362 006474 001401  BEQ  ,+4           ;BRANCH IF OK
1363 006476 104000  HLT                    ;STACK POINTER FOULED UP
1364
1365 006500 022767 006360 172072  CMP  #15, STK1     ;CHECK THE RTI ADDRESS ON THE STACK
1366 006506 001401  BEQ  ,+4           ;BRANCH IF OK
1367 006510 104000  HLT                    ;RTI ADDRESS NOT EQUAL TO #15
1368
1369 006512 026767 172140 172062  CMP  $MULR, STK2   ;CHECK THE PSW ON THE STACK
1370 006520 001401  BEQ  ,+4           ;BRANCH IF OK
1371 006522 104000  HLT                    ;RTI PSW NOT EQUAL TO 200
1372
1373 006524 026767 172074 172052  CMP  RAND,C, STK3  ;CHECK THE DATA ON THE STACK
1374 006532 001401  BEQ  ,+4           ;BRANCH IF OK
1375 006534 104000  HLT                    ;STK3 NOT EQUAL TO RAND,C
1376
1377 006536 026767 172064 172042  CMP  RAND,D, STK4  ;CHECK THE DATA ON THE STACK
1378 006544 001401  BEQ  ,+4           ;BRANCH IF OK
1379 006546 104000  HLT                    ;STK4 NOT EQUAL TO RAND,D
1380
1381 006550 026767 172044 172032  CMP  RAND,A, STK5  ;CHECK THE DATA ON THE STACK
1382 006556 001401  BEQ  ,+4           ;BRANCH IF OK
1383 006560 104000  HLT                    ;STK5 NOT EQUAL TO RAND,A
1384
1385 006562 026767 172034 172022  CMP  RAND,B, STK6  ;CHECK THE DATA ON THE STACK
1386 006570 001401  BEQ  ,+4           ;BRANCH IF OK
1387 006572 104000  HLT                    ;STK6 NOT EQUAL TO RAND,B
1388
1389 006574 012716 006602  MOV  #3S, ($R)     ;RESET THE STACK
1390 006600 000002  RTI                    ;RESTORE THE STATUS (T=BIT)
1391
1392 006602 104400  3S:  SCOPE
1393
1394
1395
1396
1397
1398
1399
1400
1401 006604 012701 000604  TST201 MOV  #STACK0,R1  ;SET UP THE STACK POINTER
1402 006610 004767 004764  JSR  PC,  PUSHR     ;PUT THE DATA ON THE STACK
1403
1404 006614 000240  NOP
1405 006616 075031  FDIV+ R1           ;FLOATING DIVIDE ON THE R1 STACK
1406
1407 006620 013767 177776 171766 1S:  MOV  #PS, SPSW     ;SAVE PROCESSOR STATUS
1408 006626 010167 171764  MOV  R1, $SP       ;SAVE THE STACK POINTER
1409 006632 026767 172022 171754  CMP  $DIVPS, SPSW  ;CHECK THE PROCESSOR STATUS
1410 006640 001401  BEQ  ,+4           ;BRANCH IF OK
1411 006642 104000  HLT                    ;PSW NOT EQUAL TO $DIVPS
1412

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1465
1466
1467
1468
1469
1470
1471
1472 007044 012702 000604 TST21: MOV #STACK0,R2 ;SET UP THE STACK POINTER
1473 007050 004767 004524 JSR PC, PUSHR ;PUT THE DATA ON THE STACK
1474
1475 007054 000240 NOP
1476 007056 075002 FADD+ R2 ;FLOATING ADD ON THE R2 STACK
1477
1478 007060 013767 177776 171526 15: MOV #PS, SPSW ;SAVE PROCESSOR STATUS
1479 007066 010267 171524 MOV R2, SSP ;SAVE THE STACK POINTER
1480 007072 026767 171532 171514 CMP SADDPS, SPSW ;CHECK THE PROCESSOR STATUS
1481 007100 001401 BEQ ,+4 ;BRANCH IF OK
1482 007102 104000 HLT ;PSW NOT EQUAL TO SADDPS
1483
1484 007104 105767 171504 TSTB SPSW ;CHECK FOR ERROR
1485 007110 100423 BMI 2$ ;BRANCH IF ERROR
1486
1487 007112 012767 000610 171550 MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
1488 007120 026767 171544 171470 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
1489 007126 001401 BEQ ,+4 ;BRANCH IF OK
1490 007130 104000 HLT ;STACK POINTER NOT EQUAL TO #STACK4
1491
1492 007132 026767 171474 171450 CMP SADD1, ANS1 ;CHECK THE ANSWER
1493 007140 001401 BEQ ,+4 ;BRANCH IF OK
1494 007142 104000 HLT ;LEFT HALF OF ANSWER WRONG
1495
1496 007144 026767 171464 171440 CMP SADD2, ANS2 ;CHECK THE ANSWER
1497 007152 001401 BEQ ,+4 ;BRANCH IF OK
1498 007154 104000 HLT ;RIGHT HALF OF ANSWER WRONG
1499
1500 007156 000451 BR 3$
1501
1502 007160 012767 000604 171502 2$: MOV #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
1503 007166 026767 171476 171422 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
1504 007174 001401 BEQ ,+4 ;BRANCH IF OK
1505 007176 104000 HLT ;STACK POINTER FOULED UP
1506
1507 007200 022767 007060 171372 CMP #1$, STK1 ;CHECK THE RTI ADDRESS ON THE STACK
1508 007206 001401 BEQ ,+4 ;BRANCH IF OK
1509 007210 104000 HLT ;RTI ADDRESS NOT EQUAL TO #1$
1510
1511 007212 026767 171420 171362 CMP SADDR, STK2 ;CHECK THE PSW ON THE STACK
1512 007220 001401 BEQ ,+4 ;BRANCH IF OK
1513 007222 104000 HLT ;RTI PSW NOT EQUAL TO 200
1514
1515 007224 026767 171374 171352 CMP RAND,C, STK3 ;CHECK THE DATA ON THE STACK
1516 007232 001401 BEQ ,+4 ;BRANCH IF OK
1517 007234 104000 HLT ;STK3 NOT EQUAL TO RAND,C
1518

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1519	007236	026767	171364	171342		CMP	RAND,D, STK4	ICHECK THE DATA ON THE STACK
1520	007244	001401				BEQ	,+4	IBRANCH IF OK
1521	007246	104000				HLT		I STK4 NOT EQUAL TO RAND,D
1522								
1523	007250	026767	171344	171332		CMP	RAND,A, STK5	ICHECK THE DATA ON THE STACK
1524	007256	001401				BEQ	,+4	IBRANCH IF OK
1525	007260	104000				HLT		I STK5 NOT EQUAL TO RAND,A
1526								
1527	007262	026767	171334	171322		CMP	RAND,B, STK6	ICHECK THE DATA ON THE STACK
1528	007270	001401				BEQ	,+4	IBRANCH IF OK
1529	007272	104000				HLT		I STK6 NOT EQUAL TO RAND,B
1530								
1531	007274	012716	007302			MOV	#35, (SP)	IRESET THE STACK
1532	007300	000002				RTI		IRESTORE THE STATUS (T=BIT)
1533								
1534	007302	104400			35:		SCOPE	
1535								
1536								
1537								
1538								
1539								
1540								
1541								
1542								
1543	007304	012703	000604			TST21	MOV #STACK0,R3	ISSET UP THE STACK POINTER
1544	007310	004767	004264				JSR PC, PUSHR	IPUT THE DATA ON THE STACK
1545								
1546	007314	000240					NOP	
1547	007316	075013					FSUB+ R3	IFLOATING SUBTRACT ON THE R3 STACK
1548								
1549	007320	013767	177776	171266	15:		MOV #NPS, SPSW	ISAVE PROCESSOR STATUS
1550	007326	010367	171264				MOV R3, SSP	ISAVE THE STACK POINTER
1551	007332	026767	171302	171254			CMP SSUBPS, SPSW	ICHECK THE PROCESSOR STATUS
1552	007340	001401					BEQ ,+4	IBRANCH IF OK
1553	007342	104000					HLT	IPSW NOT EQUAL TO SSUBPS
1554								
1555	007344	105767	171244				TSTB SPSW	ICHECK FOR ERROR
1556	007350	100423					BMI 25	IBRANCH IF ERROR
1557								
1558	007352	012767	000610	171310			MOV #STACK4, SAVSTK	ISAVE PROPER STACK ADDRESS FOR TYPING
1559	007360	026767	171304	171230			CMP SAVSTK, SSP	ICHECK THE STACK POINTER
1560	007366	001401					BEQ ,+4	IBRANCH IF OK
1561	007370	104000					HLT	I STACK POINTER NOT EQUAL TO #STACK4
1562								
1563	007372	026767	171244	171210			CMP SSUB1, ANS1	ICHECK THE ANSWER
1564	007400	001401					BEQ ,+4	IBRANCH IF OK
1565	007402	104000					HLT	I LEFT HALF OF ANSWER WRONG
1566								
1567	007404	026767	171234	171200			CMP SSUB2, ANS2	ICHECK THE ANSWER
1568	007412	001401					BEQ ,+4	IBRANCH IF OK
1569	007414	104000					HLT	IRIGHT HALF OF ANSWER WRONG
1570								
1571	007416	000451					BR 35	
1572								

 TEST 22: EXERCISE FSUB (PDP-11 FLOATING SUBTRACT INSTRUCTION)
 RAND,A,RAND,B = RAND,C,RAND,D = ANS1,ANS2
 STACK POINTER = R3

1573	007420	012767	000604	171242	2S:	MOV	#STACK0, SAVSTK	SAVE STACK ADDRESS FOR TYPING
1574	007426	026767	171236	171162		CMP	SAVSTK, SSP	CHECK THE STACK POINTER
1575	007434	001401				BEQ	,+4	BRANCH IF OK
1576	007436	104000				HLT		STACK POINTER FOULED UP
1577								
1578	007440	022767	007320	171132		CMP	#1S, STK1	CHECK THE RTI ADDRESS ON THE STACK
1579	007446	001401				BEQ	,+4	BRANCH IF OK
1580	007450	104000				HLT		RTI ADDRESS NOT EQUAL TO #1S
1581								
1582	007452	026767	171170	171122		CMP	SSUBER, STK2	CHECK THE PSW ON THE STACK
1583	007460	001401				BEQ	,+4	BRANCH IF OK
1584	007462	104000				HLT		RTI PSW NOT EQUAL TO 200
1585								
1586	007464	026767	171134	171112		CMP	RAND,C, STK3	CHECK THE DATA ON THE STACK
1587	007472	001401				BEQ	,+4	BRANCH IF OK
1588	007474	104000				HLT		STK3 NOT EQUAL TO RAND,C
1589								
1590	007476	026767	171124	171102		CMP	RAND,D, STK4	CHECK THE DATA ON THE STACK
1591	007504	001401				BEQ	,+4	BRANCH IF OK
1592	007506	104000				HLT		STK4 NOT EQUAL TO RAND,D
1593								
1594	007510	026767	171104	171072		CMP	RAND,A, STK5	CHECK THE DATA ON THE STACK
1595	007516	001401				BEQ	,+4	BRANCH IF OK
1596	007520	104000				HLT		STK5 NOT EQUAL TO RAND,A
1597								
1598	007522	026767	171074	171062		CMP	RAND,B, STK6	CHECK THE DATA ON THE STACK
1599	007530	001401				BEQ	,+4	BRANCH IF OK
1600	007532	104000				HLT		STK6 NOT EQUAL TO RAND,B
1601								
1602	007534	012716	007542			MOV	#3S, (SP)	RESET THE STACK
1603	007540	000002				RTI		RESTORE THE STATUS (T0BIT)
1604								
1605	007542	104400			3S:	SCOPE		
1606								
1607								
1608								
1609								
1610								
1611								
1612								
1613								
1614	007544	012704	000604		TST23:	MOV	#STACK0, R4	SET UP THE STACK POINTER
1615	007550	004767	004024			JSR	PC, PUSHR	PUT THE DATA ON THE STACK
1616								
1617	007554	000240				NOP		
1618	007556	075024				FMUL+	R4	FLOATING MULTIPLY ON THE R4 STACK
1619								
1620	007560	013767	177776	171026	1S:	MOV	#PS, SPSW	SAVE PROCESSOR STATUS
1621	007566	010467	171024			MOV	R4, SSP	SAVE THE STACK POINTER
1622	007572	026767	171052	171014		CMP	\$MULPS, SPSW	CHECK THE PROCESSOR STATUS
1623	007600	001401				BEQ	,+4	BRANCH IF OK
1624	007602	104000				HLT		PSW NOT EQUAL TO \$MULPS
1625								
1626	007604	105767	171004			TSTB	SPSW	CHECK FOR ERROR

TEST 23: EXERCISE FMUL (PDP-11 FLOATING MULTIPLY INSTRUCTION)
RAND,A,RAND,B * RAND,C,RAND,D = ANS1,ANS2
STACK POINTER = R4

```

1678
1679
1680
1681
1682
1683
1684
1685 010004 012705 000604
1686 010010 004767 003564
1687
1688 010014 000240
1689 010016 075035
1690
1691 010020 013767 177776 170566 1S:
1692 010026 010567 170564
1693 010032 026767 170622 170554
1694 010040 001401
1695 010042 104000
1696
1697 010044 105767 170544
1698 010050 100423
1699
1700 010052 012767 000610 170610
1701 010060 026767 170604 170530
1702 010066 001401
1703 010070 104000
1704
1705 010072 026767 170564 170510
1706 010100 001401
1707 010102 104000
1708
1709 010104 026767 170554 170500
1710 010112 001401
1711 010114 104000
1712
1713 010116 000451
1714
1715 010120 012767 000604 170542 2S:
1716 010126 026767 170536 170462
1717 010134 001401
1718 010136 104000
1719
1720 010140 022767 010020 170432
1721 010146 001401
1722 010150 104000
1723
1724 010152 026767 170510 170422
1725 010160 001401
1726 010162 104000
1727
1728 010164 026767 170434 170412
1729 010172 001401
1730 010174 104000
1731

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;TEST 24: EXERCISE FDIV (PDP-11 FLOATING DIVIDE INSTRUCTION)
; RAND,A,RAND,B / RAND,C,RAND,D = ANS1,ANS2
; STACK POINTER = R5

TST24: MOV #STACK0,R5 ;SET UP THE STACK POINTER
JSR PC, PUSHR ;PUT THE DATA ON THE STACK

NOF
FDIV+ R5 ;FLOATING DIVIDE ON THE R5 STACK

1S: MOV @#PS, SPSW ;SAVE PROCESSOR STATUS
MOV R5, SSP ;SAVE THE STACK POINTER
CMP \$DIVPS, SPSW ;CHECK THE PROCESSOR STATUS
BEQ ,+4 ;BRANCH IF OK
HLT ;PSW NOT EQUAL TO \$DIVPS

TSTB \$PSW ;CHECK FOR ERROR
BMI 2S ;BRANCH IF ERROR

MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
CMP SAVSTK, SSP ;CHECK THE STACK POINTER
BEQ ,+4 ;BRANCH IF OK
HLT ;STACK POINTER NOT EQUAL TO #STACK4

CMP \$DIV1, ANS1 ;CHECK THE ANSWER
BEQ ,+4 ;BRANCH IF OK
HLT ;LEFT HALF OF ANSWER WRONG

CMP \$DIV2, ANS2 ;CHECK THE ANSWER
BEQ ,+4 ;BRANCH IF OK
HLT ;RIGHT HALF OF ANSWER WRONG

BR 3S

2S: MOV #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
CMP SAVSTK, SSP ;CHECK THE STACK POINTER
BEQ ,+4 ;BRANCH IF OK
HLT ;STACK POINTER FOULED UP

CMP #1S, STK1 ;CHECK THE RTI ADDRESS ON THE STACK
BEQ ,+4 ;BRANCH IF OK
HLT ;RTI ADDRESS NOT EQUAL TO #1S

CMP \$DIVER, STK2 ;CHECK THE PSW ON THE STACK
BEQ ,+4 ;BRANCH IF OK
HLT ;RTI PSW NOT EQUAL TO 200

CMP RAND,C, STK3 ;CHECK THE DATA ON THE STACK
BEQ ,+4 ;BRANCH IF OK
HLT ;STK3 NOT EQUAL TO RAND.C

1732	010170	026767	170424	170402		CMP	RAND,D, STK4);CHECK THE DATA ON THE STACK
1733	010204	001401				BEQ	,+4);BRANCH IF OK
1734	010206	104000				HLT);STK4 NOT EQUAL TO RAND,D
1735								
1736	010210	026767	170404	170372		CMP	RAND,A, STK5);CHECK THE DATA ON THE STACK
1737	010216	001401				BEQ	,+4);BRANCH IF OK
1738	010220	104000				HLT);STK5 NOT EQUAL TO RAND,A
1739								
1740	010222	026767	170374	170362		CMP	RAND,B, STK6);CHECK THE DATA ON THE STACK
1741	010230	001401				BEQ	,+4);BRANCH IF OK
1742	010232	104000				HLT);STK6 NOT EQUAL TO RAND,B
1743								
1744	010234	012716	010242			MOV	#35, (SP));RESET THE STACK
1745	010240	000002				RTI);RESTORE THE STATUS (T=BIT)
1746								
1747	010242	104400			35:		SCOPE	
1748								
1749								
1750								
1751								
1752								
1753								
1754								
1755								
1756	010244	012706	000604			TST25:	MOV #STACK0,SP);SET UP THE STACK POINTER
1757	010250	004767	003324				JSR PC, PUSHR);PUT THE DATA ON THE STACK
1758								
1759	010254	000240					NOP	
1760	010256	075006					FADD+ SP);FLOATING ADD ON THE SP STACK
1761								
1762	010260	013767	177776	170326	15:	MOV	#PS, SPSW);SAVE PROCESSOR STATUS
1763	010266	010667	170324			MOV	SP, SSP);SAVE THE STACK POINTER
1764	010272	026767	170332	170314		CMP	SADDPs, SPSW);CHECK THE PROCESSOR STATUS
1765	010300	001401				BEQ	,+4);BRANCH IF OK
1766	010302	104000				HLT);PSW NOT EQUAL TO SADDPs
1767								
1768	010304	105767	170304			TSTB	SPSW);CHECK FOR ERROR
1769	010310	100424				BM!	25);BRANCH IF ERROR
1770								
1771	010312	012767	000610	170350		MOV	#STACK4, SAVSTK);SAVE PROPER STACK ADDRESS FOR TYPING
1772	010320	026767	170344	170270		CMP	SAVSTK, SSP);CHECK THE STACK POINTER
1773	010326	001401				BEQ	,+4);BRANCH IF OK
1774	010330	104000				HLT);STACK POINTER NOT EQUAL TO #STACK4
1775								
1776	010332	026767	170274	170250		CMP	SADD1, ANS1);CHECK THE ANSWER
1777	010340	001401				BEQ	,+4);BRANCH IF OK
1778	010342	104000				HLT);LEFT HALF OF ANSWER WRONG
1779								
1780	010344	026767	170264	170240		CMP	SADD2, ANS2);CHECK THE ANSWER
1781	010352	001401				BEQ	,+4);BRANCH IF OK
1782	010354	104000				HLT);RIGHT HALF OF ANSWER WRONG
1783								
1784	010356	024646				CMP	=(SP), =(SP));RESTORE THE STACK
1785	010360	000451				BR	35	

);*****
);TEST 25: EXERCISE FADD (PDP-11 FLOATING ADD INSTRUCTION)
); RAND,A,RAND,B + RAND,C,RAND,D = ANS1,ANS2
); STACK POINTER = SP
);*****

```

1786
1787 010362 012767 000600 170300 2S:  MOV  #STK1, SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
1788 010370 026767 170274 170220    CMP  SAVSTK, SSP ;CHECK THE STACK POINTER
1789 010376 001401                BEQ  ,+4 ;BRANCH IF OK
1790 010400 104000                HLT  ;STACK POINTER FOULED UP
1791
1792 010402 022767 010260 170170    CMP  #15, STK1 ;CHECK THE RTI ADDRESS ON THE STACK
1793 010410 001401                BEQ  ,+4 ;BRANCH IF OK
1794 010412 104000                HLT  ;RTI ADDRESS NOT EQUAL TO #15
1795
1796 010414 026767 170216 170160    CMP  $ADDER, STK2 ;CHECK THE PSW ON THE STACK
1797 010422 001401                BEQ  ,+4 ;BRANCH IF OK
1798 010424 104000                HLT  ;RTI PSW NOT EQUAL TO 200
1799
1800 010426 026767 170172 170150    CMP  RAND,C, STK3 ;CHECK THE DATA ON THE STACK
1801 010434 001401                BEQ  ,+4 ;BRANCH IF OK
1802 010436 104000                HLT  ;STK3 NOT EQUAL TO RAND,C
1803
1804 010440 026767 170162 170140    CMP  RAND,D, STK4 ;CHECK THE DATA ON THE STACK
1805 010446 001401                BEQ  ,+4 ;BRANCH IF OK
1806 010450 104000                HLT  ;STK4 NOT EQUAL TO RAND,D
1807
1808 010452 026767 170142 170130    CMP  RAND,A, STK5 ;CHECK THE DATA ON THE STACK
1809 010460 001401                BEQ  ,+4 ;BRANCH IF OK
1810 010462 104000                HLT  ;STK5 NOT EQUAL TO RAND,A
1811
1812 010464 026767 170132 170120    CMP  RAND,B, STK6 ;CHECK THE DATA ON THE STACK
1813 010472 001401                BEQ  ,+4 ;BRANCH IF OK
1814 010474 104000                HLT  ;STK6 NOT EQUAL TO RAND,B
1815
1816 010476 012716 010504                MOV  #3S, (SP) ;RESET THE STACK
1817 010502 000002                RTI  ;RESTORE THE STATUS (T=BIT)
1818
1819 010504 104400                3S:  SCOPE
1820
1821
1822                                     ]*****
1823                                     ]TEST 26: EXERCISE FSUB (PDP-11 FLOATING SUBTRACT INSTRUCTION)
1824                                     ] RAND,A,RAND,B = RAND,C,RAND,D = AN$1,ANS2
1825                                     ] STACK POINTER = R0
1826                                     ]*****
1827
1828 010506 012700 000604                TST26: MOV  #STACK0,R0 ;SET UP THE STACK POINTER
1829 010512 004767 003062                JSR  PC, PUSHR ;PUT THE DATA ON THE STACK
1830
1831                                NOP
1832 010520 075010                FSUB+ R0 ;FLOATING SUBTRACT ON THE R0 STACK
1833
1834 010522 013767 177776 170064 1S:  MOV  #PS, SPSW ;SAVE PROCESSOR STATUS
1835 010530 010067 170062                MOV  R0, SSP ;SAVE THE STACK POINTER
1836 010534 026767 170100 170052    CMP  $$SUBPS, SPSW ;CHECK THE PROCESSOR STATUS
1837 010542 001401                BEQ  ,+4 ;BRANCH IF OK
1838 010544 104000                HLT  ;PSW NOT EQUAL TO $$SUBPS
1839

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```

1892
1893
1894
1895
1896
1897
1898
1899 010746 012701 000604 TST27I MOV #STACK0,R1 ;SET UP THE STACK POINTER
1900 010752 004767 002622 JSR PC, PUSHR ;PUT THE DATA ON THE STACK
1901
1902 010756 000240 NOP
1903 010760 075021 FMUL+ R1 ;FLOATING MULTIPLY ON THE R1 STACK
1904
1905 010762 013767 177776 167624 1$: MOV @#PS, SPSW ;SAVE PROCESSOR STATUS
1906 010770 010167 167622 MOV R1, SSP ;SAVE THE STACK POINTER
1907 010774 026767 167650 167612 CMP $MULPS, SPSW ;CHECK THE PROCESSOR STATUS
1908 011002 001401 BEQ ,+4 ;BRANCH IF OK
1909 011004 104000 HLT ;PSW NOT EQUAL TO $MULPS
1910
1911 011006 105767 167602 TSTB SPSW ;CHECK FOR ERROR
1912 011012 100423 BMI 25 ;BRANCH IF ERROR
1913
1914 011014 012767 000610 167646 MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
1915 011022 026767 167642 167566 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
1916 011030 001401 BEQ ,+4 ;BRANCH IF OK
1917 011032 104000 HLT ;STACK POINTER NOT EQUAL TO #STACK4
1918
1919 011034 026767 167612 167546 CMP $MUL1, ANS1 ;CHECK THE ANSWER
1920 011042 001401 BEQ ,+4 ;BRANCH IF OK
1921 011044 104000 HLT ;LEFT HALF OF ANSWER WRONG
1922
1923 011046 026767 167602 167536 CMP $MUL2, ANS2 ;CHECK THE ANSWER
1924 011054 001401 BEQ ,+4 ;BRANCH IF OK
1925 011056 104000 HLT ;RIGHT HALF OF ANSWER WRONG
1926
1927 011060 000451 BR 3$
1928
1929 011062 012767 000604 167600 2$: MOV #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
1930 011070 026767 167574 167520 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
1931 011076 001401 BEQ ,+4 ;BRANCH IF OK
1932 011100 104000 HLT ;STACK POINTER FOULED UP
1933
1934 011102 022767 010762 167470 CMP #1$, STK1 ;CHECK THE RTI ADDRESS ON THE STACK
1935 011110 001401 BEQ ,+4 ;BRANCH IF OK
1936 011112 104000 HLT ;RTI ADDRESS NOT EQUAL TO #1$
1937
1938 011114 026767 167536 167460 CMP $MULR, STK2 ;CHECK THE PSW ON THE STACK
1939 011122 001401 BEQ ,+4 ;BRANCH IF OK
1940 011124 104000 HLT ;RTI PSW NOT EQUAL TO 200
1941
1942 011126 026767 167472 167450 CMP RAND,C, STK3 ;CHECK THE DATA ON THE STACK
1943 011134 001401 BEQ ,+4 ;BRANCH IF OK
1944 011136 104000 HLT ;STK3 NOT EQUAL TO RAND,C
1945

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1946 011140 026767 167462 167440      CMP      RAND,D, STK4      ;CHECK THE DATA ON THE STACK
1947 011146 001401                    BEQ      ,+4          ;BRANCH IF OK
1948 011150 104000                    HLT                                  ;STK4 NOT EQUAL TO RAND,D
1949
1950 011152 026767 167442 167432      CMP      RAND,A, STK5      ;CHECK THE DATA ON THE STACK
1951 011160 001401                    BEQ      ,+4          ;BRANCH IF OK
1952 011162 104000                    HLT                                  ;STK5 NOT EQUAL TO RAND,A
1953
1954 011164 026767 167432 167420      CMP      RAND,B, STK6      ;CHECK THE DATA ON THE STACK
1955 011172 001401                    BEQ      ,+4          ;BRANCH IF OK
1956 011174 104000                    HLT                                  ;STK6 NOT EQUAL TO RAND,B
1957
1958 011176 012716 011204              MOV      #3$, (SP)      ;RESET THE STACK
1959 011202 000002                    RTI      ;RESTORE THE STATUS (T=BIT)
1960
1961 011204 104400                    3$:     SCOPE
1962
1963
1964 ;*****
1965 ;TEST 301      EXERCISE FDIV (PDP-11 FLOATING DIVIDE INSTRUCTION)
1966 ;      RAND,A,RAND,B / RAND,C,RAND,D = ANS1,ANS2
1967 ;      STACK POINTER = R2
1968 ;*****
1969
1970 011206 012702 000604              TST301  MOV      #STACK0,R2      ;SET UP THE STACK POINTER
1971 011212 004767 002362              JSR      PC,      PUSHR      ;PUT THE DATA ON THE STACK
1972
1973 011216 000240                    NOP
1974 011220 075032                    FDIV+   R2                ;FLOATING DIVIDE ON THE R2 STACK
1975
1976 011222 013767 177776 167364 1$:   MOV      #PS,      SPSW      ;SAVE PROCESSOR STATUS
1977 011230 010267 167362              MOV      R2,      SSP      ;SAVE THE STACK POINTER
1978 011234 026767 167420 167352      CMP      $DIVPS, SPSW      ;CHECK THE PROCESSOR STATUS
1979 011242 001401                    BEQ      ,+4          ;BRANCH IF OK
1980 011244 104000                    HLT                                  ;PSW NOT EQUAL TO $DIVPS
1981
1982 011246 105767 167342              TSTB    SPSW            ;CHECK FOR ERROR
1983 011252 100423                    BMI     2$             ;BRANCH IF ERROR
1984
1985 011254 012767 000610 167406      MOV      #STACK4,SAVSTK    ;SAVE PROPER STACK ADDRESS FOR TYPING
1986 011262 026767 167402 167326      CMP      SAVSTK, SSP      ;CHECK THE STACK POINTER
1987 011270 001401                    BEQ      ,+4          ;BRANCH IF OK
1988 011272 104000                    HLT                                  ;STACK POINTER NOT EQUAL TO #STACK4
1989
1990 011274 026767 167362 167306      CMP      $DIV1,  ANS1      ;CHECK THE ANSWER
1991 011302 001401                    BEQ      ,+4          ;BRANCH IF OK
1992 011304 104000                    HLT                                  ;LEFT HALF OF ANSWER WRONG
1993
1994 011306 026767 167352 167276      CMP      $DIV2,  ANS2      ;CHECK THE ANSWER
1995 011314 001401                    BEQ      ,+4          ;BRANCH IF OK
1996 011316 104000                    HLT                                  ;RIGHT HALF OF ANSWER WRONG
1997
1998 011320 000451                    BR      3$
1999

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2000 011322 012767 000604 167340 2S:  MOV  #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
2001 011330 026767 167334 167260    CMP  SAVSTK, $SP ;CHECK THE STACK POINTER
2002 011336 001401    BEQ  ,+4 ;BRANCH IF OK
2003 011340 104000    HLT  ;STACK POINTER FOULED UP
2004
2005 011342 022767 011222 167230    CMP  #1$, STK1 ;CHECK THE RTI ADDRESS ON THE STACK
2006 011350 001401    BEQ  ,+4 ;BRANCH IF OK
2007 011352 104000    HLT  ;RTI ADDRESS NOT EQUAL TO #1$
2008
2009 011354 026767 167306 167220    CMP  $DIVER, STK2 ;CHECK THE PSW ON THE STACK
2010 011362 001401    BEQ  ,+4 ;BRANCH IF OK
2011 011364 104000    HLT  ;RTI PSW NOT EQUAL TO 200
2012
2013 011366 026767 167232 167210    CMP  RAND,C, STK3 ;CHECK THE DATA ON THE STACK
2014 011374 001401    BEQ  ,+4 ;BRANCH IF OK
2015 011376 104000    HLT  ;STK3 NOT EQUAL TO RAND,C
2016
2017 011400 026767 167222 167200    CMP  RAND,D, STK4 ;CHECK THE DATA ON THE STACK
2018 011406 001401    BEQ  ,+4 ;BRANCH IF OK
2019 011410 104000    HLT  ;STK4 NOT EQUAL TO RAND,D
2020
2021 011412 026767 167202 167170    CMP  RAND,A, STK5 ;CHECK THE DATA ON THE STACK
2022 011420 001401    BEQ  ,+4 ;BRANCH IF OK
2023 011422 104000    HLT  ;STK5 NOT EQUAL TO RAND,A
2024
2025 011424 026767 167172 167160    CMP  RAND,B, STK6 ;CHECK THE DATA ON THE STACK
2026 011432 001401    BEQ  ,+4 ;BRANCH IF OK
2027 011434 104000    HLT  ;STK6 NOT EQUAL TO RAND,B
2028
2029 011436 012716 011444    MOV  #3$, (SP) ;RESET THE STACK
2030 011442 000002    RTI  ;RESTORE THE STATUS (T=BIT)
2031
2032 011444 104400    3$:  SCOPE
2033
2034
2035 ;*****
2036 ;TEST 31: EXERCISE FADD (PDP-11 FLOATING ADD INSTRUCTION)
2037 ; RAND,A,RAND,B + RAND,C,RAND,D = AN$1,ANS2
2038 ; STACK POINTER = R3
2039 ;*****
2040
2041 011446 012703 000604 TST31: MOV  #STACK0,R3 ;SET UP THE STACK POINTER
2042 011452 004767 002122 JSR  PC, PUSHR ;PUT THE DATA ON THE STACK
2043
2044 011456 000240 NOP
2045 011460 075003 FADD+ R3 ;FLOATING ADD ON THE R3 STACK
2046
2047 011462 013767 177776 167124 1$:  MOV  @#PS, $PSW ;SAVE PROCESSOR STATUS
2048 011470 010367 167122 MOV  R3, $SP ;SAVE THE STACK POINTER
2049 011474 026767 167130 167112 CMP  $ADDPS, $PSW ;CHECK THE PROCESSOR STATUS
2050 011502 001401    BEQ  ,+4 ;BRANCH IF OK
2051 011504 104000    HLT  ;PSW NOT EQUAL TO $ADDPS
2052
2053 011506 105767 167102 TSTB $PSW ;CHECK FOR ERROR

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2105
2106
2107
2108
2109
2110
2111
2112 011706 012704 000604
2113 011712 004767 001662
2114
2115 011716 000240
2116 011720 075014
2117
2118 011722 013767 177776 166664 1$:
2119 011730 010467 166662
2120 011734 026767 166700 166652
2121 011742 001401
2122 011744 104000
2123
2124 011746 105767 166642
2125 011752 100423
2126
2127 011754 012767 000610 166706
2128 011762 026767 166702 166626
2129 011770 001401
2130 011772 104000
2131
2132 011774 026767 166642 166606
2133 012002 001401
2134 012004 104000
2135
2136 012006 026767 166632 166576
2137 012014 001401
2138 012016 104000
2139
2140 012020 000451
2141
2142 012022 012767 000604 166640 2$:
2143 012030 026767 166634 166560
2144 012036 001401
2145 012040 104000
2146
2147 012042 022767 011722 166530
2148 012050 001401
2149 012052 104000
2150
2151 012054 026767 166566 166520
2152 012062 001401
2153 012064 104000
2154
2155 012066 026767 166532 166510
2156 012074 001401
2157 012076 104000
2158

```

;*****
;TEST 32: EXERCISE FSUB (PDP-11 FLOATING SUBTRACT INSTRUCTION)
; RAND,A,RAND,B = RAND,C,RAND,D = ANS1,ANS2
; STACK POINTER = R4
;*****

```

TST32: MOV #STACK0,R4 ;SET UP THE STACK POINTER
JSR PC, PUSHF ;PUT THE DATA ON THE STACK

NOP
FSUB+ R4 ;FLOATING SUBTRACT ON THE R4 STACK

MOV #PS, SPSW ;SAVE PROCESSOR STATUS
MOV R4, SSP ;SAVE THE STACK POINTER
CMP $$SUBPS, SPSW ;CHECK THE PROCESSOR STATUS
BEQ ,+4 ;BRANCH IF OK
HLT ;PSW NOT EQUAL TO $$SUBPS

TSTB SPSW ;CHECK FOR ERROR
BMI 2$ ;BRANCH IF ERROR

MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
CMP SAVSTK, SSP ;CHECK THE STACK POINTER
BEQ ,+4 ;BRANCH IF OK
HLT ;STACK POINTER NOT EQUAL TO #STACK4

CMP $$SUB1, ANS1 ;CHECK THE ANSWER
BEQ ,+4 ;BRANCH IF OK
HLT ;LEFT HALF OF ANSWER WRONG

CMP $$SUB2, ANS2 ;CHECK THE ANSWER
BEQ ,+4 ;BRANCH IF OK
HLT ;RIGHT HALF OF ANSWER WRONG

BR 3$

MOV #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
CMP SAVSTK, SSP ;CHECK THE STACK POINTER
BEQ ,+4 ;BRANCH IF OK
HLT ;STACK POINTER FOULED UP

CMP #1$, STK1 ;CHECK THE RTI ADDRESS ON THE STACK
BEQ ,+4 ;BRANCH IF OK
HLT ;RTI ADDRESS NOT EQUAL TO #1$

CMP $$SUBER, STK2 ;CHECK THE PSW ON THE STACK
BEQ ,+4 ;BRANCH IF OK
HLT ;RTI PSW NOT EQUAL TO 200

CMP RAND,C, STK3 ;CHECK THE DATA ON THE STACK
BEQ ,+4 ;BRANCH IF OK
HLT ;STK3 NOT EQUAL TO RAND,C

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```

2159 012100 026767 166522 166500      CMP      RAND,D, STK4      ;CHECK THE DATA ON THE STACK
2160 012106 001401                    BEQ      ,+4            ;BRANCH IF OK
2161 012110 104000                    HLT                                ;STK4 NOT EQUAL TO RAND,D
2162
2163 012112 026767 166502 166470      CMP      RAND,A, STK5      ;CHECK THE DATA ON THE STACK
2164 012120 001401                    BEQ      ,+4            ;BRANCH IF OK
2165 012122 104000                    HLT                                ;STK5 NOT EQUAL TO RAND,A
2166
2167 012124 026767 166472 166460      CMP      RAND,B, STK6      ;CHECK THE DATA ON THE STACK
2168 012132 001401                    BEQ      ,+4            ;BRANCH IF OK
2169 012134 104000                    HLT                                ;STK6 NOT EQUAL TO RAND,B
2170
2171 012136 012716 012144              MOV      #3$, (SP)        ;RESET THE STACK
2172 012142 000002                    RTI                            ;RESTORE THE STATUS (T=BIT)
2173
2174 012144 104400                      3$:      SCOPE
2175
2176
2177
2178 ;*****
2179 ;TEST 33:      EXERCISE FMUL (PDP-11 FLOATING MULTIPLY INSTRUCTION)
2180 ;      RAND,A,RAND,B * RAND,C,RAND,D = ANS1,ANS2
2181 ;      STACK POINTER = R5
2182 ;*****
2183 012146 012705 000604              TST33:  MOV      #STACK0,R5      ;SET UP THE STACK POINTER
2184 012152 004767 001422              JSR      PC,      PUSHR        ;PUT THE DATA ON THE STACK
2185
2186 012156 000240                      NOP
2187 012160 075025                      FMUL+   R5                    ;FLOATING MULTIPLY ON THE R5 STACK
2188
2189 012162 013767 177776 166424      1$:      MOV      @#PS,  SPSW        ;SAVE PROCESSOR STATUS
2190 012170 010567 166422              MOV      R5,      SSP         ;SAVE THE STACK POINTER
2191 012174 026767 166450 166412      CMP      SMULPS, SPSW        ;CHECK THE PROCESSOR STATUS
2192 012202 001401                    BEQ      ,+4            ;BRANCH IF OK
2193 012204 104000                    HLT                                ;PSW NOT EQUAL TO SMULPS
2194
2195 012206 105767 166402              TSTB    SPSW              ;CHECK FOR ERROR
2196 012212 100423                    BMI     2$                ;BRANCH IF ERROR
2197
2198 012214 012767 000610 166446      MOV      #STACK4,SAVSTK     ;SAVE PROPER STACK ADDRESS FOR TYPING
2199 012222 026767 166442 166366      CMP      SAVSTK, SSP        ;CHECK THE STACK POINTER
2200 012230 001401                    BEQ      ,+4            ;BRANCH IF OK
2201 012232 104000                    HLT                                ;STACK POINTER NOT EQUAL TO #STACK4
2202
2203 012234 026767 166412 166346      CMP      SMUL1,  ANS1        ;CHECK THE ANSWER
2204 012242 001401                    BEQ      ,+4            ;BRANCH IF OK
2205 012244 104000                    HLT                                ;LEFT HALF OF ANSWER WRONG
2206
2207 012246 026767 166402 166336      CMP      SMUL2,  ANS2        ;CHECK THE ANSWER
2208 012254 001401                    BEQ      ,+4            ;BRANCH IF OK
2209 012256 104000                    HLT                                ;RIGHT HALF OF ANSWER WRONG
2210
2211 012260 000451                      BR      3$
2212

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2213 012262 012767 000604 166400 25:  MOV    #STACK0,SAVSTK  ;SAVE STACK ADDRESS FOR TYPING
2214 012270 026767 166374 166320    CMP    SAVSTK, $SP    ;CHECK THE STACK POINTER
2215 012276 001401                BEQ    ,+4          ;BRANCH IF OK
2216 012300 104000                HLT                    ;STACK POINTER FOULED UP
2217
2218 012302 022767 012162 166270    CMP    #15,   STK1   ;CHECK THE RTI ADDRESS ON THE STACK
2219 012310 001401                BEQ    ,+4          ;BRANCH IF OK
2220 012312 104000                HLT                    ;RTI ADDRESS NOT EQUAL TO #15
2221
2222 012314 026767 166336 166260    CMP    $MULR, STK2   ;CHECK THE PSW ON THE STACK
2223 012322 001401                BEQ    ,+4          ;BRANCH IF OK
2224 012324 104000                HLT                    ;RTI PSW NOT EQUAL TO 200
2225
2226 012326 026767 166272 166250    CMP    RAND,C, STK3  ;CHECK THE DATA ON THE STACK
2227 012334 001401                BEQ    ,+4          ;BRANCH IF OK
2228 012336 104000                HLT                    ;STK3 NOT EQUAL TO RAND,C
2229
2230 012340 026767 166262 166240    CMP    RAND,D, STK4  ;CHECK THE DATA ON THE STACK
2231 012346 001401                BEQ    ,+4          ;BRANCH IF OK
2232 012350 104000                HLT                    ;STK4 NOT EQUAL TO RAND,D
2233
2234 012352 026767 166242 166230    CMP    RAND,A, STK5  ;CHECK THE DATA ON THE STACK
2235 012360 001401                BEQ    ,+4          ;BRANCH IF OK
2236 012362 104000                HLT                    ;STK5 NOT EQUAL TO RAND,A
2237
2238 012364 026767 166232 166220    CMP    RAND,B, STK6  ;CHECK THE DATA ON THE STACK
2239 012372 001401                BEQ    ,+4          ;BRANCH IF OK
2240 012374 104000                HLT                    ;STK6 NOT EQUAL TO RAND,B
2241
2242 012376 012716 012404                MOV    #3$,   (SP)   ;RESET THE STACK
2243 012402 000002                RTI                    ;RESTORE THE STATUS (T=BIT)
2244
2245 012404 104400                35:  SCOPE
2246
2247
2248                                     ;*****
2249                                     ;TEST 34:   EXERCISE FDIV (PDP-11 FLOATING DIVIDE INSTRUCTION)
2250                                     ;   RAND,A,RAND,B / RAND,C,RAND,D = ANS1,ANS2
2251                                     ;   STACK POINTER = SP
2252                                     ;*****
2253
2254 012406 012706 000604  TST34: MOV    #STACK0,SP  ;SET UP THE STACK POINTER
2255 012412 004767 001162    JSR    PC,   PUSHR   ;PUT THE DATA ON THE STACK
2256
2257 012416 000240                NOP
2258 012420 075036                FDIV+  SP            ;FLOATING DIVIDE ON THE SP STACK
2259
2260 012422 013767 177776 166164 15:  MOV    @#PS,  $PSW    ;SAVE PROCESSOR STATUS
2261 012430 010667 166162    MOV    SP,   $SP    ;SAVE THE STACK POINTER
2262 012434 026767 166220 166152    CMP    $DIVPS, $PSW  ;CHECK THE PROCESSOR STATUS
2263 012442 001401                BEQ    ,+4          ;BRANCH IF OK
2264 012444 104000                HLT                    ;PSW NOT EQUAL TO $DIVPS
2265
2266 012446 105767 166142                TSTB   $PSW        ;CHECK FOR ERROR

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2319
2320 012650 062767 000001 166130 ADD #1, PCNT+2 ;COUNT PASSES
2321 012656 005567 166122 ADC PCNT
2322
2323 012662 001 DONE:
2324 012662 032737 002000 177570 BIT #SW10,@#SWR ;RING THE BELL?
2325 012670 001002 BNE 1$ ;NO!
2326 012672 000004 000007 TYPE ;BELL
2327 012676 005046 CLR =(6) ;CLEAR TRACE TRAP
2328 012700 032737 010000 177570 BIT #SW12,@#SWR ;RUN WITH TRT?
2329 012706 001010 BNE 2$
2330 012710 005167 000044 COM ;TBIT
2331 012714 100005 BPL 2$
2332 012716 052716 000020 BIS #20,(6) ;SET TRACE TRAP
2333 012722 012746 012754 MOV #3$,-(6) ;JUMP TO START OF TEST
2334 012726 000002 RTI
2335 012730 012746 012736 2$: MOV #4$,-(6) ;JUMP TO START OF TEST
2336 012734 000002 RTI ;RETURN
2337 012736 013700 000042 4$: MOV @#42,R0 ;GET MONITOR ADDRESS
2338 012742 012742 001404 BEQ 3$ ;IF NONE
2339 012744 004710 JSR 7,(0) ;GO TO MONITOR
2340 012746 000240 NOP
2341 012750 000240 NOP
2342 012752 000240 NOP
2343 012754 000137 001146 3$: JMP @#START ;RETURN
2344
2345 012760 000000 ;TBITI 0
2346
2347
2348 ;SUBROUTINE TO READ TTY INPUT AND SAVE OCTAL NUMBER
2349
2350 012762 004767 002124 READIN: JSR PC,READS
2351 012766 012702 019212 MOV #INPUT,R2
2352 012772 012501 MOV (R5)+,R1
2353 012774 005011 CLR (R1)
2354 012776 112203 1$: MOVB (R2)+,R3 ;STORE DATA
2355 013000 001420 BEQ 4$ ;BRANCH IF DONE
2356 013002 162703 000060 SUB #60,R3
2357 013006 000241 CLC
2358 013010 032703 177770 BIT #177770,R3
2359 013014 001010 BNE 2$
2360 013016 006311 ASL (R1)
2361 013020 103407 BCS 3$
2362 013022 006311 ASL (R1)
2363 013024 103405 BCS 3$
2364 013026 006311 ASL (R1)
2365 013030 103403 BCS 3$
2366 013032 050311 BIS R3,(R1)
2367 013034 000760 BR 1$
2368 2$: SEC ;SET C=BIT IF NOT
2369 3$: CLZ
2370 4$: RTS R5

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2371								
2372	013044	016746	165552	\$PUSHI	MOV	RAND,B,=(SP)		
2373	013050	016746	165544		MOV	RAND,A,=(SP)		
2374	013054	016746	165546		MOV	RAND,D,=(SP)		
2375	013060	016746	165540		MOV	RAND,C,=(SP)		
2376	013064	000134		\$POLSH:	JMP	@(R4)+		
2377								
2378	013066	005767	165536	\$POPAD:	TST	\$ADDP5		ICHECK FOR ERROR
2379	013072	001145			BNE	\$SKIP		IBRANCH IF PS SET
2380	013074	032716	077600		BIT	#77600, (SP)		ICHECK FOR ZERO
2381	013100	001010			BNE	1\$		IBRANCH IF NOT
2382	013102	013767	177776		MOV	@#PS, \$ADDP5		IZ=BIT IN PSW
2383	013110	005067	165516		CLR	\$ADD1		IZERO ANSWER
2384	013114	005067	165514		CLR	\$ADD2		
2385	013120	000532			BR	\$SKIP		
2386								
2387	013122	005716		1\$:	TST	(SP)		IGET N=BIT, CLEAR C=BIT, V=BIT
2388	013124	013767	177776		MOV	@#PS, \$ADDP5		ISET THE PSW SAVE
2389	013132	012667	165474		MOV	(SP)+, \$ADD1		
2390	013136	012667	165472		MOV	(SP)+, \$ADD2		
2391	013142	000134			JMP	@(R4)+		
2392								
2393	013144	005767	165470	\$POPSB:	TST	\$SUBPS		ICHECK FOR ERROR
2394	013150	001116			BNE	\$SKIP		IBRANCH IF PS SET
2395	013152	032716	077600		BIT	#77600, (SP)		ICHECK FOR ZERO
2396	013156	001010			BNE	1\$		IBRANCH IF NOT
2397	013160	013767	177776		MOV	@#PS, \$SUBPS		IZ=BIT IN PSW
2398	013166	005067	165450		CLR	\$SUB1		IZERO ANSWER
2399	013172	005067	165446		CLR	\$SUB2		
2400	013176	000503			BR	\$SKIP		
2401								
2402	013200	005716		1\$:	TST	(SP)		IGET N=BIT, CLEAR C=BIT, V=BIT
2403	013202	013767	177776		MOV	@#PS, \$SUBPS		ISET THE PSW SAVE
2404	013210	012667	165426		MOV	(SP)+, \$SUB1		
2405	013214	012667	165424		MOV	(SP)+, \$SUB2		
2406	013220	000134			JMP	@(R4)+		
2407								
2408	013222	005767	165422	\$POPML:	TST	\$MULPS		ICHECK FOR ERROR
2409	013226	001067			BNE	\$SKIP		IBRANCH IF PS SET
2410	013230	032716	077600		BIT	#77600, (SP)		ICHECK FOR ZERO
2411	013234	001010			BNE	1\$		IBRANCH IF NOT
2412	013236	013767	177776		MOV	@#PS, \$MULPS		IZ=BIT IN PSW
2413	013244	005067	165402		CLR	\$MUL1		IZERO ANSWER
2414	013250	005067	165400		CLR	\$MUL2		
2415	013254	000454			BR	\$SKIP		
2416								
2417	013256	005716		1\$:	TST	(SP)		IGET N=BIT, CLEAR C=BIT, V=BIT
2418	013260	013767	177776		MOV	@#PS, \$MULPS		ISET THE PSW SAVE
2419	013266	012667	165360		MOV	(SP)+, \$MUL1		
2420	013272	012667	165356		MOV	(SP)+, \$MUL2		
2421	013276	000134			JMP	@(R4)+		

2422											
2423	013300	032767	077600	165316	\$POPDV:	BIT	#77600,	RAND,C			;CHECK FOR DIVIDED BY ZERO
2424	013306	001010				BNE	1\$				
2425	013310	000277				SCC					;SET ALL CONDITION CODES
2426	013312	000244				CLZ					;CLEAR THE Z-BIT
2427	013314	013767	177776	165344		MOV	@#PS,	\$DIVER			;SET UP DIVIDE BY ZERO CC'S
2428	013322	012767	000340	165330		MOV	#340,	\$DIVPS			;SET UP PSW
2429	013330	005767	165324		1\$:	TST	\$DIVPS				;CHECK FOR ERROR
2430	013334	001024				BNE	\$SKIP				;BRANCH IF PS SET
2431	013336	032716	077600			BIT	#77600,	(SP)			;CHECK FOR ZERO
2432	013342	001010				BNE	2\$;BRANCH IF NOT
2433	013344	013767	177776	165306		MOV	@#PS,	\$DIVPS			;Z-BIT IN PSW
2434	013352	005067	165304			CLR	\$DIV1				;ZERO ANSWER
2435	013356	005067	165302			CLR	\$DIV2				
2436	013362	000411				BR	\$SKIP				
2437											
2438	013364	005716			2\$:	TST	(SP)				;GET N=BIT, CLEAR C=BIT, V=BIT
2439	013366	013767	177776	165264		MOV	@#PS,	\$DIVPS			;SET THE PSW SAVE
2440	013374	012667	165262			MOV	(SP)+,	\$DIV1			
2441	013400	012667	165260			MOV	(SP)+,	\$DIV2			
2442	013404	000134				JMP	@(R4)+				
2443											
2444	013406	022626			\$SKIP:	CMP	(SP)+,	(SP)+			;POP GARBAGE OFF THE STACK
2445	013410	000134				JMP	@(R4)+				
2446											
2447	013412	000204			SEXIT:	RTS	R4				;EXIT POLISH MODE
2448											
2449	013414	016500	000002		\$ERR:	MOV	2(5),	R0			;PUT CODE INTO R0
2450	013420	022700	004003		\$ERRAI	CMP	#4003,	R0			;CHECK FOR DIVIDE BY ZERO
2451	013424	001464				BEG	8\$;SKIP OUT
2452											
2453	013426	122700	000003			CMPB	#3,	R0			;CHECK FOR OVERFLOW
2454	013432	001006				BNE	2\$;BRANCH IF NOT
2455	013434	000257				CCC					;CLEAR ALL CONDITION CODES
2456	013436	000262				SEV					;SET THE V=BIT
2457	013440	013767	177776	165146		MOV	@#PS,	\$PSW			;SET UP PSW FOR OVERFLOW
2458	013446	000405				BR	3\$				
2459											
2460	013450	000257			2\$:	CCC					;CLEAR ALL CONDITION CODES
2461	013452	000272				SNV					;SET N=BIT AND V=BIT
2462	013454	013767	177776	165132		MOV	@#PS,	\$PSW			;SET UP PSW FOR UNDERFLOW
2463	013462	105000			3\$:	CLRB	R0				;CLEAR LOW BYTE
2464	013464	000300				SWAB	R0				;HIGH BYTE INTO LOW
2465	013466	162700	000002			SUB	#2,	R0			;CHECK FOR ADD/SUB
2466	013472	001021				BNE	5\$;BRANCH IF NOT
2467	013474	005767	165130			TST	\$ADDP				;CHECK FOR ADD
2468	013500	001007				BNE	4\$;BRANCH IF NOT
2469	013502	016767	165106	165126		MOV	\$PSW,	\$ADDER			;SET UP ADD ERROR PSW
2470	013510	012767	000340	165112		MOV	#340,	\$ADDP			;SET UP ADD PSW
2471	013516	000427				BR	8\$				
2472											
2473	013520	016767	165070	165120	4\$:	MOV	\$PSW,	\$SUBER			;SET UP SUBTRACT ERROR PSW
2474	013526	012767	000340	165104		MOV	#340,	\$SUBPS			;SET UP SUBTRACT PSW
2475	013534	000420				BR	8\$				

2476											
2477	213536	162700	000004		5S:	SUB	#4,	R0		;CHECK FOR MUL	
2478	213542	003407				BLE	6S			;BRANCH IF NOT	
2479	213544	016767	165044	165104		MOV	\$PSW,	\$MULR		;SET UP MULTIPLY ERROR PSW	
2480	213552	012767	000340	165070		MOV	#340,	\$MULPS		;SET UP MULTIPLY PSW	
2481	213560	000406				BR	8S				
2482											
2483	213562	016767	165026	165076	6S:	MOV	\$PSW,	\$DIVER		;SET UP DIVIDE ERROR PSW	
2484	213570	012767	000340	165062	7S:	MOV	#340,	\$DIVPS		;SET UP DIVIDE PSW	
2485	213576	000225			8S:	RTS	R5			;RETURN TO FORTRAN	
2486											
2487										;SUBROUTINE TO PUSH DATA ONTO STACK	
2488											
2489	213600	016767	165016	165004	PUSHR:	MOV	RAND,B,	STACK6		;PUT DATA ON THE STACK	
2490	213606	016767	165006	164774		MOV	RAND,A,	STACK4			
2491	213614	016767	165006	164764		MOV	RAND,D,	STACK2			
2492	213622	016767	164776	164754		MOV	RAND,C,	STACK0			
2493	213630	011637	000244			MOV	(SP),	@#244		;SET UP TRAP VECTOR	
2494	213634	062737	000004	000244		ADD	#4,	@#244			
2495	213642	000207				RTS	PC				
2496											
2497	213644	032737	000400	177570	SCOPES:	BIT	#SW08,@#SWR			;KILL LDUB OR LOOP ON SPEC; TEST	
2498	213652	001412				BEQ	1S				
2499	213654	013767	177570	000134		MOV	@#SWR,	SCOTMP		;SAVE SWR	
2500	213662	042767	177600	000126		BIC	#177600,	SCOTMP		;CLR ALL BUT TEST NO,	
2501	213670	126767	000122	165102		CMPB	SCOTMP,	ICNT		;ON RIGHT TEST? *SW6=0*	
2502	213676	001434				BEQ	OVERS				
2503	213700	032737	040000	177570	1S:	BIT	#SW14,@#SWR			;LOOP ON TEST	
2504	213706	001026				BNE	KITS				
2505	213710	032737	004000	177570		BIT	#SW11,@#SWR			;KILL ITERATIONS	
2506	213716	001012				BNE	SVLADS				
2507	213720	105767	165055			TSTB	ICNT+1				
2508	213724	001404				BEQ	2S			;BRANCH IF FIRST	
2509	213726	126767	000062	165045		CMPB	TIMES,	ICNT+1		;IDONE?	
2510	213734	001013				BNE	KITS			;BRANCH IF NOT	
2511	213736	112767	000001	165035	2S:	MOVB	#1,	ICNT+1		;FIRST ITERATION	
2512	213744	105267	165030		SVLADS:	INCB	ICNT			;COUNT TEST NUMBERS	
2513	213750	011667	000036			MOV	(6),	LADS		;SAVE LOOP ADDRESS	
2514	213754	016737	165020	177570		MOV	ICNT,@#DISPLAY			;DISPLAY TEST NO, AND ITERATION COUNT	
2515	213762	000002				RTI				;RETURN	
2516											
2517	213764	105267	165011		KITS:	INCB	ICNT+1				
2518	213770	016737	165004	177570	OVERS:	MOV	ICNT,@#DISPLAY			;SET UP DISPLAY	
2519	213776	005767	000010			TST	LADS			;FIRST ONE?	
2520	214002	001760				BEQ	SVLADS				
2521	214004	016716	000002			MOV	LADS,(6)			;FUDGE RETURN ADDRESS	
2522	214010	000002				RTI				;FIXES PS	
2523											
2524	214012	000000			LADS:	0				;LOOP ADDRESS	
2525	214014	000377			TIMES:	377				;RUN 377 TIMES	
2526	214016	000000			SCOTMP:	0					

2527	014020	032737	002000	177570	HLTS:	BIT	#SW10,@#SWR	IBELL ON ERROR?
2528	014026	001402				BEQ	1\$	INO = SKIP
2529	014030	000004	000007			TYPE	,BELL	IRING BELL
2530	014034	005267	164742		1\$:	INC	ERRORS	ICOUNT THE NUMBER OF ERRORS
2531	014040	032737	020000	177570		BIT	#SW13,@#SWR	ISKIP TYPEOUT IF SET
2532	014046	001017				BNE	2\$	ISKIP TYPEOUTS
2533	014050	000004	015362			TYPE	,RETURN	
2534	014054	011667	000060			MOV	(6),HLTADS	IPUT ADDRESS OF INSTRUCTION ON STACK
2535	014060	162767	000002	000052		SUB	#2,HLTADS	
2536	014066	016705	000046			MOV	HLTADS,TTY	ITYPE HLTADS IN OCTAL
2537	014072	004767	001300			JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2538	014076	000004	015370			TYPE	,SPACE+3	
2539	014102	004767	000034			JSR	PC, ERRORS	IGO TO USER ERROR ROUTINE
2540	014106	005737	177570		2\$:	TST	@#SWR	IHALT ON ERROR
2541	014112	100001				BPL	,+4	ISKIP IF CONTINUE
2542	014114	000000				HALT		IHALT ON ERROR!
2543	014116	032737	001000	177570		BIT	#SW09,@#SWR	ICHECK FOR INHIBIT LOOP ON ERROR
2544	014124	001001				BNE	,+4	ISKIP IF LOOP ON ERROR
2545	014126	000002				RTI		
2546	014130	105067	164645			CLRB	ICNT+1	
2547	014134	000167	177624			JMP	KITS	ILOOP ON TEST UNTIL NO ERRORS
2548								
2549	014140	000000				HLTADS:	0	
2550								
2551	014142	010046				ERRORS:	MOV R0, =(SP)	ISAVE R0
2552	014144	010146					MOV R1, =(SP)	ISAVE R1
2553	014146	000004	015370			TYPE,	SPACE+3	
2554	014152	016705	164442			MOV	RAND,A,TTY	ITYPE RAND,A IN OCTAL
2555	014156	004767	001214			JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2556	014162	000004	014702			TYPE,	COMMA	
2557	014166	016705	164430			MOV	RAND,B,TTY	ITYPE RAND,B IN OCTAL
2558	014172	004767	001200			JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2559	014176	013700	000244			MOV	@#244, R0	IGET PC+2 OF INSTRUCTION
2560	014202	014001				MOV	=(R0), R1	IGET THE INSTRUCTION
2561	014204	042701	177747			BIC	#177747,R1	IMASK ALL BUT TYPE (+,.,+,/)
2562	014210	006201				ASR	R1	IDIV BY 2
2563	014212	012767	014662	000006		MOV	#SIGNS, 1\$	ISSET TO TOP OF SIGN TABLE
2564	014220	060167	000002			ADD	R1, 1\$	IADD OFFSET
2565	014224	000004				TYPE		
2566	014226	014662			1\$:	SIGNS		ITYPE THE RIGHT SIGN
2567	014230	016705	164370			MOV	RAND,C,TTY	ITYPE RAND,C IN OCTAL
2568	014234	004767	001136			JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2569	014240	000004	014702			TYPE,	COMMA	
2570	014244	016705	164356			MOV	RAND,D,TTY	ITYPE RAND,D IN OCTAL
2571	014250	004767	001122			JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2572	014254	006301				ASL	R1	IRESET TABLE POINTER
2573	014256	062701	000630			ADD	#\$ADDS,R1	
2574	014262	105767	164326			TSTB	SPSW	ICHECK FOR ERROR CONDITIONS
2575	014266	100460				BMI	3\$	IBRANCH IF ERROR
2576	014270	000004	014704			TYPE,	HEAD1	
2577	014274	000004	015062			TYPE,	EXPECT	
2578	014300	012105				MOV	(R1)+,TTY	ITYPE (R1)+ IN OCTAL
2579	014302	004767	001070			JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2580	014306	000004	015370			TYPE,	SPACE+3	

2581	014312	012705	000610	MOV	#STACK4,TTY	ITYPE #STACK4 IN OCTAL
2582	014316	004767	001054	JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2583	014322	000004	015370	TYPE,	SPACE+3	
2584	014326	012105		MOV	(R1)+,TTY	ITYPE (R1)+ IN OCTAL
2585	014330	004767	001042	JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2586	014334	000004	014702	TYPE,	COMMA	
2587	014340	011105		MOV	(R1),TTY	ITYPE (R1) IN OCTAL
2588	014342	004767	001030	JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2589	014346	000004	015076	TYPE,	GOT	
2590	014352	016705	164236	MOV	\$PSW,TTY	ITYPE \$PSW IN OCTAL
2591	014356	004767	001014	JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2592	014362	000004	015370	TYPE,	SPACE+3	
2593	014366	016705	164224	MOV	\$SP,TTY	ITYPE \$SP IN OCTAL
2594	014372	004767	001000	JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2595	014376	000004	015370	TYPE,	SPACE+3	
2596	014402	016705	164202	MOV	ANS1,TTY	ITYPE ANS1 IN OCTAL
2597	014406	004767	000764	JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2598	014412	000004	014702	TYPE,	COMMA	
2599	014416	016705	164170	MOV	ANS2,TTY	ITYPE ANS2 IN OCTAL
2600	014422	004767	000750	JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2601	014426	000510		BR	75	
2602						
2603	014430	000004	014751	3S: TYPE,	HEAD2	
2604	014434	000004	015062	TYPE,	EXPECT	
2605	014440	012105		MOV	(R1)+,TTY	ITYPE (R1)+ IN OCTAL
2606	014442	004767	000730	JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2607	014446	000004	015370	TYPE,	SPACE+3	
2608	014452	016705	164212	MOV	SAVSTK,TTY	ITYPE SAVSTK IN OCTAL
2609	014456	004767	000714	JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2610	014462	000004	015370	TYPE,	SPACE+3	
2611	014466	005720		TST	(R0)+	!UPDATE R0 TO RIGHT ADDRESS
2612	014470	010005		MOV	R0,TTY	ITYPE R0 IN OCTAL
2613	014472	004767	000700	JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2614	014476	000004	015370	TYPE,	SPACE+3	
2615	014502	022121		CMP	(R1)+, (R1)+	!ADD 4 TO R1
2616	014504	011105		MOV	(R1),TTY	ITYPE (R1) IN OCTAL
2617	014506	004767	000664	JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2618	014512	000004	015370	TYPE,	SPACE+3	
2619	014516	016705	164102	MOV	RAND,C,TTY	ITYPE RAND,C IN OCTAL
2620	014522	004767	000650	JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2621	014526	000004	015370	TYPE,	SPACE+3	
2622	014532	016705	164070	MOV	RAND,D,TTY	ITYPE RAND,D IN OCTAL
2623	014536	004767	000634	JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2624	014542	000004	015370	TYPE,	SPACE+3	
2625	014546	016705	164046	MOV	RAND,A,TTY	ITYPE RAND,A IN OCTAL
2626	014552	004767	000620	JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2627	014556	000004	015370	TYPE,	SPACE+3	
2628	014562	016705	164034	MOV	RAND,B,TTY	ITYPE RAND,B IN OCTAL
2629	014566	004767	000604	JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2630	014572	000004	015076	TYPE,	GOT	
2631	014576	016705	164012	MOV	\$PSW,TTY	ITYPE \$PSW IN OCTAL
2632	014602	004767	000570	JSR	X7,PRINTR	ITYPE LEADING ZERO'S
2633	014606	000004	015370	TYPE,	SPACE+3	
2634	014612	016705	164000	MOV	\$SP,TTY	ITYPE \$SP IN OCTAL

2635	014616	004767	000554		JSR	%7,PRINTR		;TYPE LEADING ZERO'S
2636	014622	012701	000600		MOV	#STK1, R1		;SET UP TABLE POINTER
2637	014626	012700	000006		MOV	#6, R0		
2638	014632	000004	015370	6\$:	TYPE,	SPACE+3		
2639	014636	012105			MOV	(R1)+,TTY		;TYPE (R1)+ IN OCTAL
2640	014640	004767	000532		JSR	%7,PRINTR		;TYPE LEADING ZERO'S
2641	014644	005300			DEC	R0		;COUNT DOWN
2642	014646	001371			BNE	6\$;KEEP TYPING
2643	014650	000004	015362	7\$:	TYPE,	RETURN		
2644	014654	012601			MOV	(SP)+, R1		;RESTORE R1
2645	014656	012600			MOV	(SP)+, R0		;RESTORE R0
2646	014660	000207			RTS	PC		
2647								
2648	014662	025440	000040		SIGNSI	,ASCIZ	" + "	
2649	014666	026440	000040			,ASCIZ	" = "	
2650	014672	025040	000040			,ASCIZ	" * "	
2651	014676	027440	000040			,ASCIZ	" / "	
2652								
2653	014702	000054			COMMAI	,ASCIZ	","	
2654								
2655	014704	005015	020040	020040	HEAD1I	,ASCIZ	<15><12>"	PSW SP ANSWER"
2656	014712	020040	020040	020040				
2657	014720	050040	053523	020040				
2658	014726	020040	051440	020120				
2659	014734	020040	020040	020040				
2660	014742	047101	053523	051105				
2661	014750	000						
2662	014751	015	020012	020040	HEAD2I	,ASCIZ	<15><12>"	PSW SP STK1 STK2 STK3 STK4 STK5
2663	014756	020040	020040	020040				
2664	014764	020040	051520	020127				
2665	014772	020040	020040	050123				
2666	015000	020040	020040	051440				
2667	015006	045524	020061	020040				
2668	015014	051440	045524	020062				
2669	015022	020040	051440	045524				
2670	015030	020063	020040	051440				
2671	015036	045524	020064	020040				
2672	015044	051440	045524	020065				
2673	015052	020040	051440	045524				
2674	015060	000066						
2675	015062	005015	054105	042520	EXPECTI	,ASCIZ	<15><12>"EXPECTI "	
2676	015070	052103	020072	000040				
2677	015076	005015	047507	035124	GOTI	,ASCIZ	<15><12>"GOTI "	
2678	015104	020040	020040	000040				
2679						,EVEN		

2680									
2681	015112	010346		READS:	MOV	R3,=(6)		ISAVE R3	
2682	015114	012703	015212	1\$:	MOV	#INPUT,R3		IGET ADDRESS	
2683	015120	022703	015252	2\$:	CMP	#,QUES,R3		ICHECK FOR BUFFER OVERFLOW	
2684	015124	001412			BEQ	4\$		IA BORT	
2685	015126	105737	177560		TSTB	@#177560		IWAIT FOR	
2686	015132	100375			BPL	,=4		IA CHARACTER	
2687	015134	113713	177562		MOVB	@#177562,(3)		IGET CHARACTER	
2688	015140	142713	000200		BICB	#200,(3)		IGET RID OF JUNK	
2689	015144	122713	000177		CMPB	#177,(3)		IIS IT A RUBOUT	
2690	015150	001003			RNE	3\$		ISKIP IF NOT	
2691	015152	000004	015252	4\$:	TYPE	,,QUES		ITYPE A '?'	
2692	015156	000756			BR	1\$		IZAP THE BUFFER AND LOOP	
2693	015160	111367	000210	3\$:	MOVB	(3),,TYPE		ISSET UP FOR TYPING	
2694	015164	000004	015374		TYPE	,,TYPE		IECHO IT	
2695	015170	122723	000015		CMPB	#15,(3)+		ICHECK FOR RETURN	
2696	015174	001351			BNE	2\$		ILOOP IF NOT RETURN	
2697	015176	105063	177777		CLRB	=1(3)		IZAP RETURN (THE 15)	
2698	015202	000004	000012		TYPE	,12		ITYPE A LINE FEED	
2699	015206	012603			MOV	(6)+,R3		IRESTORE R3	
2700	015210	000207			RTS	PC		IRETURN	
2701									
2702	015212	000020		INPUT:	,BLKW	20			
2703	015252	006477	000012	,QUES:	,ASCIZ	"?"<15><12>			
2704									
2705	015256	010546		,IOT:	MOV	TTY,=(6)		ISAVE TTY	
2706	015260	017605	000002		MOV	@2(6),TTY		IGET ADDRESS TO BE TYPED	
2707	015264	032705	177400		BIT	#177400,TTY		IIS IT A TYPEN?	
2708	015270	001004			BNE	1\$		INO	
2709	015272	010567	000076		MOV	TTY,,TYPE		IGET THE CHARACTER	
2710	015276	012705	015374		MOV	#,TYPE,TTY		IFUDGE THE ADDRESS	
2711	015302	105715		1\$:	TSTB	(TTY)		ITERMINATOR?	
2712	015304	001406			BEQ	2\$		IGET OUT IF SO	
2713	015306	112537	177566		MOVB	(TTY)+,@#177566		ILOAD AND TYPE THE CHARACTER	
2714	015312	105737	177564		TSTB	@#177564		IIS THE PRINTER READY	
2715	015316	100375			BPL	,=4		IWAIT UNTIL IT IS	
2716	015320	000770			BR	1\$		IGET THE NEXT CHARACTER	
2717	015322	017646	000002	2\$:	MOV	@2(6),=(6)		IGET ADDRESS TO BE TYPED	
2718	015326	062766	000002	000004	ADD	#2,4(6)		IADD 2 TO THE ADDRESS	
2719	015334	022666	000002		CMP	(6)+,2(6)		IIS IT ,+2?	
2720	015340	001006			BNE	3\$		INO	
2721	015342	062705	000002		ADD	#2,TTY		IADD 2 TO THE ADDRESS	
2722	015346	042705	000001		BIC	#1,TTY		IBACK UP TO AN EVEN BYTE	
2723	015352	010566	000002		MOV	TTY,2(6)		IRESTORE ADDRESS	
2724	015356	012605		3\$:	MOV	(6)+,TTY		IRESTORE TTY	
2725	015360	000002			RTI			IRETURN	
2726									
2727	015362	005015	000	RETURN:	,ASCIZ	<15><12>		IRETURN AND LINEFEED	
2728	015365	015	020012	SPACE:	,ASCIZ	<15><12>"	"	IRETURN AND 3 SPACES	
2729	015372	000							
2730		015374		,EVEN					
2731	015374	000000		,TYPE:	0			ICHARACTER TYPE LOCATION	

2732								
2733	015376	112767	000001	000130	PRINTR:	MOVB	#1,,PR	;SET ZERO FILL SWITCH
2734	015404	000402				BR	,+6	;SKIP
2735	015406	005067	000122		PRINTS:	CLR	,PR	;SUPPRESS LEADING ZERO'S
2736	015412	112767	177772	000115		MOVB	#=6,,PR+1	;SET COUNT
2737	015420	010446				MOV	R4,=(6)	;SAVE R4
2738	015422	012704	015524			MOV	#,PRBUF,R4	;SET POINTER TO FIRST ASCII CHAR,
2739	015426	105014				CLRB	(4)	;CLEAR FIRST BYTE
2740	015430	000405				BR	,PRF	;ROTATE FIRST BIT
2741	015432	105014			,PRL:	CLRB	(4)	;CLEAR BYTE OF CHARACTER
2742	015434	006105				ROL	TTY	;ROTATE BIT INTO C
2743	015436	106114				ROLB	(4)	;PACK IT
2744	015440	006105				ROL	TTY	;ROTATE BIT INTO C
2745	015442	106114				ROLB	(4)	;PACK IT
2746	015444	006105			,PRF:	ROL	TTY	;ROTATE BIT INTO C
2747	015446	106114				ROLB	(4)	;PACK IT
2748	015450	105714				TSTB	(4)	;IS IT ZERO?
2749	015452	001402				BEQ	,+6	;SKIP INC
2750	015454	105267	000054			INCB	,PR	;SET FILL SWITCH
2751	015460	105767	000050			TSTB	,PR	;CHECK FILL SWITCH
2752	015464	001402				BEQ	,+6	;SKIP BITSET
2753	015466	152724	000060			BISB	#'0,(4)+	;MAKE INTO ASCII CHAR
2754	015472	105267	000037			INCB	,PR+1	;INC COUNT
2755	015476	001355				BNE	,PRL	;REPEAT
2756	015500	022704	015524			CMP	#,PRBUF,R4	;EMPTY BUFFER?
2757	015504	001002				BNE	,+6	;SKIP IF NOT
2758	015506	112724	000060			MOVB	#'0,(4)+	;LOAD 1 ZERO
2759	015512	105014				CLRB	(4)	;NULL TERMINATOR
2760	015514	000004	015524			TYPE	#,PRBUF	;TYPE IT
2761	015520	012604				MOV	(6)+,R4	;RESTORE R4
2762	015522	000207				RTS	PC	;RETURN
2763								
2764	015524	000004			,PRBUF:	,BLKW	4	;OUTPUT BUFFER
2765	015534	000000			,PR:	0		;COUNT AND SWITCH

```

2766
2767 015536 012777 015652 000120 PDOWN$: MOV #ILLUP,@PUVECS ;SET FOR FAST UP
2768 015544 012777 000340 000114 MOV #340,@PUVECS+2 ;PRIO17
2769 015552 010046 MOV R0,=(6) ;PUSH R0 ON STACK
2770 015554 010146 MOV R1,=(6) ;PUSH R1 ON STACK
2771 015556 010246 MOV R2,=(6) ;PUSH R2 ON STACK
2772 015560 010346 MOV R3,=(6) ;PUSH R3 ON STACK
2773 015562 010446 MOV R4,=(6) ;PUSH R4 ON STACK
2774 015564 010546 MOV R5,=(6) ;PUSH R5 ON STACK
2775 015566 010667 000064 MOV SP,SAVR6 ;SAVE SP
2776 015572 012777 015602 000064 MOV #PUPS,@PUVECS ;SET UP VECTOR
2777 015600 000000 HALT
2778
2779 015602 016706 000050 PUPS: MOV ,SAVR6,SP ;GET SP
2780 015606 005001 CLR R1 ;WAIT LOOP FOR THE TTY
2781 015610 005201 1$: INC R1 ;WAIT FOR THE INC
2782 015612 001376 BNE 1$ ;JOP WORD
2783 015614 012605 MOV (6)+,R5 ;JPOP STACK INTO R5
2784 015616 012604 MOV (6)+,R4 ;JPOP STACK INTO R4
2785 015620 012603 MOV (6)+,R3 ;JPOP STACK INTO R3
2786 015622 012602 MOV (6)+,R2 ;JPOP STACK INTO R2
2787 015624 012601 MOV (6)+,R1 ;JPOP STACK INTO R1
2788 015626 012600 MOV (6)+,R0 ;JPOP STACK INTO R0
2789 015630 012777 015536 000022 MOV #PDOWN$,@PDVECS ;SET UP THE POWER DOWN VECTOR
2790 015636 012777 000340 000016 MOV #340,@PDVECS+2 ;PRIO17
2791 015644 000004 015670 TYPE ,POWERS
2792 015650 000002 RTI
2793
2794 015652 000000 ILLUPI: HALT ;THE POWER UP SEQUENCE WAS STARTED
2795 015654 000776 BR ,=2 ;BEFORE THE POWER DOWN WAS COMPLETE
2796
2797 015656 000000 ,SAVR6: 0 ;PUT THE SP HERE
2798 015660 000024 000026 PDVECS: 24,26 ;POWER DOWN VECTOR
2799 015664 000024 000026 PUVECS: 24,26 ;POWER UP VECTOR
2800 015670 005015 047520 042527 POWERS: ,ASCIZ <15><12>"POWER"
2801 015676 000122 ,EVEN
2802
2803
2804 000001 ,END
    
```

ANS1	= 000610	ANS2	= 000612	BEGIN	= 001010	BELL	= 000007
BIT0	= 000001	BIT1	= 000002	BIT10	= 002000	BIT11	= 004000
BIT12	= 010000	BIT13	= 020000	BIT14	= 040000	BIT15	= 100000
BIT2	= 000004	BIT3	= 000010	BIT4	= 000020	BIT5	= 000040
BIT6	= 000100	BIT7	= 000200	BIT8	= 000400	BIT9	= 001000
CCC	= 000257	COMMA	014702	DISPLA	= 177570	DONE	012662
ERRORS	001002	ERRORS	014142	EXPECT	015062	FADD	= 075000
FDIV	= 075030	FISTRP	000754	FMUL	= 075020	FORTAN	001360
FSUB	= 075010	GOT	015076	HEAD1	014704	HEAD2	014751
HLT	= 104000	HLTADS	014140	HLTS	014020	ICNT	001000
ILLUP	015652	INPUT	015212	KITS	013764	LADS	014012
LEVEL0	= 000000	LEVEL1	= 000040	LEVEL2	= 000100	LEVEL3	= 000140
LEVEL4	= 000200	LEVEL5	= 000240	LEVEL6	= 000300	LEVEL7	= 000340
N	= 000035	OVERS	013770	PC	=X000007	PCNT	001004
PDOWN\$	015536	PDVECS	015660	POWERS	015670	PRINTR	015376
PRINTS	015406	PS	= 177776	PUPS	015602	PUSHR	013600
PUVECS	015664	RAND.A	000620	RAND,B	000622	RAND,C	000624
RAND.D	000626	RAND4\$	000674	READIN	012762	READS	015112
RETURN	015362	RNDFLG	000672	R0	=X000000	R1	=X000001
R2	=X000002	R3	=X000003	R4	=X000004	R5	=X000005
SAVSTK	000670	SCC	= 000277	SCOPE	= 104400	SCOPES	013644
SCOTMP	014016	SIGNS	014662	SNV	= 000272	SP	=X000006
SPACE	015365	STACK0	000604	STACK2	000606	STACK4	000610
STACK6	000612	START	001146	STK1	000600	STK2	000602
STK3	000604	STK4	000606	STK5	000610	STK6	000612
SVLADS	013744	SWR	= 177570	SW08	= 000400	SW09	= 001000
SW10	= 002000	SW11	= 004000	SW12	= 010000	SW13	= 020000
SW14	= 040000	SW15	= 100000	TIMES	014014	TST1	001440
TST10	004202	TST11	004442	TST12	004702	TST13	005142
TST14	005402	TST15	005642	TST16	006102	TST17	006344
TST2	002000	TST20	006604	TST21	007044	TST22	007304
TST23	007544	TST24	010004	TST25	010244	TST26	010506
TST27	010746	TST3	002340	TST30	011206	TST31	011446
TST32	011706	TST33	012146	TST34	012406	TST4	002700
TST5	003240	TST6	003500	TST7	003740	TTY	=X000005
TYPE	= 000004	TYPIN	001210	YESRT	000752	SADDER	000636
\$ADDP\$	000630	SADD1	000632	SADD2	000634	SADR	= ***** G
\$DIVER	000666	\$DIVPS	000660	\$DIV1	000662	\$DIV2	000664
\$DVR	= ***** G	\$ERR	013414 G	\$ERRA	013420 G	\$EXIT	013412
\$MLR	= ***** G	\$MULR	000656	\$MULPS	000650	\$MUL1	000652
\$MUL2	000654	\$POLSH	013064	\$POPAD	013066	\$POPDV	013300
\$POPML	013222	\$POPSB	013144	\$PSW	000614	\$PUSH	013044
\$SBR	= ***** G	\$SKIP	013406	\$SP	000616	\$SUBER	000646
\$SUBPS	000640	\$SUB1	000642	\$SUB2	000644	,BIT	= 177777
,IOT	015256	,PR	015534	,PRBUF	015524	,PRF	015444
,PRL	015432	,QUES	015252	,SAVR6	015656	,TBIT	012760
,TYPE	015374	,	= 015700				

000000

```

1
2      000000'
3
4
5
6
7
8
9
10
11     000000
12     000001
13     000002
14     000003
15     000004
16     000005
17     000006
18     000007
19     000000
20     000004
21     000006
22     000010
23     000012
24     177302
25     177304
26     177312
27     177316
28     000000
29 000000' 062716 100000
30      001
31
32
33
34
35
36      000
37      001
38 000004' 010446
39 000006' 005046
40 000010' 005002
41 000012' 005003
42 000014' 006366 000006
43 000020' 006166 000004
44 000024' 156603 000005
45 000030' 001574
46 000032' 106116
47 000034' 006366 000012
48 000040' 006166 000010
49 000044' 156602 000011
50 000050' 001014
51 000052' 106016
52 000054' 006066 000004
53 000060' 006066 000006
54 000064' 016666 000004 000010

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```

,TITLE SADR
,CSECT
,GLOBAL SADR,$$SBR,$$ERR
SADR ---- THE REAL ADD ROUTINE
COPYRIGHT 1971, DIGITAL EQUIPMENT CORP., MAYNARD, MASS;
REPLACE THE TWO ITEMS ON TOP OF THE STACK
WITH THEIR SUM,
$$SBR ---- THE REAL SUBTRACT ROUTINE
SUBTRACT THE TOP STACK ITEM FROM THE SECOND ITEM
REPLACE THEM BOTH WITH THE DIFFERENCE,
R0=X0
R1=X1
R2=X2
R3=X3
R4=X4
R5=X5
SP=X6
PC=X7
SIGNS=0
A1=4
B1=6
A2=8
B2=10
AC=177302
MQ=177304
NOR=177312
ASH=177316
F0=X0

$$SBR: ADD #100000,@SP ;CHANGE THE SIGN OF TOP ITEM
,IFDF FPU
SADR: ,WORD 170001 ;ISETF
,WORD 172426 ;ILDF (SP)+,F0 ;GET OPERAND
,WORD 172026 ;IADDF (SP)+,F0 ;ADD
,WORD 174046 ;ISTF F0,-(SP) ;SUM TO STACK
JMP @R4+
,ENDC
,IFNDF FPU
SADR: MOV R4,-(SP)
CLR @(SP) ;CLEAR SIGNS
CLR R2 ;CLEAR EXPONENTS
CLR R3
ASL B1(SP) ;SHIFT B1
ROL A1(SP) ;SHIFT A1
BISB A1+1(SP),R3 ;GET E1
BEQ OUT ;JUMP IF ZERO
ROLB @SP ;GET S1
ASL B2(SP) ;SHIFT B2
ROL A2(SP) ;SHIFT A2
BISB A2+1(SP),R2 ;GET E2
BNE A2NZ ;JUMP IF NOT 0
RORB @SP ;RECONSTRUCT A1,B1
ROR A1(SP)
ROR B1(SP)
MOV A1(SP),A2(SP) ;FIRST ARG TO TOP OF STACK

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55	000072'	016666	000006	000012		MOV	B1(SP),B2(SP)
56	000100'	000550				BR	OUT ;DONE
57	000102'	106166	000001		A2NZ:	RQLB	SIGNS+1(SP) ;GET S2
58	000106'	112766	000001	000011		MOVB	#1,A2+1(SP) ;INSERT NORMAL BIT
59	000114'	112766	000001	000005		MOVB	#1,A1+1(SP) ;INSERT NORMAL BIT
60	000122'	160302				SUB	R3,R2 ;R2=E2-E1, R3=E1
61	000124'	003005				BGT	EXPA ;JUMP IF E2>E1
62	000126'	016600	000010			MOV	A2(SP),R0 ;R0=A2
63	000132'	016601	000012			MOV	B2(SP),R1 ;R1=B2
64	000136'	000415				BR	SCHK ;CHECK SIGNS
65	000140'	060203			EXPA:	ADD	R2,R3 ;R2=E2-E1, R3=E2, E2>E1
66	000142'	016600	000004			MOV	A1(SP),R0 ;R0=A1
67	000146'	016601	000006			MOV	B1(SP),R1 ;R1=B1
68	000152'	016666	000010	000004		MOV	A2(SP),A1(SP)
69	000160'	016666	000012	000006		MOV	B2(SP),B1(SP)
70	000166'	000316				SWAB	@SP ;EXCHANGE SIGNS
71	000170'	005402				NEG	R2 ;E1=E2
72	000172'	126616	000001		SCHK:	CMPB	SIGNS+1(SP),@SP ;SEE IF SIGNS ARE THE SAME
73	000176'	001403				BEQ	ECHK ;YES, CHECK EXPONENTS
74	000200'	005401				NEG	R1 ;NEGATE FRACTION
75	000202'	005500				ADC	R0
76	000204'	005400				NEG	R0
77	000206'	005702			ECHK:	TST	R2
78	000210'	001450				BEQ	SHFTD ;JUMP IF E1=E2
79	000212'	022702	177747		SHFT:	CMP	#25,,R2 ;IS THERE ANY POINT IN SHIFTING?
80	000216'	003405				BLE	SHFTR ;YES
81	000220'	016600	000004			MOV	A1(SP),R0 ;NO, ANSWER IS OPERAND
82	000224'	016601	000006			MOV	B1(SP),R1 ;WITH THE LARGER EXPONENT
83	000230'	000456				BR	NORMD
84		002				,IFDF	EAE
85					SHFTR:	MOV	R1,@#MQ ;MOVE FRACTION TO AC,MQ
86						MOV	R0,@#AC
87						MOV	R2,@#ASH ;SHIFT RIGHT TO EQUALIZE EXPONENTS
88						MOV	@#MQ,R1 ;RECOVER SHIFTED FRACTION
89						MOV	@#AC,R0
90		001				,ENDC	
91		002				,IFDF	MULDIV
92					SHFTR:	,WORD	073002 ;ASHC R2,R0
93		001				,ENDC	
94		002				,IFNDF	EAE&MULDIV
95	000232'	022702	177770		SHFTR:	CMP	#8,,R2 ;CHECK # OF BITS TO SHIFT
96	000236'	003431				BLE	SHFTR0 ;JUMP IF NOT MORE THAN 1/2 WORD
97	000240'	005004				CLR	R4 ;SET UP EXTENSION BITS
98	000242'	005700				TST	R0 ;BASED ON HIGH ORDER FRACTION
99	000244'	100001				BPL	NCOMP ;JUMP IF +
100	000246'	005104				COM	R4 ;- OTHERWISE
101	000250'	022702	177760		NCOMP:	CMP	#16,,R2
102	000254'	002405				BLT	SHFTRL ;JUMP IF LESS THAN ONE WORD TO SHIFT
103	000256'	010001				MOV	R0,R1 ;SHIFT RIGHT A WHOLE WORD
104	000260'	010400				MOV	R4,R0 ;USE EXTENSION BITS
105	000262'	062702	000020			ADD	#16,,R2 ;ACCOUNT FOR SHIFT
106	000266'	001421				BEQ	SHFTD
107	000270'	022702	177770		SHFTRL:	CMP	#8,,R2
108	000274'	003412				BLE	SHFTR0 ;JUMP IF NOT MORE THAN 1/2 WORD

109	000276'	062702	000020		ADD	#16,,R2 ;SHIFT LEFT 16=X
110	000302'	006301		SHFTL:	ASL	R1
111	000304'	006100			ROL	R0
112	000306'	006104			ROL	R4
113	000310'	005302			DEC	R2 ;COUNT LOOP
114	000312'	003373			BGT	SHFTL
115	000314'	010001			MOV	R0,R1 ;PUT RESULT IN R0, R1
116	000316'	010400			MOV	R4,R0
117	000320'	000404			BR	SHFTD
118	000322'	006200		SHFTR0:	ASR	R0 ;SHIFT A MIN AND B MIN
119	000324'	006001			ROR	R1
120	000326'	005202			INC	R2 ;REDUCE EXPONENT DIFFERENCE
121	000330'	002774			BLT	SHFTR0
122		001				,ENDC
123	000332'	066600	000004	SHFTDI	ADD	A1(SP),R0 ;A1+A2
124	000336'	066601	000006		ADD	B1(SP),R1 ;B1+B2
125	000342'	005500			ADC	R0
126	000344'	126616	000001		CMPB	SIGNS+1(SP),@SP
127	000350'	001034			BNE	SUB ;GO CLEAN UP SUBTRACT
128	000352'	030027	001000		BIT	R0,#1000
129	000356'	001403			BEQ	NORMD ;JUMP IF NO NORMAL BIT OVERFLOW
130	000360'	006200			R0	
131	000362'	006001			ASR	R1
132	000364'	005203			ROR	R3 ;INCREASE EXPONENT
133	000366'	000303		NORMDI	SWAB	R3 ;MOVE EXPONENT LEFT
134	000370'	001020			BNE	OVER ;JUMP IF OVERFLOW
135	000372'	150003			BISB	R0,R3
136	000374'	006016			ROR	@SP ;INSERT SIGN
137	000376'	006003			ROR	R3
138	000400'	006001			ROR	R1
139	000402'	005501			ADC	R1 ;ROUND SUM
140	000404'	005503			ADC	R3
141	000406'	102411			BVS	OVER ;JUMP IF OVERFLOW ON ROUND
142	000410'	103410			BCS	OVER
143	000412'	010366	000010	STORE:	MOV	R3,A2(SP) ;STORE EXPONENT AND SIGN
144	000416'	010166	000012		MOV	R1,B2(SP) ;INSERT LOW ORDER FRACTION
145	000422'	005726		OUT:	TST	(SP)+ ;POP SIGNS
146	000424'	012604			MOV	(SP)+,R4
147	000426'	022626			CMP	(SP)+,(SP)+ ;POP FIRST ARGUMENT
148	000430'	000134			JMP	@(R4)+ ;DONE, RETURN
149						
150	000432'	004567	000000G	OVER:	JSR	R5,SERR ;ERROR 3,2
151	000436'	000771			BR	OUT
152	000440'	003				,BYTE 3
153	000441'	002				,BYTE 2
154						
155	000442'	005700		SUB:	TST	R0 ;CHECK HIGH ORDER RESULT FRACTION
156	000444'	003005			BGT	BIT9 ;IF POSITIVE SIGN IS OK
157	000446'	001413			BEQ	ZTEST ;CHECK FOR ZERO RESULT
158	000450'	005400			NEG	R0 ;GET ABSOLUTE VALUE
159	000452'	005401			NEG	R1
160	000454'	005600			SBC	R0
161	000456'	000316			SWAB	@SP ;EXCHANGE SIGNS
162	000460'			BIT9:		

163		002			,IFDF	EAE
164					BIT	R0,#700
165					BNE	BIT9A ;JUMP IF NOT MORE THAN 2 TO SHIFT
166					MOV	R1,@#MO ;RESULT FRACTION TO AC,MO
167					MOV	R0,@#AC
168					CLR	@#NOR ;NORMALIZE
169					SUB	@#NOR,R3 ;ADJUST EXPONENT
170					MOV	#6,@#ASH ;SHIFT TO CORRECT POSITION
171					ADD	#6,R3 ;COMPENSATE EXPONENT
172					BLE	UNDERF ;JUMP IF UNDERFLOW
173					MOV	@#AC,R0
174					MOV	@#MQ,R1 ;GET FRACTION BACK
175					BR	NORMD
176		001			,ENDC	
177	000460'	030027	000400		BIT9A BIT	R0,#400
178	000464'	001014			BNE	UTEST ;JUMP IF NORMAL BIT FOUND
179	000466'	005303			DEC	R3 ;DECREASE EXPONENT
180	000470'	006301			ASL	R1 ;DOUBLE FRACTION
181	000472'	006100			ROL	R0
182	000474'	000771			BR	BIT9A ;TRY AGAIN
183	000476'	005701		ZTEST:	TST	R1 ;CHECK LOW ORDER PART
184		002			,IFDF	EAE
185					BNE	BIT9
186					BR	ZERO
187		001			,ENDC	
188		002			,IFNDF	EAE
189	000500'	001415			BEQ	ZERO
190	000502'	000301			SWAB	R1 ;SAVE NORMALIZE SOME TIME
191	000504'	150100			BISB	R1,R0 ;MOVE BITS LEFT
192	000506'	105001			CLRB	R1
193	000510'	162703	000010		SUB	#0,,R3 ;TELL EXPONENT ABOUT IT
194	000514'	000761			BR	BIT9
195		001			,ENDC	
196	000516'	005703		UTEST:	TST	R3 ;CHECK FOR UNDERFLOW
197	000520'	003322			BGT	NORMD ;JUMP IF NONE
198	000522'	004567	000000G	UNDERF:	JSR	R5,SERR ;ERROR 5,2
199	000526'	000401			BR	UNDER
200	000530'	005			,BYTE	5
201	000531'	002			,BYTE	2
202	000532'	005001		UNDER:	CLR	R1 ;UNDERFLOW, TREAT AS 0
203	000534'	005003		ZERO:	CLR	R3 ;CLEAR EXPONENT
204	000536'	000725			BR	STORE
205		000			,ENDC	
206		000001			,END	

AC	= 177302	ASH	= 177316	A1	= 000004	A2	= 000010
A2NZ	= 000102R	BIT9	000460R	BIT9A	000460R	B1	= 000006
B2	= 000012	ECHK	000206R	EXPA	000140R	F0	=%000000
MQ	= 177304	NCOMP	000250R	NOR	= 177312	NORMD	000366R
OUT	000422R	OVER	000432R	PC	=%000007	R0	=%000000
R1	=%000001	R2	=%000002	R3	=%000003	R4	=%000004
R5	=%000005	SCHK	000172R	SHFT	000212R	SHFTD	000332R
SHFTL	000302R	SHFTR	000232R	SHFTRL	000270R	SHFTR0	000322R
SIGNS	= 000000	SP	=%000006	STORE	000412R	SUB	000442R
UNDER	000532R	UNDERF	000522R	UTEST	000516R	ZERO	000534R
ZTEST	000476R	\$ADR	000004RG	\$ERR	= ***** G	\$SBR	000000RG

000540

ERRORS DETECTED: 0

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2 000000'
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14 000000
15 000001
16 000002
17 000003
18 000004
19 000005
20 000006
21 000007
22 177304
23 177311
24 177314
25 000000
26 000010
27 000014
28 000010
29 000002
30 001
31
32
33
34
35
36 000
37 001
38 000000' 010446
39 000002' 010546
40 002
41 000004' 016602 000004
42 000010' 006302
43 000012' 006146
44 000014' 005046
45 000016' 000302
46 000020' 110216
47 000022' 001507
48 000024' 000261
49 000026' 006002
50 000030' 105002
51 000032' 156602 000013
52 000036' 005003
53 000040' 156603 000012
54 000044' 000303

,TITLE SMLR05
,CSECT
,GLOBL SMLR,SERRA
SMLR THE REAL MULTIPLY ROUTINE

SMLR V005A
COPYRIGHT 1971, DIGITAL EQUIPMENT CORP., MAYNARD, MASS;

CALLLED IN POLISH MODE,
REPLACES THE TOP TWO REALS ON THE STACK
WITH THEIR PRODUCT.

R0=X0
R1=X1
R2=X2
R3=X3
R4=X4
R5=X5
SP=X6
PC=X7
MQ=177304
SR=177311
LSH=177314
F0=X0

A=8,
B=12,
RESLT=8,
SIGN=2

SMLR: ,IFDF FPU
,WORD 170001 ,ISETF
,WORD 172426 ,ILDF (SP)+,F0 IGET MULTIPLICAND
,WORD 171026 ,IMULF (SP)+,F0 IMULTIPLY
,WORD 174046 ,ISTF F0,=(SP) IPRODUCT TO STACK
JMP 0(R4)+

SMLR: ,IFNDF FPU
MOV R4,=(SP)
MOV R5,=(SP)
,IFNDF EAE&MULDIV
MOV A+0-4(SP),R2
R2 ,ISHIFT MULTIPLICAND
ROL =(SP) ,IKEEP SIGN
CLR =(SP) ,ICLEAR EXPONENT
SWAB R2
MOV B R2,@SP ,IKEEP MULTIPLICAND EXPONENT
BEQ ZERO1 ,IJUMP IF ANSWER IS ZERO
SEC ,IINSERT NORMAL BIT
ROR R2
CLRB R2
BISB A+3(SP),R2
CLR R3
BISB A+2(SP),R3
SWAB R3

55	000046'	006366	000014	ASL	B(SP)	IShift HIGH MULTIPLIER
56	000052'	005566	000002	ADC	SIGN(SP)	IGET PRODUCT SIGN
57	000056'	105766	000015	TSTB	B+1(SP)	
58	000062'	001467		BEG	ZERO1	IJUMP IF ZERO
59	000064'	006066	000014	ROR	B(SP)	ISIGN IS NOW ZERO
60	000070'	005000		CLR	R0	ICLEAR PRODUCT
61	000072'	005001		CLR	R1	
62	000074'	016604	000016	MOV	B*2(SP),R4	IGET LOW ORDER MULTIPLIER
63	000100'	001406		BEG	B2Z	
64	000102'	012705	000017	B2NZ:	MOV	#15,,R5
65	000106'	004767	000220		JSR	PC,MULT0
66	000112'	004767	000160		JSR	PC,MULT IDO LAST LOW BIT FULL PRECISION
67	000116'	016604	000014	B2Z:	MOV	B(SP),R4 IGET HIGH ORDER BITS
68	000122'	012705	000007		MOV	#7,R5 I THERE ARE ONLY SEVEN OF THEM
69	000126'	004767	000144		JSR	PC,MULT
70	000132'	004767	000144		JSR	PC,MULT1 IGO DO THE NORMAL BIT
71	000136'	062604		ADD	(SP)+,R4	IADD EXPONENTS
72		001				
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74		002				
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109		BR	A2Z	ISHORT CUT		
110		A2NZ:	MOV	@SP,@R4	IGET B1*A2	
111			CMF	=(R4),=(R4)	IPPOINT TO AC	
112			ADD	R3,@R4	IA2, 2'S COMP CORRECTION	
113			TST	R3		
114			BPL	A2P		
115			ADD	@SP,@R4	IB1, CORRECTION	
116		A2P:	MOV	(R4)+,R1	IHIGH PRODUCT TO R1	
117		A2Z:	MOV	2(SP),(R4)+	IB2 TO MQ	
118			BNE	B2NZ		
119			TST	=(R4)	IPPOINT TO MQ	
120			BR	B2Z	ISHORT CUT	
121		B2NZ:	MOV	R2,@R4	IGET B2*A1	
122			CMF	=(R4),=(R4)	IPPOINT TO AC	
123			ADD	2(SP),@R4	IB2, CORRECTION	
124			TST	2(SP)		
125			BPL	B2P	IJUMP IF B2 +	
126			ADD	R2,@R4	IA1, CORRECTION	
127		B2P:	ADD	(R4)+,R1	IHIGH PRODUCT TO R1	
128			ADC	R0		
129		B2Z:	MOV	R2,(R4)+	IA1 TO MQ	
130			ADD	R2,R0		
131			MOV	@SP,@R4	IGET A1*B1	
132			ADD	(SP)+,R0		
133			ADD	=(R4),R1		
134			ADC	R0		
135			ADD	=(R4),R0	IAC+R0	
136			TST	(SP)+	IPOP B2	
137			MOV	(SP)+,R4	IGET SUM OF EXPONENTS	
138	001		,	ENDC		
139			,	MUL/DIV CODE		
140	002		,	IFDF	MULDIV	
141			,	(A1+A2*2**16)*(B1+B2*2**16)		
142			MOV	B*2=4(SP),R5	ILOW ORDER B	
143			MOV	B*0=4(SP),R4	IHIGH ORDER	
144			BEQ	ZERO		
145			,	WORD	073427,1	II ASHC #1,R4 IGET SIGN BIT
146			ROL	=(SP)	ISAVE IT	
147			MOV	R4,=(SP)	ISAVE EXPONENT	
148			CLRB	@SP		
149			SWAB	@SP	IRIGHT JUSTIFY	
150			,	WORD	073427,7	II ASHC #7,R4 ILEFT JUSTIFY FRACTION
151			MOV	R5,=(SP)	ISAVE B2	
152			BIS	#100000,R4	IINSERT NORMAL BIT	
153			MOV	R4,=(SP)	ISAVE B1	
154			MOV	A+2+4(SP),R3	IGET A2	
155			MOV	A+0+4(SP),R2	IGET A1	
156			BEQ	ZERO2	IJUMP IF RESULT TO BE 0	
157			,	WORD	073227,1	II ASHC #1,R2 IGET SIGN
158			ADC	6(SP)	IGET RESULT SIGN	
159			MOV	R2,R0	IGET EXPONENT	
160			CLRB	R0		
161			SWAB	R0		
162			ADD	R0,4(SP)	IGET SUM OF EXPONENTS	

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194 000140' 006101
195 000142' 006100
196 000144' 103403
197 000146' 006101
198 000150' 006100
199 000152' 005304
200 000154' 162704 000200
201 000160' 003436
202 000162' 022704 000377
203 000166' 002427
204 000170' 105001
205 000172' 150001
206 000174' 000301
207 000176' 105000
208 000200' 150400
209 000202' 000300
210 000204' 006026
211 000206' 006000
212 000210' 006001
213 000212' 005501
214 000214' 005500
215 000216' 103414
216 000220' 102413

```

,WORD 073227,7      ;;      ASHC      #7,R2      ;GET A1
BIS   #100000,R2    ;INSERT NORMAL BIT
CLR   R0            ;CLEAR ACCUMULATOR
CLR   R1
TST   R3            ;CHECK A2
BEQ   A2Z           ;JUMP IF 0
,WORD 070403      ;;      MUL      R3,R4      ;GET A2*B1
ADD   R3,R4
TST   R3
BPL   A2P           ;JUMP IF A2 +
ADD   @SP,R4       ;B1 CORRECTION
A2P:  MOV   R4,R1   ;A2*B1*2***16
A2Z:  MOV   2(SP),R4 ;B2 TO MULTIPLIER
BEQ   B2Z           ;JUMP IF 0
,WORD 070402      ;;      MUL      R2,R4      ;GET A1*B2
ADD   2(SP),R4
TST   2(SP)
BPL   B2P           ;JUMP IF B2 +
ADD   R2,R4       ;A1 CORRECTION
B2P:  ADD   R4,R1   ;A1*B2*2***16
ADC   R0
B2Z:  MOV   R2,R4   ;A1 TO MULTIPLIER
ADD   R2,R0
,WORD 070416      ;;      MUL      @SP,R4      ;GET A1*B1
ADD   (SP)+,R0
ADD   R5,R1       ;LOW ORDER A1*B1
ADC   R0
ADD   R4,R0       ;HIGH ORDER A1*B1
TST   (SP)+       ;POP B2
MOV   (SP)+,R4    ;GET SUM OF EXPONENTS
,ENDC
ROL   R1           ;SHIFT OUT NORMAL BIT
ROL   R0
NORM: NORM         ;JUMP IF IT WAS FOUND
R1
R0             ;MUST HAVE GOT IT NOW
DEC   R4         ;ADJUST EXPONENT
SUB   #200,R4    ;TAKE OUT ONE OF THE EXCESS 128'S
BLE   UNDER     ;JUMP IF UNDERFLOW
CMP   #377,R4
BLT   OVER       ;JUMP IF OVERFLOW
CLRB  R1
BISB  R0,R1
SWAB  R1
CLRB  R0
BISB  R4,R0
SWAB  R0
ROR   (SP)+      ;GET PRODUCT SIGN
ROR   R0         ;INSERT IT IN RESULT
ROR   R1
ADC   R1
ADC   R0
BCS   OVER1      ;JUMP IF OVERFLOW ON ROUND
BVS   OVER1

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```

217 200222' 010066 000010
218 200226' 010166 000012
219 200232' 012605
220 200234' 012604
221 200236' 022626
222 200240' 000134
223      002
224
225      001
226 200242' 022626
227 200244' 000411
228 200246' 005726
229 200250' 012700 006003
230 200254' 000403
231 200256' 012700 003405
232 200262' 005726
233 200264' 004567 000000G
234 200270' 005000
235 200272' 005001
236 200274' 000752
237      002
238 200276' 006204
239 200300' 103004
240 200302' 060301
241 200304' 005500
242 200306' 103406
243 200310' 060200
244 200312' 006000
245 200314' 006001
246 200316' 005305
247 200320' 003366
248 200322' 000207
249 200324' 060200
250 200326' 000261
251 200330' 000770
252 200332' 006204
253 200334' 103001
254 200336' 060200
255 200340' 006000
256 200342' 006001
257 200344' 005305
258 200346' 003371
259 200350' 000207
260      001
261      000
262      000001

```

```

OUT:  MOV      R0,RESLT(SP)      ;PUT OUT ANSWER
      MOV      R1,RESLT+2(SP)
      MOV      (SP)+,R5
      MOV      (SP)+,R4
      CMP      (SP)+,(SP)+      ;FLUSH TOP ARGUMENT
      JMP      @ (R4)+          ;RETURN
      ,IFDF  EAE!MULDIV
ZERO2: CMP      (SP)+,(SP)+      ;POP B1,R2
      ,ENDC
ZERO1: CMP      (SP)+,(SP)+      ;POP SIGN AND EXPONENT
      BR      ZERO
OVER:  TST      (SP)+          ;FLUSH SIGN
OVER1: MOV      #6003,R0        ;ERROR 3,12
      BR      ECALL
UNDER: MOV      #3405,R0        ;ERROR 5,7
      TST      (SP)+          ;FLUSH SIGN
EQALL: JSR      R5,SERRA        ;CALL ERROR
ZERO:  CLR      R0              ;CLEAR RESULT
      CLR      R1
      BR      OUT
      ,IFNDF EAE&MULDIV
MULT:  ASR      R4              ;TEST NEXT MULTIPLIER BIT
      BCC      X0              ;JUMP IF IT IS 0
MULT1: ADD      R3,R1
      ADC      R0
      BCS      COVER
      ADD      R2,R0
X0:    ROR      R0              ;NOW SHIFT PRODUCT
      ROR      R1
      DEC      R5              ;COUNT LOOP
      BGT      MULT           ;AGAIN PLEASE
      RTS      PC              ;RETURN TO CALLER
COVER: ADD      R2,R0          ;FIRST ADD OVERFLOWED R0
      SEC      ;SHOW THIS OVERFLOW TO SHIFT
      BR      X0
MULT0: ASR      R4              ;REDUCED PRECISION MULTIPLY
      BCC      X00
      ADD      R2,R0          ;USE ONLY HIGH ORDER MULTIPLICAND
X00:   ROR      R0
      ROR      R1
      DEC      R5
      BGT      MULT0
      RTS      PC
      ,ENDC
      ,ENDC
      ,END

```

A	= 000010	B	= 000014	B2NZ	000102R	B2E	000116R
COVER	000324R	ECALL	000264R	F0	=%000000	LSH	= 177314
MQ	= 177304	MULT	000276R	MULT0	000332R	MULT1	000302R
NORM	000154R	OUT	000222R	OVER	000246R	OVER1	000250R
PC	=%000007	RESLT	= 000010	R0	=%000000	R1	=%000001
R2	=%000002	R3	=%000003	R4	=%000004	R5	=%000005
SIGN	= 000002	SP	=%000006	SR	= 177311	UNDER	000256R
X0	000312R	X00	000340R	ZERO	000270R	ZERO1	000242R
\$ERRA	= ***** G	\$MLR	000000RG	,	= 000352R		

000352

ERRORS DETECTED: 0

```

1
2      000000'
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14
15
16      000000
17      000001
18      000002
19      000003
20      000004
21      000005
22      000006
23      000007
24      177304
25      177312
26      177314
27      177316
28      000000
29      000001
30      000010
31      000014
32      000014
33      001
34
35
36
37
38
39
40      000
41      001
42      000000' 010446
43      000002' 010546
44      000004' 005000
45      000006' 005001
46      000010' 005046
47      000012' 006366 000012
48      000016' 006116
49      000020' 005046
50      000022' 156616 000015
51      000026' 001452
52      000030' 156600 000014
53      000034' 000300
54      000036' 000261
  
```

```

,TITLE SDVR05
,CSECT
,GLOBL SDVR,SERRA
SDVR --- THE REAL DIVIDE ROUTINE
  
```

SDVR V005A

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```

CALLED IN THE POLISH MODE
THE NUMERATOR IS THE SECOND ITEM ON THE STACK
AND THE DENOMINATOR IS ON TOP.
TAKES THE QUOTIENT AND PUTS IT ON TOP
OF THE STACK IN THEIR PLACE
  
```

```

R0=X0
R1=X1
R2=X2
R3=X3
R4=X4
R5=X5
SP=X6
PC=X7
MQ=177304
NOR=177312
LSH=177314
ASH=177316
F0=X0
F1=X1
D=8,
N=12,
Q=12,
  
```

```

SDVR:  ,FDF      FPU
        ,WORD  170001  ISETF
        ,WORD  172526  IILDF  (SP)+,F1      IGET DIVISOR
        ,WORD  172426  IILDF  (SP)+,F0      IGET DIVIDEND
        ,WORD  174401  IIDIVF  F1,F0  IDIVIDE
        ,WORD  174046  IISTF  F0,-(SP)  IQUOTIENT TO STCK
        JMP      @(R4)+
        ,ENDC
  
```

```

SDVR:  ,IFNDF  FPU
        MOV    R4,-(SP)
        MOV    R5,-(SP)
        CLR    R0
        CLR    R1
        CLR    -(SP)
        ASL    N+0-2(SP)  ,SHIFT NUMERATOR
        ROL    @SP      IGET NUMERATOR SIGN
        CLR    =(SP)
        BISB   N+1(SP),@SP  IGET NUMERATOR EXPONENT
        BEQ    ZERO     IJUMP IF NUMERATOR IS ZERO
        BISB   N(SP),R0
        SWAB   R0
        SEC
        ,LEFT JUSTIFY NUMERATOR FRACTION
        ,INSERT NORMAL BIT
  
```

```

55 000040' 006000
56 000042' 156600 000017
57 000046' 156601 000016
58 000052' 000301
59 000054' 005002
60 000056' 005003
61 000060' 006366 000010
62 000064' 005566 000002
63 000070' 156602 000011
64 000074' 001431
65 000076' 160216
66 000100' 005002
67 000102' 156602 000010
68 000106' 000302
69 000110' 000261
70 000112' 006002
71 000114' 156602 000013
72 000120' 156603 000012
73 000124' 000303
74 000126' 020002
75 000130' 103440
76 000132' 101034
77 000134' 020103
78 000136' 101032
79 000140' 001034
80 000142' 005066 000014
81 000146' 005216
82 000150' 005005
83 000152' 000445
84 001 001
85 002
86 001
87 002
88 001
89 000154' 022626
90 000156' 000415
91 000160' 005726
92 000162' 012700 004003
93 000166' 000406
94 000170' 005746
95 000172' 012700 003003
96 000176' 000402
97 000200' 012700 002405
98 000204' 005726
99 000206' 004567 000000G
100 000212' 005066 000010
101 000216' 005066 000012
102 000222' 000445
103 000224' 006000
104 000226' 006001
105 000230' 005216
106 002
107 000232' 012704 000011
108 000236' 004767 000104
  
```

```

ROR R0
BISB N+3(SP),R0
BISB N+2(SP),R1
SWAB R1
CLR R2
CLR R3
ASL D(SP) ;SHIFT DENOMINATOR
ADC 2(SP) ;GET RESULT SIGN
BISB D+1(SP),R2 ;GET DIVISOR EXPONENT
BEO DCHK ;JUMP IF DIVISOR IS ZERO
SUB R2,@SP ;SUBTRACT EXPONENTS
CLR R2
BISB D(SP),R2 ;GET HIGH ORDER FRACTION
SWAB R2
SEC ;INSERT NORMAL BIT
ROR R2
BISB D+3(SP),R2
BISB D+2(SP),R3
SWAB R3
CMP R0,R2 ;COMPARE HIGH NUMERATOR AND DENOMINATOR
BLO DHI ;JUMP IF DENOMINATOR HIGH
;IFNDF EAE&MULDIV
BHI DLOW ;JUMP IF DENOMINATOR LOW
CMP R1,R3 ;COMPARE LOW ORDER PARTS
BHI DLOW
BNE DHI
CLR Q(SP) ;QUOTIENT FRACTION IS 1
INC @SP ;BUMP EXPONENT
CLR R5
BR FLOAT
;ENDC
;IFDF EAE&MULDIV
BHS DLOW ;JUMP IF DENOMINATOR LOW OR SAME
;ENDC
ZERO: CMP (SP)+,(SP)+ ;FLUSH EXP AND SIGN
BR ECALL1
DCHK: TST (SP)+ ;FLUSH EXP
MOV #4003,R0 ;ERROR 3,8
BR ECALL
OVER1: TST -(SP) ;FAKE SIGN
OVER: MOV #3003,R0 ;ERROR 3,6
BR ECALL
UNDER: MOV #2405,R0 ;ERROR 5,3
ECALL: TST (SP)+ ;FLUSH SIGN
JSR R5,SERRA
ECALL1: CLR Q+0-4(SP) ;RETURN 0
CLR Q+2-4(SP)
BR RTN
DLOW: ROR R0 ;HALVE NUMERATOR (C=0)
ROR R1 ;TO ENSURE THAT N<D
INC @SP ;COMPENSATE EXPONENT
;IFNDF EAE&MULDIV
DHI: MOV #9,,R4 ;GO DO FIRST 9 QUOTIENT BITS
JSR PC,DIV1
  
```


109 000242' 110566 000014
 110
 111 000246' 005704
 112 000250' 001402
 113 000252' 005005
 114 000254' 000404
 115 000256' 012704 000020
 116 000262' 004767 000060
 117 001
 118 002
 119
 120
 121
 122
 123
 124
 125
 126
 127 001
 128 002
 129
 130
 131
 132
 133
 134
 135
 136
 137
 138
 139
 140
 141
 142
 143
 144
 145
 146
 147
 148
 149
 150
 151 001
 152 002
 153
 154
 155
 156
 157
 158
 159
 160
 161
 162

MOV#B R5,Q(SP) ;SAVE ALL HIGH ORDER 0 FRACTION
 ;EXCEPT NORMAL BIT
 TST R4 ;SEE IF DONE
 BEQ NOT0 ;NO, NUMERATOR NOT 0
 CLR R5 ;ALL THE REST OF THE QUOTIENT IS ZERO
 BR FLOAT
 NOT0: MOV #16,,R4 ;GO DO 16 MORE BITS
 JSR PC,DIV1
 ;ENDC
 ;IFDF EAE:MULDIV
 DHI: CLC
 ROR R0 ;ENSURE NUM, AND DENOM, +
 ROR R1
 ROR R2 ;LOW ORDER R1 AND R3 ARE 0
 ROR R3
 ROR R3
 ROR R0
 ROR R1
 ;ENDC
 ;IFDF EAE
 MOV #MQ,R5 ;POINT TO MQ
 MOV R1,@R5 ;NUMERATOR TO AC, MQ
 MOV R0,=(R5)
 MOV R2,=(R5) ;(A+S*B)/C
 TST (R5)+ ;POINT TO AC
 MOV (R5)+,R1 ;KEEP REMAINDER
 MOV (R5)+,R4 ;KEEP QUOTIENT
 MOV R3,@R5 ;GET Q*D
 TST =(R5) ;POINT TO MQ
 ASR R1 ;SCALE R
 SUB R1,=(R5) ;Q*D=R
 DEC @#ASH
 MOV R2,=(R5) ;(Q*D=R)/C
 CMP (R5)+,(R5)+ ;MQ
 NEG @R5
 MOV #2,@#ASH ;MULT BY 4
 ADD R4,=(R5) ;Q+(Q*D=R)*S/C
 CLR @#NOR ;NORMALIZE
 SUB @#NOR,@SP ;APPLY TO EXPONENT
 MOV #6,@#LSH ;POSITION NORMAL BIT
 MOV (R5)+,Q(SP) ;STORE QUOTIENT
 MOV @R5,R5
 ;ENDC
 ;IFDF MULDIV
 MOV R0,R4 ;NUMERATOR TO DIVIDEND
 MOV R1,R5
 ;WORD 071402 ;; DIV R2,R4 ;(A+S*B)/C
 MOV R5,R1 ;SAVE REMAINDER
 MOV R4,R0 ;SAVE QUOTIENT
 ;WORD 070403 ;; MUL R3,R4 ;GET Q*D
 ASR R1 ;SCALE R
 SUB R1,R4 ;Q*D=R
 ;WORD 073427,=1 ;; ASHC #=1,R4 ;SCALE
 ;WORD 071402 ;; DIV R2,R4 ;GET (Q*D=R)/C

163
 164
 165
 166
 167
 168
 169
 170
 171
 172 001
 173 000266' 012604
 174 000270' 062704 000200
 175 000274' 003741
 176 000276' 022704 000377
 177 000302' 002733
 178 000304' 110466 000013
 179 000310' 006026
 180 000312' 006066 000010
 181 000316' 006005
 182 000320' 005505
 183 000322' 005566 000010
 184 000326' 010566 000012
 185 000332' 103716
 186 000334' 102715
 187 000336' 012605
 188 000340' 012604
 189 000342' 022626
 190 000344' 000134
 191 002
 192 000346' 006305
 193 000350' 006301
 194 000352' 006100
 195 000354' 103406
 196 000356' 020200
 197 000360' 101010
 198 000362' 103403
 199 000364' 020301
 200 000366' 101005
 201 000370' 001407
 202 000372' 160301
 203 000374' 005600
 204 000376' 160200
 205 000400' 005205
 206 000402' 005304
 207 000404' 003360
 208 000406' 000207
 209 000410' 005205
 210 000412' 000401
 211 000414' 006305
 212 000416' 005304
 213 000420' 003375
 214 000422' 005204
 215 000424' 000207
 216 001

```

NEG      R4      ;(R=Q*D)/C
,WORD   073427,=14,    ; ASHC #14,,R4 ;UNSCALE
ADD     R0,R4    ;Q+(R=Q*D)*S/C
NBST:   ,WORD   073427,1    ; ASHC #1,R4 ;SHIFT
        BMI     NBIT    ;CHECK FOR NORMAL BIT
        DEC     @SP    ;COMPENSATE EXPONENT
        BR      NBST    ;GO AGAIN
NBIT:   ,WORD   073427,=7    ; ASHC #=8,R4 ;ALIGN FRACTION
        MOV     R4,Q(SP)  ;STORE HIGH ORDER
        ,ENDC
FLOAT:  MOV     (SP)+,R4    ;PUSH UP EXPONENT
        ADD     #200,R4 ;ADD IN EXCESS 200
        BLE     UNDER    ;UNDERFLOW
        CMP     #377,R4
        BLT     OVER      ;OVERFLOW
        MOV     R4,Q+1=2(SP) ;INSERT EXPONENT IN RESULT
SIGN:   ROR     (SP)+    ;INSERT QUOTIENT SIGN
        ROR     Q+0=4(SP)
        ROR     R5
        ADC     R5      ;ROUND
        ADC     Q+0=4(SP)
        MOV     R5,Q+2=4(SP) ;INSERT LOW ORDER FRACTION
        BCS     OVER1
        BVS     OVER1
RTN:    MOV     (SP)+,R5
        MOV     (SP)+,R4
        CMP     (SP)+,(SP)+ ;FLUSH FIRST ARGUMENT
        JMP     @R4+
DIV1:   ,IFNDF EAE&MULDIY
        ASL     R5      ;SHIFT QUOTIENT
        ASL     R1      ;SHIFT NUMERATOR
        ROL     R0
        GO      ;GUARANTEED TO GO
        CMP     R2,R0   ;COMPARE HIGH DIVISOR AND DIVIDEND
        BHI     NOGO   ;JUMP IF DIVISOR BIGGER
        BLO     GO      ;JUMP IF DIVISOR SMALLER
        CMP     R3,R1   ;CHECK THE LOW ORDERS
        BHI     NOGO
        BEQ     NEQD    ;JUMP IF NUMERATOR =DENOMINATOR
GO:     SUB     R3,R1   ;N=N-D
        SBC     R0
        SUB     R2,R0
        INC     R5      ;INSERT QUOTIENT BIT
        NOGO:  DEC     R4    ;COUNT LOOP
        BGT     DIV1
        RTS     PC
NEQD:  INC     R5      ;INSERT LAST 1 BIT IN QUOTIENT
        BR      EQ1
EQ2:   ASL     R5      ;FINISH OUT QUOTIENT WITH 0'S
EQ1:   DEC     R4
        BGT     EQ2
        INC     R4      ;FLAG NO MORE NUMERATOR
RTS:   RTS     PC      ;RETURN TO CALLER
        ,ENDC
  
```

217
218

000
000001

,ENDC
,END

ASH	= 177316	D	= 000010	DCHK	000160R	DH!	000232R
DIV1	000346R	DLOW	000224R	ECALL	000204R	ECALL1	000212R
EQ1	000416R	EQ2	000414R	FLOAT	000266R	F0	=%000000
F1	=%000001	GO	000372R	LSH	= 177314	MQ	= 177304
N	= 000014	NEQD	000410R	NOGO	000402R	NOR	= 177312
NOT0	000256R	OVER	000172R	OVER1	000170R	PC	=%000007
Q	= 000014	RTN	000336R	RTS	000424R	R0	=%000000
R1	=%000001	R2	=%000002	R3	=%000003	R4	=%000004
R5	=%000005	SIGN	000310R	SP	=%000006	UNDER	000200R
ZERO	000154R	\$DVR	000000RC	\$ERRA	= ***** G	:	= 000426R

000426

ERRORS DETECTED: 0

217
218

000
000001

,ENDC
,END

ASH	= 177316	D	= 000010	DCHK	000160R	DH!	000232R
DIV1	000346R	DLOW	000224R	ECALL	000204R	ECALL1	000212R
EQ1	000416R	EQ2	000414R	FLOAT	000266R	F0	=%000000
F1	=%000001	GO	000372R	LSH	= 177314	MQ	= 177304
N	= 000014	NEQD	000410R	NOGO	000402R	NOR	= 177312
NOT0	000256R	OVER	000172R	OVER1	000170R	PC	=%000007
Q	= 000014	RTN	000336R	RTS	000424R	R0	=%000000
R1	=%000001	R2	=%000002	R3	=%000003	R4	=%000004
R5	=%000005	SIGN	000310R	SP	=%000006	UNDER	000200R
ZERO	000154R	\$DVR	000000RG	SERRA	= ***** G	:	= 000426R

000426

ERRORS DETECTED: 0

LNKX11 V021 22-AUG-72 11:41

#DBKEBA/T:17440, DBKEBA, BKEBA, ADR04, MLR05, DVR05, /E

LOAD MAP

TRANSFER ADDRESS: 000001

LOW LIMIT: 015700

HIGH LIMIT: 017440

MODULE MAINDE

SECTION ENTRY	ADDRESS	SIZE
< , ABS. >	000000	000000
\$ERR	013414	
\$ERRA	013420	
< >	015700	000000

MODULE \$ADR

SECTION ENTRY	ADDRESS	SIZE
< >	015700	000540
\$ADR	015704	
\$SRR	015700	

MODULE \$MLR05

SECTION ENTRY	ADDRESS	SIZE
< >	016440	000352
\$MLR	016440	

MODULE \$DVR05

SECTION ENTRY	ADDRESS	SIZE
< >	017012	000426
\$DVR	017012	

RUN-TIME: 2 SECONDS
2K CORE USED

