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MAINDEC-11-DERSA-A RH11-RS03-RS04 BASIC FUNCTION DIAGNOSTIC PAGE 4
DESCRIPTION

1. ABSTRACT

THIS DIAGNOSTIC WAS DESIGNED TO TEST RS03 AND RS04 DRIVES.

THIS IS A BASIC FUNCTION DIAGNOSTIC WHICH IS USED TO VERIFY THAT THE (RH11) CONTROLLER AND THE (RS03 OR RS04) DISKS ARE OPERATING CORRECTLY. THIS IS NOT A RELIABILITY DIAGNOSTIC AND THEREFORE SHOULD NOT BE USED AS ONE. THIS PROGRAM CAN TEST UP TO 8 DRIVES. THE DRIVES CAN BE INTERMIXED AND IN ANY ORDER.

IF THE OPERATOR WOULD LIKE TO CHECK THE DISK REGISTERS PRIOR TO ENTERING THIS DIAGNOSTIC, THERE ARE SOME ROUTINES IN THE BACK OF THE DIAGNOSTIC WHICH CAN BE USED. THESE ROUTINES WILL ALLOW THE OPERATOR TO LOAD THE REGISTERS THROUGH THE SWITCHES. PLEASE REFERENCE THE STARTING ADDRESSES THAT WILL TEST THE REGISTERS YOU DESIRE.

2. REQUIREMENTS

2.1 EQUIPMENT

PDP11 STANDARD COMPUTER WITH A MINIMUM OF 8K OF MEMORY, AND AN RH11 CONTROLLER WITH A RS03 OR RS04 DISK.

2.3 PRELIMINARY PROGRAMS

NONE

3. LOADING PROCEDURE

USE STANDARD PROCEDURE FOR ABS TAPES.

4. STARTING PROCEDURE

4.1 CONTROL SWITCH SETTINGS

SEE 5.1.1 (ALL DOWN FOR WORST CASE TESTING)

4.2 STARTING ADDRESS

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MAINDEC-11-DERSA-A RH11-RS03-RS04 BASIC FUNCTION DIAGNOSTIC PAGE 5
DESCRIPTION

4.3 PROGRAM AND/OR OPERATOR ACTION

LOAD PROGRAM INTO MEMORY USING ABS LOADER.

1. STARTING ADDRESS 200.

- A. SET SWITCHES (SEE SEC 5.1.1) ALL DOWN FOR WORST CASE
- B. PRESS START.
- C. THE PROGRAM WILL LOOP AND BELL WILL RING ONCE EVERY PASS
- D. THE DISPLAY ON THE 11/45 WILL SHOW THE ITERATION COUNT IN THE LEFT BYTE AND TEST NUMBER IN THE RIGHT. TO USE, SET THE DATA DISPLAY SWITCH TO THE DISPLAY POSITION.
- E. THE PROGRAM WILL TEST ALL RS03 AND RS04 DISKS.

2. STARTING ADDRESSES FOR TESTING THE RH11-RS03/04 REGISTERS USING THE SWITCH REGISTER.

- A. 250 WORD COUNT REGISTER TEST
- B. 254 BUS ADDRESS REG. TEST
- C. 260 DISK ADDRESS REG. TEST
- D. 264 DRIVE STATUS REG. TEST
- E. 270 ERROR REG. TEST
- F. 274 LOOK AHEAD REG. TEST
- G. 300 RSCS2 REG. TEST
- H. 304 ATTENTION SUMMARY REG. TEST
- I. 310 MAINTENANCE REG. TEST
- J. 314 RSCS1 REG TEST

5. OPERATIONAL SWITCH SETTINGS

SWITCH SETTINGS ARE:

- SW<15> = 1 HALT ON ERROR
- SW<14> = 1 LOOP ON TEST
- SW<13> = 1 INHIBIT TYPEOUTS
- SW<11> = 1 INHIBIT ITERATIONS OF SUBTEST
- SW<10> = 1 BELL ON ERROR
- = 0 BELL ON PASS COMPLETE
- SW<09> = 1 LOOP ON ERROR
- SW<08> = 1 LOOP ON TEST IN SW<7:0>

5.2 SUBROUTINE ABSTRACTS

5.2.1 SCOPE

THIS SUBROUTINE CALL IS PLACED BETWEEN EACH SUBTEST IN THE INSTRUCTION

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RS11-REC3-REQ4 BASIC FUNCTION DIAGNOSTIC

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SECTION. IT RECORDS THE STARTING ADDRESS OF EACH SUBTEST AS IT IS
BEING ENTERED IN LOCATION "LAD". IF A SCOPE LOOP IS REQUESTED, THE

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MAINDEC-11-DERSA-A RH11-R503-R504 BASIC FUNCTION DIAGNOSTIC PAGE 6
DESCRIPTION

CURRENT SUBTEST WILL BE LOOPED UPON. SW<11> ON A 1 INHIBITS ITERATION OF SUBTESTS. THE CONTENTS OF LAD MAY BE USED TO DETERMINE THE LAST SUBTEST SUCCESSFULLY COMPLETED.

5.2.2 HLT

THIS ROUTINE PRINTS OUT AN ERROR MESSAGE (SEE 6.1). TO INHIBIT TYPEOUTS, PUT SW<13> ON A 1.

5.2.3 TRAPCATCHER

A ".+2" - "HALT" SEQUENCE IS REPEATED FROM 0 - 776 TO CATCH ANY UNEXPECTED TRAPS. THUS ANY UNEXPECTED TRAPS OR INTERRUPTS WILL HALT AT THE VECTOR + 2.

6. ERRORS

6.1 ERROR PRINTOUT

THE FORMAT IS AS FOLLOWS:

ADR CS1 = ----- CS2 = ----- ER = -----
GOOD = ----- BAD = -----

WHERE:

CS1, CS2, ER ETC. = RS11 DISK REGISTERS.
GOOD = EXPECTED DATA.
BAD = DATA RECEIVED.

TO FIND THE FAILING TEST, LOOK AT THE LISTING ABOVE THE ADDRESS TYPED.

6.2 ERROR RECOVERY

RESTART AT 200

7. RESTRICTIONS

NONE

MAINDEC-11-DERSA-A RH11-RS03-RS04 BASIC FUNCTION DIAGNOSTIC PAGE 7
DESCRIPTION

8. MISCELLANEOUS

8.1 EXECUTION TIME

A BELL WILL RING WITHIN 1 MINUTE WITH ALL SWITCHES DOWN.

8.2 STACK POINTER

STACK IS INITIALLY SET TO 500

9. WRITE LOCK TEST

THE WRITE LOCK TEST REQUIRES OPERATOR INTERVENTION. THE STARTING ADDRESS FOR THIS TEST IS 220. THE PROGRAM WILL TELL THE OPERATOR WHICH SWITCHES HAVE TO BE SET.

10. TEST DESCRIPTION

1. TEST RSCS2

CLEAR ALL READ/WRITE BITS AND CHECK. SET ALL R/W BITS AND CHECK. NOW CLEAR AND RECHECK.

2. TEST FOR ONLINE DRIVES

SET ERROR BITS IN RSER. THIS CAUSES ATTENTION SUMMARY BITS TO SET IN RSAS. DO FOR ALL DRIVES. RSAS HAS NOT YET BEEN TESTED. SO IN THE CASE OF NO BITS IN RSAS SETTING, DRIVE 0 IS TESTED.

3. RESET TEST FOR REGISTERS

SET ALL R/W BITS IN RSCS1, RSCS2, RSBA, RSDA, RSER, RSWC, RSOB, AND RSMR. DO A RESET AND TEST ALL R/W BITS TO BE CLEARED.

4. SET AND CLEAR ALL REGISTERS

SET ALL R/W BITS IN RSCS1, RSCS2, RSBA, RSDA, RSER, RSWC, RSOB AND RSMR AND TEST. SET ALTERNATE BITS AND CHECK TO MAKE SURE BITS ARE NOT TIED TOGETHER. NOW SET ALL BITS AND CLEAR THEM TO MAKE SURE ALL CAN BE CLEARED ONCE SET.

5. RANDOM NUMBER TEST FOR RSWC AND RSDA

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THIS TEST GENERATES RANDOM NUMBERS AND LOADS THEM INTO RSWC,

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- RSDA AND RSBA.
6. TEST "CLEAR BIT" IN RSCS2
SET ALL R/W BITS IN RSCS1, RSCS2, RSBA, RSDA, RSER, RSWC, RSDB, AND RSMR. SET CLEAR BIT IN RSCS2. NOW TEST ALL R/W BITS FOR 0 IN ALL THE ABOVE REGISTERS.
 7. LOAD RSDB WITH ALL ONES AND ALL ZEROS
LOAD RSDB WITH A WORD OF ZEROS AND A WORD OF ONES. WAIT FOR "OR" TO SET AND THEN CHECK OUTPUT OF SILO. IF OR DID NOT SET ERROR MESSAGE APPEARS.
 8. TEST NO-OP FUNCTION
THE NO-OP FUNCTION IS TESTED WITH AND WITHOUT ERROR BITS SET. ALL THE REGISTERS ARE CHECKED AFTER BOTH CASES.
 9. TEST DRIVE CLEAR FUNCTION
FIRST SET ALL R/W BITS IN RSDA, RSWC, RSER, AND RSMR. DO A DRIVE CLEAR FUNCTION. NOW TEST ALL REGISTERS FOR CORRECT DATA.
 10. EXECUTE A ONE WORD WRITE FUNCTION
SET RSWC TO -1. MOV -1 INTO OUTBUF. LOAD RSBA WITH OUTBUF. DO A WRITE TEST RDY BIT FOR 0 THEN WAIT FOR IT TO SET. TIME OUT TO ERROR IF RDY BIT DOESN'T SET AND CHECK FOR ERROR CONDITIONS. TEST RSDA FOR CORRECT ADDRESS. TEST WORD COUNT FOR 0.
 11. EXECUTE A ONE WORD WRITE CHECK
SET UP RSDA, RSBA, RSWC AND OUTBUF AS IN THE WRITE FUNCTION TEST. DO A WRITE CHECK FUNCTION. TEST RDY AS DONE IN THE WRITE TEST. CHECK FOR WRITE CHECK ERROR. THEN TEST RSDA, RSWC AND RSBA FOR CORRECT DATA.
 12. TEST READ FUNCTION
SETUP RSDA, RSBA, RSWC AND OUTBUF AS IN THE WRITE FUNCTION DO A READ FUNCTION. TEST RDY BIT AS DONE IN THE WRITE FUNCTION. TEST FOR ERRORS. ALSO TEST RSDA, RSWC AND RSBA FOR CORRECT DATA.
 13. TEST BLOCK SEARCH FUNCTION, PIP AND DRY BITS AND ADDR. CONF. BIT
DO A BLOCK SEARCH FOR SECTOR 32, LOOP ON ADDR. CONF. BIT IN RSMR. IF IT DOESN'T SET, TIMEOUT. WHEN YOU GET THERE DO A

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BLOCK SEARCH FOR SECTOR 0. NOW WE KNOW THAT WE HAVE TIME TO
TEST FOR DRY AND PIP BITS BEFORE FINDING SECTOR 0. FOR PIP

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SHOULD SET AND DRY SHOULD CLEAR BEFORE FINDING SECTOR 0. ONCE SECTOR 0 IS FOUND PIP SHOULD CLEAR AND DRY SHOULD SET. IF DRY DOES NOT SET A TIME OUT ERROR WILL OCCUR INDICATING SECTOR 0 WAS NOT FOUND. SC IN RSCS1 SHOULD ALSO SET. RSBA AND RSWC SHOULD NOT MOVE, THIS IS ALSO TESTED.

14. ILLEGAL FUNCTION CODE TEST

IN THIS TEST RSBA, RSWC AND RSDA ARE SET UP AS IF TO DO A LEGAL FUNCTION. AN ILLEGAL FUNCTION IS THEN EXECUTED. THE PROGRAM TEST FOR ILF AND ERR BITS TO SET. RSBA, RSWC AND RSDA ARE ALSO TESTED FOR CORRECT DATA. THIS IS DONE FOR ALL THE ILLEGAL FUNCTIONS.

FOR AN AID IN TROUBLE SHOOTING THE ILLEGAL FUNCTION CODE CAN BE LOADED INTO LOCATION ILLTAB OR ILFTB2, DEPENDING ON WHICH ILLEGAL FUNCTION TEST YOU WISH TO LOOP ON. IN THE NEXT LOCATION, FOLLOWING THE ILLEGAL FUNCTION, A 0 MUST BE LOADED. NOW BY SETTING SWITCH 14 (LOOP ON TEST), YOU WILL LOOP ON THE ILLEGAL FUNCTION.

15. TEST PAR IN RSER

SET PAR IN RSER AND CHECK. ALSO TEST ERR IN RSDS TO SET BECAUSE OF THE PAR SETTING.

16. TEST DPR AND MOL IN RSDS

BOTH THESE BITS SHOULD BE SET IN RSDS IF THE DRIVE IS ON LINE AND UP TO SPEED.

17. LOOK AHEAD TEST

FIRST CHECK TO SEE IF SECTOR FRACTION BITS ARE MOVING. NOW SET RSDA TO 0 AND INCREMENT IT EVERY TIME THE ADDR.CONF BIT SETS. IF THE ADDR.CONF BIT DOES NOT SET IN A CERTAIN LENGTH OF TIME, A TIME OUT ERROR OCCURS.

18. PARITY TEST

THIS WILL TEST THE PARITY LOGIC ONLY IF THERE IS PARITY MEMORY ON THE SYSTEM IN LESS THAN 28K. IT WILL WRITE BAD PARITY IN A MEMORY LOCATION THEN TRY TO DO A WRITE TO THE DRIVE FROM THAT LOCATION. THIS SHOULD CAUSE A PARITY ERROR.

19. TEST WRITE CHECK ERROR

IN THIS TEST THE PROGRAM WRITES A -1 ON TO THE DISK. A 0 IS NOW FLOATED THROUGH THE WORD IN THE BUS ADDRESS LOCATION, AND A WRITE CHECK FUNCTION IS DONE. THE WCE BIT IN RSCS2 SHOULD SET AND SHOULD CAUSE THE TRE BIT IN RSCS1 TO SET. THESE BITS ARE THEN CLEARED. A WORD OF 0 IS NOW WRITTEN ON THE DISK AND

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A 1 IS FLOATED THROUGH THE WORD IN THE BUS ADDRESS AND THE
WRITE CHECK FUNCTION TEST IS REPEATED.

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20. TEST PROGRAM ERROR BIT IN RSCS2

HERE THE PROGRAM ATTEMPTS TO INITIATE A DATA TRANSFER OPERATION WHILE THE CONTROL IS CURRENTLY PERFORMING ONE. THIS SHOULD CAUSE PGE TO SET WHICH SHOULD CAUSE TRE TO SET. THESE BITS ARE THEN CLEARED. RSWC IS ALSO TESTED FOR IT SHOULD NOT BE 0 FOR THE CURRENT OPERATION SHOULD HAVE BEEN ABORTED DUE TO THE PGE ERROR.

21. TEST RMR IN RSER

HERE A WRITE COMMAND IS GIVEN AND DURING ITS EXECUTION THE PROGRAM TRIES TO MODIFY THE RSDA REG. THIS SHOULD CAUSE THE RMR BIT TO SET WHICH CAUSES THE ERR BIT TO SET. THESE BITS ARE THEN CLEARED.

22. TEST DCK IN RSER

HERE A WRITE COMMAND IS GIVEN THEN DURING THIS FUNCTION A DRIVE CLEAR COMMAND IS GIVEN. THIS SHOULD CAUSE THE DCK BIT TO SET WHICH SHOULD CAUSE THE ERR BIT TO SET. THESE BITS ARE THEN CLEARED.

23. TEST DISK ADDRESS REGISTER

LOAD THE LAST DISK ADDRESS (7777) INTO RSDA. DO A ONE WORD WRITE AND CHECK THAT RSDA INCREMENTED TO 10000.

24. TEST IAE ERROR

DO A ONE WORD WRITE BUT FIRST SET RSDA TO AN INVALID ADDRESS SUCH AS 10000. THIS SHOULD CAUSE A IAE ERROR WHICH WILL CAUSE ERR, ATA AND SC BITS TO SET. THESE BITS ARE THEN CLEARED BY LOADING A 1 INTO ATA IN RSAS.

25. TEST FOR NON-EXISTENT DISK ERROR

FIRST FIND A DRIVE THAT IS NOT ON THE SYSTEM OR OFF LINE. NOW TRY TO DO A ONE WORD WRITE TO THAT DRIVE. NED IN RSCS2 SHOULD SET WHICH SHOULD CAUSE TRE TO SET. THESE BITS ARE THEN CLEARED BY MOVING A 1 INTO TRE.

26. TEST DAO IN RSER AND LBT IN RSDS

SET RSDA TO ITS LAST ADDRESS. NOW WRITE ONE SECTOR PLUS ONE WORD. DAO SHOULD SET AND LBT SHOULD SET. THESE SHOULD CAUSE ERR, ATA, TRE AND SC TO SET. THESE ARE CLEARED BY DOING A CLEAR.

27. TEST BAI IN RSCS2

SET BAI IN RSCS2. DO A ONE WORD WRITE AND CHECK RSBA TO SEE

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IF IT INCREMENTED.

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DESCRIPTION

28. TEST NON-EXISTENT MEMORY ERROR BIT IN RSCS2

SET BITS 0 THROUGH 5 IN RMBAE FOR AN 22 BIT ADDRESS. MOV
173000 INTO RSBA. MOV -1000 INTO RSWC AND DO A WRITE
FUNCTION. THE NEM BIT SHOULD SET AND SHOULD CAUSE TRE TO
SET. CLEAR THESE BITS BY LOADING A 1 INTO TRE.

29. TEST FOR ZERO'S IN A PARTIALLY FILLED SECTOR

FIRST WRITE A COMPLETE SECTOR WITH ALL ONES. THEN DO A ONE
WORD WRITE. THE REMAINING 63 WORDS SHOULD BE WRITTEN AS
ZERO'S. NOW DO A WRITE CHECK TO COMPARE FOR THESE ZERO'S.

30. PRIORITY INTERRUPT TEST

HERE THE PROGRAM ENABLES THE INTERRUPT AND DOES A ONE WORD
WRITE FUNCTION. THE PROGRAM SHOULD NOT TRAP UNTIL THE
PROCESSOR IS DROPPED TO PRIORITY 4.

31. DYNAMIC FUNCTION TEST

WHILE ONE DRIVE IS READING, THE UNIT # IN RSCS2 IS MODIFIED.
IF THERE IS ANOTHER DRIVE ON THE SYSTEM, A DRIVE SEARCH IS
PERFORMED ON IT. THIS IS ALL DONE WHILE THE FIRST DRIVE IS
STILL READING.

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IDENTIFICATION

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RS.1-REC3-RS04 BASIC FUNCTION DIAGNOSTIC

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PRODUCT CODE: MAINDEC-11-DERSA-A-D
PRODUCT NAME: RM11-RS03-RS04 BASIC FUNCTION DIAGNOSTIC
DATE CREATED: MARCH-1975
MAINTAINER: DIAGNOSTIC GROUP
AUTHORS: STANLEY HARACKIEWICZ

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MAINDEC-11-DERSA-A RH11-R503-R504 BASIC FUNCTION DIAGNOSTIC PAGE 4
DESCRIPTION

1. ABSTRACT

THIS DIAGNOSTIC WAS DESIGNED TO TEST R503 AND R504 DRIVES.

THIS IS A BASIC FUNCTION DIAGNOSTIC WHICH IS USED TO VERIFY THAT THE (RH11) CONTROLLER AND THE (R503 OR R504) DISKS ARE OPERATING CORRECTLY. THIS IS NOT A RELIABILITY DIAGNOSTIC AND THEREFORE SHOULD NOT BE USED AS ONE. THIS PROGRAM CAN TEST UP TO 8 DRIVES. THE DRIVES CAN BE INTERMIXED AND IN ANY ORDER.

IF THE OPERATOR WOULD LIKE TO CHECK THE DISK REGISTERS PRIOR TO ENTERING THIS DIAGNOSTIC, THERE ARE SOME ROUTINES IN THE BACK OF THE DIAGNOSTIC WHICH CAN BE USED. THESE ROUTINES WILL ALLOW THE OPERATOR TO LOAD THE REGISTERS THROUGH THE SWITCHES. PLEASE REFERENCE THE STARTING ADDRESSES THAT WILL TEST THE REGISTERS YOU DESIRE.

2. REQUIREMENTS

2.1 EQUIPMENT

PDP11 STANDARD COMPUTER WITH A MINIMUM 64K OF MEMORY, AND AN RH11 CONTROLLER WITH A R503 OR R504 DISK.

2.3 PRELIMINARY PROGRAMS

NONE

3. LOADING PROCEDURE

USE STANDARD PROCEDURE FOR ABS TAPES.

4. STARTING PROCEDURE

4.1 CONTROL SWITCH SETTINGS

SEE 5.1.1 (ALL DOWN FOR WORST CASE TESTING)

4.2 STARTING ADDRESS

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MAINDEC-11-DERSA-A RH11-RS03-RS04 BASIC FUNCTION DIAGNOSTIC PAGE 5
DESCRIPTION

4.3 PROGRAM AND/OR OPERATOR ACTION

LOAD PROGRAM INTO MEMORY USING ABS LOADER.

1. STARTING ADDRESS 200.

- A. SET SWITCHES (SEE SEC 5.1.1) ALL DOWN FOR WORST CASE
- B. PRESS START.
- C. THE PROGRAM WILL LOOP AND BELL WILL RING ONCE EVERY PASS
- D. THE DISPLAY ON THE 11/45 WILL SHOW THE ITERATION COUNT IN THE LEFT BYTE AND TEST NUMBER IN THE RIGHT. TO USE, SET THE DATA DISPLAY SWITCH TO THE DISPLAY POSITION.
- E. THE PROGRAM WILL TEST ALL RS03 AND RS04 DISKS.

2. STARTING ADDRESSES FOR TESTING THE RH11-RS03/04 REGISTERS USING THE SWITCH REGISTER.

- A. 250 WORD COUNT REGISTER TEST
- B. 254 BUS ADDRESS REG. TEST
- C. 260 DISK ADDRESS REG. TEST
- D. 264 DRIVE STATUS REG. TEST
- E. 270 ERROR REG. TEST
- F. 274 LOOK AHEAD REG. TEST
- G. 300 RSCS2 REG. TEST
- H. 304 ATTENTION SUMMARY REG. TEST
- I. 310 MAINTENANCE REG. TEST
- J. 314 RSCS1 REG TEST

5. OPERATIONAL SWITCH SETTINGS

SWITCH SETTINGS ARE:

- SW<15> = 1 HALT ON ERROR
- SW<14> = 1 LOOP ON TEST
- SW<13> = 1 INHIBIT TYPEOUTS
- SW<11> = 1 INHIBIT ITERATIONS OF SUBTEST
- SW<10> = 1 BELL ON ERROR
- 0 BELL ON PASS COMPLETE
- SW<09> = 1 LOOP ON ERROR
- SW<08> = 1 LOOP ON TEST IN SW<7:0>

5.2 SUBROUTINE ABSTRACTS

5.2.1 SCOPE

THIS SUBROUTINE CALL IS PLACED BETWEEN EACH SUBTEST IN THE INSTRUCTION

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SECTION. IT RECORDS THE STARTING ADDRESS OF EACH SUBTEST AS IT IS
BEING ENTERED IN LOCATION "LAD". IF A SCOPE LOOP IS REQUESTED, THE

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MAINDEC-11-DERSA-A RH11-RS03-RS04 BASIC FUNCTION DIAGNOSTIC PAGE 6
DESCRIPTION

CURRENT SUBTEST WILL BE LOOPEO UPON. SW<11> ON A 1 INHIBITS ITERATION
OF SUBTESTS. THE CONTENTS OF LAD MAY BE USED TO DETERMINE THE LAST
SUBTEST SUCCESSFULLY COMPLETED.

5.2.2 HLT

THIS ROUTINE PRINTS OUT AN ERROR MESSAGE (SEE 6.1). TO INHIBIT
TYPEOUTS, PUT SW<13> ON A 1.

5.2.3 TRAPCATCHER

A ".+2" - "HALT" SEQUENCE IS REPEATED FROM 0 - 776 TO CATCH ANY
UNEXPECTED TRAPS. THUS ANY UNEXPECTED TRAPS OR INTERUPTS WILL HALT AT
THE VECTOR + 2.

6. ERRORS

6.1 ERROR PRINTOUT

THE FORMAT IS AS FOLLOWS:

ADR CS1 = ----- CS2 = ----- ER = -----
GOOD = ----- BAD = -----

WHERE:

CS1, CS2, ER ETC. = RS11 DISK REGISTERS.
GOOD = EXPECTED DATA.
BAD = DATA RECEIVED.

TO FIND THE FAILING TEST, LOOK AT THE LISTING ABOVE THE ADDRESS TYPED.

6.2 ERROR RECOVERY

RESTART AT 200

7. RESTRICTIONS

NONE

MAINDEC-11-DERSA-A RH11-RS03-RS04 BASIC FUNCTION DIAGNOSTIC PAGE 7
DESCRIPTION

8. MISCELLANEOUS

8.1 EXECUTION TIME

A BELL WILL RING WITHIN 1 MINUTE WITH ALL SWITCHES DOWN.

8.2 STACK POINTER

STACK IS INITIALLY SET TO 500

9. WRITE LOCK TEST

THE WRITE LOCK TEST REQUIRES OPERATOR INTERVENTION. THE STARTING ADDRESS FOR THIS TEST IS 220. THE PROGRAM WILL TELL THE OPERATOR WHICH SWITCHES HAVE TO BE SET.

10. TEST DESCRIPTION

1. TEST RSCS2

CLEAR ALL READ/WRITE BITS AND CHECK. SET ALL R/W BITS AND CHECK. NOW CLEAR AND RECHECK.

2. TEST FOR ONLINE DRIVES

SET ERPR BITS IN RSER. THIS CAUSES ATTENTION SUMMARY BITS TO SET IN RSAS. DO FOR ALL DRIVES. RSAS HAS NOT YET BEEN TESTED. SO IN THE CASE OF NO BITS IN RSAS SETTING, DRIVE 0 IS TESTED.

3. RESET TEST FOR REGISTERS

SET ALL R/W BITS IN RSCS1, RSCS2, RSBA, RSDA, RSER, RSWC, RSDB, AND RSMR. DO A RESET AND TEST ALL R/W BITS TO BE CLEARED.

4. SET AND CLEAR ALL REGISTERS

SET ALL R/W BITS IN RSCS1, RSCS2, RSBA, RSDA, RSER, RSWC, RSDB AND RSMR AND TEST. SET ALTERNATE BITS AND CHECK TO MAKE SURE BITS ARE NOT TIED TOGETHER. NOW SET ALL BITS AND CLEAR THEM TO MAKE SURE ALL CAN BE CLEARED ONCE SET.

5. RANDOM NUMBER TEST FOR RSWC AND RSDA

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THIS TEST GENERATES RANDOM NUMBERS AND LOADS THEM INTO RSWC,

MAINDEC-11-DERSA-A RH11-RS03-RS04 BASIC FUNCTION DIAGNOSTIC PAGE 8
DESCRIPTION

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RSDA AND RSBA.

6. TEST "CLEAR BIT" IN RSCS2

SET ALL R/W BITS IN RSCS1, RSCS2, RSBA, RSDA, RSER, RSWC, RSDB, AND RSMR. SET CLEAR BIT IN RSCS2. NOW TEST ALL R/W BITS FOR 0 IN ALL THE ABOVE REGISTERS.

7. LOAD RSDB WITH ALL ONES AND ALL ZEROS

LOAD RSDB WITH A WORD OF ZEROS AND A WORD OF ONES. WAIT FOR "OR" TO SET AND THEN CHECK OUTPUT OF SILO. IF OR DID NOT SET ERROR MESSAGE APPEARS.

8. TEST NO-OP FUNCTION

THE NO-OP FUNCTION IS TESTED WITH AND WITHOUT ERROR BITS SET. ALL THE REGISTERS ARE CHECKED AFTER BOTH CASES.

9. TEST DRIVE CLEAR FUNCTION

FIRST SET ALL R/W BITS IN RSDA, RSWC, RSER, AND RSMR. DO A DRIVE CLEAR FUNCTION. NOW TEST ALL REGISTERS FOR CORRECT DATA.

10. EXECUTE A ONE WORD WRITE FUNCTION

SET RSWC TO -1. MOV -1 INTO OUTBUF. LOAD RSBA WITH OUTBUF. DO A WRITE TEST RDY BIT FOR 0 THEN WAIT FOR IT TO SET. TIME OUT TO ERROR IF RDY BIT DOESN'T SET AND CHECK FOR ERROR CONDITIONS. TEST RSDA FOR CORRECT ADDRESS. TEST WORD COUNT FOR 0.

11. EXECUTE A ONE WORD WRITE CHECK

SET UP RSDA, RSBA, RSWC AND OUTBUF AS IN THE WRITE FUNCTION TEST. DO A WRITE CHECK FUNCTION. TEST RDY AS DONE IN THE WRITE TEST. CHECK FOR WRITE CHECK ERROR. THEN TEST RSDA, RSWC AND RSBA FOR CORRECT DATA.

12. TEST READ FUNCTION

SETUP RSDA, RSBA, RSWC AND OUTBUF AS IN THE WRITE FUNCTION DO A READ FUNCTION. TEST RDY BIT AS DONE IN THE WRITE FUNCTION. TEST FOR ERRORS. ALSO TEST RSDA, RSWC AND RSBA FOR CORRECT DATA.

13. TEST BLOCK SEARCH FUNCTION, PIP AND DRY BITS AND ADDR. CONF. BIT

DO A BLOCK SEARCH FOR SECTOR 32, LOOP ON ADDR. CONF. BIT IN RSMR. IF IT DOESN'T SET, TIMEOUT. WHEN YOU GET THERE DO A

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RA:NDCC-11-DERSA-A
DERSAA.P:1

RS:1-RSC3-RSD4 BASIC FUNCTION DIAGNOSTIC

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BLOCK SEARCH FOR SECTOR D. NOW WE KNOW THAT WE HAVE TIME TO
TEST FOR DRY AND PIP BITS BEFORE FINDING SECTOR C. FOR PIP

MAINDEC-11-DERSA-A RH11-RS03-RSD4 BASIC FUNCTION DIAGNOSTIC PAGE 9
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SHOULD SET AND DRY SHOULD CLEAR BEFORE FINDING SECTOR 0.
ONCE SECTOR 0 IS FOUND PIP SHOULD CLEAR AND DRY SHOULD SET.
IF DRY DOES NOT SET A TIME OUT ERROR WILL OCCUR INDICATING
SECTOR 0 WAS NOT FOUND. SC IN RSCS1 SHOULD ALSO SET. RSBA
AND RSWC SHOULD NOT MOVE, THIS IS ALSO TESTED.

14. ILLEGAL FUNCTION CODE TEST

IN THIS TEST RSBA, RSWC AND RSDA ARE SET UP AS IF TO DO A
LEGAL FUNCTION. AN ILLEGAL FUNCTION IS THEN EXECUTED. THE
PROGRAM TEST FOR ILF AND ERR BITS TO SET. RSBA, RSWC AND
RSDA ARE ALSO TESTED FOR CORRECT DATA. THIS IS DONE FOR ALL
THE ILLEGAL FUNCTIONS.

FOR AN AID IN TROUBLE SHOOTING THE ILLEGAL FUNCTION CODE CAN
BE LOADED INTO LOCATION ILLTAB OR ILFTB2, DEPENDING ON WHICH
ILLEGAL FUNCTION TEST YOU WISH TO LOOP ON. IN THE NEXT
LOCATION, FOLLOWING THE ILLEGAL FUNCTION, A 0 MUST BE LOADED.
NOW BY SETTING SWITCH 14 (LOOP ON TEST), YOU WILL LOOP ON THE
ILLEGAL FUNCTION.

15. TEST PAR IN RSER

SET PAR IN RSER AND CHECK. ALSO TEST ERR IN RSDS TO SET
BECAUSE OF THE PAR SETTING.

16. TEST DPR AND MOL IN RSDS

BOTH THESE BITS SHOULD BE SET IN RSDS IF THE DRIVE IS ON LINE
AND UP TO SPEED.

17. LOOK AHEAD TEST

FIRST CHECK TO SEE IF SECTOR FRACTION BITS ARE MOVING. NOW
SET RSDA TO 0 AND INCREMENT IT EVERY TIME THE ADDR.CONF BIT
SETS. IF THE ADDR.CONF BIT DOES NOT SET IN A CERTAIN LENGTH
OF TIME, A TIME OUT ERROR OCCURS.

18. PARITY TEST

THIS WILL TEST THE PARITY LOGIC ONLY IF THERE IS PARITY
MEMORY ON THE SYSTEM IN LESS THAN 28K. IT WILL WRITE BAD
PARITY IN A MEMORY LOCATION THEN TRY TO DO A WRITE TO THE
DRIVE FROM THAT LOCATION. THIS SHOULD CAUSE A PARITY ERROR.

19. TEST WRITE CHECK ERROR

IN THIS TEST THE PROGRAM WRITES A -1 ON TO THE DISK. A 0 IS
NOW FLOATED THROUGH THE WORD IN THE BUS ADDRESS LOCATION, AND
A WRITE CHECK FUNCTION IS DONE. THE WCE BIT IN RSCS2 SHOULD
SET AND SHOULD CAUSE THE TRE BIT IN RSCS1 TO SET. THESE BITS
ARE THEN CLEARED. A WORD OF 0 IS NOW WRITTEN ON THE DISK AND

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DERSAA.P11

RS11-REC3-RSQ4 BASIC FUNCTION DIAGNOSTIC

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W : IS FLOATED THROUGH THE WORD IN THE BUS ADDRESS AND THE
WRITE CHECK FUNCTION TEST IS REPEATED.

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20. TEST PROGRAM ERROR BIT IN RSCS2

HERE THE PROGRAM ATTEMPTS TO INITIATE A DATA TRANSFER OPERATION WHILE THE CONTROL IS CURRENTLY PERFORMING ONE. THIS SHOULD CAUSE PGE TO SET WHICH SHOULD CAUSE TRE TO SET. THESE BITS ARE THEN CLEARED. RSWC IS ALSO TESTED FOR IT SHOULD NOT BE 0 FOR THE CURRENT OPERATION SHOULD HAVE BEEN ABORTED DUE TO THE PGE ERROR.

21. TEST RMR IN RSER

HERE A WRITE COMMAND IS GIVEN AND DURING ITS EXECUTION THE PROGRAM TRYS TO MODIFY THE RSDA REG. THIS SHOULD CAUSE THE RMR BIT TO SET WHICH CAUSES THE ERR BIT TO SET. THESE BITS ARE THEN CLEARED.

22. TEST DCK IN RSER

HERE A WRITE COMMAND IS GIVEN THEN DURING THIS FUNCTION A DRIVE CLEAR COMMAND IS GIVEN. THIS SHOULD CAUSE THE DCK BIT TO SET WHICH SHOULD CAUSE THE ERR BIT TO SET. THESE BITS ARE THEN CLEARED.

23. TEST DISK ADDRESS REGISTER

LOAD THE LAST DISK ADDRESS (7777) INTO RSDA. DO A ONE WORD WRITE AND CHECK THAT RSDA INCREMENTED TO 10000.

24. TEST IAE ERROR

DO A ONE WORD WRITE BUT FIRST SET RSDA TO AN INVALID ADDRESS SUCH AS 10000. THIS SHOULD CAUSE A IAE ERROR WHICH WILL CAUSE ERR, ATA AND SC BITS TO SET. THESE BITS ARE THEN CLEARED BY LOADING A 1 INTO ATA IN RSAS.

25. TEST FOR NON-EXISTENT DISK ERROR

FIRST FIND A DRIVE THAT IS NOT ON THE SYSTEM OR OFF LINE. NOW TRY TO DO A ONE WORD WRITE TO THAT DRIVE. NED IN RSCS2 SHOULD SET WHICH SHOULD CAUSE TRE TO SET. THESE BITS ARE THEN CLEARED BY MOVING A 1 INTO TRE.

26. TEST DAO IN RSER AND LBT IN RSDS

SET RSDA TO ITS LAST ADDRESS. NOW WRITE ONE SECTOR PLUS ONE WORD. DAO SHOULD SET AND LBT SHOULD SET. THESE SHOULD CAUSE ERR, ATA, TRE AND SC TO SET. THESE ARE CLEARED BY DOING A CLEAR.

27. TEST BAI IN RSCS2

SET BAI IN RSCS2. DO A ONE WORD WRITE AND CHECK RSBA TO SEE

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MAINDEC-11-DERSA-A
DERSAA.P11

RS11-RSC3-RS04 BASIC FUNCTION DIAGNOSTIC

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IF IT INCREMENTED.

V.S.

MAINDEC-11-DERSA-A RH11-RS03-RS04 BASIC FUNCTION DIAGNOSTIC PAGE 11
DESCRIPTION

28. TEST NON-EXISTENT MEMORY ERROR BIT IN RSCS2

SET BITS 0 THROUGH 5 IN RMBAE FOR AN 22 BIT ADDRESS. MOV
173000 INTO RSBA. MOV -1000 INTO RSWC AND DO A WRITE
FUNCTION. THE NEM BIT SHOULD SET AND SHOULD CAUSE TRE TO
SET. CLEAR THESE BITS BY LOADING A 1 INTO TRE.

29. TEST FOR ZERO'S IN A PARTIALLY FILLED SECTOR

FIRST WRITE A COMPLETE SECTOR WITH ALL ONES. THEN DO A ONE
WORD WRITE. THE REMAINING 63 WORDS SHOULD BE WRITTEN AS
ZERO'S. NOW DO A WRITE CHECK TO COMPARE FOR THESE ZERO'S.

30. PRIORITY INTERRUPT TEST

HERE THE PROGRAM ENABLES THE INTERRUPT AND DOES A ONE WORD
WRITE FUNCTION. THE PROGRAM SHOULD NOT TRAP UNTIL THE
PROCESSOR IS DROPPED TO PRIORITY 4.

31. DYNAMIC FUNCTION TEST

WHILE ONE DRIVE IS READING, THE UNIT # IN RSCS2 IS MODIFIED.
IF THERE IS ANOTHER DRIVE ON THE SYSTEM, A DRIVE SEARCH IS
PERFORMED ON IT. THIS IS ALL DONE WHILE THE FIRST DRIVE IS
STILL READING.

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IDENTIFICATION

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RS11-R503-R504 BASIC FUNCTION DIAGNOSTIC

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PRODUCT CODE: MAINDEC-11-DERSA-A-D
PRODUCT NAME: RH11-R503-R504 BASIC FUNCTION DIAGNOSTIC
DATE CREATED: MARCH-1975
MAINTAINER: DIAGNOSTIC GROUP
AUTHORS: STANLEY HARACKIEWICZ

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MAINDEC-11-DERSA-A RH11-RS03-RS04 BASIC FUNCTION DIAGNOSTIC PAGE 4
DESCRIPTION

1. ABSTRACT

THIS DIAGNOSTIC WAS DESIGNED TO TEST RS03 AND RS04 DRIVES.

THIS IS A BASIC FUNCTION DIAGNOSTIC WHICH IS USED TO VERIFY THAT THE (RH11) CONTROLLER AND THE (RS03 OR RS04) DISKS ARE OPERATING CORRECTLY. THIS IS NOT A RELIABILITY DIAGNOSTIC AND THEREFORE SHOULD NOT BE USED AS ONE. THIS PROGRAM CAN TEST UP TO 9 DRIVES. THE DRIVES CAN BE INTERMIXED AND IN ANY ORDER.

IF THE OPERATOR WOULD LIKE TO CHECK THE DISK REGISTERS PRIOR TO ENTERING THIS DIAGNOSTIC, THERE ARE SOME ROUTINES IN THE BACK OF THE DIAGNOSTIC WHICH CAN BE USED. THESE ROUTINES WILL ALLOW THE OPERATOR TO LOAD THE REGISTERS THROUGH THE SWITCHES. PLEASE REFERENCE THE STARTING ADDRESSES THAT WILL TEST THE REGISTERS YOU DESIRE.

2. REQUIREMENTS

2.1 EQUIPMENT

PDP11 STANDARD COMPUTER WITH A MINIMUM 64K BK OF MEMORY, AND AN RH11 CONTROLLER WITH A RS03 OR RS04 DISK.

2.3 PRELIMINARY PROGRAMS

NONE

3. LOADING PROCEDURE

USE STANDARD PROCEDURE FOR ABS TAPES.

4. STARTING PROCEDURE

4.1 CONTROL SWITCH SETTINGS

SEE 5.1.1 (ALL DOWN FOR WORST CASE TESTING)

4.2 STARTING ADDRESS

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MAINDEC-11-DERSA-A RH11-RS03-RS04 BASIC FUNCTION DIAGNOSTIC PAGE 5
DESCRIPTION

4.3 PROGRAM AND/OR OPERATOR ACTION

LOAD PROGRAM INTO MEMORY USING ABS LOADER.

1. STARTING ADDRESS 200.

- A. SET SWITCHES (SEE SEC 5.1.1) ALL DOWN FOR WORST CASE
- B. PRESS START.
- C. THE PROGRAM WILL LOOP AND BELL WILL RING ONCE EVERY PASS
- D. THE DISPLAY ON THE 11/45 WILL SHOW THE ITERATION COUNT IN THE LEFT BYTE AND TEST NUMBER IN THE RIGHT. TO USE, SET THE DATA DISPLAY SWITCH TO THE DISPLAY POSITION.
- E. THE PROGRAM WILL TEST ALL RS03 AND RS04 DISKS.

2. STARTING ADDRESSES FOR TESTING THE RH11-RS03/04 REGISTERS USING THE SWITCH REGISTER.

- A. 250 WORD COUNT REGISTER TEST
- B. 254 BUS ADDRESS REG. TEST
- C. 260 DISK ADDRESS REG. TEST
- D. 264 DRIVE STATUS REG. TEST
- E. 270 ERROR REG. TEST
- F. 274 LOOK AHEAD REG. TEST
- G. 300 RSCS2 REG. TEST
- H. 304 ATTENTION SUMMARY REG. TEST
- I. 310 MAINTENANCE REG. TEST
- J. 314 RSCS1 REG TEST

5. OPERATIONAL SWITCH SETTINGS

SWITCH SETTINGS ARE:

- SW<15> = 1 HALT ON ERROR
- SW<14> = 1 LOOP ON TEST
- SW<13> = 1 INHIBIT TYPEOUTS
- SW<11> = 1 INHIBIT ITERATIONS OF SUBTEST
- SW<10> = 1 BELL ON ERROR
- 0 BELL ON PASS COMPLETE
- SW<09> = 1 LOOP ON ERROR
- SW<08> = 1 LOOP ON TEST IN SW<7:0>

5.2 SUBROUTINE ABSTRACTS

5.2.1 SCOPE

THIS SUBROUTINE CALL IS PLACED BETWEEN EACH SUBTEST IN THE INSTRUCTION

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SECTION. IT RECORDS THE STARTING ADDRESS OF EACH SUBTEST AS IT IS
BEING ENTERED IN LOCATION "LAD". IF A SCOPE LOOP IS REQUESTED, THE

MAINDEC-11-DERSA-A RH11-R503-R504 BASIC FUNCTION DIAGNOSTIC PAGE 6
DESCRIPTION

CURRENT SUBTEST WILL BE LOOPED UPON. SW<11> ON A 1 INHIBITS ITERATION OF SUBTESTS. THE CONTENTS OF LAD MAY BE USED TO DETERMINE THE LAST SUBTEST SUCCESSFULLY COMPLETED.

5.2.2 HLT

THIS ROUTINE PRINTS OUT AN ERROR MESSAGE (SEE 6.1). TO INHIBIT TYPEOUTS, PUT SW<13> ON A 1.

5.2.3 TRAPCATCHER

A ".+2" - "HALT" SEQUENCE IS REPEATED FROM 0 - 776 TO CATCH ANY UNEXPECTED TRAPS. THUS ANY UNEXPECTED TRAPS OR INTERRUPTS WILL HALT AT THE VECTOR + 2.

6. ERRORS

6.1 ERROR PRINTOUT

THE FORMAT IS AS FOLLOWS:

ADR CS1 = ----- CS2 = ----- ER = -----
GOOD = ----- BAD = -----

WHERE:

CS1, CS2, ER ETC. = RS11 DISK REGISTERS.
GOOD = EXPECTED DATA.
BAD = DATA RECEIVED.

TO FIND THE FAILING TEST, LOOK AT THE LISTING ABOVE THE ADDRESS TYPED.

6.2 ERROR RECOVERY

RESTART AT 200

7. RESTRICTIONS

NONE

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MAINDEC-11-DERSA-A RH11-RS03-RS04 BASIC FUNCTION DIAGNOSTIC PAGE 7
DESCRIPTION

8. MISCELLANEOUS

8.1 EXECUTION TIME

A BELL WILL RING WITHIN 1 MINUTE WITH ALL SWITCHES DOWN.

8.2 STACK POINTER

STACK IS INITIALLY SET TO 500

9. WRITE LOCK TEST

THE WRITE LOCK TEST REQUIRES OPERATOR INTERVENTION. THE STARTING ADDRESS FOR THIS TEST IS 220. THE PROGRAM WILL TELL THE OPERATOR WHICH SWITCHES HAVE TO BE SET.

10. TEST DESCRIPTION

1. TEST RSCS2

CLEAR ALL READ/WRITE BITS AND CHECK. SET ALL R/W BITS AND CHECK. NOW CLEAR AND RECHECK.

2. TEST FOR ONLINE DRIVES

SET ERROR BITS IN RSER. THIS CAUSES ATTENTION SUMMARY BITS TO SET IN RSAS. DO FOR ALL DRIVES. RSAS HAS NOT YET BEEN TESTED. SO IN THE CASE OF NO BITS IN RSAS SETTING, DRIVE 0 IS TESTED.

3. RESET TEST FOR REGISTERS

SET ALL R/W BITS IN RSCS1, RSCS2, RSBA, RSDA, RSER, RSWC, RSDB, AND RSMR. DO A RESET AND TEST ALL R/W BITS TO BE CLEARED.

4. SET AND CLEAR ALL REGISTERS

SET ALL R/W BITS IN RSCS1, RSCS2, RSBA, RSDA, RSER, RSWC, RSDB AND RSMR AND TEST. SET ALTERNATE BITS AND CHECK TO MAKE SURE BITS ARE NOT TIED TOGETHER. NOW SET ALL BITS AND CLEAR THEM TO MAKE SURE ALL CAN BE CLEARED ONCE SET.

5. RANDOM NUMBER TEST FOR RSWC AND RSDA

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MAINDEC-11-DERSA-A
DERSAA.P11

RS:1-REC3-RS04 BASIC FUNCTION DIAGNOSTIC

C04

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1440
1441

THIS TEST GENERATES RANDOM NUMBERS AND LOADS THEM INTO RSWC.

MAINDEC-11-DERSA-A RH11-RS03-RS04 BASIC FUNCTION DIAGNOSTIC PAGE 8
DESCRIPTION

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RSDA AND RSBA.

6. TEST "CLEAR BIT" IN RSC52

SET ALL R/W BITS IN RSC51, RSC52, RSBA, RSDA, R5ER, R5WC, R5DB, AND R5MR. SET CLEAR BIT IN RSC52. NOW TEST ALL R/W BITS FOR 0 IN ALL THE ABOVE REGISTERS.

7. LOAD R5DB WITH ALL ONES AND ALL ZEROS

LOAD R5DB WITH A WORD OF ZEROS AND A WORD OF ONES. WAIT FOR "OR" TO SET AND THEN CHECK OUTPUT OF SILO. IF OR DID NOT SET ERROR MESSAGE APPEARS.

8. TEST NO-OP FUNCTION

THE NO-OP FUNCTION IS TESTED WITH AND WITHOUT ERROR BITS SET. ALL THE REGISTERS ARE CHECKED AFTER BOTH CASES.

9. TEST DRIVE CLEAR FUNCTION

FIRST SET ALL R/W BITS IN RSDA, R5WC, R5ER, AND R5MR. DO A DRIVE CLEAR FUNCTION. NOW TEST ALL REGISTERS FOR CORRECT DATA.

10. EXECUTE A ONE WORD WRITE FUNCTION

SET R5WC TO -1. MOV -1 INTO OUTBUF. LOAD RSBA WITH OUTBUF. DO A WRITE TEST RDY BIT FOR 0 THEN WAIT FOR IT TO SET. TIME OUT TO ERROR IF RDY BIT DOESN'T SET AND CHECK FOR ERROR CONDITIONS. TEST RSDA FOR CORRECT ADDRESS. TEST WORD COUNT FOR 0.

11. EXECUTE A ONE WORD WRITE CHECK

SET UP RSDA, RSBA, R5WC AND OUTBUF AS IN THE WRITE FUNCTION TEST. DO A WRITE CHECK FUNCTION. TEST RDY AS DONE IN THE WRITE TEST. CHECK FOR WRITE CHECK ERROR. THEN TEST RSDA, R5WC AND RSBA FOR CORRECT DATA.

12. TEST READ FUNCTION

SETUP RSDA, RSBA, R5WC AND OUTBUF AS IN THE WRITE FUNCTION DO A READ FUNCTION. TEST RDY BIT AS DONE IN THE WRITE FUNCTION. TEST FOR ERRORS. ALSO TEST RSDA, R5WC AND RSBA FOR CORRECT DATA.

13. TEST BLOCK SEARCH FUNCTION, PIP AND DRY BITS AND ADDR. CONF. BIT

DO A BLOCK SEARCH FOR SECTOR 32, LOOP ON ADDR. CONF. BIT IN R5MR. IF IT DOESN'T SET, TIMEOUT. WHEN YOU GET THERE DO A

E04

MAINDEC-11-DERSA-A
DERSAA.P11

RS11-RSC3-RSC4 BASIC FUNCTION DIAGNOSTIC

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1498
1499

BLOCK SEARCH FOR SECTOR D. NOW WE KNOW THAT WE HAVE TIME TO
TEST FOR DRY AND PIP BITS BEFORE FINDING SECTOR C. FOR PIP

MAINDEC-11-DERSA-A RH11-RSD3-RSD4 BASIC FUNCTION DIAGNOSTIC PAGE 9
DESCRIPTION

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SHOULD SET AND DRY SHOULD CLEAR BEFORE FINDING SECTOR 0. ONCE SECTOR 0 IS FOUND PIP SHOULD CLEAR AND DRY SHOULD SET. IF DRY DOES NOT SET A TIME OUT ERROR ILL OCCUR INDICATING SECTOR 0 WAS NOT FOUND. SC IN RSCS1 SHOULD ALSO SET. RSBA AND RSWC SHOULD NOT MOVE, THIS IS ALSO TESTED.

14. ILLEGAL FUNCTION CODE TEST

IN THIS TEST RSBA, RSWC AND RSDA ARE SET UP AS IF TO DO A LEGAL FUNCTION. AN ILLEGAL FUNCTION IS THEN EXECUTED. THE PROGRAM TEST FOR ILF AND ERR BITS TO SET. RSBA, RSWC AND RSDA ARE ALSO TESTED FOR CORRECT DATA. THIS IS DONE FOR ALL THE ILLEGAL FUNCTIONS.

FOR AN AID IN TROUBLE SHOOTING THE ILLEGAL FUNCTION CODE CAN BE LOADED INTO LOCATION ILLTAB OR ILFTB2, DEPENDING ON WHICH ILLEGAL FUNCTION TEST YOU WISH TO LOOP ON. IN THE NEXT LOCATION, FOLLOWING THE ILLEGAL FUNCTION, A 0 MUST BE LOADED. NOW BY SETTING SWITCH 14 (LOOP ON TEST), YOU WILL LOOP ON THE ILLEGAL FUNCTION.

15. TEST PAR IN RSER

SET PAR IN RSER AND CHECK. ALSO TEST ERR IN RSDS TO SET BECAUSE OF THE PAR SETTING.

16. TEST DPR AND MOL IN RSDS

BOTH THESE BITS SHOULD BE SET IN RSDS IF THE DRIVE IS ON LINE AND UP TO SPEED.

17. LOOK AHEAD TEST

FIRST CHECK TO SEE IF SECTOR FRACTION BITS ARE MOVING. NOW SET RSDA TO 0 AND INCREMENT IT EVERY TIME THE ADDR.CONF BIT SETS. IF THE ADDR.CONF BIT DOES NOT SET IN A CERTAIN LENGTH OF TIME, A TIME OUT ERROR OCCURS.

18. PARITY TEST

THIS WILL TEST THE PARITY LOGIC ONLY IF THERE IS PARITY MEMORY ON THE SYSTEM IN LESS THAN 28K. IT WILL WRITE BAD PARITY IN A MEMORY LOCATION THEN TRY TO DO A WRITE TO THE DRIVE FROM THAT LOCATION. THIS SHOULD CAUSE A PARITY ERROR.

19. TEST WRITE CHECK ERROR

IN THIS TEST THE PROGRAM WRITES A -1 ON TO THE DISK. A 0 IS NOW FLOATED THROUGH THE WORD IN THE BUS ADDRESS LOCATION, AND A WRITE CHECK FUNCTION IS DONE. THE WCE BIT IN RSCS2 SHOULD SET AND SHOULD CAUSE THE TRE BIT IN RSCS1 TO SET. THESE BITS ARE THEN CLEARED. A WORD OF 0 IS NOW WRITTEN ON THE DISK AND

G04

MAINDEC-111-DERSA-A
DERSA.P11

RS11-REC3-RS04 BASIC FUNCTION DIAGNOSTIC

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1556
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H 1 IS FLOATED THROUGH THE WORD IN THE BUS ADDRESS AND THE
WRITE CHECK FUNCTION TEST IS REPEATED.

MAINDEC-11-DERSA-A RH11-RS03-RS04 BASIC FUNCTION DIAGNOSTIC PAGE 10
 DESCRIPTION

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20. TEST PROGRAM ERROR BIT IN RSCS2

HERE THE PROGRAM ATTEMPTS TO INITIATE A DATA TRANSFER OPERATION WHILE THE CONTROL IS CURRENTLY PERFORMING ONE. THIS SHOULD CAUSE PGE TO SET WHICH SHOULD CAUSE TRE TO SET. THESE BITS ARE THEN CLEARED. RSWC IS ALSO TESTED FOR IT SHOULD NOT BE 0 FOR THE CURRENT OPERATION SHOULD HAVE BEEN ABORTED DUE TO THE PGE ERROR.

21. TEST RMR IN RSER

HERE A WRITE COMMAND IS GIVEN AND DURING ITS EXECUTION THE PROGRAM TRYS TO MODIFY THE RSDA REG. THIS SHOULD CAUSE THE RMR BIT TO SET WHICH CAUSES THE ERR BIT TO SET. THESE BITS ARE THEN CLEARED.

22. TEST DCK IN RSER

HERE A WRITE COMMAND IS GIVEN THEN DURING THIS FUNCTION A DRIVE CLEAR COMMAND IS GIVEN. THIS SHOULD CAUSE THE DCK BIT TO SET WHICH SHOULD CAUSE THE ERR BIT TO SET. THESE BITS ARE THEN CLEARED.

23. TEST DISK ADDRESS REGISTER

LOAD THE LAST DISK ADDRESS (7777) INTO RSDA. DO A ONE WORD WRITE AND CHECK THAT RSDA INCREMENTED TO 10000.

24. TEST IAE ERROR

DO A ONE WORD WRITE BUT FIRST SET RSDA TO AN INVALID ADDRESS SUCH AS 10000. THIS SHOULD CAUSE A IAE ERROR WHICH WILL CAUSE ERR, ATA AND SC BITS TO SET. THESE BITS ARE THEN CLEARED BY LOADING A 1 INTO ATA IN RSAS.

25. TEST FOR NON-EXISTENT DISK ERROR

FIRST FIND A DRIVE THAT IS NOT ON THE SYSTEM OR OFF LINE. NOW TRY TO DO A ONE WORD WRITE TO THAT DRIVE. NED IN RSCS2 SHOULD SET WHICH SHOULD CAUSE TRE TO SET. THESE BITS ARE THEN CLEARED BY MOVING A 1 INTO TRE.

26. TEST DAO IN RSER AND LBT IN RSDS

SET RSDA TO ITS LAST ADDRESS. NOW WRITE ONE SECTOR PLUS ONE WORD. DAO SHOULD SET AND LBT SHOULD SET. THESE SHOULD CAUSE ERR, ATA, TRE AND SC TO SET. THESE ARE CLEARED BY DOING A CLEAR.

27. TEST BAI IN RSCS2

SET BAI IN RSCS2. DO A ONE WORD WRITE AND CHECK RSBA TO SEE

1614

IF IT INCREMENTED.

MAINDEC-11-DERSA-A RH11-RS03-RS04 BASIC FUNCTION DIAGNOSTIC PAGE 11
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28. TEST NON-EXISTENT MEMORY ERROR BIT IN RSCS2

SET BITS 0 THROUGH 5 IN RMBAE FOR AN 22 BIT ADDRESS. MOV 173000 INTO RSBA. MOV -1000 INTO RSMC AND DO A WRITE FUNCTION. THE NEM BIT SHOULD SET AND SHOULD CAUSE TRE TO SET. CLEAR THESE BITS BY LOADING A 1 INTO TRE.

29. TEST FOR ZERO'S IN A PARTIALLY FILLED SECTOR

FIRST WRITE A COMPLETE SECTOR WITH ALL ONES. THEN DO A ONE WORD WRITE. THE REMAINING 63 WORDS SHOULD BE WRITTEN AS ZERO'S. NOW DO A WRITE CHECK TO COMPARE FOR THESE ZERO'S.

30. PRIORITY INTERRUPT TEST

HERE THE PROGRAM ENABLES THE INTERRUPT AND DOES A ONE WORD WRITE FUNCTION. THE PROGRAM SHOULD NOT TRAP UNTIL THE PROCESSOR IS DROPPED TO PRIORITY 4.

31. DYNAMIC FUNCTION TEST

WHILE ONE DRIVE IS READING, THE UNIT # IN RSCS2 IS MODIFIED. IF THERE IS ANOTHER DRIVE ON THE SYSTEM, A DRIVE SEARCH IS PERFORMED ON IT. THIS IS ALL DONE WHILE THE FIRST DRIVE IS STILL READING.

%
;TITLE MAINDEC-11-DERSA-A RS11-RS03-RS04 BASIC FUNCTION DIAGNOSTIC
;COPYRIGHT 1973,1974 DIGITAL EQUIPMENT CORP., MAYNARD, MASS.
;PROGRAM BY STANLEY HARACKIEWICZ

SWITCH	USE
SW15= 100000	;HALT ON ERROR
SW14= 40000	;LOOP ON TEST
SW13= 20000	;INHIBIT ERROR TYPEOUTS
SW12= 10000	;INHIBIT ITERATIONS
SW11= 4000	;0 - BELL ON PASS COMPLETE
SW10= 2000	;1 - BELL ON ERROR
SW9= 1000	;LOOP ON ERROR
SW8= 400	;LOOP ON TEST IN SW(7:0)
0	;TRAP CATCHER FROM 0 - 776
46	;HOOKS FOR ACT 11
52	
200	
JMP @#STRT	
220	
JMP @#WRTLCK	;WRITE LOCK TEST

100000
040000
020000
010000
004000
002000
001000
000400
000000
000046
020102
000052
040000
000200
000200 000137 000230
000220
000220 000137 022264

1696
1697
1698
1699
1700
1701
1702
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000001
104000
177776
177776
177570
177570
000007
000000
000001
000002
000003
000004
000005
000006
000007
000001
000002
000004
000010
000020
000040
000100
000200
000400
001000
002000
004000
010000
020000
040000
100000
000001
000000

N= 1
HLT= EMT
PS= 177776
PSW= PS
SWR= 177570
DISPLAY=SWR
BELL= 7
R0= %0
R1= %1
R2= %2
R3= %3
R4= %4
R5= %5
SP= %6
PC= %7
BIT0= 1
BIT1= 2
BIT2= 4
BIT3= 10
BIT4= 20
BIT5= 40
BIT6= 100
BIT7= 200
BIT8= 400
BIT9= 1000
BIT10= 2000
BIT11= 4000
BIT12= 10000
BIT13= 20000
BIT14= 40000
BIT15= 100000

GOOD= %1
BAD= %0

;INITALIZE FOR NEWTST
;SET HLT TO EMT FOR ERROR TYPEOUTS
;PROCESSOR STATUS
;PROCESSOR STATUS WORD
;SWITCH REGISTER
;DISPLAY REGISTER
;BELL
;R0 - DEFINE REGISTERS
;R1
;R2
;R3
;R4
;R5
;R6 - STACK POINTER
;R7 - PROGRAM COUNTER
;BIT EQUATES

;FOR GOOD DATA
;FOR BAD DATA

1731 001000 001000
 1732
 1733 001000 000000
 1734 001002 000000
 1735 001004 000000 000000
 1736 001010 000000
 1737 001012 000000
 1738 001014 001000
 1739 001016 177564
 1740 001020 177566
 1741
 1742 001100 001100
 1743 001100 000000
 1744 001102 000000
 1745
 1746
 1747
 1748 001104 172040
 1749 001106 172050
 1750 001110 172042
 1751 001112 172044
 1752 001114 172046
 1753 001116 172052
 1754 001120 172054
 1755 001122 172056
 1756 001124 172060
 1757 001126 172062
 1758 001130 172064
 1759 001132 172066
 1760 001134 172070
 1761 001136 172072
 1762 001140 000204
 1763 001142 000206
 1764 001144 172041
 1765 001146 172051
 1766 001150 172043
 1767 001152 172045
 1768
 1769
 1770
 1771 177572
 1772 172340
 1773 172342
 1774 172344
 1775 172356
 1776 172300
 1777 172302
 1778 172304
 1779 172316
 1780 000006
 1781 000000
 1782

000000

. = 1000

ICNT: 0
 ERRORS: 0
 PCNT: 0,0
 LAD: 0
 HLTADR: 0
 FILCHR: 1000
 TPS: 177564
 TPB: 177566

; LH = ITERATION COUNT ; RH = TEST NO.
 ; ERROR COUNT
 ; 2 WORD PASS COUNT
 ; LOOP ADDRESS FOR SCOPE
 ; ADDRESS OF LAST HLT INSTRUCTION EXECUTED
 ; FILCHR=0 (CHAR) ; FILCHR+1=2 (COUNT)
 ; OUTPUT STATUS REGISTER
 ; OUTPUT BUFFER

. = 1100

SAVBAD: 0
 OBUFSV: 0

; LOC FOR ILLEGAL FUNCTION CODE
 ; LOC OF OUTBUF

;DISK I/O REGISTERS

RSCS1: 172040
 RSCS2: 172050
 RSWC: 172042
 RSBA: 172044
 RSDA: 172046
 RSDS: 172052
 RSER: 172054
 RSAS: 172056
 RSLA: 172060
 RSDB: 172062
 RSMR: 172064
 RSDT: 172066
 RSBAE: 172070
 RSCS3: 172072
 RSVEC: 204
 RSVCP5: 206
 RSCS1B: 172041
 RSCS2B: 172051
 RSWCB: 172043
 RSBAB: 172045

;DISK CONTROL + STATUS REGISTER
 ;DISK CONTROL + STATUS REGISTER
 ;WORD COUNT REGISTER
 ;BUS ADDRESS
 ;DISK ADDRESS (DESIRED ADDRESS)
 ;DRIVE STATUS
 ;ERROR REG.
 ;ATTENTION SUMMARY
 ;LOOK AHEAD
 ;DATA BUFFER REGISTER
 ;MAINTENANCE REGISTER
 ;DRIVE TYPE REGISTER

; INTERRUPT VECTOR
 ; INTERRUPT PRIO. VECTOR
 ; ODD BYTE ADD FOR CS1
 ; ODD BYTE ADD FOR CS2
 ; ODD BYTE ADD FOR CW
 ; ODD BYTE ADD FOR BA

;MEMORY MANAGEMENT REGISTER ASSIGNMENTS

SRO=177572
 KIPAR0=172340
 KIPAR1=172342
 KIPAR2=172344
 KIPAR7=172356
 KIPDR0=172300
 KIPDR1=172302
 KIPDR2=172304
 KIPDR7=172316
 RW=6
 UP=00

1783 :BIT ASSIGNMENTS FOR ERROR TYPEOUTS
 1784 :THE RS REGISTERS ARE DIVIDED INTO 3 GROUPS.
 1785 :CS1 CS2 AND ER ARE IN THE FIRST GROUP. THIS GROUP IS ALWAYS
 1786 :TYPED WITH EITHER OF THE OTHER GROUPS. AS, BA, DA, WC AND DS
 1787 :ARE IN THE SECOND GROUP. DT, DB, MR, AND LA ARE IN THE 3RD
 1788 :GROUP. YOU CAN NOT INTERMIX GROUP 2 OR 3. THEY HAVE
 1789 :TO BE TYPED SEPERATELY.
 1790 :EXAMPLE: HLT !CS1 AS BA
 1791 : HLT !CS1!DT!DB
 1792 :

1793	000001	CS1=1	:CONTROL AND STATUS 1
1794	000002	ER=2	:CONTROL AND STATUS 2
1795	000004	DA=4	:DESIRED ADD
1796	000010	WC=10	:WORD COUNT
1797	000020	BA=20	:BUS ADDRESS
1798	000040	DS=40	:DRIVE STATUS
1799	000100	AS=100	:ATTENTION SUMMARY
1800	000200	CS2=200	:CONTROL AND STATUS REG
1801	000204	LA=204	:LOOK AHEAD
1802	000210	DB=210	:DATA BUFFER
1803	000220	MR=220	:MAINTENANCE
1804	000240	DT=240	:DRIVE TYPE

;BIT ASSIGNMENTS FOR THE REGISTER BITS

1805			
1806			
1807			
1808	040000	TRE=40000	:TRANSFER ERROR CS1
1809	100000	SC=100000	:SPECIAL CONDITIONS CS1
1810	000100	IR=100	:INPUT READY CS2
1811	000200	OR=200	:OUTPUT READY CS2
1812	002000	PGE=2000	:PROGRAM ERROR-CS2
1813	010000	NED=10000	:NON-EXISTENT DRIVE CS2
1814	040000	WCE=40000	:WRITE CHECK ERROR-CS2
1815	100000	DLT=100000	:DATA LATE ERROR CS2
1816	000200	DRY=200	:DRIVE READY DS
1817	020000	PIP=20000	:POSITIONING IN PROGRESS DS
1818	002000	LBT=2000	:LAST BLOCK TRANSFER-DS
1819	040000	ERR=40000	:ERROR DS
1820	100000	ATA=100000	:ATTENTION ACTIVE-DS
1821	001000	DAO=1000	:DISK OVERFLOW ERROR-ER
1822	100000	DCK=100000	:DATA CHECK ERROR-ER
1823	000010	BAI=10	:BUS ADDR INCREMENT INHIBIT
1824	000100	IE=100	:INTERRUPT INABLE CS1

```

1825
1826
1827 001154 146723
1828 001156 000000
1829 001160 000000
1830 001162 00030C
1831 001164 000000
1832 001166 000000
1833 001170 000000
1834 001172 000000
1835 000004
1836 172100
1837 001174 000000
1838 001176 000000

```

:WORKING LOCATIONS

```

RANNU: 146723      :RANDOM NUMBER PRIME
UNNUM: 0           :UNIT CURRENTLY BEING TESTED
UNITSV: 0          :SET BIT=UNIT ON BUS
UNCMP: 0           :FOR COMPARING FOR # OF DEVICE
ONCEE: 0           :DID WE TEST ANY DRIVES
RSC4DT: 0          :CLR IF RS03 SET IF RS04
TIMSV: 0           :SAVE LOC FOR TIME
ROB!: 0            :PORT SWITCH
MWP=4             :WRITE WRONG PARITY
MPRO=172100      :PARITY REG
SPORT: 0          :BUFFER ADDR FOR -B- PORT
SAVEE: 0          :WORK LOC

```

```

:DISCRIPTION OF ONCEE BITS
:BIT0 MEANS FOUND DRIVE
:BIT11 DO TKSEL TEST
:BIT12 TYPE COULD NOT FIND NED ONLY ONCE
:BIT13 TYPE NO MEM ON B PORT ONLY ONCE
:BIT14 0- DO WCE WITH 0 -1 DO WCE WITH 1
:BIT15 MEANS ERROR FOUND

```

:RH11 WORK REGISTERS
(CAN BE CHANGED IN ANY ROUTINE)

```

1849
1850 001200 000000
1851 001202 000000
1852 001204 000000

```

```

WORK: 0
WORK1: 0
WORK2: 0

```

```

1853
1854 001206 012706 000500
1855 001212 012737 021040 000024
1856 001220 012737 000340 000026
1857 001226 012737 020470 000030
1858 001234 012737 000340 000032
1859 001242 012737 021440 000034
1860 001250 012737 000340 000036
1861 001256 005067 177516
1862 001262 005067 177522
1863 001266 042767 143777 177570
1864 001274 012767 024550 177600
1865 001302 032767 004000 177654
1866 001310 001402
1867 001312 030137 021736

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BEGIN:  MOV    #500,SP      :SET STACK TO *** 500 ***
        MOV    #.POWER,2#24 :SET UP PF VECTOR
        MOV    #340,2#26    :LOCK OUT THE WORLD
        MOV    #.HLT,2#30    :SET ENT VECTOR
        MOV    #340,2#32    :LOCK UP
        MOV    #.TRAP,2#34   :SET TRAP VECTOR
        MOV    #340,2#36    :LOCK UP
        CLR    ICNT         :INIT ICNT
        CLR    LAD          :INIT LAD
        BIC    #143777,ONCEE :CLEAR ONCEE
        MOV    #OUTBUF,OBUSV :SAVE LOC OF OUTBUFFER
        BIT    #BIT11,ONCEE  :DO TKSEL TEST?
        BEQ    +6           :NO
        JMP    2#TKSEL      :YES

```



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1915 001606 006367 177350 TRYNX:  A&L  UNCMP      ;CHECK NEXT BIT FOR DRIVE
1916 001612 103403          BCS  CHCKDV   ;DID WE TEST ANY REG?
1917 001614 005267 177336          INC  UNNUM    ;INC UNIT #
1918 001620 000705          BR   STTEST   ;CHECK FOR NEXT DRIVE
1919
1920 001622 032767 000001 177334 CHCKDV: BIT   #BIT0,ONCE  ;DID WE TEST ANY DRIVES?
1921 001630 001057          BNE  DONEE   ;YES WE DID TEST A DRIVE
1922 001632 012767 100000 177322          MOV  #100000,UNCMP ;NO DRIVES TESTED, COULD NOT SET
1923 001640 005067 177312          CLR  UNNUM    ;ANY AS BITS, THUS DEFAULTS TO
1924 001644 032767 020000 175716          BIT  #BIT13,SWR   ;INHIBIT TYPE OUT?
1925 001652 001045          BNE  4$      ;YES
1926 001654 016746 177276          MOV  UNNUM,-(6)  ;PUT UNNUM ON STACK
1927 001660 104406          TYPES      ;TYPE STACK IN OCTAL - SUPRESS
1928 001662 104402 000040          TYPE  ,40      ;TYPE SPACE
1929 001666 104402 001672          TYPE  ,.+2     ;ASCIZ <15><12>"COULD NOT FIND DRIVE WILL TEST DRIVE 0
1930 001764 000000          HALT
1931 001766 000402          4$: BR   NOWGO ;TEST DRIVE 0
1932 001770 000167 016042          DONEE: JMP  DONE ;GET OUT
1933
1934          ;THIS TEST IS DESIGNED TO TEST THE ABILITY OF RESET
1935          ;TO CLEAR ALL THE RH AND RS REGISTERS
1936
1937 001774 012767 024550 177100 NOWGO:  MOV  #OUTBUF,OBUSV ;SAVE LOC OF OUTBUFFER
1938 002002 052767 000001 177154          BIS  #BIT0,ONCE  ;SET FOUND DRIVE FLAG
1939 002010 016767 016452 177152          MOV  TIMES,TIMSV ;SAVE TIME
1940 002016 012767 000001 016442          MOV  #1,TIMES   ;ONLY TEST ONCE
1941          ;*****
1942          ;TEST 2      RESET TEST FOR REGISTERS
1943          ;*****
1944 002024 104400          TST2:  SCOPE
1945 002026 012737 000340 177776          MOV  #340,#PS   ;LOCK OUT INTERRUPTS
1946 002034 016777 177116 177044          MOV  UNNUM,RS2  ;GET UNIT #
1947 002042 012777 177776 177034          MOV  #177776,RS1 ;SET ALL
1948 002050 012777 177777 177034          MOV  #177777,RSBA ;POSSIBLE R/W
1949 002056 012777 177777 177030          MOV  #177777,RSDA ;BITS IN THESE REGISTERS
1950 002064 012777 177777 177026          MOV  #177777,RSER
1951 002072 012777 177777 177030          MOV  #177777,RSMR
1952 002100 012777 177777 177002          MOV  #177777,RSWC
1953 002106 012777 177737 176772          MOV  #177737,RS2
1954 002114 000005          RESET
1955          ;CLEAR ALL BITS IN ALL REG.
1956
1957          ;TEST RS2 FOR CLEARED BITS
1958 002116 022777 000100 176762          CMP  #100,RS2  ;DID THESE BITS GET CLEARED?
1959 002124 001401          BEQ  +4        ;YES
1960 002126 104200          HLT  !CS2     ;(417) SHOULD BE CLEARED IN CS2
1961 002130 016777 177022 176750          MOV  UNNUM,RS2  ;PUT # OF UNIT IN TEST IN CS2
1962 002136 022777 010600 176752          CMP  #10600,RS ;IS DPR AND MOL SET?
1963 002144 001401          BEQ  +4        ;YES
1964 002146 104040          HLT  !DS     ;NO WHY NOT?
1965
1966 002150 022777 004200 176726          ;TEST CONTROL AND STATUS REG 1
1967 002156 001401          CMP  #4200,RS1 ;DID THE READY BIT SET?
1968 002160 104001          BEQ  +4        ;YES
          HLT  !CS1 ;READY SHOULD BE SET

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1969                                     ;TEST BUS ADDRESS REGISTER
1970
1971 002162 005777 176724               TST   @RSBA           ;IS BA REG. CLEARED
1972 002166 001401                       BEQ   :+4             ;YES
1973 002170 104020                       HLT   !BA             ;SHOULD BE 0
1974
1975                                     ;TEST DISK ADDRESS REGISTER
1976
1977 002172 005777 176716               TST   @RSDA           ;IS DA CLEARED
1978 002176 001401                       BEQ   :+4             ;YES
1979 002200 104004                       HLT   !DA             ;SHOULD BE 0
1980
1981                                     ;TEST ERROR REG RSER
1982
1983 002202 005777 176712               TST   @RSER           ;DID RSER CLEAR?
1984 002206 001401                       BEQ   :+4             ;YES
1985 002210 104002                       HLT   !ER             ;BITS(157015) SHOULD BE CLEARED
1986
1987                                     ;TEST RS MAINTENANCE REGISTER
1988
1989 002212 032777 000077 176710       BIT   #77,@RSMR       ;DID THESE BITS GET CLEARED
1990 002220 001401                       BEQ   :+4             ;YES
1991 002222 104220                       HLT   !MR             ;BITS(77) SHOULD BE 0
1992
1993                                     ;TEST WC REG IT SHOULD NOT CHANGE
1994
1995 002224 022777 177777 176656       CMP   #177777,@RSWC   ;DID IT CHANGE?
1996 002232 001401                       BEQ   :+4             ;NO
1997 002234 104010                       HLT   !WC             ;RESET SHOULD NOT MODIFY RSWC
1998
1999                                     ;TEST RSAS
2000
2001 002236 005777 176660               TST   @RSAS           ;IS REG CLEAR
2002 002242 001401                       BEQ   :+4             ;YES
2003 002244 104100                       HLT   !AS             ;NO

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2004 ;*****
2005 ;TEST 3 TEST CLEAR BIT IN CS2 ON ALL THE R/W BITS
2006 ;*****
2007 002246 104400 TST3: SCOPE
2008
2009 002250 012737 000340 177776 TTAGG: MOV #340, @RPS ;LOCK OUT INTERRUPTS
2010 002256 016777 176674 176622 MOV UNNUM, @RSCS2 ;GET UNIT #
2011 002264 012777 043576 176612 MOV #43576, @RSCS1 ;SET ALL
2012 002272 012777 177777 176612 MOV #177777, @RSBA ;POSSIBLE
2013 002300 012777 177777 176606 MOV #177777, @RSDA ;REGISTERS
2014 002306 012777 177777 176604 MOV #177777, @RSER
2015 002314 012777 177777 176604 MOV #177777, @RSD8
2016 002322 012777 177777 176560 MOV #177777, @RSWC
2017 002330 012777 020417 176550 MOV #20417, @RSCS2
2018 002336 012777 000071 176564 MOV #71, @RSMR
2019 002344 012777 000040 176534 MOV #40, @RSCS2 ;CLEAR ALL BITS
2020 002352 022777 000100 176526 CMP #100, @RSCS2 ;DID THE RIGHT BITS CLEAR?
2021 002360 001401 BEQ +4 ;YES
2022 002362 104200 HLT !CS2 ;(417) SHOULD BE CLEARED IN CS2
2023 002364 016777 176566 176514 MOV UNNUM, @RSCS2 ;GET DRIVE NUMBER
2024 002372 032777 173577 176504 BIT #173577, @RSCS1 ;DID ALL BITS GET CLEARED
2025 002400 001401 BEQ +4 ;YES
2026 002402 104001 HLT !CS1 ;NO, ALL BITS SHOULD BE 0
2027 ;TEST BUS ADDRESS REGISTER
2028
2029 002404 005777 176502 TST @RSBA ;IS BA REG. CLEARED
2030 002410 001401 BEQ +4 ;YES
2031 002412 104020 HLT !BA ;SHOULD BE 0
2032
2033 ;TEST DISK ADDRESS REGISTER
2034
2035 002414 005777 176474 TST @RSDA ;IS DA CLEARED
2036 002420 001401 BEQ +4 ;YES
2037 002422 104020 HLT !BA ;SHOULD BE 0
2038
2039 ;TEST ERROR REG RSER
2040
2041 002424 005777 176470 TST @RSER ;DID THESE BITS GET CLEARED
2042 002430 001401 BEQ +4 ;YES
2043 002432 104002 HLT !ER ;BITS(157015) SHOULD BE CLEARED
2044
2045 ;TEST RS MAINTENANCE REGISTER
2046 002434 032777 000077 176466 BIT #77, @RSMR ;DID THESE BITS GET CLEARED
2047 002442 001401 BEQ +4 ;YES
2048 002444 104220 HLT !MR ;BITS(77) SHOULD BE 0
2049
2050 ;TEST WC REG. IT SHOULD NOT CHANGE
2051 002446 022777 177777 176434 CMP #177777, @RSWC ;DID WC CHANGE
2052 002454 001401 BEQ +4 ;NO
2053 002456 104010 HLT !WC ;WHY DID IT CHANGE?

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2054 ;*****
2055 ;TEST 4 SET AND CLEAR ALL REGISTERS
2056 ;*****
2057 002460 104400 TST4: SCOPE
2058 ;CAN WE SET THE FUNCTION BITS IN THE RSCS1 REG.
2059 ;BITS 7,6,5,4,3,2&1
2060
2061 002462 104414 CLRDK ;CLEAR ALL RS REG
2062 002464 016767 176500 015774 MOV TIMSV,TIMES ;GET TIME
2063 002472 012777 003576 176404 MOV #3576,RSCS1 ;SET DISK FUNCTION BITS
2064 002500 022777 005776 176376 CMP #5776,RSCS1 ;ARE THESE BITS SET?
2065 002506 001401 BEQ .+4 ;NO
2066 002510 104001 HLT !CS1 ;SHOULD = 3776
2067 002512 012777 002524 176364 MOV #2524,RSCS1 ;SET THESE BITS
2068 002520 022777 004724 176356 CMP #4724,RSCS1 ;DID THEY SET
2069 002526 001401 BEQ .+4 ;YES
2070 002530 104001 HLT !CS1 ;SHOULD BE 2725
2071 002532 012777 001052 176344 MOV #1052,RSCS1 ;SET THESE BITS
2072 002540 022777 005252 176336 CMP #5252,RSCS1 ;ARE THEY =?
2073 002546 001401 BEQ .+4 ;YES
2074 002550 104001 HLT !CS1 ;SHOULD = 1252
2075 002552 104400 TST5: SCOPE
2076 ;CLEAR THE FUNCTION BITS
2077
2078 002554 012777 043576 176322 MOV #43576,RSCS1 ;SET DISK FUNCTION BITS
2079 002562 005077 176316 CLR RSCS1
2080 002566 022777 004200 176310 CMP #4200,RSCS1 ;IS THE READY BIT SET
2081 002574 001401 BEQ .+4 ;YES
2082 002576 104001 HLT !CS1 ;RSCS1 SHOULD = 4200
2083
2084 ;*****
2085 ;TEST 6 TEST RSCS2
2086 ;*****
2087 002600 104400 TST6: SCOPE
2088
2089 002602 000005 RESET ;CLEAR WORLD
2090 002604 022777 000100 176274 CMP #100,RSCS2 ;DID THEY CLEAR?
2091 002612 001401 BEQ .+4 ;YES
2092 002614 104200 HLT !CS2 ;NO
2093 002616 012777 021037 176262 MOV #21037,RSCS2 ;SET BITS 21017
2094 002624 022777 000137 176254 CMP #137,RSCS2 ;DID THESE BITS GET SET
2095 002632 001405 BEQ 1$ ;YES
2096 002634 017700 MOV RSCS2,BAD
2097 002640 012701 000137 MOV #137,GOOD ;WHAT CS2 SHOULD =
2098 002644 104000 HLT ;CS2 = BAD GOOD = CORRECT ANS

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2099	002646	012777	020025	176232	IS:	MOV	#20025, @RSCS2	:SET THESE BITS
2100	002654	022777	000125	176224		CMP	#125, @RSCS2	:DID THESE BITS GET SET
2101	002662	001401				BEQ	:+4	:YES
2102	002664	104200				HLT	:CS2	:NO CS2 SHOULD = 20125
2103	002666	012777	0C0012	176212		MOV	#12, @RSCS2	:LOAD THESE BITS
2104	002674	022777	000112	176204		CMP	#112, @RSCS2	:DID THESE BITS GET SET IN CS2
2105	002702	001401				BEQ	:+4	:YES
2106	002704	104200				HLT	:CS2	:BAD = CS2 GOOD = CORRECT ANS
2107	002706	012777	177777	176172		MOV	#-1, @RSCS2	:SET BITS
2108	002714	005077	176166			CLR	@RSCS2	:CLEAR THEM
2109	002720	022777	000100	176160		CMP	#100, @RSCS2	:DID CLEAR WORK
2110	002726	001401				BEQ	:+4	:YES
2111	002730	104200				HLT	:CS2	:R/W BITS DID NOT CLEAR
2112	002732	016777	176220	176146		MOV	UNNUM, @RSCS2	:GET UNIT #
2113	002740	104400			TST7:	SCOPE		
2114					;CAN WE	SET ALL THE RSBA BITS		
2115								
2116	002742	012777	177777	176142		MOV	#177777, @RSBA	:SET THE BITS
2117	002750	022777	177776	176134		CMP	#177776, @RSBA	:DID THEY SET
2118	002756	001401				BEQ	:+4	:YES
2119	002760	104020				HLT	:BA	:BITS 17776 SHOULD BE SET
2120	002762	012777	125252	176122		MOV	#125252, @RSBA	:SET THESE BITS
2121	002770	022777	125252	176114		CMP	#125252, @RSBA	:ARE THEY =
2122	002776	001401				BEQ	:+4	:YES
2123	003000	104020				HLT	:BA	:SHOULD BE 125252
2124	003002	012777	052524	176102		MOV	#52524, @RSBA	:SET THESE BITS
2125	003010	022777	052524	176074		CMP	#52524, @RSBA	:ARE THEY =
2126	003016	001401				BEQ	:+4	:YES
2127	003020	104020				HLT	:BA	:SHOULD BE 52524
2128					TST10:	SCOPE		
2129	003022	104400			;FLOAT A	1 THROUGH RSBA		
2130								
2131								
2132	003024	012701	000002		FLOTBA:	MOV	#2, GOOD	:GET A 2
2133	003030	000241				CLC		:CLEAR CARRY
2134	003032	010177	176054		IS:	MOV	GOOD, @RSBA	:FLOAT NUMBER
2135	003036	017700	176050			MOV	@RSBA, BAD	:GET BA
2136	003042	020100				CMP	GOOD, BAD	:COMPARE BA
2137	003044	001401				BEQ	:+4	:BA CORRECT
2138	003046	104000				HLT		:BAD=BA GOOD=CORRECT AN.
2139	003050	006101				ROL	GOOD	:ROTATE NUMBER
2140	003052	103367				BCC	IS	:LOOP TILL DONE

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2141 003054 104400          TST11: SCOPE
2142
2143          :CLEAR THE RSBA REGISTER
2144
2145 003056 012777 177777 176026      MOV    #177777, @RSBA    ;SET RSBA EQUAL TO ALL ONES
2146 003064 005077 176022              CLR    @RSBA
2147 003070 005777 176016              TST    @RSBA            ;TEST FOR BIT0 SET IN RSBA (READ ONLY BIT)
2148 003074 001401              BEQ    .+4              ;YES
2149 003076 104020              HLT    !BA              ;NO
2150 003100 104400          TST12: SCOPE
2151
2152          ;CAN WE SET ALL BITS IN RSWC REGISTER
2153
2154 003102 012777 177777 176000      MOV    #177777, @RSWC    ;SET WC BITS
2155 003110 022777 177777 175772      CMP    #177777, @RSWC    ;ARE ALL BITS SET
2156 003116 001401              BEQ    .+4              ;YES
2157 003120 104010              HLT    !WC              ;NO
2158 003122 012777 125252 175760      MOV    #125252, @RSWC    ;SET THESE BITS
2159 003130 022777 125252 175752      CMP    #125252, @RSWC    ;ARE THEY =
2160 003136 001401              BEQ    .+4              ;YES
2161 003140 104010              HLT    !WC              ;SHOULD BE 125252
2162 003142 012777 052525 175740      MOV    #52525, @RSWC     ;SET THESE BITS
2163 003150 022777 052525 175732      CMP    #52525, @RSWC     ;ARE THEY =
2164 003156 001401              BEQ    .+4              ;YES
2165 003160 104010              HLT    !WC              ;SHOULD BE 152525
2166 003162 104400          TST13: SCOPE
2167
2168          ;FLOAT A 1 THROUGH RSWC
2169
2170 003164 012701 000001      FLOTWC: MOV    #1, GOOD    ;GET A 1
2171 003170 000241              CLC                    ;CLEAR CARRY
2172 003172 010177 175712      1$:    MOV    GOOD, @RSWC    ;FLOAT NUMBER
2173 003176 017700 175706      MOV    @RSWC, BAD        ;GET WC
2174 003202 020100              CMP    GOOD, BAD        ;COMPARE WC
2175 003204 001401              BEQ    .+4              ;WC CORRECT
2176 003206 104000              HLT                    ;BAD=WC GOOD=CORRECT ANS
2177 003210 006101              ROL                    ;ROTATE NUMBER
2178 003212 103367              BCC    1$              ;LOOP TILL DONE

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2179          ;CLEAR THE WORD COUNT REGISTER
2180 003214 104400 TST14: SCOPE
2181
2182 003216 012777 177777 175664      MOV    #177777, @RSWC ;SET RSWC REGISTER EQUAL TO ALL ONES
2183 003224 005077 175660              CLR    @RSWC
2184 003230 005777 175654              TST    @RSWC ;DID ALL BITS GET CLEARED
2185 003234 001401              BEQ    +4 ;YES
2186 003236 104010              HLT    !WC ;NO
2187 003240 104400 TST15: SCOPE
2188
2189          ;CAN WE SET ALL THE BITS IN THE RSDA REGISTER.
2190
2191 003242 012777 177777 175644      MOV    #177777, @RSDA ;SET ALL BITS
2192 003250 022777 177777 175636      CMP    #177777, @RSDA ;ARE THE BITS SET
2193 003256 001401              BEQ    +4 ;YES
2194 003260 104004              HLT    !DA ;NO
2195 003262 012777 125252 175624      MOV    #125252, @RSDA ;SET THESE BITS
2196 003270 022777 125252 175616      CMP    #125252, @RSDA ;ARE THEY =
2197 003276 001401              BEQ    +4 ;YES
2198 003300 104004              HLT    !DA ;SHOULD BE 125252
2199 003302 012777 052525 175604      MOV    #52525, @RSDA ;SET THESE BITS
2200 003310 022777 052525 175576      CMP    #52525, @RSDA ;ARE THEY =
2201 003316 001401              BEQ    +4 ;YES
2202 003320 104004              HLT    !DA ;SHOULD BE 52525
2203 003322 104400 TST16: SCOPE
2204
2205          ;FLOAT A 1 THROUGH RSDA
2206
2207 003324 012701 000001 FLOTDA: MOV    #1, GOOD ;GET A 1
2208 003330 000241              CLC    ;CLEAR CARRY
2209 003332 010177 175556 1$: MOV    GOOD, @RSDA ;FLOAT NUMBER
2210 003336 017700 175552              MOV    @RSDA, BAD ;GET DA
2211 003342 020100              CMP    GOOD, BAD ;COMPARE DA
2212 003344 001401              BEQ    +4 ;DA CORRECT
2213 003346 104000              HLT    ;BAD=DA GOOD=CORRECT ANS
2214 003350 006101              ROL    GOOD ;ROTATE NUMBER
2215 003352 103367              BCC    1$ ;LOOP TILL DONE

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2216                                     ;CAN WE CLEAR THE RSDA REG.
2217 003354 104400 TST17: SCOPE
2218                                     ;
2219 003356 012777 177777 175530 MOV #177777, @RSDA ;SET RSDA TO ALL ONES
2220 003364 005077 175524 CLR @RSDA ;
2221 003370 005777 175520 TST @RSDA ;TEST FOR ZERO RSDA
2222 003374 001401 BEQ .+4 ;YES
2223 003376 104004 HLT !DA ;ANS SHOULD BE 0
2224 003400 104400 TST20: SCOPE
2225                                     ;
2226                                     ;SET AND CLEAR THE RSER REG.
2227                                     ;
2228 003402 012777 177777 175510 MOV #177777, @RSER ;SET THESE BITS
2229 003410 022777 177017 175502 CMP #177017, @RSER ;DID THEY SET
2230 003416 001401 BEQ .+4 ;YES
2231 003420 104002 HLT !ER ;RSER SHOULD = 157017
2232 003422 112777 000001 175470 MOVB #1, @RSER ;A MOVB INST
2233 003430 022777 000001 175462 CMP #1, @RSER ;SHOULD MODIFY COMPLETE WD
2234 003436 001401 BEQ .+4 ;OK
2235 003440 104002 HLT !ER
2236                                     ;
2237 003442 104400 TST21: SCOPE
2238                                     ;
2239 003444 012777 052005 175446 MOV #52005, @RSER ;SET THESE BITS
2240 003452 022777 052005 175440 CMP #52005, @RSER ;DID THEY SET
2241 003460 001401 BEQ .+4 ;YES
2242 003462 104002 HLT !ER ;ER SHOULD = 52005
2243 003464 104400 TST22: SCOPE
2244                                     ;
2245 003466 012777 125012 175424 MOV #125012, @RSER ;SET THESE BITS
2246 003474 022777 125012 175416 CMP #125012, @RSER ;DID THEY SET
2247 003502 001401 BEQ .+4 ;YES
2248 003504 104002 HLT !ER ;ER SHOULD = 105012

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2249	003506	104400			TST23: SCOPE		
2250							
2251	003510	012777	177017	175402	MOV	#177017, @RSER	; SET THESE BITS
2252	003516	005077	175376		CLR	@RSER	; CLEAR THEM
2253	003522	005777	175372		TST	@RSER	; DID THEY CLEAR
2254	003526	001401			BEQ	:+4	; YES
2255	003530	104002			HLT	:ER	; SHOULD = 0
2256	003532	104400			TST24: SCOPE		
2257							
2258					; SET AND CLEAR RSMR		
2259							
2260	003534	012777	000070	175366	MOV	#70, @RSMR	; SET THESE BITS
2261	003542	017767	175362	175430	MOV	@RSMR, WORK	; PUT INTO WORKABLE REG
2262	003550	042767	177700	175422	BIC	#177700, WORK	; CLEAR JUNK
2263	003556	022767	000070	175414	CMP	#70, WORK	; DID THEY SET
2264	003564	001401			BEQ	:+4	; YES
2265	003566	104220			HLT	:MR	; SHOULD = 70
2266	003570	104400			TST25: SCOPE		
2267							
2268	003572	012777	000070	175330	MOV	#70, @RSMR	; SET BITS
2269	003600	005077	175324		CLR	@RSMR	; CLEAR THEM
2270	003604	032777	000077	175316	BIT	#77, @RSMR	; DID THEY CLEAR
2271	003612	001401			BEQ	:+4	; YES
2272	003614	104220			HLT	:MR	; BITS (77) SHOULD = 0
2273	003616	104400			TST26: SCOPE		
2274							
2275	003620	012777	000050	175302	MOV	#50, @RSMR	; SET BITS
2276	003626	017767	175276	175344	MOV	@RSMR, WORK	; PUT IN WORKABLE REG
2277	003634	042767	177700	175336	BIC	#177700, WORK	; CLEAR JUNK
2278	003642	022767	000050	175330	CMP	#50, WORK	; DID THESE BITS SET
2279	003630	001401			BEQ	:+4	; YES
2280	003652	104220			HLT	:MR	; BITS (50) SHOULD BE SET
2281	003654	104400			TST27: SCOPE		
2282							
2283	003656	012777	000020	175244	MOV	#20, @RSMR	; SET BITS
2284	003664	017767	175240	175306	MOV	@RSMR, WORK	; PUT INTO WORKABLE REG
2285	003672	042767	177700	175300	BIC	#177700, WORK	; CLEAR JUNK
2286	003700	022767	000020	175272	CMP	#20, WORK	; DID THEY SET
2287	003706	001401			BEQ	:+4	; YES
2288	003710	104220			HLT	:MR	; MR SHOULD AT LEAST HAVE A (21)

```

2289 ;*****
2290 ;TEST 30 LOAD RANDOM NUMBERS INTO RSWC, RSDA AND RSBA
2291 ;*****
2292 TST30: SCOPE
2293
2294 003712 104400
2294 003714 012767 001000 175256 RANTS: MOV #1000,WORK ;MAKE TABLE 1000 WDS LONG
2295 003722 012701 024550 MOV #OUTBUF,R1 ;GET STARTING LOC OF TABLE
2296 003726 004567 020360 JSR R5,RANDOM ;GENERATE #
2297 003732 012704 024550 MOV #OUTBUF,R4 ;SETUP FOR COMPARE
2298 003736 012767 003744 175044 #LOP1,LAD ;SETUP LOOP ADDR
2299 003744 012703 001000 LOP1: MOV #1000,R3 ;LOAD TEST COUNTER
2300 003750 005303 4$: DEC R3 ;DONE WITH COMPARE?
2301 003752 001413 BEQ 1$ ;YES
2302 003754 016705 175130 MOV RSWC,R5 ;GET WC ADDRESS
2303 003760 011415 MOV (R4),(R5) ;LOAD WC
2304 003762 021524 CMP (R5),(R4)+ ;IS IT CORRECT?
2305 003764 001771 BEQ 4$ ;YES
2306 003766 017700 175116 MOV #RSWC,BAD ;GET BAD WC
2307 003772 014401 MOV -(R4),GOOD ;GET GOOD ANS
2308 003774 104000 HLT ;TYPE THEM OUT
2309 003776 005724 TST (R4)+ ;UPDATE RANDOM NUMBER
2310 004000 000763 BR 4$ ;CONT
2311 004002 012704 024550 1$: MOV #OUTBUF,R4 ;GET STARTING LOC OF TABLE
2312 004006 012767 004014 174774 #LOP2,LAD ;SETUP LOOP ADDR
2313 004014 012703 001000 LOP2: MOV #1000,R3 ;SETUP TEST COUNTER
2314 004020 005303 3$: DEC R3 ;DONE YET?
2315 004022 001413 BEQ 1$ ;YES
2316 004024 016705 175064 MOV RSDA,R5 ;LOAD DA ADDRESS INTO R5
2317 004030 011415 MOV (R4),(R5) ;LOAD DA
2318 004032 021524 CMP (R5),(R4)+ ;IS IT CORRECT?
2319 004034 001771 BEQ 3$ ;YES
2320 004036 017700 175052 MOV #RSDA,BAD ;GET BAD DATA
2321 004042 014401 MOV -(R4),GOOD ;GET GOOD DATA
2322 004044 104000 HLT ;TYPE IT OUT
2323 004046 005724 TST (R4)+ ;UPDATE RANDOM NUMBER
2324 004050 000763 BR 3$ ;CONTINUE
2325 004052 012704 024550 1$: MOV #OUTBUF,R4 ;GET STARTING LOC OF TABLE
2326 004056 012767 004064 174724 #LOP3,LAD ;SETUP LOOP ADDR
2327 004064 012703 001000 LOP3: MOV #1000,R3 ;SETUP TEST COUNTER
2328 004070 005303 3$: DEC R3 ;DONE YET?
2329 004072 001416 BEQ 2$ ;YES
2330 004074 016705 175012 MOV RSBA,R5 ;LOAD ADDRESS OF BA INTO R5
2331 004100 011415 MOV (R4),(R5) ;LOAD BA
2332 004102 042714 000001 BIC #BIT0,(R4) ;CLEAR BIT 0
2333 004106 021514 CMP (R5),(R4) ;IS IT CORRECT?
2334 004110 001767 BEQ 3$ ;YES
2335 004112 017700 174774 MOV #RSBA,BAD ;GET BAD DATA
2336 004116 011401 MOV (R4),GOOD ;GET GOOD DATA
2337 004120 104000 HLT ;TYPE IT OUT
2338 004122 000400 BR 1$ ;GET OUT
2339 004124 005724 1$: TST (R4)+ ;GET NEW NUMBER
2340 004126 000760 BR 3$ ;CONTINUE
2341 004130 000240 2$: NOP

```



```

2342 ;*****
2343 ;TEST 31 TEST ODD BYTE INSTRUCTIONS ON CS1, CS2, WC AND BA
2344 ;*****
2345 004132 104400 TST31: SCOPE
2346
2347 004134 104414 BITST: CLDK ;CLEAR ALL RS REG
2348 004136 012777 003566 174740 MOV #3566,ARSCS1 ;LOAD CS1
2349 004144 112777 000005 174772 MOVB #5,ARSCS1B ;LOAD BIT
2350 004152 022777 004766 174724 CMP #4766,ARSCS1 ;DID IT LOAD?
2351 004160 001401 BEQ .+4 ;YES
2352 004162 104001 HLT !CS1
2353 004164 112777 000032 174712 MOVB #32,ARSCS1
2354 004172 022777 004632 174704 CMP #4632,ARSCS1
2355 004200 001401 BEQ .+4
2356 004202 104001 HLT !CS1 ;CS1 SHOULD = 4632
2357
2358 004204 104400 TST32: SCOPE
2359
2360 004206 016777 174744 174672 BITCS2: MOV UNNUM,ARSCS2 ;LOAD UNIT NUMBER
2361 004214 052777 177400 174664 BIS #177400,ARSCS2 ;LOAD ALL BITS
2362 004222 105077 174720 CLRB ARSCS2B ;CLR UPPER BYTE
2363 004226 016701 174724 MOV UNNUM,GOOD ;GET UNIT NO.
2364 004232 052701 000100 BIS #100,GOOD ;SET OR BIT
2365 004236 017700 174644 MOV ARSCS2,BAD ;GET CS2
2366 004242 020001 CMP BAD,GOOD ;IS CS2 CORRECT?
2367 004244 001401 BEQ .+4 ;YES
2368 004246 104000 HLT ;LOAD BYTE DID NOT WORK
2369
2370 004250 104400 TST33: SCOPE
2371
2372 004252 012777 025252 174630 BITWC: MOV #25252,ARSWC ;LOAD WC
2373 004260 112777 000377 174662 MOVB #377,ARSWCB ;LOAD BIT
2374 004266 022777 177652 174614 CMP #177652,ARSWC ;DID IT LOAD?
2375 004274 001401 BEQ .+4 ;YES
2376 004276 104010 HLT !WC ;NO WC SHOULD =177652
2377 004300 112777 000123 174602 MOVB #123,ARSWC
2378 004306 022777 177523 174574 CMP #177523,ARSWC
2379 004314 001401 BEQ .+4
2380 004316 104010 HLT !WC ;WC SHOULD = 177523
2381
2382 004320 104400 TST34: SCOPE
2383
2384 004322 012777 025252 174562 BITBA: MOV #25252,ARSB A ;LOAD DA
2385 004330 112777 000377 174614 MOVB #377,ARSBAB ;LOAD BIT
2386 004336 022777 177652 174546 CMP #177652,ARSB A ;DID IT LOAD?
2387 004344 001401 BEQ .+4 ;YES
2388 004346 104020 HLT !BA ;DA SHOULD =177652
2389 004350 112777 000125 174534 MOVB #125,ARSB A
2390 004356 022777 177524 174526 CMP #177524,ARSB A
2391 004364 001401 BEQ .+4
2392 004366 104020 HLT !BA ;BA SHOULD = 177525
2393 004370 104414 CLDK ;CLEAR ALL RS REG

```

2394	004372	104400
2395	004374	104414
2396	004376	005077
2397	004402	012777
2398	004410	012767
2399	004416	012701
2400	004422	056701
2401	004428	017700
2402	004432	020100
2403	004434	001404
2404	004436	005367
2405	004442	001371
2406	004444	104200
2407	004446	005001
2408	004450	017700
2409	004454	020100
2410	004456	001401
2411	004460	104000
2412	004462	012701
2413	004466	017700
2414	004472	020100
2415	004474	001401
2416	004476	104000

174524
177777 174516
002000 174562

```

TST35: SCOPE
ZERONE: CLDK
          CLR
          MOV #177777,DRSDB
          MOV #2000,WORK
          MOV #300,GOOD
          BIS UNNUM,GOOD
25:      MOV DRSCS2,BAD
          CMP GOOD,BAD
          BEQ 35
          DEC WORK
          BNE 25
          HLT !CS2
35:      CLR GOOD
          MOV DRSDB,BAD
          CMP GOOD,BAD
          BEQ .+4
          HLT
          MOV #-1,GOOD
          MOV DRSDB,BAD
          CMP GOOD,BAD
          BEQ .+4
          HLT

```

```

: CLEAR ALL RS REG
: LOAD DB WITH ALL 0
: LOAD DB WITH ALL ONES
: TIME OUT ROUTINE
: GET CORRECT FOR CS2

: GET CS2
: IS IT CORRECT?
: YES
: TO WAIT FOR OR
: TO SET
: OR SHOULD BE SET

: LOAD BAD WITH DB
: IS BAD CORRECT
: YES
: COULD NOT FLOAT 0 THROUGH DB
: LOAD GOOD WITH ANS
: GET DATA FROM DB
: IS DB CORRECT
: YES
: BAD SHOULD = 1777777

```



```

004624 104400
004626 104414
004630 012777 000007 174262
004636 036777 174320 174256
004644 001001
004646 104100
004650 012767 177777 017672
004656 013777 001102 174226
004664 012777 177777 174216
004672 012777 000001 174204
004700 032777 000001 174176
004706 001401
004710 104001
004712 022777 150600 174176
004720 001401
004722 104040
004724 022777 177777 174156
004732 001401
004734 104010
004736 005777 174152
004742 001401
004744 104004
004746 023777 001102 174136
004754 001401
004756 104020
004760 036777 174176 174134
004766 001001
004770 104100
004772 022777 000007 174120
005000 001401
005002 104002

```

```

:*****
:TEST 37 TEST NO-OP FUNCTION WITH ERROR BITS SET
:*****
TST37: SCOPE
NNOOP: CLROK
MOV #7,RSER ;CLEAR ALL RS REG
BIT UNCMP,RSAS ;LOAD ER
BNE +4 ;IS ATA BIT SET?
HLT :AS ;YES
MOV #177777,OUTBUF ;AS BIT SHOULD BE SET
MOV @#OBUFSV,RSBA ;DATA TO BE XFERED
MOV #-1,RSWC ;SET UP CURRENT ADDRESS
MOV #1,RSCSI ;LOAD WC WITH -1
BIT #1,RSCSI ;DO NO-OP FUNCTION
BEQ +4 ;DID GO BIT CLEAR
HLT :CS1 ;YES
CMP #150600,RSDS ;GO BIT SHOULD BE CLEARED
BEQ +4 ;DID ERR BITS SET?
HLT :DS ;NO
CMP #-1,RSWC ;ERR BIT SHOULD BE 0
BEQ +4 ;DID WC MOVE?
HLT :WC ;NO
TST @RSDA ;WC SHOULD = 177777
BEQ +4 ;DID DA MOV
HLT :DA ;NO
CMP @#OBUFSV,RSBA ;DA SHOULD =0
BEQ +4 ;DID BA MOVE
HLT :BA ;NO
BIT UNCMP,RSAS ;BA MOVED
BNE +4 ;AS SHOULD BE SET
HLT :AS ;IS IT?
CMP #7,RSER ;NO
BEQ +4 ;DID ER CHANGE?
HLT :ER ;NO
;ER SHOULD NOT CHANGE

```

01-20
21-28
29-36
37-44
45-52
53-60
61-68
69-76
77-84
85-92
93-100
101-108
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117-124
125-132
133-140
141-148
149-156
157-164
165-172
173-180
181-188
189-196
197-204
205-212
213-220
221-228
229-236
237-244
245-252
253-260
261-268
269-276
277-284
285-292
293-300
301-308
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317-324
325-332
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341-348
349-356
357-364
365-372
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389-396
397-404
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437-444
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453-460
461-468
469-476
477-484
485-492
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509-516
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525-532
533-540
541-548
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581-588
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597-604
605-612
613-620
621-628
629-636
637-644
645-652
653-660
661-668
669-676
677-684
685-692
693-700
701-708
709-716
717-724
725-732
733-740
741-748
749-756
757-764
765-772
773-780
781-788
789-796
797-804
805-812
813-820
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829-836
837-844
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853-860
861-868
869-876
877-884
885-892
893-900
901-908
909-916
917-924
925-932
933-940
941-948
949-956
957-964
965-972
973-980
981-988
989-996
997-1004
1005-1012
1013-1020
1021-1028
1029-1036
1037-1044
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1093-1100
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2005-2012
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F06

MAINDEC-11-DERSA-A
DERSAA.P11 TST41

RS11-REC3-RSD4 BASIC FUNCTION DIAGNOSTIC
TEST NO-OP FUNCTION CODE 21 WITH ERROR BITS SET

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```
11 005142 104400
12
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16 005144 104414
17 005146 012777 000007 173744
18 005154 036777 174002 173740
19 005162 001001
20 005164 104100
21 005166 012767 177777 017354
22 005174 013777 001102 173710
23 005202 012777 177777 173700
24 005210 012777 000021 173666
25 005216 032777 000001 173660
26 005224 001401
27 005226 104001
28 005230 022777 150600 173660
29 005236 001401
30 005240 104040
31 005242 022777 177777 173640
32 005246 001401
33 005248 104010
34 005254 005777 173634
35 005260 001401
36 005262 104004
37 005264 023777 001102 173620
38 005272 001401
39 005274 104020
40 005276 036777 173660 173616
41 005304 001001
42 005306 104100
43 005310 022777 000007 173602
44 005316 001401
45 005320 104002
46 005322 022777 104220 173554
47 005330 001401
48 005332 104040
```

```
*****
:TEST 41 TEST NO-OP FUNCTION CODE 21 WITH ERROR BITS SET
*****
TST41: SCOPE

NNOP21: CLDK          :CLEAR ALL RS REG
MOV #7,RSER          :LOAD ER
BIT UNCMP,RSAS       :IS ATA BIT SET?
BNE .+4              :YES
HLT !AS              :AS BIT SHOULD BE SET
MOV #177777,OUTBUF   :DATA TO BE XFERED
MOV #0BUFV,RSBA      :SET UP CURRENT ADDRESS
MOV #-1,RSWC         :LOAD WC WITH -1
MOV #21,RS1          :DO NO-OP FUNCTION
BIT #1,RS1           :DID GO BIT CLEAR
BEQ .+4              :YES
HLT !CS1             :GO BIT SHOULD BE CLEARED
CMP #150600,RS1      :DID ERR BITS SET?
BEQ .+4              :NO
HLT !DS             :ERR BIT SHOULD BE 0
CMP #-1,RSWC         :DID WC MOVE?
BEQ .+4              :NO
HLT !WC             :WC SHOULD = 177777
TST RSDA             :DID DA MOV
BEQ .+4              :NO
HLT !DA             :DA SHOULD =0
CMP #0BUFV,RSBA     :DID BA MOVE
BEQ .+4              :NO
HLT !BA             :BA MOVED
BIT UNCMP,RSAS       :AS SHOULD BE SET
BNE .+4              :IS IT?
HLT !AS             :NO
CMP #7,RSER         :DID ER CHANGE?
BEQ .+4              :NO
HLT !ER             :ER SHOULD NOT CHANGE
CMP #104220,RS1     :IS CS1 CORRECT?
BEQ .+4              :YES
HLT !DS             :CS1 SHOULD = 104220
```

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2549
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2552 005334 104400
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2556 005336 104414
2557 005340 012777 177777 173542
2558 005346 012777 177777 173540
2559 005354 012777 177017 173536
2560 005362 012777 000070 173540
2561 005370 012777 000011 173506
2562 005376 017700 173504
2563 005402 042700 177640
2564 005406 016701 173544
2565 005412 052701 000100
2566 005416 020100
2567 005420 001401
2568 005422 104000
2569 005424 005777 173464
2570 005430 001401
2571 005432 104004
2572 005434 005777 173460
2573 005440 001401
2574 005442 104002
2575 005444 017767 173460 173526
2576 005452 042767 177707 173520
2577 005460 022767 000070 173512
2578 005466 001401
2579 005470 104220
2580 005472 022777 004210 173404
2581 005500 001401
2582 005502 104001
2583 005504 036777 173452 173410
2584 005512 001401
2585 005514 104100
2586 005516 022777 177777 173364
2587 005524 001401
2588 005526 104010

```

```

:*****
:TEST 42 TEST DRIVE CLEAR FUNCTION WITH ERRORS SET
:*****
TST42: SCOPE
;FIRST SET ALL R/W BITS IN DISK REG
;DO DRIVE CLEAR-ALL R/W BITS SHOULD BE CLEARED

DRCLR: CLDRK ;CLEAR ALL RS REG
MOV #177777,@RSWC ;LOAD RSWC
MOV #177777,@RSDA ;SET ALL POSSIBLE
MOV #177017,@RSER ;BITS IN DISK REG
MOV #70,@RSMR ;SET THESE BITS
MOV #11,@RSCS1 ;SET DRIVE CLEAR
MOV @RSCS2,BAD ;GET CS2 DATA
BIC #177640,BAD ;CLEAR JUNK
MOV UNNUM,GOOD ;GET DRIVE UNIT
BIS #100,GOOD ;SET IR BIT
CMP GOOD,BAD ;IS UNIT # THE SAME
BEQ .+4 ;YES
HLT ;UNIT # IN CS2 GOT MODIFIED
TST @RSDA ;DID DA CLEAR
BEQ .+4 ;YES
HLT !DA ;DA SHOULD BE 0
TST @RSER ;DID ER CLEAR
BEQ .+4 ;YES
HLT !ER ;ER SHOULD BE CLEARED
MOV @RSMR,WORK ;GET MR REG
BIC #177707,WORK ;CLEAR JUNK
CMP #70,WORK ;IS 70 STILL SET IN MR?
BEQ .+4 ;YES
HLT !MR ;BITS 70 SHOULD NOT CLEAR
CMP #4210,@RSCS1 ;DID THESE BITS CLEAR?
BEQ .+4 ;YES
HLT !CS1 ;CS1 SHOULD =4210
BIT UNCMP,@RSAS ;AS SHOULD NOT SET
BEQ .+4 ;ON A DRIVE CLEAR FUN
HLT !AS ;WHY DID AS SET?
CMP #177777,@RSWC ;DID RSWC CHANGE?
BEQ .+4 ;NO
HLT !WC

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```

2589 ;DO ONE WORD WRITE
2590 ;*****
2591 ;TEST 43 EXECUTE THE ONE WORD WRITE
2592 ;*****
2593 TST43: SCOPE
2594
2595 005530 104400 WRTST: CLRDK ;CLEAR ALL RS REG
2596 005532 104414 MOV #177777,OUTBUF ;DATA TO BE X-FERED
2597 005534 012767 177777 017006 @#OBUFSV,@RSBA ;SET UP CURRENT ADDRESS
2598 005542 013777 001102 173342 MOV #-1,@RSWC ;SET WORD COUNT TO -1
2599 005550 012777 177777 173332 15: MOV #60,@RSCS1 ;SET FUNCTION WITH NO GO BIT
2600 005556 012777 000060 173320 CMP #-1,@RSWC ;DID WC MOVE?
2601 005564 022777 177777 173316 BEQ .+4 ;NO
2602 005572 001401 HLT ;WC ;WC MOVED
2603 005574 104010 CMP @#OBUFSV,@RSBA ;DID RSBA MOVE?
2604 005576 023777 001102 173306 BEQ .+4 ;NO
2605 005604 001401 HLT ;BA ;BA MOVED
2606 005606 104020 BIS #BIT0,@RSCS1 ;SET GO BIT
2607 005610 052777 000001 173266 25: TSTB @RSCS1 ;TEST FOR RDY=0
2608 005616 105777 173262 BPL .+4 ;RDY=0
2609 005622 100001 HLT ;CS1 ;RDY SHOULD = 0
2610 005624 104001 JSR PC,WAITRY ;WAIT FOR READY
2611 005626 004767 016424 HLT ;CS1 ;SHOULD = 260 RDY NEVER CAME UP
2612 005632 104001 CMP #1,@RSDA ;IS RSDA CORRECT
2613 005634 022777 000001 173252 BEQ .+4 ;RSDA OK
2614 005642 001401 HLT ;DA ;SHOULD = 1 SHOULD INCREMENT
2615 005644 104004 CMP #4260,@RSCS1 ;IS ERROR FLAG SET?
2616 005646 022777 004260 173230 35: BEQ .+4 ;NO! X-FER OK
2617 005654 001401 HLT ;CS1!ER!DS!DA ;ERROR DURING X-FER
2618 005656 104047 TST @RSWC ;FETCH WORD COUNT
2619 005660 005777 173224 45: BEQ .+4 ;WORD COUNT DID OVERFLOW
2620 005664 001401 HLT ;WC ;SHOULD = 0 FAILED TO INCREMENT
2621 005666 104010 CMP #10600,@RSDS ;IS RSDS OK?
2622 005670 022777 010600 173220 BEQ .+4 ;YES
2623 005676 001401 HLT ;DS!DA ;NO
2624 005700 104044 MOV UNLUM,GOOD ;GET UNIT #
2625 005702 016701 173250 BIS #100,GOOD ;SET IR BIT
2626 005706 052701 000100 MOV @RSCS2,BAD ;GET CS2
2627 005712 017709 173170 CMP GOOD,BAD ;IS CS2 CORRECT?
2628 005716 020100 BEQ .+4 ;YES
2629 005720 001401 HLT ;BAD = CS2 GOOD IS CORRECT ANS
2630 005722 104000 MOV @RSBA,BAD ;GET BA DATA
2631 005724 017700 173162 @#OBUFSV,GOOD ;WHAT RSBA SHOULD EQUAL
2632 005730 013701 001102 ADD #2,GOOD ;UPDATE OUTBUFFER
2633 005734 062701 000C02 CMP GOOD,BAD ;IS RSBA CORRECT
2634 005740 020100 BEQ .+4 ;YES
2635 005742 001401 HLT ;BA FAILED TO INCREMENT
2636 005744 104000 TST @RSER ;DID ANY ERRORS SET?
2637 005746 005777 173146 BEQ .+4 ;NO
2638 005752 001401 HLT ;DS

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;TEST READ FUNCTION
;*****
;TEST 44 EXECUTE THE ONE WORD READ
;*****
TST44: SCOPE

RDST: CLRDK ;CLEAR ALL RS REG
CLR OUTBUF ;CLR TO READ INTO
MOV #0,OUTBUF ;SET UP CURRENT ADDRESS
MOV #1,RSWC ;SET WORD COUNT TO -1
1$: MOV #71,RS1 ;GO READ
2$: TSTB RS1 ;TEST FOR BUSY=1
BPL +4 ;BUSY SET
HLT !CS1 ;BUSY NOT SET
JSR PC,WAITRY ;WAIT FOR READY
HLT !CS1 ;TIMEOUT RDY DID NOT SET
CMP #BIT0,RS1 ;WAS RSDA INCREMENTED BY 1
BEQ +4 ;RSDA OK
HLT !DA!ER!DS ;RSDA SHOULD CONTAIN A 1
3$: CMP #4270,RS1 ;IS ERROR FLAG SET?
BEQ +4 ;NO! X-FER OK
HLT !CS1!ER!DS ;RSCS1 SHOULD = 270
4$: TST RSWC ;TEST WC
BEQ +4 ;WORD COUNT DID OVERFLOW
HLT !WC ;SHOULD = 0
MOV UNUM,GOOD ;GET CORRECT
BIS #100,GOOD ;ANS OF CS2
MOV RS2,BAD ;GET CS2
CMP GOOD,BAD ;IS CS2 CORRECT?
BEQ +4 ;YES
HLT ;GOOD = CORRECT ANS FOR CS2
MOV RSBA,BAD ;FETCH CURRENT ADDRESS
MOV #0,OUTBUF,GOOD ;WHAT RSBA SHOULD EQUAL
ADD #2,GOOD ;UPDATE IT
CMP BAD,GOOD ;IS RSBA CORRECT
BEQ +4 ;YES EXECUTE CONTINUE
HLT ;RSBA FAILED TO INCREMENT
MOV OUTBUF,BAD ;GET DATA READ FROM DISK
MOV #-1,GOOD ;GET CORRECT ANS
CMP GOOD,BAD ;IS OUTBUF CORRECT
BEQ +4 ;YES
HLT ;GOOD=CORRECT ANS BAD=DATA READ FROM DISK

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2685 006144 104400
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2690 006146 104414
2691 006150 012767 177777 016372
2692 006156 013777 001102 172726
2693 006164 012777 177777 172716
2694 006172 012777 000051 172704
2695 006200 105777 172700
2696 006204 100001
2697 006206 104001
2698 006210 004767 016042
2699 006214 104001
2700 006216 016701 172734
2701 006222 052701 000100
2702 006226 017700 172654
2703 006232 020100
2704 006234 001401
2705 006236 104000
2706 006240 022777 004250 172636 2\$:
2707 006246 001401
2708 006250 104046
2709 006252 022777 000001 172634 3\$:
2710 006260 001401
2711 006262 104004
2712 006264 005777 172620
2713 006270 001401
2714 006272 104010
2715 006274 017700 172612
2716 006300 013701 001102
2717 006304 062701 000002
2718 006310 020001
2719 006312 001401
2720 006314 104000

```

;*****
;TEST 45 TEST WRITE CHECK
;*****
TST45: SCOPE
;DO A ONE WORD WRITE CHECK

; * * *EXECUTE THE ONE WORD WRITE CHECK* * *

WRCKT: CLDRK ;CLEAR ALL RS REG
MOV #177777,OUTBUF ;DATA TO BE X-FERED
MOV @#OBUFSV,@RSBA ;SET UP CURRENT ADDRESS
MOV #-1,@RSWC ;SET WORD COUNT TO -1
1$: MOV #51,@RSCS1 ;GO WRITE CHECK
2$: TSTB @RSCS1 ;TEST FOR READY
BPL .+4 ;NOT READY
HLT !CS1 ;BUSY FAILED TO SET
RSWCWT: JSR PC,WAITRY ;WAIT FOR READY
HLT !CS1 ;BUSY FAILED TO CLEAR
MOV UNNUM,GOOD ;GET UNIT #
BIS #100,GOOD ;SET BIT IR
MOV @RSCS2,BAD ;GET CS2
CMP GOOD,BAD ;IS CS2 CORRECT?
BEQ .+4 ;YES
HLT ;GOOD = CORRECT ANS FOR CS2
2$: CMP #4250,@RSCS1 ;ANY ERRORS?
BEQ .+4 ;X-FER OK
HLT !DA!ER!DS ;ERROR DUR X-FER
3$: CMP #BIT0,@RSDA ;WAS DAR INCREMENTED BY 1
BEQ .+4 ;RSDA OK
HLT !DA ;DAR SHOULD = 1
TST @RSWC ;TEST FOR OVERFLOW
BEQ .+4 ;WORD COUNT DID OVERFLOW
HLT !WC ;SHOULD = 0
MOV @RSBA,BAD ;FETCH CURRENT ADDRESS
MOV @#OBUFSV,GOOD ;WHAT RSBA SHOULD EQUAL
ADD #2,GOOD ;UPDATE IT
CMP BAD,GOOD ;IS RSBA CORRECT
BEQ .+4 ;YES EXECUTE CONTINUE
HLT ;RSBA FAILED TO INCREMENT

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2721 006316 016767 172560 172650 NXM:   MOV   OBUFSV,BPORTT ;SAVE -B- PORT BUFFER
2722 006324 012767 024550 172550      MOV   #OUTBUF,OBUSV  ;RESTORE OBUFSV
2723
2724 ;DESELECT THEN SELECT UNIT NUMBER IN RSCS2 CHECK TIMING
2725 ;*****
2726 ;TEST 46 DESELECT THEN SELECT UNIT NUMBER TIMING TEST
2727 ;*****
2728 006332 104400 TST46: SCOPE
2729
2730 UNITST: CLRDK ;CLEAR ALL RS REG
2731 006334 104414 CLR      R4 ;CLEAR R4
2732 006336 005004 CMP      R4,UNNUM ;IS THIS CORRECT UNIT #?
2733 006340 020467 172612 3NE     3$ ;NO THEN USE IT
2734 006344 001001 3NE     R4 ;GET WRONG DRIVE
2735 006346 005204 INC      R4 ;GET WRONG DRIVE
2736 006350 012767 177777 016172 3S:   MOV   #177777,OUTBUF ;DATA TO BE X-FERED
2737 006354 013777 001102 172526 MOV   @#OBUFSV,@RSBA ;SET UP CURRENT ADDRESS
2738 006356 012777 177777 172516 MOV   #-1,@RSWC ;SET WORD COUNT TO -1
2739 006372 012703 000061 MOV   #61,R3 ;GET WRITE FUNCTION
2740 006376 016705 172554 MOV   UNNUM,R5 ;GET CORRECT UNIT #
2741 006402 012701 172040 MOV   #172040,R1 ;GET CS1 REG
2742 006406 010461 000010 MOV   R4,10(R1) ;LOAD WRONG UNIT # INTO CS2
2743 006412 000240 NOP ;WAIT FOR DRIVE TO SETTLE
2744 006414 010561 000010 1S:   MOV   R5,10(R1) ;LOAD CORRECT UNIT #
2745 006420 010311 3S:   MOV   R3,(R1) ;LOAD FUNCTION IN CS1
2746 006422 004767 015630 JSR     PC,WAITRY ;WAIT FOR READY
2747 006426 104001 HLT     !CS1 ;SHOULD = 260 RDY NEVER CAME UP
2748 006430 022777 004260 172446 CMP     #4260,@RSCS1 ;IS ERROR FLAG SET?
2749 006436 001401 BEQ     .+4 ;NO! X-FER OK
2750 006440 104047 HLT     !CS1!ER!DS!DA ;ERROR DURING X-FER
2751 006442 022777 000001 172444 CMP     #1,@RSDA ;IS RSDA CORRECT
2752 006450 001401 BEQ     .+4 ;RSDA OK
2753 006452 104004 HLT     !DA ;SHOULD = 1 SHOULD INCREMENT
2754 006454 005777 172430 TST     @RSWC ;FETCH WORD COUNT
2755 006460 001401 BEQ     .+4 ;WORD COUNT DID OVERFLOW
2756 006462 104010 HLT     !WC ;SHOULD = 0 FAILED TO INCREMENT
2757 006464 022777 010600 172424 CMP     #10600,@RSDS ;IS RSDS OK?
2758 006472 001401 BEQ     .+4 ;YES
2759 006474 104044 HLT     !DS!DA ;NO
2760 006476 016701 172454 MOV     UNNUM,GOOD ;GET UNIT #
2761 006502 052701 000100 BIS     #100,GOOD ;SET IR BIT
2762 006506 017700 172374 MOV     @RSCS2,BAD ;GET CS2
2763 006512 020100 CMP     GOOD,BAD ;IS CS2 CORRECT?
2764 006514 001401 BEQ     .+4 ;YES
2765 006516 104000 HLT     ;BAD = CS2 GOOD IS CORRECT ANS
2766 006520 017700 172366 MOV     @RSBA,BAD ;GET BA DATA
2767 006524 013701 001102 MOV     @#OBUFSV,GOOD ;WHAT RSBA SHOULD EQUAL
2768 006530 062701 000002 ADD     #2,GOOD ;UPDATE OUTBUFFER
2769 006534 020100 CMP     GOOD,BAD ;IS RSBA CORRECT
2770 006540 104000 BEQ     .+4 ;YES
                HLT     ;BA FAILED TO INCREMENT

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2771 ;TEST CURRENT ADDRESS INHIBIT-BAI IN RSCS2
2772 ;DO A ONE WORD WRITE AND SEE
2773 ;IF RSBA INCREMENTED AFTER THE X-FER
2774
2775 ;*****
2776 ;TEST 47 TEST BAI IN RSCS2
2777 ;*****
2778 006542 104400 TST47: SCOPE
2779
2780 006544 104414 BAITST: CLRDK ;CLEAR ALL RS REG
2781 006546 013777 001102 172336 MOV @#0BUFSV,@RSBA ;SET UP CURRENT ADDR
2782 006554 012777 177777 172326 MOV #-1,@RSWC ;SET WORD COUNT TO -1
2783 006562 052777 000010 172316 BIS #BAI,@RSCS2 ;SET BAI BIT
2784 006570 012777 000061 172306 MOV #61,@RSCS1 ;WRITE
2785 006576 004767 015454 JSR PC,WAITRY ;WAIT FOR READY
2786 006602 104001 HLT !CS1 ;RDY DID NOT SET
2787 006604 013701 001102 1S: MOV @#0BUFSV,GOOD ;WHAT RSBA SHOULD BE
2788 006610 017700 172276 MOV @RSBA,BAD ;WHAT RSBA IS
2789 006614 020100 CMP GOOD,BAD ;COMPARE
2790 006616 001401 BEQ .+4 ;YES
2791 006620 104000 HLT ;BAD=OUTBUF GOOD = CORRECT ANS
2792 006622 005777 172272 TST @RSER ;ANY ERRORS?
2793 006626 001401 BEQ .+4 ;NO
2794 006630 104040 HLT !DS ;YES
2795 006632 104414 CLRDK ;CLEAR ALL RS REG
2796 006634 032777 000010 172244 BIT #BAI,@RSCS2 ;DID BAI CLEAR?
2797 006642 001401 BEQ .+4 ;YES
2798 006644 104002 HLT !ER ;BAI DID NOT SET

```

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2799 ;*****
2800 ;TEST 50 TEST NON-EXISTENT MEMORY ERROR BIT IN CS2
2801 ;*****
2802 006646 104400 †TST50: SCOPE
2803
2804 006650 104414 NXMTSM: CLRDK ;CLEAR ALL RS REG
2805 006652 052777 000010 172226 BIS #BAI,RS0CS2 ;SET BAI BIT
2806 006660 012777 177777 172222 MOV #-1,RSWC ;SET UP WORD COUNT
2807 006666 012777 173000 172216 MOV #173000,RSBA ;SET UP CURRENT ADDRESS
2808 006674 012777 000077 172232 MOV #77,RSBAE ;AND EXTENDED BITS
2809 006702 012777 001471 172174 MOV #1471,RS0CS1 ;READ AND LOAD A16 +A17 FOR 18 BIT ADDRESS
2810 006710 004767 015342 JSR PC,WAITRY ;WAIT FOR READY
2811 006714 104040 HLT !DS ;READY NEVER CAME UP
2812 006716 016701 172234 TSTNEM: MOV UNNUM,GOOD ;GET UNIT NO.
2813 006722 052701 004310 BIS #4310,GOOD ;SET BAI+OR BITS
2814 006726 017700 172154 MOV RS0CS2,BAD ;GET CS2
2815 006732 020100 CMP GOOD,BAD ;IS CS2 CORRECT?
2816 006734 001401 BEQ .+4 ;YES
2817 006736 104000 HLT ;BAD=CS2 GOOD=CORRECT ANS FOR CS2
2818 006740 022777 145670 172136 CMP #145670,RS0CS1 ;DID TRE SET?
2819 006746 001401 BEQ .+4 ;YES
2820 006750 104001 HLT ;CS1 ;TRE SHOULD SET BECAUSE OF NEM
2821 006752 012777 040000 172124 MOV #TRE,RS0CS1 ;CLEAR TRE
2822 006760 017700 172122 MOV RS0CS2,BAD ;GET CS2
2823 006764 016701 172166 MOV UNNUM,GOOD ;GET DRIVE
2824 006770 052701 000310 BIS #310,GOOD ;SET IR
2825 006774 020100 CMP GOOD,BAD ;IS CS2 CORRECT?
2826 006776 001401 BEQ .+4 ;YES
2827 007000 104200 HLT !CS2 ;CS2-BAD GOOD IS CORRECT ANS FOR CS2

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2828 ;*****
2829 ;TEST 51 TEST BLOCK SEARCH FUNCTION, PIP AND DRY BIT AND ADDR. CONF BIT
2830 ;*****
2831 †TST51: SCOPE
2832 007002 104400
2833
2834 007004 104414 BLOCK: CLRDK ;CLEAR ALL RS REG
2835 007006 012777 000032 172100 MOV #32,RS0DA ;DO A SEARCH FOR SECTOR 32
2836 007014 013777 001102 172070 MOV #0BUFSV,RSBA ;LOAD REGS. TO MAKE
2837 007022 012777 177777 172060 MOV #-1,RSWC ;SURE THEY DO NOT CHANGE
2838 007030 005067 172144 CLR WORK ;SETUP FOR TIMEOUT ROUTINE
2839 007034 032777 001000 172066 4$: BIT #1000,RSMR ;WAIT FOR DISK TO
2840 007042 001004 BNE 3$ ;REACH SECTOR 32
2841 007044 005367 172130 DEC WORK ;TIME OUT
2842 007050 001371 BNE 4$ ;ROUTINE
2843 007052 104200 HLT !MR ;COULD NOT FIND SECTOR 32

```

2844	007054	005077	172034		3\$:	CLR	DRSDA	;NOW SEARCH FOR 0
2845	007060	012777	000031	172016		MOV	#31,DRSCS1	;DO A BLOCK SEARCH FUNCTION
2846	007066	032777	000200	172022		BIT	#DRY,DRSDS	;IS DRY CLEARED?
2847	007074	001402				BEQ	1\$;YES
2848	007076	104040				HLT	!DS	;DRY SHOULD BE CLEARED DURING A BLOCK SEARCH
2849	007100	000500				BR	0OUT	;GET OUT BECAUSE OF TIMING
2850	007102	032777	020000	172006	1\$:	BIT	#20000,DRSDS	;IS PIP SET?
2851	007110	001001				BNE	.+4	;YES
2852	007112	104040				HLT	!DS	;PIP SHOULD BE SET
2853	007114	012701	020000			MOV	#20000,GOOD	;SETUP FOR TIMEOUT ROUTINE
2854	007120	005301			2\$:	DEC	GOOD	;DO TIMEOUT
2855	007122	001466				BEQ	TTMOUT	;TIMED OUT
2856	007124	032777	000200	171764		BIT	#DRY,DRSDS	;DID DRY SET?
2857	007132	001772				BEQ	2\$;NO
2858	007134	022777	110600	171754		CMP	#110600,DRSDS	;DID PIP CLEAR?
2859	007142	001401				BEQ	.+4	;YES
2860	007144	104040				HLT	!DS	;PIP BIT DID NOT CLEAR
2861	007146	022777	104230	171730		CMP	#104230,DRSCS1	;DID SC SET?
2862	007154	001401				BEQ	.+4	;YES
2863	007156	104041				HLT	!CS1!DS	;SC DID NOT SET
2864	007160	016767	171772	172012		MOV	UNNUM,WORK	;GET CORRECT AS BIT
2865	007166	005001				CLR	GOOD	;IN RSAS REG
2866	007170	000261				ROL	SEC	;THAT SHOULD
2867	007172	006101			5\$:	ROL	GOOD	;BE SET
2868	007174	005767	172000			TST	WORK	
2869	007200	001403				BEQ	6\$	
2870	007202	005367	171772			DEC	WORK	
2871	007206	000771				BR	5\$	
2872	007210	020177	171706		6\$:	CMP	GOOD,DRSAS	;IS RSAS CORRECT?
2873	007214	001403				BEQ	7\$;YES
2874	007216	017700	171700			MOV	DRSAS,BAD	;NO
2875	007222	104000				HLT		
2876	007224	010177	171672		7\$:	MOV	GOOD,DRSAS	;CLEAR AS REG
2877	007230	005777	171666			TST	DRSAS	;DID IT CLEAR?
2878	007234	001401				BEQ	.+4	;YES
2879	007236	104100				HLT	!AS	;NO
2880	007240	022777	010600	171650		CMP	#10600,DRSDS	;DID ATA CLEAR?
2881	007246	001401				BEQ	.+4	;YES
2882	007250	104040				HLT	!DS	;NO
2883	007252	023777	001102	171632		CMP	#0BUFSV,DRSBA	;DID BA MOVE?
2884	007254	001401				BEQ	.+4	;NO
2885	007256	104021				HLT	!CS1!BA	;BA MOVED WHY?
2886	007264	022777	177777	171616		CMP	#-1,DRSWC	;DID WC MOVE?
2887	007272	001401				BEQ	.+4	;NO
2888	007274	104010				HLT	!WC	;WC MOVED WHY?
2889	007276	000401				BR	0OUT	;DONE GET OUT
2890	007300	104040				HLT	!DS	;DRY NEVER CAME UP
2891	007302					0OUT:		;DONE

```

2892
2893
2894
2895 007302 104400
2896
2897
2898
2899
2900
2901
2902 007304 016767 011156 171656
2903 007312 012767 000010 011146
2904 007320 104414
2905 007322 012703 024464
2906 007326 012300
2907 007330 001513
2908 007332 013777 001102 171552
2909 007340 012777 177777 171542
2910 007346 010077 171532
2911 007352 042700 000001
2912 007356 010001
2913 007360 105777 171520
2914 007364 100375
2915 007366 052701 104200
2916 007372 017700 171506
2917 007376 020100
2918 007400 001401
2919 007402 104000
2920 007404 022777 000001 171506
2921 007412 001401
2922 007414 104043
2923 007416 022777 150600 171472
2924 007424 001401
2925 007426 104043
2926 007430 017700 171452
2927 007434 016701 171516
2928 007440 052701 000100
2929 007444 020100
2930 007446 001401
2931 007450 104000
2932 007452 016701 171504
2933 007456 042701 177400
2934 007462 017700 171434
2935 007466 020001
2936 007470 001401
2937 007472 104100

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:*****
:TEST 52 ILLEGAL FUNCTION CODE TEST CODE 3 TO 51
:*****
↑TST52: SCOPE

:TEST ILF BIT IN RSR AND ERR BIT IN RSDS
:ALSO CHECKS TO SEE IF WC,BA, OR DA GOT MODIFIED
:IF WISHING TO LOOP ON ONE FUNCTION ONLY, LOAD
:FUNCTION INTO LOCATION ILLTAB: AND 0 IN FOLLOWING LOCATION

ILLS1: MOV TIMES,TIMSV ;SAVE LOOP COUNT
MOV #10,TIMES ;LOOP TEN TIMES
CLRDK ;CLEAR ALL RS REG
15: MOV #ILLTAB,R3 ;GET STARTING ADD OF TABLE
35: MOV (R3)+,BAD ;GET ILL FUN
BEQ ILFON ;DONE GET OUT
MOV @#BUFSV,@RSBA ;SET UP REGS.
MOV #-1,@RSWC ;TO CHECK FOR CHANGE
25: MOV BAD,@RSCS1 ;DO ILLEGAL FUNCTION
BIC #BIT0,BAD ;CLEAR GO BIT
MOV BAD,GOOD ;MOV ILLEGAL FUN INTO GOOD
65: TSTB @RSCS1 ;RDY SET?
BPL 65 ;NO
BIS #104200,GOOD ;SET ERROR BITS
45: MOV @RSCS1,BAD ;PUT CS1 INTO BAD
CMP GOOD,BAD ;IS CS1 CORRECT?
BEQ .+4 ;YES
HLT ;GOOD IS WHAT CS1 SHOULD =BAD=CS1
CMP #1,@RSER ;DID ILF SET?
BEQ .+4 ;YES
HLT ;ILF DID NOT SET
CMP #150600,@RSDS ;IS DS GOOD?
BEQ .+4 ;YES
HLT ;ERR DID NOT SET
MOV @RSCS2,BAD ;GET CS2
MOV UNUM,GOOD ;GET UNIT #
BIS #100,GOOD ;SET IR BIT
CMP GOOD,BAD ;IS CS2 CORRECT?
BEQ .+4 ;YES
HLT ;GOOD = CORRECT ANS FOR CS2
MOV UNCMP,GOOD ;GET CORRECT DRIVE
BIC #177400,GOOD ;CLEAR UNWANTED BITS
MOV @RSAS,BAD ;GET RSAS REG
CMP BAD,GOOD ;DID CORRECT UNIT ANSWER?
BEQ .+4 ;YES
HLT ;AS ;NO WRONG DRIVE ANSWERED

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2938 007474 023777 001102 171410      CMP      @#0BUFSV,@RSBA      :DID BA MOVE
2939 007502 001401                BEQ      +4                  :NO
2940 007504 104021                HLT      !CS1!BA            :BA MOVED ON AN ILLEGAL FUNCTION
2941 007506 022777 177777 171374      CMP      #-1,@RSWC          :DID WC MOVE?
2942 007514 001401                BEQ      +4                  :NO
2943 007516 104011                HLT      !CS1!WC            :WC MOVED
2944 007520 005777 171370      TST      @RSDA              :DID DA MOVE
2945 007524 001401                BEQ      +4                  :NO
2946 007526 104005                HLT      !CS1!DA            :DA MOVED
2947 007530 104414                CLRK                     :CLEAR ALL ERRORS
2948 007532 005777 171362      TST      @RSER              :DID ERRORS CLEAR
2949 007536 001401                BEQ      +4                  :YES
2950 007540 104040                HLT      !DS                  :ILF DID NOT CLEAR
2951 007542 022777 004200 171334      CMP      #4200,@RSCS1      :DID ERRORS IN CS1 CLEAR
2952 007544 001401                BEQ      +4                  :YES
2953 007546 104040                HLT      !DS
2954 007554 000167 177546      J        3$
2955 007560                :CONTINUE UNTIL DONE
2956                :DONE WITH ILLEGAL FUNCTION TEST
2957                :TEST ILF BIT IN RSER AND ERR BIT IN RSDS
2958                :ALSO CHECKS TO SEE IF WC,BA, OR DA GOT MODIFIED
2959                :IF WISHING TO LOOP ON ONE FUNCTION ONLY, LOAD
2960                :FUNCTION INTO LOCATION ILFTB2: AND 0 IN FOLLOWING LOCATION
2961 007560 104414                ILLFUN: CLRK
2962 007562 012703 024526      1$:  MOV      #ILFTB2,R3      :CLEAR ALL RS REG
2963 007564 012300                3$:  MOV      (R3)+,BAD      :GET TABLE OF ILL FUNS.
2964 007570 001402                BEQ      2$                  :GET ILL FUN
2965 007572 000167 000422      JMP      ILFDNE              :DO THIS FUNCTION
2966 007576 013777 001102 171306 2$:  MOV      @#0BUFSV,@RSBA      :DONE GET OUT
2967 007604 012777 177777 171276      MOV      #-1,@RSWC          :SET UP REGS.
2968 007612 010067 171364      MOV      BAD,WORK1          :TO CHECK FOR CHANGE
2969 007616 042767 177707 171356      BIC      #177707,WORK1      :SHOULD WE TEST
2970 007624 010077 171254      MOV      BAD,@RSCS1         :BA AND WC
2971 007630 042700 000001      10$: BIC      #BIT0,BAD         :DO ILLEGAL FUNCTION
2972 007634 010001                MOV      BAD,GOOD          :CLEAR GO BIT
2973 007636 105777 171242      6$:  TSTB     @RSCS1           :MOV ILLEGAL FUN INTO GOOD
2974 007642 100375                BPL      6$                  :RDY SET?
2975 007644 052701 144200      BIS      #144200,GOOD       :NO
2976 007650 017700 171230      MOV      @RSCS1,BAD        :SET ERROR BITS
2977 007654 020100      CMP      GOOD,BAD          :PUT CS1 INTO BAD
2978 007656 001401                BEQ      +4                  :IS CS1 CORRECT?
2979 007660 104000      HLT      !CS1!ER!DS        :YES
2980 007662 022777 000001 171230      CMP      #1,@RSER          :GOOD IS WHAT CS1 SHOULD =BAD=CS1
2981 007670 001401                BEQ      +4                  :DID ILF SET?
2982 007672 104043      HLT      !CS1!ER!DS        :YES
2983 007674 022777 150600 171214      CMP      #150600,@RSDS     :ILF DID NOT SET
2984 007702 001401                BEQ      +4                  :IS DS GOOD?
2985 007704 104043      HLT      !CS1!ER!DS        :YES
2986 007706 005777 171202      TST      @RSDA              :ERR DID NOT SET
2987 007712 001401                BEQ      +4                  :DID DA MOVE?
2988 007714 104005      HLT      !CS1!DA            :NO
2989 007716 022767 000060 171256      CMP      #60,WORK1         :DA MOVED
2990 007724 001430                BEQ      11$                 :IS THIS AN ILL WRITE FUN?
2991 007726 022767 000050 171246      CMP      #50,WORK1         :YES
2992 007734 001455                BEQ      7$                  :IS THIS AN ILL WRITE CHECK FUN?
2993 007736 017700 171144      ;WORK1=70      MOV      @RSCS2,BAD        :YES--BRANCH
;GET CS2

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2994	007742	016701	171210		MOV	UNNUM,GOOD	:GET UNIT #
2995	007746	052701	001100		BIS	#1100,GOOD	:SET IR BIT
2996	007752	020100			CMP	GOOD,BAD	:IS CS2 CORRECT?
2997	007754	001401			BEQ	.+4	:YES
2998	007756	104000			HLT		:GOOD = CORRECT ANS FOR CS2
2999	007760	023777	001102	171124	CMP	2#0BUFSV,2RSBA	:DID BA MOVE
3000	007766	001401			BEQ	.+4	:NO
3001	007770	104021			HLT	:CS1:BA	:BA MOVED ON AN ILLEGAL FUNCTION
3002	007772	022777	177777	171110	CMP	#-1,2RSWC	:DID WC MOVE?
3003	010000	001401			BEQ	.+4	:NO
3004	010002	104011			HLT	:CS1:WC	:WC MOVED
3005	010004	000471			BR	45	:CONTINUE UNTIL DONE
3006							
3007	010006	017700	171074		115: MOV	2RSCS2,BAD	:GET CS2
3008	010012	016701	171140		MOV	UNNUM,GOOD	:GET UNIT #
3009	010016	052701	001300		BIS	#1300,GOOD	:SET IR BIT
3010	010022	020100			CMP	GOOD,BAD	:IS CS2 CORRECT?
3011	010024	001401			BEQ	.+4	:YES
3012	010026	104000			HLT		:GOOD = CORRECT ANS FOR CS2
3013	010030	013767	001102	171142	MOV	2#0BUFSV,WORK	:GET BUFFER ADDR.
3014	010036	062767	000002	171134	ADD	#2,WORK	:UPDATE IT
3015	010044	026777	171130	171040	CMP	WORK,2RSBA	:DID BA MOVE
3016	010052	001401			BEQ	.+4	:YES
3017	010054	104021			HLT	:CS1:BA	:BA MOVED ON AN ILLEGAL FUNCTION
3018	010056	022777	000000	171024	CMP	#0,2RSWC	:DID WC MOVE?
3019	010064	001401			BEQ	.+4	:YES
3020	010066	104011			HLT	:CS1:WC	:BAD WC
3021							
3022	010070	017700	171012		75: MOV	2RSCS2,BAD	:GET CS2
3023	010074	016701	171056		MOV	UNNUM,GOOD	:GET UNIT #
3024	010100	052701	001300		BIS	#1300,GOOD	:SET IR BIT
3025	010104	020100			CMP	GOOD,BAD	:IS CS2 CORRECT?
3026	010106	001401			BEQ	.+4	:YES
3027	010110	104000			HLT		:GOOD = CORRECT ANS FOR CS2
3028	010112	013767	001102	171060	MOV	2#0BUFSV,WORK	:GET BUFFER ADDR.
3029	010120	062767	000002	171052	ADD	#2,WORK	:UPDATE IT
3030	010126	022763	000057	177776	CMP	#57,-2(R3)	:WRITE CHECK REV.?
3031	010134	001003			BNE	85	:NO--BRANCH
3032	010136	162767	000004	171034	SUB	#4,WORK	:YES--BUFFER ADDR. GOES BACKWARDS
3033	010144	026777	171030	170740	85: CMP	WORK,2RSBA	:DID BA MOVE
3034	010152	001401			BEQ	.+4	:YES
3035	010154	104021			HLT	:CS1:BA	:BA DIDN'T INCREMENT PROPERLY
3036	010156	022777	000000	170724	CMP	#0,2RSWC	:DID WC MOVE?
3037	010164	001401			BEQ	.+4	:YES
3038	010166	104011			HLT	:CS1:WC	:WC DIDN'T INCREMENT PROPERLY
3039	010170	104414			45: CLRCK		:CLEAR ALL ERRORS
3040	010172	022777	004200	170704	CMP	#4200,2RSCS1	:DID ERRORS CLEAR
3041	010200	001401			BEQ	.+4	:YES
3042	010202	104040			HLT	:DS	:NO
3043	010204	005777	170710		TST	2RSER	:DID ERROR CLEAR
3044	010210	001401			BEQ	.+4	:NO
3045	010212	104040			HLT	:DS	:YES
3046	010214	000167	177346		JMP	35	:CONTINUE UNTIL DONE
3047	010220						:DONE WITH ILLEGAL FUNCTION TEST
3048							:*****
3049							:TEST 53 TEST ILLEGAL FUNCTION CODE 67

```

*****
3050                                     TST53: SCOPE
3051 010220 104400
3052
3053 010222 104414 ILF67: CLDK ;CLEAR ALL RS REG
3054 010224 012777 177777 170656 MOV #-1,RSWC ;SET WC TO -1
3055 010232 012767 024552 170740 MOV #(OUTBUF+2!2),WORK ;GET OUTBUF ADD.
3056 010240 016777 170734 170644 MOV WORK,RSBA ;LOAD ADDR.
3057 010246 012777 000067 170630 MOV #67,RSCS1 ;DO FUNCTION 67
3058 010254 105777 170624 15: TSTB RSCS1 ;DONE YET?
3059 010260 100375 BPL 15 ;NO
3060 010262 017700 170624 MOV RSBA,BAD ;GET BA REG
3061 010266 012701 024550 MOV #(OUTBUF+28!C3),GOOD ;GET CORRECT ANS FOR RSBA
3062 010272 020100 CMP GOOD,BAD ;IS RSBA CORRECT?
3063 010274 001401 BEQ .+4 ;YES
3064 010276 104000 HLT ;BAD=RSBA GOOD=CORRECT ANS.
3065 010300 016701 170652 MOV UNNUM,GOOD ;GET UNIT NUMBER
3066 010304 052701 001300 BIS #1300,GOOD ;SET IR AND OR BITS
3067 010310 017700 170572 MOV RSCS2,BAD ;GET CS2
3068 010314 020100 CMP GOOD,BAD ;IS CS2 CORRECT?
3069 010316 001401 BEQ .+4 ;YES
3070 010320 104000 HLT ;BAD=CS2 GOOD=CORRECT ANS
3071 010322 022777 000001 170570 CMP #1,RSER ;IS RSER CORRECT?
3072 010330 001401 BEQ .+4 ;YES
3073 010332 104002 HLT ;ER IS WRONG
3074 010334 104414 CLDK ;CLEAR ALL RS REG
3075 010336 005777 170556 TST RSER ;DID ERROR CLEAR
3076 010342 001401 BEQ .+4 ;YES
3077 010344 104040 HLT ;NO
3078 010346 012777 177700 170534 MOV #-100,RSWC ;SET WC TO -100
3079 010354 012767 024566 170616 MOV #OUTBUF+16!2,WORK ;GET OUTBUF ADD.
3080 010362 016777 170612 170522 MOV WORK,RSBA ;LOAD ADDR
3081 010370 012777 000067 170506 MOV #67,RSCS1 ;DO FUNCTION 67
3082 010376 105777 170502 25: TSTB RSCS1 ;DONE YET?
3083 010402 100375 BPL 25 ;NO
3084 010404 012701 024546 MOV #24546,GOOD ;IF PROG. IS MODIFIED THIS
3085 ;NUMBER WILL HAVE TO BE CHANGED.
3086 010410 017700 170476 MOV RSBA,BAD ;GET BA REG
3087 010414 020100 CMP GOOD,BAD ;IS RSBA CORRECT?
3088 010416 001401 BEQ .+4 ;YES
3089 010420 104000 HLT ;BAD=RSBA GOOD=CORRECT ANS.
3090 010422 016701 170530 MOV UNNUM,GOOD ;GET UNIT NUMBER
3091 010426 052701 001200 BIS #1200,GOOD ;SET OR BIT
3092 010432 017700 170450 MOV RSCS2,BAD ;GET CS2
3093 010436 020100 CMP GOOD,BAD ;IS CS2 CORRECT?
3094 010440 001401 BEQ .+4 ;YES
3095 010442 104000 HLT ;BAD=CS2 GOOD=CORRECT ANS
3096 010444 022777 000001 170446 CMP #1,RSER ;IS RSER CORRECT?
3097 010452 001401 BEQ .+4 ;YES
3098 010454 104002 HLT ;ER IS WRONG
3099 010456 012777 040011 170420 MOV #40011,RSCS1 ;CLEAR ERRORS
3100 010464 022777 004210 170412 CMP #4210,RSCS1 ;DID THEY CLEAR IN CS1
3101 010472 001401 BEQ .+4 ;YES
3102 010474 104040 HLT ;NO
3103 010476 005777 170416 TST RSER ;DID RSER CLEAR
3104 010502 001401 BEQ .+4 ;YES
3105 010504 104040 HLT ;NO

```

F07

MAINDEC-11-DERSA-A
DERSAA.P11

TST53

RS11-RS03-RS04 BASIC FUNCTION DIAGNOSTIC
TEST ILLEGAL FUNCTION CODE 67

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3106	010506	005777	170410
3107	010512	001401	
3108	010514	104100	

TST	RSAS
BEQ	+4
HLT	AS

:DID RSAS CLEAR
:YES
:NO

```

3109
3110 ;*****
3111 ;TEST 54 TEST PAR IN RSER
3112 ;*****
3113 010516 104400 TST54: SCOPE
3114
3115 010520 016767 170444 007740 PARTST: MOV TIMSV,TIMES ;RESTORE LOOP #
3116 010526 104414 CLDK ;CLEAR ALL RS REG
3117 010530 012777 000010 170362 1S: MOV #10,RSER ;SET PAR
3118 010536 022777 150600 170352 CMP #150600,RSDS ;DID ERR,ATA AND DRY SET?
3119 010544 001401 BEQ .+4 ;YES
3120 010546 104042 HLT !DS!ER ;ER SHOULD SET IF PAR SETS IN RSER
3121 010550 104414 CLDK ;CLEAR ALL RS REG
3122 010552 005777 170342 TST RSER ;DID PAR CLEAR?
3123 010556 001401 BEQ .+4 ;YES
3124 010560 104002 HLT !ER ;PAR DID NOT CLEAR BY CLEAR BIT
3125 010562 022777 010600 170326 CMP #10600,RSDS ;DID ERROR BITS CLEAR
3126 010570 001401 BEQ .+4 ;YES
3127 010572 104040 HLT !DS ;NO

```

```

3128 ;CHECK BITS 12 TO 15 FOR 0
3129 ;CHECK SECTOR FRACTION TO WATCH FOR MOVEMENT
3130 ;CHECK CS BITS IN LA AND ADDRESS CONFIRM IN MR REG
3131
3132 ;*****
3133 ;TEST 55 LOOK AHEAD TEST
3134 ;*****
3135 010574 104400 TST55: SCOPE
3136
3137 010576 032777 170000 170320 LATST: BIT #170000,DRSLA ;ARE BITS 12 TO 15 CLEARED?
3138 010604 001401 BEQ +4 ;YES
3139 010606 104204 HLT !LA ;BITS 12 TO 15 SHOULD BE CLEARED
3140 010610 104400 TST56: SCOPE
3141
3142 ;NOW TEST MOVEMENT IN SF BITS
3143
3144 010612 012767 171005 170360 MOV #171005,WORK ;SET UP FOR TIME OUT ROUTINE
3145 010620 017701 170300 MOV DRSLA,GOOD ;GET READING FROM LA
3146 010624 042701 007700 BIC #7700,GOOD ;GET RID OF CS BITS
3147 010630 005367 170344 IS: DEC WORK ;WAIT FOR DISK
3148 010634 001407 BEQ ERRR ;TYPE ERROR
3149 010636 017700 170262 MOV DRSLA,BAD ;READ LA
3150 010642 042700 007700 BIC #7700,BAD ;CLEAR CS BITS
3151 010646 020100 CMP GOOD,BAD ;DID SF BITS CHANGE?
3152 010650 001767 BEQ IS ;WAIT FOR TIME OUT
3153 010652 000422 BR LATDON ;LA OK CONT
3154 010654 ERRR:
3155 010654 104402 010660 TYPE .+2 ;ASCIZ <15><12>"SECTOR FRACTIONS NOT MOVING"
3156 010716 104204 HLT !LA ;TYPE LOOK AHEAD REG
3157 010720 LATDON: ;DONE CONT.

```

```

3158 ;*****
3159 ;TEST 57 CHECK CS BITS TO INCREMENT AND ADDRESS CONFIRM BIT IN MR
3160 ;*****
3161 010720 104400 TST57: SCOPE
3162
3163 010722 016767 007540 170240 CSTST: MOV TIMES,TIMSV ;SAVE LOOP CT
3164 010730 012767 000010 007530 MOV #10,TIMES ;LOOP 10 TIMES
3165 010736 104414 CLRDK ;CLEAR ALL RS REG.
3166 010740 012701 001000 MOV #1000,GOOD ;LOAD COUNTER
3167 010744 032777 001000 170156 BIT #BIT9,ARSMR ;IS ADD CONFIRM BIT 0?
3168 010752 001407 BEQ ADDCF ;YES CONTINUE
3169 010754 005301 DEC GOOD ;WAIT FOR
3170 010756 001376 BNE -2 ;DISK TO MOVE
3171 010760 032777 001000 170142 BIT #BIT9,ARSMR ;IS ADD. CON. BIT BIT 0?
3172 010766 001401 BEQ +4 ;YES
3173 010770 104220 HLT !MR ;ADD. CONF. BIT ALWAYS A 1
3174
3175 ;NOW TEST TA BITS AND ADD. CON. BIT IN MR
3176
3177 010772 012777 177777 170114 ADDCF: MOV #-1,ARSDA ;INIT RSDA
3178 011000 012767 177777 170172 1$: MOV #-1,WORK ;SETUP TIMEOUT COUNTER
3179 011006 005277 170102 ARSDA ;GET NEXT SECTOR
3180 011012 022777 010000 170074 CMP #1000,ARSDA ;DONE ALL YET?
3181 011020 001433 BEQ DONCS ;YES
3182 011022 005367 170152 2$: DEC WORK ;DO TIMEOUT ROUTINE
3183 011026 001427 BEQ TMEOUT ;ADD. CON. NEVER CAME UP
3184 011030 032777 001000 170072 BIT #1000,ARSMR ;DID ADD CONFIRM BIT SET?
3185 011036 001771 BEQ 2$ ;YES
3186 011040 017700 170060 MOV ARSLA,BAD ;GET LA
3187 011044 042700 000077 BIC #77,BAD ;CLEAR SF BITS
3188 011050 012767 000006 170124 MOV #6,WORK1 ;SET UP COUNTER
3189 011056 006000 3$: ROR BAD ;MOV SA BITS RIGHT
3190 011060 005367 170116 DEC WORK1 ;DO 6 TIMES
3191 011064 001374 BNE 3$ ;DONE YET?
3192 011066 017701 170022 MOV ARSDA,GOOD ;GET DA
3193 011072 042701 177700 BIC #177700,GOOD ;CLEAR JUNK
3194 011076 020100 CMP GOOD,BAD ;ARE SA BITS = IN DA AND LA REG.?
3195 011100 001401 BEQ +4 ;OK
3196 011102 104000 HLT ;GOOD =DA BAD = LA
3197 011104 000735 BR 1$ ;NO WAIT
3198
3199 011106 104262 TMEOUT: HLT !MR!ER!DS ;ADDRESS CONFIRM BIT NEVER SET COULD BE BAD OR
3200 ;BAD LA OR BAD COMPARE BETWEEN LA AND DA
3201 011110 DONCS: ;TEST DONE CONTINUE

```

```

3202 ;*****
3203 ;TEST 60 PARITY TEST
3204 ;*****
3205 011110 104400 TST60: SCOPE
3206
3207 011112 016767 170052 007346 PART: MOV TIMSV,TIMES ;RESTORE LOOP COUNTER
3208 011120 012737 011300 000004 MOV #PRT1,R4 ;SETUP TIME OUT VECTOR
3209 011126 012737 000340 000006 MOV #340,R6
3210 011134 012702 172100 MOV #MPRO,R2 ;GET PAR REG
3211 011140 005712 TSTAGN: TST (R2) ;DOES IT EXIST
3212 011142 012712 000004 MOV #WWP,R2 ;YES SET WRITE WRONG PARITY
3213 011146 012701 001200 MOV #WORK,R1 ;GET TEST LOCATION
3214 011152 011111 15: MOV @R1,@R1 ;WRITE WRONG PARITY
3215 011154 005711 TST @R1 ;READ IT
3216 011156 005712 TST @R2 ;DID PARITY ERROR SET?
3217 011160 100402 BMI 2$ ;YES
3218 011162 005012 CLR @R2 ;CLEAR PARITY REG
3219 011164 000446 BR PRT1 ;GET NEXT PARITY REG
3220 011166 012712 000001 2$: MOV #1,@R2 ;CLEAR WWP AND ENABLE BAD PARITY
3221 011172 104414 CLDRK ;CLEAR ALL RS REG
3222 011174 012767 177777 013346 MOV #177777,OUTBUF ;DATA TO BE X-FERED
3223 011202 012777 001200 167702 MOV #WORK,@RSBA ;SET UP CURRENT ADDRESS
3224 011210 012777 177777 167672 MOV #-1,@R5WC ;SET WORD COUNT TO -1
3225 011215 012777 000061 167660 MOV #61,@RSCS1 ;GO WRITE
3226 011224 105777 167654 3$: TSTB @RSCS1 ;DONE YET?
3227 011230 100375 BPL 3$ ;NO WAIT
3228 011232 005012 CLR @R2 ;CLEAR PARITY REG
3229 011234 005067 167740 CLR WORK ;WRITE GOOD PARITY
3230 011240 017700 167642 MOV @RSCS2,BAD ;GET CS2
3231 011244 012701 020100 MOV #20100,GOOD
3232 011250 056701 167702 BIS UNNUM,GOOD ;GET CORRECT AND FOR CS2
3233 011254 020100 CMP GOOD,BAD ;IS CS2 CORRECT?
3234 011256 001401 BEQ .+4 ;YES
3235 011260 104000 HLT ;CS2 SHOULD = GOOD
3236 011262 022777 144260 167614 CMP #144260,@RSCS1 ;IS CS1 CORRECT?
3237 011270 001401 BEQ .+4 ;YES
3238 011272 104040 HLT ;OS
3239 011274 000167 000020 JMP NOPAR ;GET OUT
3240
3241 ;TRAPOUT ROUTINE
3242
3243 011300 022626 PRT1: CMP (6)+(6)+ ;CLEAR STACK
3244 011302 022702 172136 PRT1: CMP #172136,R2 ;DONE YET?
3245 011306 001404 BEQ NOPAR ;YES NO PAR REG
3246 011310 052702 000002 ADD #2,R2 ;NO TRY AGAIN
3247 011314 000167 177620 JMP TSTAGN ;RETRY
3248
3249 011320 012737 000006 000004 NOPAR: MOV #6,R4
3250 011326 005037 000006 CLR @R6

```

```

3251 :*****
3252 :TEST 61 TEST WRITE CHECK ERROR
3253 :*****
3254 011332 10440C TST61: SCOPE
3255
3256 ;WRITE A WORD OF 0 AND FLOAT A 1 THROUGH IT TO CAUSE WCE
3257 ;SET BIT14 IN ONCE AND WRITE A WD OF -1 AND FLOAT 0
3258 ;TO CAUSE WCE
3259
3260 011334 104414 WCETST: CLRDK ;CLEAR ALL RS REG
3261 011336 005067 013206 CLR ;WRITE A WD OF 0
3262 011342 013777 001102 167542 WCETT: MOV @#OBUFSV,@RSBA ;SET UP CURRENT ADDRESS
3263 011350 012777 177777 167532 MOV #-1,@RSWC ;SET WORD COUNT TO -1
3264 011356 012777 000061 167520 MOV #61,@RSCS1 ;GO WRITE
3265 011364 105777 167514 3$: TSTB @RSCS1 ;DONE YET?
3266 011370 100375 BPL 3$ ;NO WAIT
3267 011372 032767 040000 167564 BIT #BIT14,ONCE ;WRITE A 1 OR 0?
3268 011400 001410 BEQ 2$ ;WRITE A 0
3269 011402 012767 177777 013140 MOV #-1,OUTBUF ;WRITE A 1
3270 011410 000241 CLC ;CLEAR CARRY
3271 011412 006167 013132 6$: ROL OUTBUF ;FLOAT A 0 THROUGH BAD WD
3272 011416 103123 WCEDON ;DONE GET OUT
3273 011420 000406 BR 5$ ;CHECK WCE
3274 011422 005067 013122 2$: CLR OUTBUF ;WRITE A 0
3275 011426 000261 SEC ;SET CARRY
3276 011430 006167 013114 1$: ROL OUTBUF ;FLOAT A 1
3277 011434 103503 BCS WCEDNE ;GET OUT WHEN DONE
3278 011436 013777 001102 167446 5$: MOV @#OBUFSV,@RSBA ;SET UP CURRENT ADDRESS
3279 011444 012777 177777 167436 MOV #-1,@RSWC ;SET WORD COUNT TO -1
3280 011452 005077 167436 CLR @RSDA
3281 011456 012777 000051 167420 MOV #51,@RSCS1 ;GO WRITE CHECK
3282 011464 105777 167414 4$: TSTB @RSCS1 ;READY YET?
3283 011470 100375 BPL 4$ ;NO WAIT
3284 011472 017700 167410 MOV @RSCS2,BAD ;GET CS2
3285 011476 016701 167454 MOV UNNUM,GOOD ;SET UNIT #
3286 011502 052701 040300 BIS #40300,GOOD ;SET BITS
3287 011506 020100 CMP GOOD,BAD ;IS CS2 CORRECT?
3288 011510 001413 BEQ 7$ ;YES
3289 011512 104000 HLT ;BAD=CS2 GOOD=CORRECT ANS
3290 011514 016700 013030 MOV OUTBUF,BAD ;GET BAD WD THAT SHOULD CAUSE WCE
3291 011520 005001 CLR GOOD ;GET GOOD WD IF WRITING 0
3292 011522 032767 040000 167434 BIT #BIT14,ONCE ;ARE WE WRITING 1 OR 0
3293 011530 001402 BEQ 8$ ;0
3294 011532 012701 177777 MOV #-1,GOOD ;GET GOOD WD FOR 1
3295 011536 104000 8$: HLT ;GOOD = CORRECT WD WRITTEN
;BAD = INCORRECT WD THAT WCE DID NOT CATCH
3296

```


3297	011540	022777	144250	167336	7\$:	CMP	#144250, @RSCS1	; DID TRE SET?
3298	011546	001401				BEQ	.+4	; YES
3299	011550	104043				HLT	!CS1!ER!DS	; TRE SHOULD SET IF WCE SETS
3300	011552	017700	167334			MOV	@RSBA, BAD	; FETCH CURRENT ADDRESS
3301	011556	013701	0C1102			MOV	@#OBUFSV, GOOD	; WHAT RSBA SHOULD EQUAL
3302	011562	062701	000002			ADD	#2, GOOD	; UPDATE IT
3303	011566	020001				CMP	BAD, GOOD	; IS RSBA CORRECT
3304	011570	001401				BEQ	.+4	; YES EXECUTE CONTINUE
3305	011572	104000				HLT		; RSBA FAILED TO INCREMENT
3306	011574	104414				CLRDK		; CLEAR ALL RS REG
3307	011576	016701	167354			MOV	UNNUM, GOOD	; PUT DRIVE IN GOOD
3308	011602	052701	000100			BIS	#100, GOOD	; SET IR BIT
3309	011606	017700	167274			MOV	@RSCS2, BAD	; GET CS2
3310	011612	020100				CMP	GOOD, BAD	; IS CS2 CORRECT
3311	011614	001401				BEQ	.+4	; YES
3312	011616	104000				HLT		; BAD =CS2 GOOD IS CORRECT ANS
3313	011620	022777	004200	167256		CMP	#4200, @RSCS1	; DID TRE CLEAR?
3314	011626	001401				BEQ	.+4	; YES
3315	011630	104001				HLT	!CS1	; TRE DID NOT CLEAR WITH CLEAR
3316	011632	032767	040000	167324		BIT	#BIT14, ONCEE	; FLOATION A 1 OR 0?
3317	011640	001673				BEQ	1\$; FLOAT 1
3318	011642	000663				BR	6\$; FLOAT 0
3319	011644	052767	040000	167312	WCEDNE:	BIS	#BIT14, ONCEE	; SET BIT14
3320	011652	104414				CLRDK		
3321	011654	012767	177777	012666		MOV	#-1, OUTBUFF	
3322	011662	000167	177454			JMP	WCETT	; NOW WRITE -1 IN OUTBUF
3323	011666	042767	040000	167270	WCEDON:	BIC	#BIT14, ONCEE	; CLEAR TEST FLAG

```

3324      ;*****
3325      ;TEST 62                TEST PROGRAM ERROR BIT IN RSCS2
3326      ;*****
3327 011674 104400      †TST62: SCOPE
3328
3329      PGETST: CLRDK      ;CLEAR ALL RS REG
3330 011700 012767 177777 012642      MOV #177777,OUTBUF ;DATA TO BE X-FERED
3331 011706 013777 001102 167176      MOV @#OBUFSV,@RSBA ;SET UP CURRENT ADDRESS
3332 011714 012777 177000 167166      MOV #177000,@RSWC ;SET WORD COUNT
3333 011722 012777 000061 167154      MOV #61,@RSCS1 ;GO WRITE
3334 011730 105777 167150      2$: TSTB @RSCS1 ;IS RDY CLEARED YET?
3335 011734 100775      BMI 2$ ;NO WAIT
3336 011736 012777 000071 167140      MOV #71,@RSCS1 ;GO READ
3337 011744 004767 012306      JSR PC,WAITRY ;WAIT FOR READY
3338 011750 104001      HLT !CS1 ;RDY NEVER CAME UP
3339 011752 022777 144260 167124      CMP #144260,@RSCS1 ;IS CS1 CORRECT?
3340 011760 001401      BEQ .+4 ;YES
3341 011762 104001      HLT !CS1 ;TRE SHOULD SET BY SETTING PGE
3342 011764 005777 167132      TST @RSAS ;AS SHOULD = 0
3343 011770 001401      BEQ .+4 ;YES
3344 011772 104100      HLT !AS ;RSAS SHOULD = 0
3345 011774 016701 167156      MOV UNNUM,GOOD ;GET UNIT #
3346 012000 052701 002200      BIS #2200,GOOD ;SET PGE, IR, AND OR
3347 012004 017700 167076      MOV @RSCS2,BAD ;GET CS2
3348 012010 020100      CMP GOOD,BAD ;IS IT CORRECT?
3349 012012 001401      BEQ .+4 ;YES
3350 012014 104000      HLT ;BAD = CS2
3351 012016 005777 167066      TST @RSWC ;SHOULD NOT BE 0
3352 012022 001001      BNE .+4 ;BECAUSE PGE SHOULD ABORT
3353 012024 104011      HLT !WC!CS1 ;CURRENT OPERATION
3354 012026 005777 167066      TST @RSER ;DID ANY ERRORS SET?
3355 012032 001401      BEQ .+4 ;NO
3356 012034 104040      HLT !DS ;RMR SHOULD BE SET
3357 012036 052777 040000 167040      BIS #TRE,@RSCS1 ;CLEAR ERRORS
3358 012044 042701 002000      BIC #PGE,GOOD ;CLEAR PGE ERROR
3359 012050 017700 167032      MOV @RSCS2,BAD ;GET CS2
3360 012054 020100      CMP GOOD,BAD ;IS CS2 CORRECT?
3361 012056 001401      BEQ .+4 ;YES
3362 012060 104200      HLT !CS2 ;PGE DID NOT CLEAR BY CLEARING TRE BAD = CS2
3363 012062 022777 004260 167014      CMP #4260,@RSCS1 ;DID SC CLEAR
3364 012070 001401      BEQ .+4 ;YES
3365 012072 104040      HLT !DS ;DID NOT CLEAR BY CLEARING TRE

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3366
3367
3368
3369 012074 104400
3370
3371 012076 104414
3372 012100 013777 001102 167004
3373 012106 012777 177700 166774
3374 012114 012703 172060
3375 012120 011304
3376 012122 042704 000017
3377 012126 022704 000020
3378 012132 001372
3379 012134 012777 000031 166742
3380 012142 012777 007777 166744
3381 012150 004767 012102
3382 012154 104001
3383 012156 022777 000004 166734
3384 012164 001401
3385 012166 104002
3386 012170 022777 007777 166716
3387 012176 001001
3388 012200 104004
3389 012202 022777 150600 166706
3390 012210 001401
3391 012212 104042
3392 012214 022777 104230 166662
3393 012222 001401
3394 012224 104040
3395 012226 104414
3396 012230 005777 166664
3397 012234 001401
3398 012236 104002
3399 012240 022777 004200 166636
3400 012246 001401
3401 012250 104040
3402
3403
3404
3405
3406 012252 104400
3407
3408 012254 104414
3409 012256 013777 001102 166626
3410 012264 012777 177700 166616
3411 012272 012777 000061 166604
3412 012300 105777 166600
3413 012304 100775
3414 012306 012777 177773 166604

```

```

:*****
:TEST 63 TEST RMR IN RSER REGISTER TRYING TO WRITE INTO RSDA
:*****
TST63: SCOPE

RMRT1: CLRDK ;CLEAR ALL RS REG
MOV #0BUFSV,RSBA ;SET UP CURRENT ADDRESS
MOV #177700,RSWC ;SET WORD COUNT
MOV #172060,R3 ;GET RSLA REG
1$: MOV @R3,R4 ;WAIT FOR
BIC #17,R4 ;THE MIDDLE
CMP #20,R4 ;OF SECTOR 0
BNE 1$ ;BEFORE DOING A SEARCH
MOV #31,RSRCS1 ;SEARCH
MOV #7777,RSRSDA ;CAUSE ERROR
JSR PC,WAITRY ;WAIT FOR READY
HLT :CS1 ;RDY NEVER CAME UP
CMP #4,RSER ;DID RMR SET?
BEQ .+4 ;YES
HLT :ER ;ER SHOULD = 4
CMP #7777,RSRSDA ;DID DA GET MODIFIED?
BNE .+4 ;NO
HLT :DA
CMP #150600,RSRSDS ;DID ERR SET?
BEQ .+4 ;YES
HLT :DS!ER ;ER DID NOT SET BECAUSE OF RMR
CMP #104230,RSRCS1 ;IS CS1 CORRECT?
BEQ .+4 ;YES
HLT :DS ;CS1 SHOULD = 144260
CLRDK ;CLEAR ALL RS REG
TST @RSER ;DID RMR CLEAR?
BEQ .+4 ;YES
HLT :ER ;RMR DID NOT CLEAR WITH A CLEAR
CMP #4200,RSRCS1 ;IS CS1 CORRECT?
BEQ .+4 ;YES
HLT :DS ;NO

```

```

:*****
:TEST 64 TEST RMR IN RSER REGISTER TRYING TO WRITE INTO RSER
:*****
TST64: SCOPE

RMRT2: CLRDK ;CLEAR ALL RS REG
MOV #0BUFSV,RSBA ;SET UP CURRENT ADDRESS
MOV #177700,RSWC ;SET WORD COUNT
MOV #61,RSRCS1 ;GO WRITE
2$: TSTB @RSRCS1 ;IS RDY SET?
BMI 2$ ;YES WAIT FOR IT TO CLEAR
MOV #177773,RSRER ;CAUSE ERROR

```

3415	012314	004767	011736			JSR	PC WAITRY		
3416	012320	104001				HLT	:CS1		:RDY NEVER CAME UP
3417	012322	022777	000004	166570		CMP	#4,RSER		:DID RMR SET?
3418	012330	001401				BEQ	+4		:YES
3419	012332	104002				HLT	:ER		:ER SHOULD = 4
3420	012334	022777	150600	166554		CMP	#150600,RSOS		:DID ERR SET?
3421	012342	001401				BEQ	+4		:YES
3422	012344	104042				HLT	:DS!ER		:ERR DID NOT SET BECAUSE OF RMR
3423	012346	022777	144260	166530		CMP	#144260,RSOS1		:IS CS1 CORRECT?
3424	012354	001401				BEQ	+4		:YES
3425	012356	104040				HLT	:DS		:CS1 SHOULD = 144260
3426	012360	104414				CLDRK			:CLEAR ALL RS REG

:TEST 65 TEST RMR IN RSEP REGISTER TRYING TO WRITE INTO RSCS1

3431	012362	104400				TST65:	SCOPE		
3432									
3433	012364	104414				RMRT3:	CLDRK		:CLEAR ALL RS REG
3434	012366	013777	001102	166516		MOV	#0BUFSV,RSBA		:SET UP CURRENT ADDRESS
3435	012374	012777	177700	166506		MOV	#177700,RSWC		:SET WORD COUNT
3436	012402	012777	000061	166474		MOV	#61,RSOS1		:GO WRITE
3437	012410	105777	166470		25:	TSTB	RSOS1		:IS RDY SET?
3438	012414	100775				BMI	25		:YES WAIT FOR IT TO CLEAR
3439	012416	012777	000030	166460		MOV	#30,RSOS1		:CAUSE ERROR
3440	012424	004767	011626			JSR	PC WAITRY		:WAIT FOR READY
3441	012430	104001				HLT	:CS1		:RDY NEVER CAME UP
3442	012432	022777	000004	166460		CMP	#4,RSER		:DID RMR SET?
3443	012440	001401				BEQ	+4		:YES
3444	012442	104002				HLT	:ER		:ER SHOULD = 4
3445	012444	022777	150600	166444		CMP	#150600,RSOS		:DID ERR SET?
3446	012452	001401				BEQ	+4		:YES
3447	012454	104042				HLT	:DS!ER		:ERR DID NOT SET BECAUSE OF RMR
3448	012456	022777	144260	166420		CMP	#144260,RSOS1		:IS CS1 CORRECT?
3449	012464	001401				BEQ	+4		:YES
3450	012466	104040				HLT	:DS		:CS1 SHOULD = 144260
3451	012470	104414				CLDRK			:CLEAR ALL RS REG

```

*****
:TEST 66 TEST THAT RMR DOES NOT SET BY WRITTING INTO RSAS
*****
TST66: SCOPE

3453 012472 104400
3454 012474 104414
3457 012474 104414 RMRT4: CLRDK ;CLEAR ALL RS REG
3458 012476 013777 001102 166406 MOV #0BUFSV,RSBA ;SET UP CURRENT ADDRESS
3459 012504 012777 :77700 166376 MOV #177700,RSWC ;SET WORD COUNT
3460 012512 012703 172060 MOV #172060,R3 ;GET RSLA REG
3461 012516 011304 15: MOV #R3,R4 ;WAIT FOR
3462 012520 042704 000017 BIC #17,R4 ;THE MIDDLE
3463 012524 022704 000020 CMP #20,R4 ;OF SECTOR 0
3464 012530 001372 15: BNE 15 ;BEFORE DOING A SEARCH
3465 012532 012777 000031 166344 MOV #31,RS0S1 ;SEARCH
3466 012540 012777 000000 166354 MOV #0,RSAS ;TRY TO CAUSE ERROR
3467 012546 005067 166426 CLR WORK ;CLEAR COUNTER
3468 012552 032777 000200 166336 25: BIT #BIT7,RS0S ;WAIT FOR DRY
3469 012560 001004 BNE 35 ;READY CONT
3470 012562 005267 166412 INC WORK ;COUNT
3471 012566 001371 BNE 25 ;RETRY
3472 012570 104001 HLT !CS1 ;RDY NEVER CAME UP
3473 012572 005777 166322 35: TST #RSER ;DID RMR SET?
3474 012576 001401 BEQ +4 ;NO
3475 012600 104002 HLT !ER ;ER SHOULD = 0
3476 012602 022777 110600 166306 CMP #110600,RS0S ;DID ERR SET?
3477 012610 001401 BEQ +4 ;NO
3478 012612 104042 HLT !DS!ER ;DS SHOULD = 110600
3479 012614 022777 104230 166262 CMP #104230,RS0S1 ;IS CS1 CORRECT?
3480 012622 001401 BEQ +4 ;YES
3481 012624 104040 HLT !DS ;CS1 SHOULD = 144260
3482 012626 104414 CLRDK ;CLEAR ALL RS REG
3483 012630 022777 004200 166246 CMP #4200,RS0S1 ;IS CS1 CORRECT?
3484 012636 001401 BEQ +4 ;YES
3485 012640 104040 HLT !DS ;NO

```

```

3486
3487
3488
3489 012642 104400
3490
3491
3492
3493 012644 104414
3494 012646 012767 177500 166330
3495 012654 005767 166306
3496 012660 001003
3497 012662 012767 177600 166314
3498 012670 016777 166310 166212
3499 012676 012767 177777 011644
3500 012704 013777 001102 166200
3501 012712 052777 000010 166166
3502 012720 012777 000061 166156
3503 012726 105777 166152
3504 012732 100375
3505 012734 005077 166154
3506 012740 005067 011604
3507 012744 013777 001102 166140
3508 012752 012777 177777 166130
3509 012760 012702 172060
3510 012764 011203
3511 012766 042703 000077
3512 012772 022703 004000
3513 012776 001372
3514 013000 012777 000061 166076
3515 013006 011203
3516 013010 042703 000017
3517 013014 022703 000020
3518 013020 001372
3519 013022 012777 000040 166056
3520 013030 016777 166122 166050

```

```

*****
:TEST 67 TEST DCK IN RSER
*****
TEST67: SCOPE

:DO A WRITE AND THEN A CLEAR FUNCTION THAT SHOULD CAUSE DCK TO SET

DCKTST: CLRDK ;CLEAR ALL RS REG
MOV #177500,WORK2 ;GET WC FOR RS04
TST RS04DT ;IS THIS A RS04?
BNE 1$ ;YES
MOV #177600,WORK2 ;NO
1$: MOV WORK2,RSWC ;LOAD WC
MOV #-1,OUTBUF ;WRITE -1
MOV @#0BUFSV,RSBA ;SET UP CURRENT ADDRESS
4$: BIS #10,RS04 ;SET BAI BIT
MOV #61,RS04 ;GO WRITE
5$: TSTB RS04 ;IS RDY SET?
BPL 5$ ;WAIT FOR WRITE TO FINISH
2$: CLR RS04 ;SET DSK ADDRESS TO 0
CLR OUTBUF ;WRITE 0
MOV @#0BUFSV,RSBA ;SET UP CURRENT ADDRESS
MOV #-1,RSWC ;LOAD WC
3$: MOV #172060,R2 ;PUT RSLA ADDR INTO R2
MOV (R2),R3 ;GET LA AND WAIT FOR
BIC #77,R3 ;SECTOR 40
CMP #4000,R3 ;BEFORE
BNE 3$ ;WRITING
MOV #61,RS04 ;GO WRITE
6$: MOV (R2),R3 ;GET RSLA AND WAIT FOR
BIC #17,R3 ;MIDDLE OF SECTOR
CMP #20,R3 ;0 BEFORE EXECUTING
BNE 6$ ;A CLEAR FUNCTION
MOV #40,RS04 ;CLEAR ALL REG. DO IT THIS WAY
MOV UNNUM,RS04 ;DO NOT USE TRAP

```

3521	013036	105777	166042		INCR:	TSTB	DRSCS1	: IS BUSY CLEARED
3522	013042	100401				BMI	5\$: FLAG CLEARED
3523	013044	104001				HLT	!CS1	: RDY NEVER CAME UP
3524	013046	013777	001102	166036	6\$:	MOV	DR#0BUFSV,DRSBA	: SET UP CURRENT ADDRESS
3525	013054	016777	166124	166026		MOV	WORK2,DRSWC	: LOAD WC
3526	013062	012777	000071	166014		MOV	#71,DRSCS1	: GO READ
3527	013070	105777	166010		5\$:	TSTB	DRSCS1	: IS RDY SET?
3528	013074	100375				BPL	5\$: WAIT FOR READ TO FINISH
3529	013076	022777	100000	166014		CMP	#100000,DRSER	: DID DCK SET?
3530	013104	001401				BEQ	.+4	: YES
3531	013106	104002				HLT	!ER	: DCK DID NOT SET
3532	013110	022777	150600	166000		CMP	#150600,DRSDS	: DID ERR SET?
3533	013116	001401				BEQ	.+4	: YES
3534	013120	104040				HLT	!DS	: ER DID NOT SET BY DCK
3535	013122	022777	144270	165754		CMP	#144270,DRSCS1	: IS CS1 CORRECT?
3536	013130	001401				BEQ	.+4	: YES
3537	013132	104044				HLT	!DS!DA	
3538	013134	017700	165746			MOV	DRSCS2,BAD	: GET CS2
3539	013140	016701	166012			MOV	UNNUM,GOOD	: GET UNIT #
3540	013144	052701	000100			BIS	#100,GOOD	: SET IR
3541	013150	020100				CMP	GOOD,BAD	: IS CS2 CORRECT?
3542	013152	001401				BEQ	.+4	: YES
3543	013154	104000				HLT		
3544	013156	012701	177700			MOV	#177700,GOOD	: NO
3545	013162	017700	165722		1\$:	MOV	DRSWC,BAD	: DID TRANSFER STOP AT END OF SECTOR?
3546	013166	020100				CMP	GOOD,BAD	
3547	013170	001401				BEQ	.+4	: YES
3548	013172	104000				HLT		: NO
3549	013174	012701	024550			MOV	#OUTBUF,GOOD	: GET BA
3550	013200	005767	165762			TST	RS04DT	: RS04?
3551	013204	001003				BNE	2\$: YES
3552	013206	062701	000200			ADD	#200,GOOD	: RS03
3553	013212	000402				BR	3\$	
3554	013214	062701	000400		2\$:	ADD	#400,GOOD	: GET CORRECT ANS FOR BA
3555	013220	017700	165666		3\$:	MOV	DRSBA,BAD	: GET BA
3556	013224	020001				CMP	BAD,GOOD	: IS BA CORRECT?
3557	013226	001401				BEQ	.+4	: YES
3558	013230	104000				HLT		: NO
3559	013232	104414				CLROK		: CLEAR ALL RS REG
3560	013234	005777	165660			TST	DRSER	: DID DCK CLEAR?
3561	013240	001401				BEQ	.+4	: YES
3562	013242	104002				HLT	!ER	: DCK DID NOT CLEAR WITH CLEAR
3563	013244	012777	177500	165636		MOV	#177500,DRSWC	: CLEAR DCK ON
3564	013252	013777	001102	165632		MOV	DR#0BUFSV,DRSBA	: DRIVE BY WRITING
3565	013260	012777	000061	165616		MOV	#61,DRSCS1	: GOOD DATA
3566	013266	105777	165612		4\$:	TSTB	DRSCS1	: ON DRIVE
3567	013272	100375				BPL	4\$	

```

3568 ;TEST THE ABILITY OF THE DISK CONTROL TO
3569 ;INCREMENT THE TRACK REGISTER.
3570
3571 ;A ONE WORD WRITE WILL BE EXECUTED
3572 ;RSDA=7777 RSWC = -1
3573 ;AT THE COMPLETION OF THE WRITE RSDA = 10000
3574 ;*****
3575 ;TEST 70 TEST DISK ADDRESS REGISTER
3576 ;*****
3577 013274 104400 TST70: SCOPE
3578
3579 013276 104414 DKADR: CLRDK ;CLEAR ALL RS REG
3590 013300 012777 177777 165602 MOV #177777,@RSWC ;SET WORD COUNT TO -1
3581 013306 013777 001102 165576 MOV @#0BUFSV,@RSBA ;SET UP CURRENT ADDRESS
3582 013314 012777 007777 165572 MOV #7777,@RSDA ;SET RSDA TO ALL ONES
3583 013322 012777 000061 165554 MOV #61,@RSCS1 ;GO WRITE ONE WORD
3584 013330 004767 010722 JSR PC,WAITRY ;WAIT FOR READY
3585 013334 104001 HLT !CS1 ;RDY DID NOT COME UP
3586 013336 027727 165552 010000 5$: CMP @RSDA,#10000 ;DOES RSDA=0
3587 013344 001401 BEQ +4 ;RSDA OK
3588 013346 104004 HLT !DA ;DA DID NOT INCREMENT
3589
3590 ;*****
3591 ;TEST 71 TEST IAE ERROR
3592 ;*****
3593 013350 104400 TST71: SCOPE
3594
3595 ;IAE ERROR SHOULD SET ERR,ATA AND SC BITS
3596
3597 013352 104414 IAERR: CLRDK ;CLEAR ALL RS REG
3598 013354 012777 177777 165526 MOV #177777,@RSWC ;SET WC TO -1
3599 013362 013777 001102 165522 MOV @#0BUFSV,@RSBA ;SET UP BUS ADDRESS
3600 013370 012777 017777 165516 MOV #177777,@RSDA ;SET DA TO RECEIVE ERROR
3601 013376 012777 000061 165500 MOV #61,@RSCS1 ;GO WRITE ONE WD
3602 013404 105777 165474 7$: TSTB @RSCS1 ;TEST FOR ERR OR RDY
3603 013410 100401 BMI +4 ;OK CONT.
3604 013412 000774 BR 7$ ;WAIT
3605 013414 022777 002000 165476 CMP #2000,@RSER ;DID IAE SET?
3606 013422 001401 BEQ +4 ;YES
3607 013424 104002 HLT !ER ;IAE SHOULD BE SET
3608 013426 022777 150600 165462 CMP #150600,@RSDS ;DID ERR SET?
3609 013434 001401 BEQ +4 ;YES
3610 013436 104140 HLT !DS!AS ;ERR SHOULD BE SET
3611 013440 022777 144260 165436 CMP #144260,@RSCS1 ;DID SC SET?
3612 013446 001401 BEQ +4 ;YES
3613 013450 104001 HLT !CS1 ;SC SHOULD BE SET
3614 013452 104414 CLRDK ;CLEAR ALL RS REG
3615 013454 005777 165440 TST @RSER ;CLR ERRORS?
3616 013460 001401 BEQ +4 ;YES
3617 013462 104002 HLT !ER ;ERR DID NOT CLR WITH 40 IN CS2

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3618 ; IN THIS ROUTINE THE PROGRAM WILL GENERATE A
3619 ; NON-EXISTENT DISK ERROR
3620
3621 ; *****
3622 ; TEST 72 TEST FOR NON-EXISTENT DISK ERROR
3623 ; *****
3624 TST72: SCOPE
3625
3626 013464 104400 NEDTST: CLROK ; CLEAR ALL RS REG
3627 013470 012767 000401 165502 MOV #401,WORK ; SET UP FOR N.E.D. NUMBER
3628 013476 005001 CLR GOOD ; LOOK FOR NON EXISTENT DRIVES
3629 013500 036767 165474 165452 1$: WORK,UNITSV ; ON THE SYSTEM
3630 013506 001405 BEQ 3$ ; FOUND NON EXISTENT DRIVE
3631 013510 005201 INC GOOD ; CONTAINS UNIT #
3632 013512 006167 165462 ROL WORK ; KEEP LOOKING
3633 013516 103452 BCS NEDDON ; COULD NOT FIND ANY NON EXISTENT DRIVES
3634 013520 000767 1$ BR ; LOOK FOR NED
3635 013522 010177 165360 3$: MOV GOOD,RS2 ; LOAD NED IN CS2
3636 013526 005077 165362 CLR ARSDA ; WRITE DRIVE REG
3637 013532 005777 165362 TST ARSER ; DID ANY BITS SET IN RSER?
3638 013536 001401 BEQ .+4 ; NO
3639 013540 104040 HLT DS ; WHY DID RSER CHANGE?
3640 013542 017700 165340 MOV ARSCS2,BAD ; GET CS2
3641 013546 052701 010100 BIS #10100,GOOD ; SET NED AND IR
3642 013552 020100 CMP GOOD,BAD ; IS CS2 CORRECT?
3643 013554 001401 BEQ .+4 ; YES
3644 013556 104000 HLT ; GOOD=CORRECT CS2 BAD=CS2
3645 013560 022777 160200 165316 CMP #160200,ARSCS1 ; IS CS1 CORRECT?
3646 013566 001401 BEQ .+4 ; YES
3647 013570 104200 HLT CS2 ; TRE SHOULD SET BY NED ERROR
3648 013572 005777 165324 TST ARSAS ; DID ANY BITS SET?
3649 013576 001401 BEQ .+4 ; NO
3650 013600 104100 HLT AS ; WHY DID AT BITS SET?
3651 013602 112777 000100 165334 MOVB #100,ARSCS1B ; CLEAR TRE
3652 013610 032777 010000 165270 BIT #NED,ARSCS2 ; DID NED CLEAR
3653 013616 001401 BEQ .+4 ; YES
3654 013620 104200 HLT CS2 ; NED DID NOT CLEAR
3655 013622 017767 165266 165350 MOV ARSDA,WORK ; READ DRIVE REG
3656 013630 032777 010000 165250 BIT #NED,ARSCS2 ; DID NED SET?
3657 013636 001001 BNE .+4
3658 013640 104040 HLT DS ; NED DID NOT SET
3659 013642 000431 BR NNDD ; GET OUT
3660 013644 032767 010000 165312 NEDDON: BIT #BIT12,ONCEE ; DWAS THIS TYPED BEFORE?
3661 013652 001025 BNE NNDD ; YES
3662 013654 104402 013660 TYPE .+2 ; ASCIZ <15><12>"COULD NOT FIND A NON-EXISTENT DRIVE"
3663 013726 052767 010000 165230 NNDD: BIS #BIT12,ONCEE ; SET TYPED FLAG

```

```

3664
3665
3666
3667 013734 104400
3668
3669 013736 104414
3670 013740 012777 177577 165142
3671 013746 005767 165214
3672 013752 001003
3673 013754 012777 177677 165126
3674 013762 012777 007777 165124
3675 013770 013777 001102 165114
3676 013776 012777 000061 165100
3677 014004 004767 010246
3678 014010 104001
3679 014012 022777 001000 165100
3680 014020 001401
3681 014022 104002
3682 014024 022777 152600 165004
3683 014032 001401
3684 014034 104040
3685 014036 005777 165042
3686 014042 100401
3687 014044 104001
3688 014046 104414
3689 014050 022777 010600 165040
3690 014056 001401
3691 014060 104040
3692 014062 005777 165032
3693 014066 001401
3694 014070 104002

```

```

:*****
:TEST 73 TEST THAT DAO IN RSER AND LBT IN RSDS DO SET
:*****
TS773: SCOPE

DAOTST: CLDK
MOV #-201, @RSWC ;CLEAR ALL RS REG
TST R504D ;LOAD WC FOR R504
BNE IS ;IS THIS A R504?
MOV #-101, @RSWC ;YES
MOV #7777, @RSDA ;NO
MOV @#0BUF5V, @RSBA ;SET RSDA=TO ALL ONES
MOV #61, @RSCS1 ;CURRENT ADDRESS=OUTBUF
JSR PC, WAITRY ;WRITE
HLT !CS1 ;WAIT FOR READY
CMP #1000, @RSER ;RDY DID NOT SET
BEQ +4 ;DID DAO SET?
HLT !ER ;YES
CMP #152600, @RSDS ;DAO DID NOT SET
BEQ +4 ;DID LBT SET?
HLT !DS ;YES
TST @RSCS1 ;LBT DID NOT SET
BMI +4 ;IS ERROR FLAG SET
HLT !CS1 ;ERROR IS SET
CLDK ;SC DID NOT SET
CMP #10600, @RSDS ;CLEAR ALL RS REG
BEQ +4 ;DID ATA +LBT CLEAR
HLT !DS ;YES
TST @RSER ;ATA DID NOT CLEAR BY CLR BIT
BEQ +4 ;DID DAO CLEAR?
HLT !ER ;YES
;DAO DID NOT CLEAR WITH CLEAR

```

```

3695
3696
3697
3698 014072 104400
3699
3700 014074 104414
3701 014076 012777 177601 165004
3702 014104 005767 165056
3703 014110 001003
3704 014112 012777 177701 164770
3705 014120 012777 007777 164766
3706 014126 013777 001102 164756
3707 014134 012777 000061 164742
3708 014142 004767 010110
3709 014146 104001
3710 014150 005777 164744
3711 014154 001401
3712 014156 104002
3713 014160 022777 012600 164730
3714 014166 001401
3715 014170 104040
3716 014172 005777 164706
3717 014176 100001
3718 014200 104001
3719 014202 104414
3720 014204 022777 010600 164704
3721 014212 001401
3722 014214 104040

```

```

;*****
;TEST 74 TEST THAT LBT DOES SET AND DAO DOES NOT
;*****
TST74: SCOPE
DAOTT: CLDK
MOV #-177, @R5WC ;CLEAR ALL RS REG
TST R504D1 ;LOAD WC FOR R504
BNE 1$ ;IS THIS A R504?
MOV #-77, @R5WC ;YES
1$: MOV #7777, @R5DA ;NO
2$: MOV @#0BUF5V, @R5BA ;SET R5DA=TO ALL ONES
MOV #61, @R5CS1 ;CURRENT ADDRESS=OUTBUF
JSR PC, WAITRY ;WRITE
HLT !CS1 ;WAIT FOR READY
TST @R5ER ;RDY DID NOT SET
BEQ .+4 ;ANY ERRORS?
HLT !ER ;NO
CMP #12600, @R5DS ;YES
BEQ .+4 ;DID LBT SET?
HLT !DS ;LBT DID NOT SET
TST @R5CS1 ;IS ERROR FLAG SET
BPL .+4 ;NO
HLT !CS1 ;ERROR
CLDK ;CLEAR ALL RS REG
CMP #10600, @R5DS ;DID LBT CLEAR
BEQ .+4 ;YES
HLT !DS ;ATA DID NOT CLEAR BY CLR BIT

```

```

3723
3724
3725
3726 014216 104400
3727
3728 014220 104414
3729 014222 012777 177017 164670
3730 014230 017700 164666
3731 014234 016701 164722
3732 014240 042701 177400
3733 014244 020100
3734 014246 001401
3735 014250 104100
3736 014252 022777 104200 164624
3737 014260 001401
3738 014262 104040
3739 014264 016777 164672 164630
3740 014272 005777 164624
3741 014276 001401
3742 014300 104100
3743
3744 014302 022777 004200 164574
3745 014310 001401
3746 014312 104002
3747 014314 012767 177777 010226
3748 014322 013777 001102 164562
3749 014330 012777 177777 164552
3750 014336 012777 000071 164540
3751 014344 032777 000001 164532
3752 014352 001401
3753 014354 104001
3754 014356 105777 164522
3755 014362 100375
3756 014364 022777 144270 164512
3757 014372 001401
3758 014374 104002

```

```

;*****
;TEST 75 EXECUTE FUNCTION WITH ERROR BITS SET
;*****
TST75: SCOPE
ERTST: CLRDK ;CLEAR ALL RS REG
MOV #177017, @RSER ;LOAD ER
MOV @RSAS, BAD ;GET AS REG
MOV UNCMP, GOOD ;GET UNIT ATA BIT
BIC #177400, GOOD ;CLEAR JUNK
CMP GOOD, BAD ;IS AS REG CORRECT?
BEQ +4 ;YES
HLT !AS ;AS BIT SHOULD BE SET
CMP #104200, @RSCS1 ;DID ERAS SET IN CS1?
BEQ +4 ;YES
HLT !DS ;CS1 SHOULD =104200
MOV UNCMP, @RSAS ;CLEAR ATA BIT
TST @RSAS ;DID IT CLEAR?
BEQ +4 ;YES
HLT !AS ;COULD NOT CLEAR AS BIT
;BY LOADING A 1 INTO IT
CMP #4200, @RSCS1 ;DID SC CLEAR BY
BEQ +4 ;CLEARING ATA
HLT !ER ;NO
MOV #177777, OUTBUF ;DATA TO BE XFERRED
MOV @#0BUFSV, @RSBA ;SET UP CURRENT ADDRESS
MOV #-1, @RSMC ;LOAD WC WITH -1
MOV #71, @RSCS1 ;DO READ FUNCTION
BIT #1, @RSCS1 ;DID GO BIT CLEAR
BEQ +4 ;YES
HLT !CS1 ;GO BIT SHOULD BE CLEARED
;WAIT FOR READY
BPL 1$ ;WAIT
CMP #144270, @RSCS1 ;DID ERAS CLEAR BY SETTING GO BIT?
BEQ +4 ;YES
HLT !ER ;NO

```

3759	014376	017700	164504	MOV	RSRCS2,BAD	:GET CS2
3760	014402	012701	001100	MOV	#1100,GOOD	:GET CORRECT ANS
3761	014406	056701	164544	BIS	UNNUM,GOOD	:GET UNIT #
3762	014412	020100		CMP	GOOD,BAD	:IS CS2 CORRECT?
3763	014414	001401		BEQ	.+4	:YES
3764	014416	104000		HLT		:GOOD = WHAT CS2 SHOULD =
3765	014420	022777	150600 164470	CMP	#150600,RSRDS	:DID ERR BITS SET?
3766	014426	001401		BEQ	.+4	:NO
3767	014430	104040		HLT	:DS	:ERR BIT SHOULD BE 1
3768	014432	022777	177777 164450	CMP	#-1,RSRWC	:DID WC MOVE?
3769	014440	001401		BEQ	.+4	:NO
3770	014442	104010		HLT	:WC	:WC SHOULD = 1777777
3771	014444	005777	164444	TST	RSRSDA	:DID DA MOV
3772	014450	001401		BEQ	.+4	:NO
3773	014452	104004		HLT	:DA	:DA SHOULD =0
3774	014454	023777	001102 164430	CMP	#0BUFSV,RSRBA	:DID BA MOVE
3775	014462	001401		BEQ	.+4	:NO
3776	014464	104020		HLT	:BA	:BA MOVED
3777	014466	036777	164470 164426	BIT	UNCMP,RSRAS	:AS SHOULD BE SET
3778	014474	001001		BNE	.+4	:IS IT?
3779	014476	104100		HLT	:AS	:NO
3780	014500	022777	177017 164412	CMP	#177017,RSRER	:DID ER CHANGE?
3781	014506	001401		BEQ	.+4	:NO
3782	014510	104002		HLT	:ER	:ER SHOULD NOT CHANGE

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3783 :*****
3784 ;TEST 76 PAT AND MCPE TEST
3785 :*****
3786 014512 104400 †TST76: SCOPE
3787
3788 014514 104414 PATST: CLRDK ;CLEAR ALL RS REG
3789 014516 052777 000020 164362 BIS #BIT4, @RSCS2 ;SET PAT
3790 014524 005777 164400 TST @RSMR ;READ DRIVE REG
3791 014530 017700 164352 MOV @RSCS2, BAD ;GET CS2
3792 014534 012701 000120 MOV #120, GOOD ;MDPE SHOULD
3793 014540 056701 164412 BIS UNNUM, GOOD ;NOT SET
3794 014544 020100 CMP GOOD, BAD ;IS CS2 CORRECT?
3795 014546 001401 BEQ .+4 ;YES
3796 014550 104000 HLT ;BAD = CS2 GOOD = CORRECT ANS
3797 014552 012777 000010 164350 MOV #10, @RSMR ;CAUSE PAR TO SET IN RSER
3798 014560 022777 000010 164332 CMP #10, @RSER ;DID PAR SET?
3799 014566 001401 BEQ .+4 ;YES
3800 014570 104140 HLT ;AS!DS
3801 014572 017700 164324 MOV @RSAS, BAD ;GET AS REG
3802 014576 016701 164360 MOV UNCMP, GOOD ;GET UNIT ATA BIT
3803 014602 042701 177400 BIC #177400, GOOD ;CLEAR JUNK
3804 014606 020100 CMP GOOD, BAD ;IS AS REG CORRECT?
3805 014610 001401 BEQ .+4 ;YES
3806 014612 104100 HLT ;AS ;AS BIT SHOULD BE SET
3807 014614 022777 104200 164262 CMP #104200, @RSCS1 ;DID ERRS SET IN CS1?
3808 014622 001401 BEQ .+4 ;YES
3809 014624 104040 HLT ;DS ;CS1 SHOULD =104200
3810 014626 104414 CLRDK ;CLEAR RS REG
3811 014630 022777 004200 164246 CMP #4200, @RSCS1 ;IS CS1 CORRECT?
3812 014636 001401 BEQ .+4 ;CLEARING ATA
3813 014640 104002 HLT ;ER ;NO
3814 014642 017700 164240 MOV @RSCS2, BAD ;CHECK TO SEE
3815 014646 016701 164304 MOV UNNUM, GOOD ;IF PAT CLEARS
3816 014652 052701 000100 BIS #100, GOOD
3817 014656 020100 CMP GOOD, BAD
3818 014660 001401 BEQ .+4
3819 014662 104000 HLT ;PAT DID NOT CLEAR

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3820 ;*****
3821 ;TEST 77 SET PAT BIT AND LOAD FUNCTION
3822 ;*****
3823 014664 104400 †TST77: SCOPE
3824
3825 014666 104414 SETPAT: CLRDK ;CLEAR ALL REG
3826 014670 052777 000010 164210 BIS #BAI,‡RSCS2 ;SET BAI
3827 014676 012767 177777 007644 MOV #177777,OUTBUF ;DATA TO BE XFERED
3828 014704 013777 001102 164200 MOV ‡#OBUFSV,‡RSBA ;SET UP CURRENT ADDRESS
3829 014712 012777 177000 164170 MOV #-1000,‡RSWC ;LOAD WC WITH -1
3830 014720 052777 000020 164160 BIS #BIT4,‡RSCS2 ;SET PAT BIT
3831 014726 012777 000071 164150 MOV #71,‡RSCS1 ;DO READ FUNCTION
3832 014734 105777 164144 1$: TSTB ‡RSCS1 ;WAIT FOR READY
3833 014740 100375 BFL 1$ ;WAIT
3834 014742 022777 144270 164134 CMP #144270,‡RSCS1 ;DID CS1 GET LOADED?
3835 014750 001401 BEQ .+4 ;NO
3836 014752 104040 HLT !DS ;IT SHOULD NOT
3837 014754 023777 001102 164130 CMP ‡#OBUFSV,‡RSBA ;DID BA MOVE?
3838 014762 001401 BEQ .+4 ;NO
3839 014764 104020 HLT !BA ;YES
3840 014766 022777 177000 164114 CMP #-1000,‡RSWC ;DID WC MOVE?
3841 014774 001401 BEQ .+4 ;NO
3842 014776 104010 HLT !WC ;YES WHY?
3843
3844 ;*****
3845 ;TEST 100 DO FUNCTION THEN SET PAT BIT
3846 ;*****
3847 015000 104400 †TST100: SCOPE
3848 015002 104414 FUNDO: CLRDK ;CLEAR ALL REG
3849 015004 052777 000010 164074 BIS #BAI,‡RSCS2 ;SET BAI
3850 015012 012767 177777 007530 MOV #177777,OUTBUF ;DATA TO BE XFERED
3851 015020 013777 001102 164064 MOV ‡#OBUFSV,‡RSBA ;SET UP CURRENT ADDRESS
3852 015026 012777 177000 164054 MOV #-1000,‡RSWC ;LOAD WC WITH -1
3853 015034 012777 000071 164042 3$: MOV #71,‡RSCS1 ;DO A READ
3854 015042 100774 BMI 3$ ;WAIT FOR BUSY
3855 015044 052777 000020 164034 BIS #BIT4,‡RSCS2 ;SET PAT
3856 015052 105777 164026 2$: TSTB ‡RSCS1 ;WAIT FOR READY
3857 015056 100375 BFL 2$
3858 015060 022777 144270 164016 CMP #144270,‡RSCS1 ;DID MCPE SET?
3859 015066 001401 BEQ .+4 ;NO
3860 015070 104002 HLT !ER ;YES
3861 015072 017700 164010 MOV ‡RSCS2,BAD ;GET CS2
3862 015076 012701 000730 MOV #730,GOOD ;GET CORRECT ANS
3863 015102 056701 164050 BIS UNNUM,GOOD ;GET UNIT #
3864 015106 020100 CMP GOOD,BAD ;IS CS2 CORRECT?
3865 015110 001401 BEQ .+4 ;YES
3866 015112 104000 HLT ;GOOD = WHAT CS2 SHOULD =

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3867 015114 022777 010600 163774      CMP      #10600, @RS05      ;DID ERR BITS SET?
3868 015122 001401                      BEQ      .+4          ;NO
3869 015124 104040                      HLT      !DS         ;ERR BIT SHOULD BE 1
3870 015126 005777 163766      TST      @RSER       ;IS ER CLEAR?
3871 015132 001401                      BEQ      .+4          ;YES
3872 015134 104044                      HLT      !DS!DA     ;NO ERRORS SHOULD BE SET
3873 015136 104414                      CLRDK    ;CLEAR ALL RS REG
3874 015140 022777 004200 163736      CMP      #4200, @RSCS1 ;IS CS1 CORRECT?
3875 015146 001401                      BEQ      .+4          ;YES
3876 015150 104040                      HLT      !DS
3877
;*****
3878 ;TEST 101 TEST PAR BY SETTING PAT
3879 ;*****
3880 015152 104400      TST101: SCOPE
3881
3882 015154 104414      PATTST: CLRDK      ;CLEAR ALL RS REG
3883 015156 052777 000010 163722      BIS      #BAI, @RSCS2 ;SET BAI
3884 015164 012767 177777 007356      MOV      #177777, @OUTBUF ;DATA TO BE XFERED
3885 015172 013777 001102 163712      MOV      @#0BUFSV, @RSBA ;SET UP CURRENT ADDRESS
3886 015200 012777 177000 163702      MOV      #-1000, @RSWC ;LOAD WC WITH -1
3887 015206 012777 000061 163670      MOV      #61, @RSCS1 ;DO WRITE FUNCTION
3888 015214 105777 163664      1$: TSTB @RSCS1 ;WAIT FOR READY
3889 015220 100775                      BMI      1$          ;WAIT
3890 015222 052777 000020 163656      BIS      #BIT4, @RSCS2 ;SET PAT
3891 015230 105777 163650      2$: TSTB @RSCS1 ;WAIT FOR READY
3892 015234 100375                      BPL      2$
3893 015236 022777 144260 163640      CMP      #144260, @RSCS1 ;DID MCPE SET?
3894 015244 001401                      BEQ      .+4          ;NO
3895 015246 104002                      HLT      !ER         ;YES
3896 015250 017700 163632      MOV      @RSCS2, BAD ;GET CS2
3897 015254 012701 000230      MOV      #230, GOOD ;GET CORRECT ANS-- DO NOT CHECK IR - REASON 50 CYCLE
3898 015260 056701 163672      BIS      UNNUM, GOOD ;GET UNIT #
3899 015264 042700 000100      BIC      #BIT6, BAD ;CLEAR IR BIT FOR CS2 COMPARE
3900 015270 020100      CMP      GOOD, BAD ;IS CS2 CORRECT?
3901 015272 001401                      BEQ      .+4          ;YES
3902 015274 104000                      HLT      ;GOOD = WHAT CS2 SHOULD =
3903 015276 022777 150600 163612      CMP      #150600, @RS05 ;DID ERR BITS SET?
3904 015304 001401                      BEQ      .+4          ;NO
3905 015306 104040                      HLT      !DS         ;ERR BIT SHOULD BE 1
3906 015310 022777 000010 163602      CMP      #10, @RSER ;DID PAR SET?
3907 015316 001401                      BEQ      .+4          ;YES
3908 015320 104044                      HLT      !DS!DA     ;NO
3909 015322 104414      CLRDK    ;CLEAR ALL RS REG
3910 015324 022777 004200 163552      CMP      #4200, @RSCS1 ;IS CS1 CORRECT?
3911 015332 001401                      BEQ      .+4          ;YES
3912 015334 104040                      HLT      !DS
3913 015336 005777 163556      TST      @RSER      ;DID PAR CLEAR?
3914 015342 001401                      BEQ      .+4          ;YES
3915 015344 104040                      HLT      !DS

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3916 :*****
3917 :TEST 102 TEST THE ABILITY TO FILL THE LAST SECTOR
3918 :*****
3919 015346 104400 TST102: SCOPE
3920
3921 015350 104414 LASTSC: CLRDK ;CLEAR ALL RS REG
3922 015352 012777 007777 163534 MOV #7777,@RSDA ;SET RSDA=TO ALL ONES
3923 015360 012777 177700 163522 MOV #-100,@R5WC ;WORD COUNT=-100
3924 015366 005767 163574 TST R504D1 ;IS THIS A RS04?
3925 015372 001403 BEQ 15 ;NO
3926 015374 012777 177600 163506 MOV #-200,@R5WC ;YES
3927 015402 013777 001102 163502 15: MOV @#0BUF5V,@R5BA ;CURRENT ADDRESS=OUTBUF
3928 015410 012777 000061 163466 MOV #61,@R5CS1 ;WRITE
3929 015416 004767 006634 JSR PC WAITRY ;WAIT FOR READY
3930 015422 104001 HLT !CS1 ;RDY DID NOT SET
3931 015424 005777 163470 TST @R5ER ;DID ANY ERROR BITS SET?
3932 015430 001401 BEQ +4 ;NO
3933 015432 104002 HLT !ER ;GOT AN ERROR
3934 015434 022777 012600 163454 CMP #12600,@R5DS ;DID LBT SET?
3935 015442 001401 BEQ +4 ;YES
3936 015444 104040 HLT !DS ;LBT DID NOT SET
3937 015446 005777 163432 TST @R5CS1 ;IS ERROR FLAG SET
3938 015452 100001 BPL +4 ;ERROR IS SET
3939 015454 104001 HLT !CS1 ;SC DID NOT SET
3940 015456 104414 CLRDK ;CLEAR ALL RS REG
3941 015460 022777 010600 163430 CMP #10600,@R5DS ;DID ATA +LBT CLEAR
3942 015466 001401 BEQ +4 ;YES
3943 015470 104040 HLT !DS ;ATA DID NOT CLEAR BY CLR BIT

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3944
3945
3946
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3950
3951 015472 104400
3952
3953 015474 104414
3954 015476 012767 177777 007044
3955 015504 013777 001102 163400
3956 015512 012777 177600 163370
3957 015520 005767 163442
3958 015524 001003
3959 015526 012777 177700 163354
3960 015534 052777 000010 163344
3961 015542 012777 000061 163334
3962 015550 105777 163330
3963 015554 100375
3964 015556 005077 163332
3965 015562 012767 177777 006760
3966 015570 013777 001102 163314
3967 015576 012777 177777 163304
3968 015604 052777 000010 163274
3969 015612 012777 000061 163264
3970 015620 105777 163260
3971 015624 100375
3972 015626 042777 000010 163252
3973 015634 005767 163326
3974 015640 001404
3975 015642 012767 000200 163330
3976 015650 000403
3977 015652 012767 000100 163320
3978 015660 013701 001102
3979 015664 012721 177777
3980 015670 005021
3981 015672 005367 163302
3982 015676 001374
3983 015700 005077 163210
3984 015704 013777 001102 163200
3985 015712 005767 163250
3986 015716 001404
3987 015720 012777 177600 163162
3988 015726 000403
3989 015730 012777 177700 163152
3990 015736 012777 000051 163140

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:FILL SECTOR WITH ALL ONES.
:NOW WRITE 1ST WORD IN SECTOR
:TEST REMAINING 63 WORDS FOR 0

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:*****
:TEST 103 TEST FOR ZERO'S IN PARTIAL FILLED SECTOR
:*****

```

TST103: SCOPE

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SECT: CLRDK          :CLEAR ALL RS REG
MOV      #-1,OUTBUF  :PUT - INTO OUTBUF
MOV      2#0BUFV,RSBA :SET UP CURRENT ADDR
MOV      #-200,RSWC   :LOAD WC FOR RS04
TST      RS04DT       :RS04?
BNE      5$           :YES
MOV      #-100,RSWC   :SET WORD COUNT TO -100
5$: BIS    #BAI,RS0S2  :SET BAI BIT
MOV      #61,RS0S1    :WRITE
3$: TSTB   RS0S1       :IS RDY SET?
BPL      3$          :NO
CLR      RS0A         :SET DSK ADDRESS TO 0
MOV      #-1,OUTBUF  :PUT 177777 INTO OUTBUF
MOV      2#0BUFV,RSBA :SET UP CURRENT ADDR
MOV      #-1,RSWC    :SET WORD COUNT TO -1
BIS      #10,RS0S2   :SET BAI BIT
MOV      #61,RS0S1   :WRITE
1$: TSTB   RS0S1       :IS RDY SET?
BPL      1$          :NO
BIC      #10,RS0S2   :CLEAR BAI BIT
TST      RS04DT       :RS04?
BEQ      7$          :NO
MOV      #200,WORK    :YES
BR       8$          :CONT
7$: MOV    #100,WORK   :SET UP BUFFER
8$: MOV    2#0BUFV,R1  :GET STARTING ADD OF BUF
MOV      #-1,(R1)+    :LOAD FIRST WD WITH -1
6$: CLR    (R1)+       :LOAD REST WITH 0
DEC      WORK         :DONE YET?
BNE      6$          :NO
CLR      RS0A         :SET DSK ADDRESS TO 0
MOV      2#0BUFV,RSBA :SET UP CURRENT ADDR
TST      RS04DT       :RS04?
BEQ      9$          :NO
MOV      #-200,RSWC   :YES
BR       10$         :CONT
9$: MOV    #-100,RSWC  :SET WORD COUNT TO -100
10$: MOV   #51,RS0S1  :WRITE CHECK

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3991 015744 032777 000200 163132 25: BIT #200,DRSCS1 ;IS RDY SET?
3992 015752 001774 BEG 25 ;NO
3993 015754 016701 163176 MOV UNNUM,GOOD ;GET UNIT #
3994 015760 052701 000100 BIS #100,GOOD ;SET IR BIT
3995 015764 017700 163116 MOV DRSCS2,BAD ;GET CS2
3996 015770 020100 CMP GOOD,BAD ;IS CS2 CORRECT?
3997 015772 001401 SEQ +4 ;YES
3998 015774 104002 HLT ;ER ;THERE WAS A WRITE CHECK ERROR
3999
4000 ;*****
4001 ;TEST 104 IF MEMORY MANAGEMENT IS AVAILABLE CHECK THE EXTENDED MEMORY ADDR
4002 ;*****
4003 015776 104400 TST104: SCOPE
4004
4005 016000 104414 EXTST: CLRDK ;CLEAR ALL RS REG.
4006 016002 016767 002460 163160 MOV TIMES,TIMSV ;SAVE LOOP #
4007 016010 012767 000010 002450 MOV #10,TIMES ;LOOP 10 TIMES
4008 016016 012767 016522 161760 MOV #EXTRP,4 ;SETUP TIMEOUT TRAP
4009 016024 012767 000340 161754 MOV #340,6
4010 016032 005737 177572 TST 2#SR0 ;IF MEMORY MANAGEMENT IS NOT
4011 ;AVAILABLE THE PROGRAM WILL TRAP
4012 ;AND TRANSFER TO END OF THE TEST
4013 016036 012767 016514 161740 MOV #EXTRP,4
4014 016044 012737 007600 172356 MOV #7600,2#KIPAR7 ;OPEN I/O REGISTERS
4015 016052 005037 172340 CLR 2#KIPAR0 ;FREE FIRST 4K
4016 016056 012737 000200 172342 MOV #200,2#KIPAR1 ;ENABLE SECOND 4K
4017 016064 012737 002000 172344 MOV #2000,2#KIPAR2
4018 016072 012737 177406 172300 MOV #400*256.-400+UP+RW,2#KIPDR0 ;SET KIPDR0=RW UP 400 BLOCKS
4019 016100 012737 177406 172302 MOV #400*256.-400+UP+RW,2#KIPDR1 ;SET KIPDR1=RW UP 400 BLOCKS
4020 016106 012737 177406 172304 MOV #400*256.-400+UP+RW,2#KIPDR2 ;SET KIPDR2=RW UP 400 BLOCKS
4021 016114 012737 177406 172316 MOV #400*256.-400+UP+RW,2#KIPDR7 ;SET KIPDR7=RW UP 400 BLOCKS
4022 016122 012737 000001 177572 MOV #1,2#SR0 ;TURN ON MEMORY MANAGEMENT
4023 016130 012702 040000 MOV #40000,R2 ;R2 EQUALS BASE ADDR

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E09

INDEC-11-DERSA-A
DERSAA.P11

TST104

RS11-REC3-RS04 BASIC FUNCTION DIAGNOSTIC
IF MEMORY MANAGEMENT IS AVAILABLE CHECK THE EXTENDED MEMORY ADDRESS BITS

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4024	016134	012712	177777			7\$:	MOV	#177777,(R2)	;INSERT PATTERN INTO 200000
4025	016140	012777	177776	162742			MOV	#-2,RSWC	;SETUP WORDCOUNT
4026	016146	012777	177777	162736			MOV	#177777,RSBA	;SETUP BUS ADDR
4027	016154	012777	000061	162722			MOV	#61,RSCSI	;WRITE TWO WORDS ON DISK. RSBA
4028									;STARTS AT 177777 TO FORCE CARRY
4029									;TO SET A16
4030	016162	105777	162716				TSTB	RSCSI	;WAIT FOR READY
4031	016166	100375					BPL	-4	
4032	016170	005777	162710				TST	RSCSI	
4033	016174	100002					BPL	1\$	
4034	016176	104046					HLT	!ER!DA!DS	;STATUS ERROR AFTER 2 WORD WRITE
4035	016200	000447					BR	2\$;USING MEXO
4036	016202	022777	004660	162674		1\$:	CMP	#4660,RSCSI	;IS CSI CORRECT
4037	016210	001402					BEQ	3\$;YES
4038	016212	104002					HLT	!ER	;CS2 DID NOT COMPARE
4039	016214	000441					BR	2\$	
4040	016216	005012				3\$:	CLR	(R2)	;CLEAR LOCATION 20000
4041	016220	005077	162670				CLR	RSBA	;SETUP DA
4042	016224	012777	177777	162660			MOV	#177777,RSBA	;SETUP BA
4043	016232	012777	177776	162650			MOV	#-2,RSWC	;SETUP WC
4044	016240	012777	000071	162636			MOV	#71,RSCSI	;READ TWO WORDS INTO LOCATIONS
4045									;177777 AND 200000.
4046	016246	105777	162632				TSTB	RSCSI	;WAIT FOR READY
4047	016252	100375					BPL	-4	
4048	016254	005777	162624				TST	RSCSI	;ANY ERRORS?
4049	016260	100002					BPL	4\$;BRANCH IF NO
4050	016262	104002					HLT	!ER	;ERROR AFTER READING 2 WORDS
4051	016264	000415					BR	2\$	
4052	016266	022777	004670	162610		4\$:	CMP	#4670,RSCSI	;IS CSI CORRECT?
4053	016274	001402					BEQ	5\$;YES
4054	016276	104002					HLT	!ER	;CSI DID NOT COMPARE
4055	016300	000407					BR	2\$;READ STARTING AT 177777
4056	016302	022712	177777			5\$:	CMP	#177777,(R2)	;WAS DATA READ INTO LOCATION
4057	016306	001404					BEQ	2\$;200000 CORRECTLY? - BRANCH IF YES
4058	016310	012701	177777				MOV	#177777,GOOD	
4059	016314	011200					MOV	(R2),BAD	
4060	016316	104000					HLT		;DATA COMPARE ERROR AT 200000
4061	016320	000240				2\$:	NOP		

F09

MAINDEC-11-DERSA-A
 DERSAA.P11 TST104

RS11-RSC3-R504 BASIC FUNCTION DIAGNOSTIC
 IF MEMORY MANAGEMENT IS AVAILABLE CHECK THE EXTENDED MEMORY ADDRESS BITS

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4062	016322	104414			EXTT1:	CLDK			;CLEAR ALL REG
4063	016324	012737	004000	172344		MOV	#4000, @KIPAR2		
4064	016332	012702	040000			MOV	#40000, R2		;R2 EQUALS THE BASE ADDR
4065	016336	012712	177777		7\$:	MOV	#177777, (R2)		;INSERT PATTERN INTO 40000C
4066	016342	012777	177777	162542		MOV	#177777, @RSBA		;SETUP BUS ADDR
4067	016350	012777	177776	162532		MOV	#-2, @RSWC		;LOAD WC
4068	016356	012777	000461	162520		MOV	#461, @RSCS1		;SET BIT A16 AND WRITE
4069	016364	105777	162514			TSTB	@RSCS1		;WAIT FOR READY
4070	016370	100375				BPL	-4		
4071	016372	005777	162506			TST	@RSCS1		;ANY ERRORS?
4072	016376	100001				BPL	4\$;BRANCH IF NO
4073	016400	104002				HLT	!ER		;ERROR AFTER READING 2 WORDS
4074	016402	022777	005260	162474	4\$:	CMP	#5260, @RSCS1		;IS CS1 CORRECT?
4075	016410	001401				BEQ	10\$;BRANCH IF YES
4076	016412	104002				HLT	!ER		;NO CS1 DID NOT COMPARE
4077									;READ STARTING AT 377777
4078	016414	005012			10\$:	CLR	(R2)		;CLEAR LOCATION 400000
4079									;READ TWO WORDS STARTING AT 377777
4080	016416	012777	177776	162464		MOV	#-2, @RSWC		;SETUP WC
4081	016424	005077	162464			CLR	@RSDA		;SETUP DA
4082	016430	012777	177777	162454		MOV	#177777, @RSBA		
4083	016436	012777	000471	162440		MOV	#471, @RSCS1		;CLEAR A 17 SET A16, READ
4084	016444	105777	162434			TSTB	@RSCS1		;WAIT FOR READY
4085	016450	100375				BPL	-4		
4086	016452	005777	162426			TST	@RSCS1		;ANY ERRORS?
4087	016456	100002				BPL	11\$;BRANCH IF NO
4088	016460	104002				HLT	!ER		;ERROR WHILE READING TWO WORDS
4089	016462	000414				BR	EXTRP		
4090	016464	022777	005270	162412	11\$:	CMP	#5270, @RSCS1		;IS CS1 CORRECT?
4091	016472	001401				BEQ	12\$;BRANCH IF YES
4092	016474	104002				HLT	!ER		;CS1 DID NOT COMPARE
4093									;READ STARTING AT 377777
4094	016476	022712	177777		12\$:	CMP	#177777, (R2)		;WAS DATA READ INTO LOCATION 400000
4095	016502	001404				BEQ	EXTRP		;CORRECTLY? - BRANCH IF YES
4096	016504	012701	177777			MOV	#177777, GOOD		
4097	016510	011200				MOV	(R2), BAD		
4098	016512	104000				HLT			;DATA COMPARE ERROR AT 400000 IF
4099									;RECEIVED=0 - LOCATION WASN'T ACCESSED
4100	016514	005037	177572		EXTRP:	CLR	@SRD		;TURN OFF MEMORY MANAGEMENT
4101	016520	000401				BR	EXT1		
4102	016522	000240			EXTRP:	NOP			;UPDATE TEST NUMBERS
4103	016524	012706	000500		EXT1:	MOV	#500, SP		;RESTORE STACK
4104	016530	012767	000006	161246	MEMOUT:	MOV	#6, 4		
4105	016536	005067	161244			CLR	6		

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4106 :*****
4107 :TEST 105 TEST PROGRAM INTERRUPT BY MOVING 300 INTO RSCS1
4108 :*****
4109 †TST105: SCOPE
4110
4111 QES: CLRDK ;CLEAR ALL DRIVES
4112 MOV #500,SP ;SETUP STACK
4113 MOV #PGTRAP,ARSVEC ;SET UP VECTOR
4114 MOV #340,ARVCPS ;SET TRAP PS
4115 MOV #200,APS ;SET PS AT PRIORITY 4
4116 MOV #300,ARCS1 ;THIS SHOULD CAUSE A TRAP
4117 MOV #500,WORK ;SETUP LOOP
4118 1$: DEC WORK ;DEC LOOP SHOULD
4119 BNE 1$ ;INTERRUPE BEFORE LOOP IS DONE
4120 HLT :CS1 ;SHOULD NEVER GET HERE
4121 JMP GESDON ;GET OUT
4122
4123 PGTRAP: CMP (6)+,(6)+ ;TRAP OK
4124 CMP #4200,ARCS1 ;DID IE CLEAR?
4125 BEQ ;YES
4126 HLT :CS1 ;IE SHOULD BE CLEARED
4127
4128 GESDON:
4129
4130 :*****
4131 :TEST 106 TEST THAT DISK DOES NOT INTERRUPT WHEN PS IS AT 5
4132 :*****
4133 †TST106: SCOPE
4134
4135 INTR5: MOV #500,SP ;SETUP STACK
4136 MOV TIMSV,TIMES ;RESTORE LOOP COUNTER
4137 CLRDK ;CLEAR ALL RS REG
4138 MOV #INT112,ARSVEC ;SET UP INTERRUPT VECTOR
4139 MOV #340,ARVCPS ;SET PRIO.
4140 MOV #240,APS ;LOCK OUT ALL INTERRUPTS ABOVE
4141 MOV APS,BAD ;GET PS
4142 MOV #17777,ARSWC ;SET WORD COUNT TO -1
4143 MOV #0BUNSV,AR5BA ;LOAD CURRENT ADDRESS
4144 MOV #161,ARCS1 ;GO WRITE (INTERRUPT ENABLED)
4145 JSR PC,WAITRY ;WAIT FOR READY
4146 HLT :CS1 ;NO ROY NEVER CAME UP
4147 BR INTDON ;RESTART ROUTINE
4148 ;PROCESSOR SHOULD NOT TRAP TO INT112
4149
4149 INT112: MOV #240,GOOD ;WHAT PS SHOULD HAVE
4150 HLT ;GOOD = CORRECT ANS FOR PS
4151 INTDON: ;DONE GET OUT

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4152 ;*****
4153 ;TEST 107 TEST THAT DISK DOES INTERRUPT WHEN PS IS AT 4
4154 ;*****
4155 016744 104400 TST107: SCOPE
4156
4157 016746 012706 000500 INTR4: MOV #500,SP ;SETUP STACK
4158 016752 104414 CLRDK ;CLEAR ALL RS REG
4159 016754 012777 017050 162156 MOV #INT114,RSVEC ;SET UP DISK TRAP VECTOR
4160 016762 012777 000340 162152 MOV #340,RSVCPS ;SET PRIO.
4161 016770 012737 000200 177776 MOV #200,PS ;SET PROCESSOR TO PRIORITY 4
4162 016776 013700 177776 MOV PS,BAD ;GET PS
4163 017002 012701 000200 MOV #200,GOOD ;GET CORRECT PS
4164 017006 012777 177777 162074 MOV #177777,RSWC ;SET WORD COUNT TO -1
4165 017014 013777 001102 162070 MOV #0BUFV,RSBA ;LOAD CURRENT ADDRESS
4166 017022 012777 000161 162054 MOV #161,RSCSI ;WRITE INTERRUPT ENABLE
4167 017030 005067 162144 CLR WORK
4168 017034 005267 162140 INC WORK ;WAIT FOR INTERRUPT TO OCCUR
4169 017040 001375 BNE -4
4170 017042 104000 HLT ;GOOD=CORRECT PS BAD=WRONG PS
4171 017044 104042 HLT !ER!DS
4172 017046 000405 BR DONINT ;CONT
4173 017050 022777 004260 162026 INT114: CMP #4260,RSCSI ;DID IE CLEAR?
4174 017056 001401 BEQ +4 ;YES
4175 017060 104001 HLT !CSI ;WHY DID NOT IE CLEAR
4176 017062
4177
4178 ;*****
4179 ;TEST 110 TEST INTERRUPT ON ERROR
4180 ;*****
4181 017062 104400 TST110: SCOPE
4182
4183 017064 012706 000500 ERINT: MOV #500,SP ;SETUP STACK
4184 017070 012737 000200 177776 MOV #200,PS ;SET PS AT PRI 4
4185 017076 012777 017154 162034 MOV #ERRINT,RSVEC ;SET UP INTERRUPT ADD.
4186 017104 104414 CLRDK ;CLEAR ALL RS REG
4187 017106 012777 000340 162026 MOV #340,RSVCPS ;SET PRIO.
4188 017114 012777 177777 161772 MOV #177777,RSDA ;SET RSDA=TO ALL ONES
4189 017122 012777 177600 161760 MOV #177600,RSWC ;WORD COUNT=-200
4190 017130 013777 001102 161754 MOV #0BUFV,RSBA ;CURRENT ADDRESS=OUTBUF
4191 017136 012777 000161 161740 MOV #161,RSCSI ;WRITE
4192 017144 004767 005106 JSR PC,WAITRY ;WAIT FOR READY
4193 017150 104042 IS: HLT !ER!DS ;Y DIDN'T PGM INTERRUPT IS RDY SET?
4194 017152 000406 BR FINTST ;GET OUT
4195 017154 022777 144260 161722 ERRINT: CMP #144260,RSCSI ;IS CSI RIGHT?
4196 017162 001401 BEQ +4 ;YES
4197 017164 104042 HLT !ER!DS
4198 017166 022626 CMP (6)+,(6)+ ;CLEAR STACK
4199 017170

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4200 :*****
4201 :TEST 111 DYNAMIC FUNCTION TEST
4202 :*****
4203 017170 104400 †TST111: SCOPE
4204 :EXECUTE FUNCTION MODIFY UNIT # AND DO A DRIVE SEARCH
4205 :DRIVE SEARCH WILL ONLY BE DONE IF THERE ARE AT LEAST 2 DRIVES
4206 :2ND DRIVE MAY NOT BE TESTED YET SO IF THIS TEST FAILS CHECK 2ND DRIVE
4207 :BEFORE TRYING TO DEBUG THIS TEST
4208
4209 017172 104414 MODNUM: CLRDK ;CLEAR ALL RS REG
4210 017174 016767 001266 161766 MOV TIMES,TIMSV ;SAVE LOOP COUNT
4211 017202 012767 000010 001256 MOV #10,TIMES ;LOOP ONLY 10 TIMES
4212 017210 005067 161766 CLR WORK1 ;CLEAR WORK LOC.
4213 017214 005003 CLR R3
4214 017216 005004 CLR R4
4215 017220 012702 020120 MOV #DVTAB,R2 ;SETUP TABLE
4216 017224 012767 000401 161746 MOV #401,WORK ;SETUP TO TEST FOR MORE DRIVES
4217 017232 036767 161742 161720 7$: BIT WORK,UNITSV ;IS DRIVE ON SYSTEM?
4218 017240 001403 BEQ 6$ ;NO
4219 017242 020467 161710 CMP R4,UNNUM ;IS THIS THE SAME DRIVE?
4220 017246 001017 BNE 8$ ;NO
4221 017250 005204 6$: INC R4 ;UPDATE 2ND UNIT #
4222 017252 000241 CLC
4223 017254 006167 161720 ROL WORK ;CHECK FOR NEXT DRIVE
4224 017260 103364 BCC 7$ ;NOT DONE YET
4225 017262 032767 000010 161712 BIT #BIT3,WORK1 ;MULTI DRIVE?
4226 017270 001016 BNE 12$ ;YES
4227 017272 016705 161660 MOV UNNUM,RS ;LOAD UNIT NO
4228 017276 005205 INC RS ;CHANGE IT
4229 017300 042705 177770 BIC #177770,RS ;CLEAR JUNK
4230 017304 000410 BR 12$
4231 017306 052767 000010 161666 8$: BIS #BIT3,WORK1 ;SET FOUND MULTI DRIVE
4232 017314 010422 MOV R4,(R2)+ ;LOAD UNT # INTO TABLE
4233 017316 005203 INC R3 ;COUNT # OF DRIVES
4234 017320 010367 161652 MOV R3,SAVEE ;SAVE IT
4235 017324 000751 BR 6$
4236 017326 012777 017442 161604 12$: MOV #TSTVEC,RSVEC ;SETUP INT. TRAP
4237 017334 012777 000340 161600 MOV #340,RSVCPS ;SETUP PRIO.
4238 017342 005067 005202 CLR OUTBUF ;CLR TO READ INTO
4239 017346 013777 001102 161536 MOV #0BUF SV,RSBA ;SET UP CURRENT ADDRESS
4240 017354 012777 177000 161526 MOV #-1000,RSWC ;SET WORD COUNT
4241 017362 012777 000060 161524 1$: MOV #60,RSDA ;LOAD DA
4242 017370 012702 020120 MOV #DVTAB,R2 ;GET TABLE
4243 017374 012777 000161 161502 MOV #161,RS CS1 ;GO WRITE
4244 017402 005703 TST R3 ;MORE THEN 1 DRIVE?
4245 017404 001003 BNE 13$ ;YES
4246 017406 010577 161474 MOV R5,RS CS2 ;NO MODIFY UNIT #
4247 017412 000407 BR 14$

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4248	017414	012277	161466		13\$:	MOV	(R2)+, @RSCS2	; LOAD UNIT#
4249	017420	012777	000131	161456		MOV	#131, @RSCS1	; DO SEARCH
4250	017426	005303				DEC	R3	; DONE ALL DRIVES YET?
4251	017430	001371				BNE	13\$; NO
4252	017432	012737	000200	177776	14\$:	MOV	#200, @#PS	; ENABLE INTERRUPTS
4253	017440	000001				WTOV:	WAIT	
4254	017442	016777	161510	161436	TSTVEC:	MOV	UNNUM, @RSCS2	; GET 1ST DRIVE
4255	017450	017700	161430			MOV	@RSCS1, BAD	; GET CS1
4256	017454	042700	100000			BIC	#BIT15, BAD	; CLEAR SC
4257	017460	012701	004260			MOV	#4260, GOOD	; GET CORRECT ANS
4258	017464	020100				CMP	GOOD, BAD	; IS CS1 CORRECT?
4259	017466	001402				BEQ	4\$; NO! X-FER OK
4260	017470	104000				HLT		; CS1 SHOULD = 14270 OR 4270
4261	017472	104140				HLT	!DS!AS	
4262	017474	005777	161410		4\$:	TST	@RSCS1	; TEST WC
4263	017500	001401				BEQ	.+4	; WORD COUNT DID OVERFLOW
4264	017502	104010				HLT	!WC	; SHOULD = 0
4265	017504	016701	161446			MOV	UNNUM, GOOD	; GET CORRECT
4266	017510	052701	000100			BIS	#100, GOOD	; ANS OF CS2
4267	017514	017700	161366			MOV	@RSCS2, BAD	; GET CS2
4268	017520	020100				CMP	GOOD, BAD	; IS CS2 CORRECT?
4269	017522	001401				BEQ	.+4	; YES
4270	017524	104000				HLT		; GOOD = CORRECT ANS FOR CS2
4271	017526	017700	161360			MOV	@RSBA, BAD	; FETCH CURRENT ADDRESS
4272	017532	013701	001102			MOV	@#OBUFSV, GOOD	; WHAT RSBA SHOULD EQUAL
4273	017536	062701	002000			ADD	#2000, GOOD	; UPDATE IT
4274	017542	020001				CMP	BAD, GOOD	; IS RSBA CORRECT
4275	017544	001401				BEQ	.+4	; YES EXECUTE CONTINUE
4276	017546	104000				HLT		; RSBA FAILED TO INCREMENT
4277	017550	005767	161412		2\$:	TST	RS04DT	; IS THIS A RS04?
4278	017554	001005				BNE	5\$; YES
4279	017556	022777	000070	161330		CMP	#70, @RSDA	; IS DA CORRECT?
4280	017564	001406				BEQ	3\$; YES
4281	017566	104104				HLT	!DA!AS	; DA NOT CORRECT
4282	017570	022777	000064	161316	5\$:	CMP	#64, @RSDA	; WAS RSDA INCREMENTED
4283	017576	001401				BEQ	.+4	; RSDA OK
4284	017600	104046				HLT	!DA!ER!DS	; RSDA SHOULD CONTAIN A 64
4285	017602	012777	040000	161274	3\$:	MOV	#TRE, @RSCS1	; CLEAR ALL ERRORS IF ANY
4286	017610	032767	000010	161364		BIT	#BIT3, WORK1	; MULTI DRIVE?
4287	017616	001461				BEQ	WTDV1	; NO
4288	017620	012777	017644	161312	1\$:	MOV	#TTVEC, @RSVEC	; SETUP INT FOR NEXT DRIVE
4289	017626	012716	017642			MOV	#WTDV2, (SP)	; GET WAIT
4290	017632	012777	000100	161244		MOV	#100, @RSCS1	; SET IE
4291	017640	000002				RTI		; RETURN
4292	017642	000001				WTDV2:	WAIT	

```

:SERVICE ROUTINE FOR SEARCH FUNCTIONS
4293          ;TTVEC: CLR R2 ;CLEAR UNIT #
4294 017644 005002          CLC
4295 017646 000241          MOV #401,WORK
4296 017650 012767 000401 161322          BIT WORK,ARSAS ;DID THIS DRIVE INT?
4297 017656 036777 161316 161236 15:      BNE 25 ;YES
4298 017664 001006          INC R2 ;UPDATE UNIT #
4299 017666 005202          CLC
4300 017670 000241          ROL WORK
4301 017672 006167 161302          BCC 15
4302 017676 103367          HLT AS ;WHY DID WE INT WITH NO ATA???
4303 017700 104100          MOV R2,ARSCS2 ;GET DRIVE
4304 017702 010277 161200 25:          CMP #110600,ARSOS ;DID PIP CLEAR?
4305 017706 022777 110600 161202          BEQ +4 ;YES
4306 017714 001401          HLT DS:AS ;PIP BIT DID NOT CLEAR
4307 017716 104140          CMP #104230,ARSCS1 ;DID SC SET?
4308 017720 022777 104230 161156          BEQ +4 ;YES
4309 017726 001401          HLT AS:DS ;SC DID NOT SET
4310 017730 104140          DEC SAVEE ;COUNT # OF INT
4311 017732 005367 161240          BEQ WTDV1 ;DONE YET?
4312 017736 001411          MOV WORK,ARSAS ;CLEAR AS
4313 017740 016777 161234 161154          MOV #100,ARSCS1 ;SET IE
4314 017746 012777 000100 161130          MOV #WTDV2,(SP) ;RETURN TO WAIT
4315 017754 012716 017642          RTI
4316 017760 000002          MOV #340,#PS
4317 017762 012737 000340 177776 WTDV1:  MOV #500,SP ;CLEAR STACK
4318 017770 012706 000500          MOV RSVCP5,ARSEVC ;RESTORE INT VECTOR
4319 017774 016777 161142 161136          CLR ARSVCPS
4320 020002 005077 161134          MODDON: SCOPE ;DONE
4321 020006 104400          MOV TIMSV,TIMES ;RESTORE LOOP COUNT
4322 020010 016767 161154 000450          MOV #340,#PS ;RESTORE PS
4323 020016 012737 000340 177776          MOV #1,ICNT ;FUGE TEST NUMBERS
4324 020024 012767 000001 160746          MOV #TRYNX ;TEST NEXT DRIVE
4325 020032 000137 001606          JMP $DONE - BELL AND SCOPE ROUTINE
4326          .SBTTL
4327
4328 020036 104400          DONE:  SCOPE ;TERMINATING SCOPE FOR LOOPING
4329 020040 062767 000001 160740          ADD #1,PCNT+2 ;ADD 1 TO THE PASS COUNT
4330 020046 005567 160732          ADC PCNT ;MAKE IT DOUBLE PREC.
4331 020052 032737 002000 177570          BIT #SW10,#SWR ;RING THE BELL?
4332 020060 001004          BNE 45 ;NO!
4333 020062 104402 020066          TYPE +2 ;ASCIZ <BELL><177>
4334 020072 013700 000042          45:  MOV #42,RO ;GET MONITOR ADDRESS
4335 020076 001405          BEQ SEND1 ;IF NONE
4336 020100 000005          RESET
4337 020102 004710          SENDAD: JSR 7,(0) ;GO TO MONITOR
4338 020104 000240 000240 000240          SEND1:  MOV 240,240,240 ;SAVE ROOM FOR ACT11
4339 020112 000137 001206          JMP #BEGIN ;RETURN
4340
4341 020116 000000          .TBIT: 0 ;T BIT FLAG
4342
4343 020120 000010          DVTAB: .BLKW 10

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4354 020140 010446
4355 020142 010546
4356 020144 017605 000004
4357 020150 032705 177400
4358 020154 001002
4359 020156 016605 000004
4360 020162 105715
4361 020164 001423
4362 020166 122715 000012
4363 020172 001012
4364 020174 116704 160615
4365 020200 116777 160610 160612
4366 020206 105777 160604
4367 020212 100375
4368 020214 005304
4369 020216 001370
4370 020220 112577 160574
4371 020224 105777 160566
4372 020230 100375
4373 020232 000753
4374 020234 017646 000004
4375 020240 062766 000002 000006
4376 020246 022666 000004
4377 020252 001006
4378 020254 062705 000002
4379 020260 042705 000001
4380 020264 010566 000004
4381 020270 012605
4382 020272 012604
4383 020274 000002
    
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.SBTTL $TYPE - TTY TYPEOUT ROUTINE

;THIS ROUTINE IS USE TO TYPE ASCII MESSAGES ON THE TTY. THE
;CALL CAN BE IN ONE OF 3 FORMS: 1) "TYPE ADR" - TYPES THE
;MESSAGE STARTING IN LOCATION "ADR:" 2) "TYPE CHAR" - TYPES
;THE ASCII "CHAR", AND 3) "PRINT <<15><12>"MESSAGE") - TYPES
;THE MESSAGE WHICH IS INLINE ASCII. THE FILLER CHARACTER WHICH IS
;TYPED AFTER A LINE FEED IS IN FILCHR AND THE NUMBER OF FILLERS
;IS IN FILCHR+1.

.TYPE: MOV R4,-(6) ;SAVE R4
MOV R5,-(6) ;SAVE R5
MOV @4(6),R5 ;GET ADDRESS TO BE TYPED
BIT #177400,R5 ;IS IT A TYPED?
BNE 1$ ;NO
MOV 4(6),R5 ;GET ADDRESS OF CHARACTER
1$: TSTB (R5) ;TERMINATOR?
BEQ 2$ ;GET OUT IF SO
CMPB #12,(R5) ;IS THE CHAR A LINE FEED
BNE 4$ ;NO - GET OUT
MOVB FILCHR+1,R4 ;GET THE FILL COUNT
5$: MOVB FILCHR,@TPB ;TYPE A FILLER
TSTB @TPB ;DONE YET?
BPL .-4 ;NO - WAIT
DEC R4 ;DEC COUNT
BNE 5$ ;LOOP UNTIL 0
MOVB (R5)+,@TPB ;LOAD AND TYPE THE CHARACTER
TSTB @TPB ;IS THE PRINTER READY
BPL .-4 ;WAIT UNTIL IT IS
BR 1$ ;GET THE NEXT CHARACTER
2$: MOV @4(6),-(6) ;GET ADDRESS TO BE TYPED
ADD #2,6(6) ;ADD 2 TO THE ADDRESS
CMP (6)+,4(6) ;IS IT .+2?
BNE 3$ ;NO
ADD #2,R5 ;ADD 2 TO THE ADDRESS
BIC #1,R5 ;BACK UP TO AN EVEN BYTE
MOV R5,4(6) ;RESTORE ADDRESS
3$: MOV (6)+,R5 ;RESTORE R5
MOV (6)+,R4 ;RESTORE R4
RTI ;RETURN
    
```

.SBTTL \$\$SCOPE - SCOPE LOOP HANDLER

; THIS ROUTINE HANDLES THE ITERATIONS, LOOPING, ERROR
; LOOPING, AND THE DISPLAYING OF THE TEST NUMBER.
; "SCOPE" IS PLACED BETWEEN EACH SUBTEST IN THE TEST AND
; RECORDS THE STARTING ADDRESS OF THE SUBTEST IN "LAD:"

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4394 4394 020276 032737 000400 177570 .SCOPE: BIT        #SW8,@#SWR        ; LOOP ON SPEC. TEST?
4395 4395 020304 001404                    BEQ        1$                    ; NO LOOP ON SPEC. TEST
4396 4396 020306 123767 177570 160464 .CMPB      @#SWR,ICNT        ; ON RIGHT TEST? *SW7-0*
4397 4397 020314 001453                    BEQ        .OVER                    ; NOT RIGHT TEST
4398 4398 020316 032737 040000 177570 1$: BIT        #SW14,@#SWR        ; LOOP ON TEST?
4399 4399 020324 001045                    BNE        .KIT                    ; LOOP ON TEST IS SET
4400 4400 020326 000416                    BR         3$                    ; SKIP - NOP FOR XOR TESTER
4401 4401 020330 013746 000004            MOV        @#4,-(6)            ; PUSH @#4 ON STACK
4402 4402 020334 012737 020354 000004 .MOV        #4$,@#4                ; SET FOR TIMEOUT
4403 4403 020342 005737 177060            TST        @#177060            ; ERROR ON XOR?
4404 4404 020346 012637 000004            MOV        (6)+,@#4            ; POP STACK INTO @#4
4405 4405 020352 000422                    BR         .SVLAD                ; NO ERROR - GO TO NEXT TEST
4406 4406 020354 022626                    .CMP        (6)+,(6)+            ; CLEAR STACK
4407 4407 020356 012637 000004            MOV        (6)+,@#4            ; POP STACK INTO @#4
4408 4408 020362 000426                    BR         .KIT                    ; ERROR - LOOP ON TEST
4409 4409 020364 032737 004000 177570 3$: BIT        #SW11,@#SWR        ; KILL ITERATIONS
4410 4410 020372 001012                    BNE        .SVLAD                ; YES - KILL ITERATIONS
4411 4411 020374 105767 160401            TSTB      ICNT+1                ; FIRST ONE?
4412 4412 020400 001404                    BEQ        2$                    ; BRANCH IF FIRST
4413 4413 020402 126767 000060 160371 .CMPB      TIMES,ICNT+1        ; DONE?
4414 4414 020410 003013                    BGT        .KIT                    ; BRANCH IF NOT
4415 4415 020412 112767 000001 160361 2$: MOVB      #1,ICNT+1            ; FIRST ITERATION
4416 4416 020420 105267 160354            .SVLAD: INCB      ICNT                    ; COUNT TEST NUMBERS
4417 4417 020424 011667 160360            MOV        (6) LAD                ; SAVE LOOP ADDRESS
4418 4418 020430 016737 160344 177570 .MOV        ICNT,@#DISPLAY        ; DISPLAY TEST NO. AND ITERATION COUNT
4419 4419 020436 000002                    RTI                                ; RETURN
4420 4420 020440 105267 160335            .KIT: INCB      ICNT+1                    ; INC THE ITERATION COUNT
4421 4421 020444 016737 160330 177570 .OVER: MOV        ICNT,@#DISPLAY        ; SET UP DISPLAY
4422 4422 020452 005767 160332            TST        LAD                    ; FIRST ONE?
4423 4423 020456 001760                    BEQ        .SVLAD                ; YES
4424 4424 020460 016716 160324            MOV        LAD,(6)                ; FUDGE RETURN ADDRESS
4425 4425 020464 000002                    RTI                                ; FIXES PS
4426 4426 020466 000100                    TIMES: 100                    ; RUN 100 TIMES

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4426                                     .SBTTL          $HLT - HLT ROUTINE (ERROR TYPEOUT)
4427
4428                                     ; THIS ROUTINE PRINTS OUT ERROR MESSAGES STARTING WITH THE
4429                                     ; ADDRESS OF THE "HLT". IT ALSO COUNTS THE NUMBER OF ERRORS
4430                                     ; AND HAS THE CAPABILITY OF LOOPING ON ERROR, BELL ON ERROR,
4431                                     ; "HALT" ON ERROR, AND INHIBIT TYPEOUTS. AN OPTIONAL ARGUMENT
4432                                     ; (HLT+3) WILL BE PLACED IN ".HLTCT:" FOR ADITIONAL TYPEOUTS.
4433
4434 020470 032737 002000 177570 .HLT:  BIT      #SW10,@#SWR      ; BELL ON ERROR?
4435 020476 001402                BEQ      1$              ; NO - SKIP
4436 020500 104402 000007                TYPE     ,BELL          ; RING BELL
4437 020504 005267 160272                1$:    INC      ERRORS      ; COUNT THE NUMBER OF ERRORS
4438 020510 032737 C30000 177570 .HLT:  BIT      #SW13,@#SWR      ; SKIP TYPEOUT IF SET
4439 020516 001025                BNE     2$              ; SKIP TYPEOUTS
4440 020520 104402 020524                TYPE     ,.+2          ; .ASCIZ <15><12>
4441 020530 011667 160256                MOV     (6),HLTADR     ; PUT ADDRESS OF INSTRUCTION ON STACK
4442 020534 162767 000002 160250        SUB     #2,HLTADR     ; FUDGE ADDRESS
4443 020542 117767 160244 000054        MOVB   @HLTADR,.HLTCT ; GET HLT ARGUMENT
4444 020550 016746 160236                MOV     HLTADR,-(6)   ; PUT HLTADR ON STACK
4445 020554 104404                TYPEO   ; TYPE STACK IN OCTAL
4446 020556 104402 020562                TYPE     ,.+2          ; .ASCIZ " "
4447 020566 004767 002752                JSR     PC,RSREG      ; GO TO USER ERROR ROUTINE
4448 020572 005737 177570                2$:    TST     @#SWR      ; HALT ON ERROR
4449 020576 100001                BPL     .+4           ; SKIP IF CONTINUE
4450 020600 000000                HALT   ; HALT ON ERROR!
4451 020602 032737 001000 177570 .HLT:  BIT      #SW9,@#SWR     ; CHECK FOR INHIBIT LOOP ON ERROR
4452 020610 001003                BNE     3$              ; SKIP IF LOOP ON ERROR
4453 020612 105067 160163                CLRB   ICNT+1        ; CLEAR ITERATION COUNT
4454 020616 000002                RTI    ; RETURN
4455 020620 000167 177614                3$:    JMP     .KIT      ; LOOP ON TEST UNTIL NO ERRORS
4456
4457 020624 000000                .HLTCT: 0              ; HLT ARGUMENT

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+158 .SBTTL SOCIAL - OCTAL TYPEOUT ROUTINE
+159
+160 :THIS ROUTINE IS USED TO TYPE AN OCTAL NUMBER ON THE TTY. IT WILL TYPE
+161 :ALL 6 CHARACTERS, SUPPRESS LEADING ZEROES, OR TYPE THE
+162 :16 BITS. IT IS CALLED VIA THE TYOCT, TYPBIT, OR TYPOCS MACRO'S.
+163
+164 020626 012767 170101 000160 .TYPEB: MOV #170101,.PR ;SET BIT FLAG AND 16. CHARACTER COUNT
+165 020634 000411 BR .PTIT ;NOW TYPE IT IN BIT FORM
+166 020636 112767 000001 000150 .TYPEO: MOVB #1,.PR ;SET ZERO FILL SWITCH
+167 020644 000402 BR .+6 ;SKIP
+168 020646 005067 000142 .TYPES: CLR .PR ;SUPPRESS LEADING ZERO'S
+169 020652 112767 177772 000135 MOVB #-6,.PR+1 ;SET COUNT
+170 020660
+171 020660 010446 MOV R4,-(6) ;PUSH R4 ON STACK
+172 020662 010546 MOV R5,-(6) ;PUSH R5 ON STACK
+173 020664 016605 000010 MOV 10(6),R5 ;GET THE DATA
+174 020670 012704 021016 MOV #.PR+2,R4 ;SET POINTER TO FIRST ASCII CHAR.
+175 020674 105014 CLRB (4) ;CLEAR FIRST BYTE
+176 020676 000411 BR .PRF ;ROTATE FIRST BIT
+177 020700 105014 .PRL: CLRB (4) ;CLEAR BYTE OF CHARACTER
+178 020702 032767 000100 000104 BIT #100,.PR ;BIT TYPING MODE?
+179 020710 001004 BNE .PRF ;YES - SKIP 2 ROTATES
+180 020712 006105 ROL R5 ;ROTATE BIT INTO C
+181 020714 106114 ROLB (4) ;PACK IT
+182 020716 006105 ROL R5 ;ROTATE BIT INTO C
+183 020720 106114 ROLB (4) ;PACK IT
+184 020722 006105 .PRF: ROL R5 ;ROTATE BIT INTO C
+185 020724 106114 ROLB (4) ;PACK IT
+186 020726 105714 TSTB (4) ;IS IT ZERO?
+187 020730 001402 BEQ .+6 ;SKIP INC
+188 020732 105267 000056 INCB .PR ;SET FILL SWITCH
+189 020736 105767 000052 TSTB .PR ;CHECK FILL SWITCH
+190 020742 001402 BEQ .+6 ;SKIP BITSET
+191 020744 152724 000060 BISB #'0,(4)+ ;MAKE INTO ASCII CHAR
+192 020750 105267 000041 INCB .PR+1 ;INC COUNT
+193 020754 001351 BNE .PRL ;REPEAT
+194 020756 022704 021016 CMP #.PR+2,R4 ;EMPTY BUFFER?
+195 020762 001002 BNE .+6 ;SKIP IF NOT
+196 020764 112724 000060 MOVB #'0,(4)+ ;LOAD 1 ZERO
+197 020770 105014 CLRB (4) ;NULL TERMINATOR
+198 020772 104402 021016 TYPE .PR+2 ;TYPE IT
+199 020776 012605 MOV (6)+,R5 ;POP STACK INTO R5
+500 021000 012604 MOV (6)+,R4 ;POP STACK INTO R4
+501 021002 016666 000002 000004 MOV 2(6),4(6) ;GET RID OF
+502 021010 012616 MOV (6)+,(6) ;DATA WORD
+503 021012 000002 RTI ;RETURN
+504
+505 021014 000012 .PR: .BLKW 12 ;COUNT, SWITCH, AND OUTPUT BUFFER

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4506          .SBTTL          SPOWER - POWER DOWN AND UP ROUTINES
4507
4508          ;THIS IS THE POWER FAIL ROUTINE WHICH WILL SAVE ALL
4509          ;THE GENERAL REGISTERS AND USER DEFINED REGISTERS THEN
4510          ;WAIT FOR POWER TO GO DOWN AND BE RESTORED.
4511          ;IF THERE ISN'T ENOUGH TIME FOR SAVING ALL THE REGISTERS,
4512          ;THE PROGRAM WILL HALT AT '.ILLUP'.
4513
4514          021040 012777 021164 000124 .POWER: MOV      #.ILLUP,0.PUVEC ;SET FOR FAST UP
4515          021046 012777 000340 000120      MOV      #340,0.PUVECS+2 ;PRIO:7
4516          021054 010046          MOV      R0,-(6) ;PUSH R0 ON STACK
4517          021056 010146          MOV      R1,-(6) ;PUSH R1 ON STACK
4518          021060 010246          MOV      R2,-(6) ;PUSH R2 ON STACK
4519          021062 010346          MOV      R3,-(6) ;PUSH R3 ON STACK
4520          021064 010446          MOV      R4,-(6) ;PUSH R4 ON STACK
4521          021066 010546          MOV      R5,-(6) ;PUSH R5 ON STACK
4522          021070 010667 000074          MOV      SP,.SAVR6 ;SAVE SP
4523          021074 012777 021104 000070          MOV      #.POWUP,0.PUVEC ;SET UP VECTOR
4524          021102 000000          HALT      ;WAIT FOR PF
4525
4526          021104 016706 000060          .PCWUP: MOV      .SAVR6,SP ;GET SP
4527          021110 005001          CLR      R1 ;WAIT LOOP FOR THE TTY
4528          021112 005201          1$: INC     R1 ;WAIT FOR THE INC
4529          021114 001376          BNE     1$ ;OF WORD
4530          021116 012605          MOV      (6)+,R5 ;POP STACK INTO R5
4531          021120 012604          MOV      (6)+,R4 ;POP STACK INTO R4
4532          021122 012603          MOV      (6)+,R3 ;POP STACK INTO R3
4533          021124 012602          MOV      (6)+,R2 ;POP STACK INTO R2
4534          021126 012601          MOV      (6)+,R1 ;POP STACK INTO R1
4535          021130 012600          MOV      (6)+,R0 ;POP STACK INTO R0
4536          021132 012737 021040 000024          MOV      #.POWER,0#24 ;SET UP THE POWER DOWN VECTOR
4537          021140 012737 000340 000026          MOV      #340,0#26 ;PRIO:7
4538          021146 104402 021152          TYPE    ..+2 ;ASCIZ (15)(12)"POWER"
4539          021162 000002          RTI     ;RETURN
4540
4541          021164 000000          .ILLUP: HALT ;THE POWER UP SEQUENCE WAS STARTED
4542          021166 000776          BR      -2 ;BEFORE THE POWER DOWN WAS COMPLETE
4543
4544          021170 000000          .SAVR6: 0 ;PUT THE SP HERE
4545          021172 000024 000026          .PUVEC: 24,26 ;POWER UP VECTOR

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4551 021175 C11646
4552 021200 016666 000004 000002
4553 021206 010146
4554 021210 010246
4555 021212 010346
4556 021214 104412
4557 021216 005001
4558 021220 012703 021420
4559 021224 112302
4560 021226 001417
4561 021230 122702 000060
4562 021234 003022
4563 021236 122702 000067
4564 021240 002417
4565 021244 006002
4566 021246 006002
4567 021250 006002
4568 021254 006101
4569 021258 006102
4570 021260 006101
4571 021262 006102
4572 021264 006101
4573 021266 000757
4574 021268 010166 000012
4575 021270 012603
4576 021272 012602
4577 021274 012601
4578 021300 000002
4579
4580 021302
4581 021302 104402 021306
4582 021312 000740

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.SBTTL \$RDOCT - OCTAL INPUT ROUTINE

: THIS ROUTINE CALLS RDLIN, INPUTS A LINE FROM THE TTY AND CONVERTS
: IT INTO AN OCTAL NUMBER WHICH IS THE FIRST WORD ON THE STACK.

```

.RDOCT: MOV (6),-(6) ; MOVE THE PC
MOV 4(6),2(6) ; MOVE THE PS
MOV R1,-(6) ; PUSH R1 ON STACK
MOV R2,-(6) ; PUSH R2 ON STACK
MOV R3,-(6) ; PUSH R3 ON STACK
45: RDLIN ; READ A LINE INTO INPUT
CLR R1 ; INIT DATA WORD
MOV #INPUT,R3 ; INIT POINTER
15: MOVB (3)+,R2 ; GET A BYTE
BEQ 2$ ; GET OUT IF ZERO
CMPB #'0,R2 ; CHECK FOR 0 OR GREATER
BGT 3$ ; ERROR - LESS THAN 0
CMPB #'7,R2 ; CHECK FOR 7 OR LESS
BLT 3$ ; ERROR - GREATER THAN 7
ROR R2 ; GET
ROR R2 ; INTO
ROR R2 ; POSITION
ROL R1 ; FIRST BIT
ROL R2 ; GET
ROL R1 ; SECOND BIT
ROL R2 ; GET
BR 1$ ; THIRD BIT
2$: MOV R1,12(6) ; LOOP
MOV (6)+,R3 ; SAVE THE RESULT
MOV (6)+,R2 ; POP STACK INTO R3
MOV (6)+,R1 ; POP STACK INTO R2
RTI ; POP STACK INTO R1
; RETURN
3$: TYPE 4$+2 ; .ASCIZ "??"(15)<(12)
BR 4$ ; TRY AGAIN

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021314 010546
021316 012705 021420
021322 022705 021440
021328 001412
021330 105737 177560
021334 100375
021336 113715 177562
021342 142715 000200
021346 122715 000177
021352 001005
021354 104402 021360
021364 000754
021366 111527 000000
021372 104402 021370
021376 122725 000015
021402 001347
021404 105065 177777
021410 104402 000012
021414 012605
021416 000002
021420 000020
021440 011646
021442 162716 000002
021446 017616 000000
021452 062716 115060
021456 013607
021460 020276
021462 020140
021464 020636
021466 020646
021470 021176
021472 021314
021474 023526

.SBTTL \$RDLIN - TTY INPUT ROUTINE

; THIS ROUTINE INPUTS A LINE TERMINATED BY A RETURN INTO ADDRESS
; INPUT AND RETURNS A LINE FEED. THE BUFFER HAS A NULL TERMINATOR
; INSTEAD OF THE RETURN. RUBOUTS ARE HANDLED BY RETYPING
; THE LINE. BUFFER OVERFLOW ERRORS LIKE A RUBOUT.

```
.RDLIN: MOV R5, -(6) ;SAVE R5
13: MOV #INPUT, R5 ;GET ADDRESS
25: CMP #INPUT+16., R5 ;BUFFER FULL?
; YES - TYPE "?"
; WAIT FOR
; A CHARACTER
; GET CHARACTER
; GET RID OF JUNK
; IS IT A RUBOUT
; SKIP IF NOT
45: BNE 35
; ASCIZ "?"(15)<(12)
; ZAP THE BUFFER AND LOOP
35: MOV (5), #0 ;SET UP FOR TYPING
; ECHO IT
; CHECK FOR RETURN
; LOOP IF NOT RETURN
; ZAP RETURN (THE 15)
; TYPE A LINE FEED
; RESTORE R5
; RETURN
; TYPE
; BR
; MOV
; TYPE
; CMPB
; BNE
; CLRB
; TYPE
; MOV
; RTI
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INPUT: .BLKB 16. ;TTY INPUT AREA
.SBTTL \$TRAP - TRAP HANDLER

; THIS ROUTINE DECODES A TRAP CALL AND JUMPS TO THE APROPRATE
; SUBROUTINE. THE CALL IS A "TRAP+N" WHERE N IS A MULTIPLE OF 2.
; THE "SET" MACRO WILL CREATE THE TABLE NEEDED. IT HAS TO
; FOLLOW THIS MACRO.

```
.TRAP: MOV (6), -(6) ;GET ADDRESS OF TRAP +2
SUB #2, (6) ;MAKE IT ADDRESS OF TRAP
MOV @((6)), (6) ;GET TRAP INSTRUCTION
ADD #.TRAP+2-TRAP, (6) ;GET DATA AND MAKE IT AN OFFSET
;TRP: MOV @((6))+, PC ;GO TO PROPER SUBROUTINE
```

.SCOPE = TRAP+0 (104400)
.TYPE = TRAP+2 (104402)
.TYPE0 = TRAP+4 (104404)
.TYPES = TRAP+6 (104406)
.RDOCT = TRAP+10 (104410)
.RDLIN = TRAP+12 (104412)
.CLROK = TRAP+14 (104414)

```

4633          ;ROUTINE TO ALLOW THE OPERATOR TO SET BITS
4634          ;IN THE I/O REGISTERS VIA THE SWITCH REGISTER
4635
4636          ;WORD COUNT REGISTER
4637 021476 016777 156064 157404 SRSWC: MOV     SWR, @R5WC      ;MOV SWR INTO WORD COUNT REG
4638 021504 017737 157406 177570      MOV     @R5WC, @#DISPLAY ;DISPLAY IN LIGHTS
4639 021512 000771          BR         SRSWC
4640
4641          ;CURRENT ADDRESS REGISTER
4642 021514 016777 156050 157370 SRSBA: MOV     SWR, @R5BA      ;MOV SWR INTO CURRENT ADDR REG
4643 021522 017737 157364 177570      MOV     @R5BA, @#DISPLAY ;SHOW IN LIGHTS
4644 021530 000771          BR         SRSBA
4645
4646          ;DISK ADDRESS REGISTER
4647 021532 016777 156032 157354 SRSDA: MOV     SWR, @R5DA      ;MOV SWR INTO DISK ADDR REG
4648 021540 017737 157350 177570      MOV     @R5DA, @#DISPLAY ;SHOW IN LIGHTS
4649 021546 000771          BR         SRSDA
4650
4651          ;DRIVE STATUS REGISTER
4652 021550 016777 156014 157340 SRSDS: MOV     SWR, @R5DS      ;MOV SWR INTO DRIVE STATUS
4653 021556 017737 157334 177570      MOV     @R5DS, @#DISPLAY ;SHOW IN LIGHTS
4654 021564 000771          BR         SRSDS
4655
4656          ;DRIVE ERROR REGISTER
4657 021566 016777 155776 157324 SRSER: MOV     SWR, @R5SER      ;LOAD ER REG
4658 021574 017737 157320 177570      MOV     @R5SER, @#DISPLAY ;DISPLAY IT IN LIGHTS
4659 021602 000771          BR         SRSER      ;LOOP
4660
4661          ;WATCH LOOK AHEAD REGISTER
4662 021604 017737 157314 177570 SRSLA: MOV     SWR, @R5SLA      ;SHOW IN LIGHTS
4663 021612 000774          BR         SRSLA

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4664					:RSCS2 REGISTER		
4665	021614	016777	155750	157264	SRC52: MOV	SWR, @RSCS2	:LOAD CS2
4666	021622	017737	157260	177570	MOV	@RSCS2, @#DISPLAY	:DISPLAY IT
4667	021630	000771			BR	SRC52	
4668							
4669					:RSAS REGISTER		
4670	021632	016777	155732	157262	SRAS: MOV	SWR, @RSAS	:LOAD RSAS
4671	021640	017737	157256	177570	MOV	@RSAS, @#DISPLAY	:DISPLAY IT
4672	021646	000771			BR	SRAS	
4673							
4674					:RSMR REGISTER		
4675	021650	016777	155714	157252	RSMRR: MOV	SWR, @RSMR	:LOAD RSMR
4676	021656	017737	157246	177570	MOV	@RSMR, @#DISPLAY	:DISPLAY IT
4677	021664	000771			BR	RSMRR	
4678							
4679					:DISK CONTROL STATUS REGISTER		
4680	021666	012737	000340	177776	SRSCS1: MOV	#340, @#PS	:LOCK UP INTERRUPTS
4681	021674	012777	177777	157206	MOV	#177777, @R5WC	:SET WORD COUNT -1 WORD
4682	021702	013777	001102	157202	MOV	@#BUFSV, @RSBA	:SET UP CURRENT ADDRESS
4683	021710	016777	155654	157166	MOV	SWR, @RSCS1	:MOV SWR INTO CONTROL REG
4684	021716	032777	000001	157160	BIT	@BIT0, @RSCS1	:IS FUNCTION BITS SET
4685	021724	001760			BEQ	SRSCS1	:FUNCTION BITS NOT SET
4686	021726	105777	157152		DKBUSY: TSTB	@RSCS1	:TEST FOR DISK READY
4687	021732	100375			BPL	DKBUSY	:DISK STILL NOT READY
4688	021734	000754			BR	SRSCS1	:DISK NOT BUSY SECT NEW CR

```

4689 ; THIS ROUTINE GIVES THE OPERATOR THE ABILITY TO
4690 ; SELECT DA, WC, UNIT # AND DESIRED PATTERN. PATTERN = NUMBER TYPED
4691 ; WITH SW12 SET THE PROGRAM WILL LOOP ON A READ WITH LOC OUTBUF+2 AS
4692 ; THE BA ADDR. WITH BIT12 0 IN THE SWR THE PROGRAM
4693 ; WILL WRITE WITH OUTBUF AS THE BA ADDR. BAI IS ALWAYS SET
4694 ; SWITCHES 0 TO 11 WILL DETERMINE THE DA
4695 ; EXAMPLE:
4696 ; TYPE UNIT # 5
4697 ; TYPE POSITIVE (OCTAL) WC 64
4698 ; TYPE PATTERN DESIRED 1252525
4699 021736 012706 000500 TKSEL: MOV #500,SP ;SET STACK
4700 021742 000240 NOP
4701 021744 104402 021750 RDOCT ,.+2 ;.ASCIZ <15><12>"TYPE UNIT # "
4702 021770 104410 RDOCT
4703 021772 012667 157160 MOV (6)+,UNNUM
4704 021776 104402 022002 TYPE ,.+2 ;.ASCIZ <15><12>"TYPE POSITIVE (OCTAL) WC "
4705 022036 104410 RDOCT
4706 022040 012667 157134 MOV (6)+,WORK
4707 022044 005167 157130 COM WORK
4708 022050 104402 022054 TYPE ,.+2 ;.ASCIZ <15><12>"TYPE PATTERN DESIRED "
4709 022104 104410 RDOCT
4710 022106 012667 002436 MOV (6)+,OUTBUF
4711 022112 016767 155452 157064 TK1: MOV SWR,WORK2 ;SAVE SWR
4712 022120 104414 TK2: CLRDK ;CLEAR ALL RS REG
4713 022122 052777 000010 156756 BIS #BIT3,RS052 ;SET BAI
4714 022130 016767 155434 157044 MOV SWR,WORK1 ;GET SWR FOR DSK ADDR
4715 022136 042767 170000 157036 BIC #170000,WORK1 ;CLEAR UNIT #
4716 022144 016777 157032 156742 TKS: MOV WORK1,RS0A ;LOAD THE DA
4717 022152 016777 157022 156730 MOV WORK,RSWC ;LOAD WORD COUNT
4718 022160 032767 010000 155402 BIT #BIT12,SWR ;READ?
4719 022166 001412 BEQ MTE ;NO
4720 022170 012777 024552 156714 MOV #OUTBUF+2,RSBA ;LOAD CURRENT ADDRESS
4721 022176 012777 000071 156700 MOV #71,RS0S1 ;GO AND READ
4722 022204 105777 156674 WT: TSTB RS0S1 ;TEST FOR READY
4723 022210 100375 BPL -4
4724 022212 000407 BR SWRCHG
4725 022214 012777 024550 156670 WTE: MOV #OUTBUF,RSBA
4726 022222 012777 000061 156654 MOV #61,RS0S1
4727 022230 000765 BR WT
4728 022232 017737 156646 177570 SWRCHG: MOV RS0S1,#DISPLAY ;DISPLAY CS1
4729 022240 005777 156640 TST RS0S1 ;ANY ERRORS?
4730 022244 100001 BPL IS ;NO
4731 022246 104014 HLT !DA!WC
4732 022250 026767 155314 156726 IS: CMP SWR,WORK2 ;DID SWR CHANGE?
4733 022256 001315 BNE TK1 ;YES
4734 022260 000717 BR TK2 ;NO

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4735 ;*****
4736 ;TEST 112 WRITE LOCK TEST
4737 ;*****
4738 022262 104400 †TST112: SCOPE
4739
4740 WRTLCK:
4741 022264 104402 022270 TYPE ,.+2 ;.ASCIZ <15><12>"LOAD SW WITH UNIT # AND CONT"
4742 022330 000000 HALT
4743 022332 013767 177570 156616 MOV @#SWR,UNNUM ;GET UNIT #
4744 022340 104414 CLRK ;CLEAR ALL REG
4745 022342 005067 002202 CLR OUTBUF ;PUT A 0 INTO DATA BUFFER
4746 022346 012777 024550 156536 MOV #OUTBUF,@RSDA ;SETUP REG TO
4747 022354 012777 007700 156532 MOV #7700,@RSDA ;TO A WRITE
4748 022362 012777 177777 156520 MOV #-1,@RSC
4749 022370 012777 000061 156506 MOV #61,@RSCS1
4750 022376 105777 156502 6$: TSTB @RSCS1 ;WAIT FOR DONE
4751 022402 100375 BPL 6$
4752 022404 104402 022410 TYPE ,.+2 ;.ASCIZ <15><12>"SET WRITE LOCK ENABLE AND CONTINUE"
4753 022456 000000 HALT
4754 022460 012777 000000 156426 1$: MOV #0,@RSDA
4755 022466 022777 014600 156422 CMP #14600,@RSDS
4756 022474 001401 BEQ .+4
4757 022476 104044 HLT ;DS!DA ;DS SHOULD=14600
4758 022500 012777 000100 156406 MOV #100,@RSDA
4759 022506 022777 010600 156402 CMP #10600,@RSDS
4760 022514 001401 BEQ .+4
4761 022516 104044 HLT ;DS!DA ;DS SHOULD=10600
4762 022520 104402 022524 TYPE ,.+2 ;.ASCIZ <15><12>"SET WRT LOC SW 0 AND CONT"
4763 022560 000000 HALT
4764 022562 022777 014600 156326 2$: CMP #14600,@RSDS
4765 022570 001401 BEQ .+4
4766 022572 104044 HLT ;DS!DA ;DS SHOULD=14600
4767 022574 012777 000300 156312 MOV #300,@RSDA
4768 022602 022777 010600 156306 CMP #10600,@RSDS
4769 022610 001401 BEQ .+4
4770 022612 104044 HLT ;DS!DA ;DS SHOULD=10600
4771 022614 104402 022620 TYPE ,.+2 ;.ASCIZ <15><12>"SET WRT LOC SW 1 AND CONT"
4772 022654 000000 HALT
4773 022656 022777 014600 156232 3$: CMP #14600,@RSDS
4774 022664 001401 BEQ .+4
4775 022666 104044 HLT ;DS!DA ;DS SHOULD=14600
4776 022670 012777 000700 156216 MOV #700,@RSDA
4777 022676 022777 010600 156212 CMP #10600,@RSDS
4778 022704 001401 BEQ .+4
4779 022706 104044 HLT ;DS!DA ;DS SHOULD=10600
4780 022710 104402 022714 TYPE ,.+2 ;.ASCIZ <15><12>"SET WRT LCK SW 2 AND CONT"
4781 022750 000000 HALT

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4782 022752 022777 014600 156136 4$: CMP #14600, @RSDS
4783 022760 001401 BEQ .+4
4784 022762 104044 HLT !DS!DA ;DS SHOULD=14600
4785 022764 012777 MOV #1700, @RSDA
4786 022772 022777 001700 156122 CMP #10600, @RSDS
4787 023000 001401 BEQ .+4
4788 023002 104044 HLT !DS!DA ;DS SHOULD=10600
4789 023004 104402 023010 TYPE ;.ASCIZ <15><12>"SET WRT LCK SW 3 AND CONT"
4790 023044 000000 HALT: HALT
4791 023046 022777 014600 156042 CMP #14600, @RSDS
4792 023054 001401 BEQ .+4
4793 023056 104044 HLT !DS!DA ;DS SHOULD=14600
4794 023060 012777 003700 156026 MOV #3700, @RSDA
4795 023066 022777 010600 156022 CMP #10600, @RSDS
4796 023074 001401 BEQ .+4
4797 023076 104044 HLT !DS!DA ;DS SHOULD=10600
4798 023100 104402 023104 TYPE ;.ASCIZ <15><12>"SET WRT LCK SW 4 AND CONT"
4799 023140 000000 HALT: HALT
4800 023142 022777 014600 155746 CMP #14600, @RSDS
4801 023150 001401 BEQ .+4
4802 023152 104044 HLT !DS!DA ;DS SHOULD=14600
4803 023154 012777 006000 155732 MOV #6000, @RSDA
4804 023162 022777 010600 155726 CMP #10600, @RSDS
4805 023170 001401 BEQ .+4
4806 023172 104044 HLT !DS!DA ;DS SHOULD=10600
4807 023174 104402 023200 TYPE ;.ASCIZ <15><12>"SET WRT LCK SW 5 AND CONT"
4808 023234 000000 HALT: HALT
4809 023236 022777 014600 155652 CMP #14600, @RSDS
4810 023244 001401 BEQ .+4
4811 023246 104044 HLT !DS!DA ;DS SHOULD=14600
4812 023250 012777 007700 155636 MOV #7700, @RSDA
4813 023256 022777 014600 155632 CMP #14600, @RSDS
4814 023264 001401 BEQ .+4
4815 023266 104044 HLT !DS!DA ;DS SHOULD=14600
4816 023270 012767 177777 001252 MOV #-1, @OUTBUF ;PUT A 1 INTO DATA BUFFER
4817 023276 012777 024550 155606 MOV @OUTBUF, @RSBA ;SETUP REG TO
4818 023304 012777 007700 155602 MOV #7700, @RSDA ;TO A WRITE
4819 023312 012777 177777 155570 MOV #-1, @RSWC
4820 023320 012777 000061 155556 MOV #61, @RSCS1 ;TRY TO WRITE
4821 023326 105777 155552 7$: TSTB @RSCS1 ;WAIT FOR DONE
4822 023332 100375 BPL 7$
4823 023334 105777 155544 5$: TSTB @RSCS1 ;WAIT FOR READY
4824 023340 100375 BPL 5$
4825 023342 022777 154600 155546 CMP #154600, @RSDS
4826 023350 001401 BEQ .+4
4827 023352 104044 HLT !DS!DA ;DS SHOULD=154600

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4828	023354	022777	004000	155536		CMP	#4000, @RSER	
4829	023362	001401				BEG	.+4	
4830	023364	104044				HLT	!DS!DA	;ER SHOULD=4000
4831	023366	104414				CLDRK		;CLEAR ALL REG
4832	023370	022777	014600	.55520		CMP	#14600, @RSDS	;DID WLE CLEAR?
4833	023376	001401				BEG	.+4	;NO
4834	023400	104040				HLT	!DS	;A CLEAR SHOULD NOT CLEAR WLE
4935	023402	005067	001142			CLR	OUTBUF	;PUT A 0 INTO DATA BUFFER
4836	023406	012777	024550	155476		MOV	#OUTBUF, @RSBA	;SETUP REG TO
4837	023414	012777	007700	155472		MOV	#7700, @RSDA	;TO A WRITE
4838	023422	012777	177777	155460		MOV	#-1, @RSWC	
4839	023430	012777	000071	155446		MOV	#71, @RSCS1	;DO A READ TO SEE IF DISK DID
4840	023436	105777	155442		8\$:	TSTB	@RSCS1	;ACTUALLY GET WRITTEN ON TO
4841	023442	100375				BPL	8\$;WAIT FOR DONE
4842	023444	005767	001100			TST	OUTBUF	;IS DATA STILL 0 ON THE DSK?
4843	023450	001401				BEG	.+4	;YES
4844	023452	104040				HLT	!DS	;NO DSK DID GET WRITTEN ONTO WITH WLE SET
4845	023454	104402	023460			TYPE	.,+2	;.ASCIZ <15><12>"RESET ALL W/L SWITCHES"
4846	023512	000000				HALT		
4847	023514	022777	010600	155374		CMP	#10600, @RSDS	;DID WLE CLEAR
4848	023522	001401				BEG	.+4	;YES
4849	023524	104040				HLT	!DS	;NO
4850								;CLEAR ALL DISK REGISTERS
4851	023526	012777	000040	155352		.CLDRK: MOV	#40, @RSCS2	;CLEAR ALL DSK REG
4852	023534	016777	155416	155344		MOV	UNNUM, @RSCS2	;GET UNIT NUMBER
4853	023542	000002				RTI		

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4854                                     ;ERROR TYPTXTOUT ROUTINE
4855
4856 023544 005767 175054 RSREG: TST .HLTCT ;SHOULD WE TYPTXT GOOD AND BAD
4857 023550 001021 BNE 8$ ;NO
4858 023552 104402 023556 TYPE .+2 ;.ASCIZ " BAD="
4859 023564 010046 MOV BAD,-(6) ;PUT BAD ON STACK
4860 023566 104404 TYPEO ;TYPE STACK IN OCTAL
4861 023570 104402 023574 TYPE .+2 ;.ASCIZ " GOOD="
4862 023604 010146 MOV GOOD,-(6) ;PUT GOOD ON STACK
4863 023606 104404 TYPEO ;TYPE STACK IN OCTAL
4864 023610 000167 000432 JMP PTDONE ;GET OUT
4865 023614
4866 023614 104402 023620 8$: TYPE .+2 ;.ASCIZ " CS1="
4867 023626 017746 155252 MOV @RSCS1,-(6) ;PUT @RSCS1 ON STACK
4868 023632 104404 TYPEO ;TYPE STACK IN OCTAL
4869 023634
4870 023634 104402 023640 1$: TYPE .+2 ;.ASCIZ " ER="
4871 023646 017746 155246 MOV @RSEER,-(6) ;PUT @RSEER ON STACK
4872 023652 104404 TYPEO ;TYPE STACK IN OCTAL
4873 023654
4874 023654 104402 023660 2$: TYPE .+2 ;.ASCIZ " CS2="
4875 023666 017746 155214 MOV @RSCS2,-(6) ;PUT @RSCS2 ON STACK
4876 023672 104404 TYPEO ;TYPE STACK IN OCTAL
4877 023674 032767 000200 174722 BIT #200,.HLTCT ;TYPTXT SECOND SET ?
4878 023702 001076 BNE SEEC ;YES
4879 023704 032767 000100 174712 BIT #AS,.HLTCT ;TYPTXT ER ?
4880 023712 001410 BEQ 3$ ;NO
4881 023714 104402 023720 TYPE .+2 ;.ASCIZ " AS="
4882 023726 017746 155170 MOV @RSAS,-(6) ;PUT @RSAS ON STACK
4883 023732 104404 TYPEO ;TYPE STACK IN OCTAL
4884 023734 032767 000020 174662 3$: BIT #BA,.HLTCT ;TYPTXT BUS ADDRESS
4885 023742 001410 BEQ 4$ ;NO
4886 023744 104402 023750 TYPE .+2 ;.ASCIZ " BA="
4887 023756 017746 155130 MOV @RSBA,-(6) ;PUT @RSBA ON STACK
4888 023762 104404 TYPEO ;TYPE STACK IN OCTAL
4889 023764 032767 000004 174632 4$: BIT #DA,.HLTCT ;TYPTXT DA ?
4890 023772 001410 BEQ 5$ ;NO
4891 023774 104402 024000 TYPE .+2 ;.ASCIZ " DA="
4892 024006 017746 155102 MOV @RSDA,-(6) ;PUT @RSDA ON STACK
4893 024012 104404 TYPEO ;TYPE STACK IN OCTAL
4894 024014 032767 000010 174602 5$: BIT #WC,.HLTCT ;TYPTXT WC?
4895 024022 001410 BEQ 6$ ;NO
4896 024024 104402 024030 TYPE .+2 ;.ASCIZ " WC="
4897 024036 017746 155046 MOV @RSWC,-(6) ;PUT @RSWC ON STACK
4898 024042 104404 TYPEO ;TYPE STACK IN OCTAL

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4899	024044	032767	000040	174552	6\$:	BIT	#DS,.HLTCT	;DRIVE STATUS
4900	024052	001475				BEQ	PTDONE	;NO
4901	024054	104402	024060			TYPE	.+2	;ASCIZ "DS="
4902	024066	017746	155024			MOV	DRSDS,-(6)	;PUT DRSDS ON STACK
4903	024077	104404				TYPE0		;TYPE STACK IN OCTAL
4904	024074	000167	000146			JMP	PTDONE	;GET OUT
4905	024100	042767	000200	174516	SEEC:	BIC	#200,.HLTCT	;CLEAR COMMON BIT
4906	024106	032767	000240	174510		BIT	#DT,.HLTCT	;TYPTXT DRIVE TYPE?
4907	024114	001410				BEQ	9\$;NO
4908	024116	104402	024122			TYPE	.+2	;ASCIZ "DT="
4909	024130	017746	154776			MOV	DRSDT,-(6)	;PUT DRSDT ON STACK
4910	024134	104404				TYPE0		;TYPE STACK IN OCTAL
4911	024136	032767	000210	174460	9\$:	BIT	#DB,.HLTCT	;TYPTXT DATA BUFFER
4912	024144	001410				BEQ	10\$;NO
4913	024146	104402	024152			TYPE	.+2	;ASCIZ "DB="
4914	024160	017746	154742			MOV	DRSDB,-(6)	;PUT DRSDDB ON STACK
4915	024164	104404				TYPE0		;TYPE STACK IN OCTAL
4916	024166	032767	000220	174430	10\$:	BIT	#MR,.HLTCT	;TYPTXT MN?
4917	024174	001410				BEQ	11\$;NO
4918	024176	104402	024202			TYPE	.+2	;ASCIZ "MR="
4919	024210	017746	154714			MOV	DRSMR,-(6)	;PUT DRSMR ON STACK
4920	024214	104404				TYPE0		;TYPE STACK IN OCTAL
4921	024216	032767	000204	174400	11\$:	BIT	#LA,.HLTCT	;TYPTXT LA?
4922	024224	001410				BEQ	PTDONE	;NO
4923	024226	104402	024232			TYPE	.+2	;ASCIZ "LA="
4924	024240	017746	154660			MOV	DRSLA,-(6)	;PUT DRSLA ON STACK
4925	024244	104404				TYPE0		;TYPE STACK IN OCTAL
4926	024246	052767	100000	154710	PTDONE:	BIS	#BIT15,ONCEE	;SET FORND ERROR FLAG
4927	024254	000207				RTS	PC	
4928								
4929	024256	005067	154716		WAITRY:	CLR	WORK	;CLEAR COUNTER
4930	024262	105777	154616		1\$:	TSTB	DRSCS1	;TEST READY
4931	024266	100406				BMI	2\$;OK CONT
4932	024270	005267	154704			INC	WORK	;UPDATE COUNTER
4933	024274	005767	154700			TST	WORK	;DONE YET?
4934	024300	001403				BEQ	3\$;READY DID NOT COME UP
4935	024302	000767				BR	1\$;CONTINUE WAITING
4936	024304	062716	000002		2\$:	ADD	#2,(SP)	;UPDATE RETURN PC
4937	024310	000207			3\$:	RTS	PC	;RETURN

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4938                                     ;RANDOM DATA GENERATOR SUBROUTINE
4939
4940 024312 016767 000136 000140 RANDOM: MOV     LONUM,LOSAV
4941 024320 016767 000132 000134          MOV     HINUM,HISAV
4942 024326 016700 000122          RAND1: MOV     LONUM,R0      ;SET UP R0 WITH 5 DIGITS LOW
4943 024332 016704 000120          MOV     HINUM,R4      ;SET UP R1 WITH 5 DIGITS HIGH
4944 024336 012703 000007          MOV     #7,R3        ;SET UP SHIFT COUNT
4945 024342 005002          CLR     R2           ;CLEAR R2
4946 024344 006300          SHIFT: ASL    R0      ;SHIFT R0 LEFT AND
4947 024346 006104          ROL    R4           ;ROTATE CARRY INTO LSB OF R1 INTO
4948 024350 006102          ROL    R2           ;ROTATE CARRY OUT OF R1 INTO R2
4949 024352 005303          DEC    R3           ;DECREMENT R3
4950 024354 001373          BNE   SHIFT        ;CONTINUE SHIFT LOOP
4951 024356 066700 000072          ADD    LONUM,R0     ;ADDN IN NUMBER TO MAKE X 129
4952 024362 005504          ADC    R4           ;PROPOGATE CARRY
4953 024364 066704 000066          ADD    HINUM,R4     ;ADDN IN NUMBER TO MAKE X 129
4954 024370 005502          ADC    R2           ;PROPOGATE CARRY
4955 024372 062700 001057          ADD    #1057,R0     ;ADDN LOW CONSTANT
4956 024376 005504          ADC    R4           ;PROPOGATE CARRIES
4957 024400 005502          ADC    R2           ;PROPOGATE AGAIN
4958 024402 062704 047401          ADD    #47401,R4    ;ADDN HIGH CONSTANT
4959 024406 005502          ADC    R2           ;PROPOGATE CARRY
4960 024410 062702 000006          ADD    #6,R2        ;ADDN HIGHEST CONSTANT
4961 024414 062700 000002          ADD    #2,R0        ;REPRIME R0 WITH HIGH DIGIT
4962 024420 005504          ADC    R4           ;PROPOGATE CARRY
4963 024422 010067 000026          MOV    R0,LONUM     ;PUT R0 BACK IN LONUM
4964 024426 010021          MOV    R0,(R1)+     ;LOAD WC
4965 024430 005367 154544          DEC    WORK
4966 024434 001406          BEQ   EXGEN
4967 024436 010467 000014          MOV    R4,HINUM     ;PUT R1 BACK IN HINUM
4968 024442 010421          MOV    R4,(1)+     ;HOLD HINUM FOR PROGRAM
4969 024444 005367 154530          DEC    WORK
4970 024450 001326          BNE   RAND1
4971 024452 000205          EXGEN: RTS
4972 024454 000000          LONUM: 0
4973 024456 000000          HINUM: 0
4974 024460 000000          LOSAV: 0
4975 024462 000000          HISAV: 0
4976 024464          RANEND:

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AS11-AS03-AS04 BASIC FUNCTION DIAGNOSTIC
WRITE LOCK TEST

AS11-AS03-AS04
WRITE LOCK TEST

: TABLES FOR ILLEGAL FUNCTION TESTS

4999	024464	000003
4999	024466	000005
4999	024470	000007
4999	024472	000013
4999	024474	000015
4999	024476	000017
4999	024500	000023
4999	024502	000025
4999	024504	000027
4999	024506	000033
4999	024510	000035
4999	024512	000037
4999	024514	000041
4999	024516	000043
4999	024520	000045
4999	024522	000047
4999	024524	000000
4999	024526	000053
4999	024530	000055
5000	024532	000057
5001	024534	000063
5002	024536	000065
5003	024540	000073
5004	024542	000075
5005	024544	000077
5006	024546	000000
5007	024550	000300
5008	025350	000200
5009		000001

ILLTAB: 3
 3
 5
 7
 9
 11
 13
 15
 17
 19
 21
 23
 25
 27
 29
 31
 33
 35
 37
 39
 41
 43
 45
 47
 0
 53
 55
 57
 63
 65
 73
 75
 77
 0
 .BLKW 300
 .BLKW 300
 .END

ADOCF	= 010772	3168	3177*											
AS	= 000100	1799*	2003	2444	2454	2476	2507	2520	2542	2585	2879	2937	3108	3244
		3610	3650	3735	3742	3779	3800	3806	4261	4281	4303	4307	4310	4879
		1820*												
ATA	= 100000	1834*												
ROB1	= 001171	1797*	1973	2031	2037	2119	2123	2127	2149	2388	2592	2441	2473	2504
SA	= 000020	2539	2605	2885	2940	3001	3017	3035	3776	3839	4884			
		1729*	2096*	2135*	2136	2173*	2174	2210*	2211	2306*	2320*	2335*	2365*	2366
BAC	= 000000	2401*	2402	2408*	2409	2413*	2414	2562*	2563*	2566	2626*	2627	2630*	2633
		2667*	2668	2671*	2674	2677*	2679	2702*	2703	2715*	2718	2761*	2762	2765*
		2768	2798*	2789	2814*	2815	2822*	2825	2874*	2906*	2910	2911*	2912	2916*
		2917	2926*	2929	2934*	2935	2962*	2967	2969	2970*	2971	2975*	2976	2993*
		2996	3007*	3010	3022*	3025	3060*	3062	3069*	3068	3086*	3087	3092*	3093
		3149*	3150*	3151	3186*	3187*	3189*	3194	3230*	3233	3284*	3287	3290*	3300*
		3303	3309*	3310	3347*	3348	3359*	3360	3538*	3541	3545*	3546	3555*	3556
		3640*	3642	3730*	3733	3759*	3762	3791*	3794	3801*	3804	3814*	3817	3861*
		3864	3896*	3899*	3900	3995*	3996	4059*	4097*	4140*	4162*	4255*	4256*	4258
		4267*	4268	4271*	4274	4859								
		1823*	2783	2796	2805	3826	3849	3883	3960					
BAI	= 000010	2780*												
BAITST	= 006544	1674	1692	1854*	4339									
BEGIN	= 001206	1703*	4334	4436										
BELL	= 000007	2384*												
BITBA	= 004322	2360*												
BITCS2	= 004206	2347*												
BITST	= 004134	2372*												
BITMC	= 004252	1712*	1920	1938	2332	2606	2656	2709	2911	2970	4684			
BITO	= 000001	1713*												
BIT1	= 000002	1722*												
BIT10	= 002000	1673	1691	1723*	1865									
BIT11	= 004000	1724*	3660	3663	4718									
BIT12	= 010000	1725*	1884	1904	1924									
BIT13	= 020000	1665	1726*	3267	3292	3316	3319	3323						
BIT14	= 040000	1727*	1887	1906	1913	4256	4925							
BIT15	= 100000	1714*												
BIT2	= 000004	1715*	4225	4231	4286	4713								
BIT3	= 000010	1716*	3789	3830	3855	3890								
BIT4	= 000020	1717*												
BIT5	= 000040	1718*	3899											
BIT6	= 000100	1719*	3468											
BIT7	= 000200	1720*												
BIT8	= 000400	1721*	3167	3171										
BIT9	= 001000	2834*												
BLOCK	= 007004	1837*	2721*											
REPORT	= 001174	1916	1920*											
CHKOV	= 001622	2061	2347	2393	2395	2422	2450	2485	2516	2556	2595	2646	2690	2730
CLROK	= 104414	2780	2795	2804	2834	2904	2947	2960	3039	3053	3074	3116	3121	3165
		3221	3260	3306	3320	3329	3371	3395	3408	3426	3433	3451	3457	3482
		3493	3559	3579	3597	3614	3626	3669	3688	3700	3719	3728	3788	3810
		3825	3848	3873	3882	3909	3921	3940	3953	4005	4062	4111	4136	4158
		4186	4209	4632*	4712	4744	4831							
		3163*												
CSTST	= 010722	1793*	1968	2026	2066	2070	2074	2082	2352	2356	2429	2461	2492	2527
CS1	= 000001	2582	2609	2611	2617	2653	2655	2661	2697	2699	2746	2749	2786	2820
		2863	2885	2922	2925	2940	2943	2946	2981	2984	2987	3001	3004	3017
		3020	3035	3038	3299	3315	3338	3341	3353	3382	3416	3441	3472	3523

3000 = .000001

1728#	2097*	2132*	2134	2136	2139*	2170*	2172	2174	2177*	2207*	2209	2211
2214*	2307*	2321*	2336*	2363*	2364*	2366	2399*	2400*	2402	2407*	2409	2412*
2414	2564*	2565*	2566	2624*	2625*	2627	2631*	2632*	2633	2665*	2666*	2668
2672*	2673*	2674	2678*	2679	2700*	2701*	2703	2716*	2717*	2718	2759*	2760*
2762	2766*	2767*	2768	2787*	2789	2812*	2813*	2815	2823*	2824*	2825	2853*
2854*	2865*	2867*	2872	2876	2912*	2915*	2917	2927*	2928*	2929	2932*	2933*
2935	2971*	2974*	2976	2994*	2995*	2996	3008*	3009*	3010	3023*	3024*	3025
3061*	3062	3065*	3066*	3068	3084*	3087	3090*	3091*	3093	3145*	3146*	3151
3166*	3169*	3192*	3193*	3194	3231*	3232*	3233	3285*	3286*	3287	3291*	3294*
3301*	3302*	3303	3307*	3308*	3310	3345*	3346*	3348	3358*	3360	3359*	3540*
3541	3544*	3546	3549*	3552*	3554*	3556	3628*	3631*	3635	3641*	3642	3731*
3732*	3733	3760*	3761*	3762	3792*	3793*	3794	3802*	3803*	3804	3815*	3816*
3817	3862*	3863*	3864	3897*	3898*	3900	3993*	3994*	3996	4058*	4096*	4149*
4163*	4257*	4258	4265*	4266*	4268	4272*	4273*	4274	4862			
4790#												
4941	4943	4953	4967*	4973#								
4941*	4975#											
1698#	1960	1964	1968	1973	1979	1985	1991	1997	2003	2022	2026	2031
2037	2043	2048	2053	2066	2070	2074	2082	2092	2098	2102	2106	2111
2119	2123	2127	2138	2149	2157	2161	2165	2176	2186	2194	2198	2202
2213	2223	2231	2235	2242	2248	2255	2265	2272	2280	2288	2308	2322
2327	2352	2356	2368	2376	2380	2388	2392	2406	2411	2416	2423	2432
2435	2438	2441	2444	2454	2461	2464	2467	2470	2473	2476	2479	2492
2495	2498	2501	2504	2507	2510	2520	2527	2530	2533	2536	2539	2542
2545	2548	2568	2571	2574	2579	2582	2585	2588	2602	2605	2609	2611
2614	2617	2620	2623	2629	2635	2638	2653	2655	2658	2661	2664	2670
2676	2681	2697	2699	2705	2708	2711	2714	2720	2746	2749	2752	2755
2758	2764	2770	2786	2791	2794	2798	2811	2817	2820	2827	2843	2848
2852	2860	2863	2875	2879	2882	2885	2888	2890	2919	2922	2925	2931
2937	2940	2943	2946	2950	2953	2978	2981	2984	2987	2998	3001	3004
3012	3017	3020	3027	3035	3038	3042	3045	3064	3070	3073	3077	3089
3095	3098	3102	3105	3108	3120	3124	3127	3139	3156	3173	3196	3199
3235	3238	3289	3295	3299	3305	3312	3315	3338	3341	3344	3350	3353
3356	3362	3365	3382	3385	3388	3391	3394	3398	3401	3416	3419	3422
3425	3441	3444	3447	3450	3472	3475	3478	3481	3485	3523	3531	3534
3537	3543	3548	3558	3562	3585	3588	3607	3610	3613	3617	3639	3644
3647	3650	3654	3658	3678	3681	3684	3687	3691	3694	3709	3712	3715
3718	3722	3735	3738	3742	3746	3753	3758	3764	3767	3770	3773	3776
3779	3782	3796	3800	3806	3809	3813	3819	3836	3839	3842	3860	3866
3869	3872	3875	3895	3902	3905	3908	3912	3915	3930	3933	3936	3939
3943	3998	4034	4038	4050	4054	4060	4073	4076	4088	4092	4098	4120
4126	4145	4150	4170	4171	4175	4193	4197	4260	4261	4264	4270	4276
4281	4284	4303	4307	4310	4731	4757	4761	4766	4770	4775	4779	4784
4788	4793	4797	4802	4806	4811	4815	4827	4830	4834	4844	4849	
1737#	4441*	4442*	4443	4444	4458							
3597#												
1733#	1861*	4324*	4393	4408	4410	4412*	4413*	4415	4418*	4419	4425	4453*
1824#												
2907	2955#											
2964	3047#											
2961	4998#											
3053#												
2960#												
2905	4980#											
2902#												
5008#												

HALT 023044
MINUM 024456
HISAV 024462
HLT = 104000

HLTAOR 001012
IAERR 013352
ICNT 001000
IE = 000100
ILFDN 007560
ILFDNE 010220
ILFTB2 024526
ILF67 010222
ILLFUN 007560
ILLTAB 024464
ILLS1 007304
IMBUF 025350

NYMTSM	006650	2804#												
OBUSV	001102	1744#	1864*	1937*	2424	2439	2456	2471	2487	2502	2522	2537	2597	2603
		2631	2648	2672	2692	2716	2721	2722*	2736	2766	2781	2787	2836	2883
		2908	2938	2965	2999	3013	3028	3262	3278	3301	3331	3372	3409	3434
		3458	3500	3507	3524	3564	3581	3599	3675	3706	3748	3774	3828	3837
		3851	3885	3927	3955	3966	3978	3984	4142	4165	4190	4239	4272	4682
ONCEE	001164	1673*	1691*	1831#	1863*	1865	1887*	1906	1913*	1920	1938*	3267	3292	3316
		3319*	3323*	3660	3663*	4926*								
OUT	007302	2849	2889	2891#										
OR	= 000200	1811#												
OUT	020032	4325#												
OUTBUF	024550	1864	1937	2295	2297	2311	2325	2423*	2455*	2486*	2521*	2596*	2647*	2677
		2691*	2722	2735*	3055	3061	3079	3222*	3261*	3269*	3271*	3274*	3276*	3290
		3321*	3330*	3499*	3506*	3549	3747*	3827*	3850*	3884*	3954*	3965*	4238*	4710*
		4720	4725	4745*	4746	4816*	4817	4835*	4836	4842	5007#			
PART	011112	3207#												
PARTST	010520	3115#												
PATST	014514	3788#												
PATST	015154	3882#												
PC	=%000007	1711#	2610*	2654*	2698*	2745*	2785*	2810*	3337*	3381*	3415*	3440*	3584*	3677*
		3708#	3929*	4144*	4192*	4447*	4624*	4927*	4937*					
		1735#	4329*	4330*	4341									
PCNT	001004	1812#	3358											
PGE	= 002000	3329#												
PGETST	011676	4113	4123#											
PGTRAP	016624	1817#												
PIP	= 020000	3208	3243#											
PRTP	011300	3219	3244#											
PRTP1	011302	1699#	1700	1945*	2009*	4115*	4139*	4140	4161*	4162	4184*	4252*	4317*	4323*
PS	= 177776	4680*												
		1700#												
PSW	= 177776	4864	4900	4904	4922	4926#								
PTDONE	024246	4111#												
QES	016544	4121	4127#											
QESDOM	016640	1869#	1941#	2004#	2054#	2075#	2084#	2113#	2129#	2141#	2150#	2166#	2180#	2187#
QQ	= 000001	2203#	2217#	2224#	2237#	2243#	2249#	2256#	2266#	2273#	2281#	2289#	2342#	2358#
		2370#	2382#	2394#	2417#	2445#	2480#	2511#	2549#	2590#	2641#	2682#	2725#	2775#
		2799#	2829#	2892#	3048#	3110#	3132#	3140#	3158#	3202#	3251#	3324#	3366#	3403#
		3428#	3452#	3486#	3574#	3590#	3621#	3664#	3695#	3723#	3783#	3820#	3844#	3877#
		3916#	3948#	4000#	4106#	4129#	4152#	4178#	4200#	4735#				
RANDOM	024312	2296	4940#											
RAND1	024326	4942#	4970											
RANEND	024464	4976#												
RANNU	001154	1827#												
RANTS	003714	2294#												
ROLIN	= 104412	4556	4631#											
RDOCT	= 104410	4630#	4702	4705	4709									
ROTST	005760	2646#												
RMRT1	012076	3371#												
RMRT2	012254	3408#												
RMRT3	012364	3433#												
RMRT4	012474	3457#												
RSAS	001122	1755#	1881	2001	2442	2452	2474	2505	2518	2540	2583	2872	2874	2876*
		2877	2934	3106	3342	3466*	3648	3730	3739*	3740	3777	3801	4297	4313*
		4670*	4671	4882										
RSBA	001112	1751#	1948*	1971	2012*	2029	2116*	2117	2120*	2121	2124*	2125	2134*	2135

		2145*	2146*	2147	2330	2335	2384*	2386	2389*	2390	2424*	2439	2456*	2471
		2487*	2502	2522*	2537	2597*	2603	2630	2648*	2671	2692*	2715	2736*	2765
		2781*	2788	2807*	2836*	2883	2908*	2938	2965*	2999	3015	3033	3056*	3060
		3080*	3086	3223*	3262*	3278*	3300	3331*	3372*	3409*	3434*	3458*	3500*	3507*
		3524*	3555	3564*	3581*	3599*	3675*	3706*	3748*	3774	3828*	3837	3851*	3885*
		3927*	3955*	3966*	3984*	4026*	4042*	4066*	4082*	4142*	4155*	4190*	4239*	4271
		4642*	4643	4682*	4720*	4725*	4746*	4817*	4836*	4887				
RSBQ8	001152	1767*	2385*											
RSBAE	001134	1760*	2808*											
RSCS1	001104	1748*	1947*	1966	2011*	2024	2063*	2064	2067*	2068	2071*	2072	2078*	2079*
		2080	2348*	2350	2353*	2354	2426*	2427	2458*	2459	2489*	2490	2508	2524*
		2525	2546	2561*	2580	2599*	2606*	2607	2615	2650*	2651	2659	2694*	2695
		2706	2747	2784*	2809*	2818	2821*	2845*	2861	2910*	2913	2916	2951	2969*
		2972	2975	3040	3057*	3058	3081*	3082	3099*	3100	3225*	3226	3236	3264*
		3265	3281*	3282	3297	3313	3333*	3334	3336*	3339	3357*	3363	3379*	3392
		3399	3411*	3412	3423	3436*	3437	3439*	3448	3465*	3479	3483	3502*	3503
		3514*	3521	3526*	3527	3535	3565*	3566	3583*	3601*	3602	3611	3645	3676*
		3685	3707*	3716	3736	3744	3750*	3751	3754	3756	3807	3811	3831*	3832
		3834	3853*	3856	3858	3874	3887*	3888	3891	3893	3910	3928*	3937	3961*
		3962	3969*	3970	3990*	3991	4027*	4030	4032	4036	4044*	4046	4048	4052
		4068*	4069	4071	4074	4083*	4084	4086	4090	4116*	4124	4143*	4166*	4173
		4191*	4195	4243*	4249*	4255	4285*	4290*	4308	4314*	4683*	4684	4686	4721*
		4722	4726*	4728	4729	4749*	4750	4820*	4821	4823	4839*	4840	4867	4930
RSCS18	001144	1764*	2349*	3651*										
RSCS2	001106	1749*	1875*	1879*	1890*	1946*	1953*	1958	1961*	2010*	2017*	2019*	2020	2023*
		2090	2093*	2094	2096	2099*	2100	2103*	2104	2107*	2108*	2109	2112*	2360*
		2361*	2365	2401	2562	2626	2667	2702	2761	2783*	2796	2805*	2814	2822
		2926	2993	3007	3022	3067	3092	3230	3284	3309	3347	3359	3501*	3519*
		3520*	3538	3635*	3640	3652	3656	3759	3789*	3791	3814	3826*	3830*	3849*
		3855*	3861	3883*	3890*	3896	3960*	3968*	3972*	3995	4246*	4248*	4254*	4267
		4304*	4665*	4666	4713*	4851*	4852*	4875						
RSCS28	001146	1765*	2362*											
RSCS3	001136	1761*												
RSDA	001114	1752*	1949*	1977	2013*	2035	2191*	2192	2195*	2196	2199*	2200	2209*	2210
		2219*	2220*	2221	2316	2320	2436	2468	2499	2534	2558*	2569	2612	2656
		2709	2750	2835*	2844*	2944	2985	3177*	3179*	3180	3192	3280*	3380*	3386
		3505*	3582*	3586	3600*	3636*	3655	3674*	3705*	3771	3922*	3964*	3983*	4041*
		4081*	4188*	4241*	4279	4282	4647*	4648	4716*	4747*	4754*	4758*	4767*	4776*
		4785*	4794*	4803*	4812*	4818*	4837*	4892						
RSD8	001126	1757*	2015*	2396*	2397*	2408	2413	4914						
RSDS	001116	1753*	1962	2462	2528	2621	2756	2846	2850	2856	2858	2880	2923	2982
		3118	3125	3389	3420	3445	3468	3476	3532	3608	3682	3689	3713	3720
		3765	3867	3903	3934	3941	4305	4652*	4653	4755	4759	4764	4768	4773
		4777	4782	4786	4791	4795	4800	4804	4809	4813	4825	4832	4847	4902
RSDT	001132	1759*	1891	1895	1899	1901	4909							
RSER	001120	1754*	1876*	1950*	1983	2014*	2041	2228*	2229	2232*	2233	2239*	2240	2245*
		2246	2251*	2252*	2253	2430	2451*	2477	2493	2517*	2543	2559*	2572	2636
		2792	2920	2948	2979	3043	3071	3075	3096	3103	3117*	3122	3354	3383
		3396	3414*	3417	3442	3473	3529	3560	3605	3615	3637	3679	3692	3710
		3729*	3780	3798	3870	3906	3913	3931	4657*	4658	4828	4871		
RSLA	001124	1756*	3137	3145	3149	3186	4662	4924						
RSMR	001130	1758*	1951*	1989	2018*	2046	2260*	2261	2268*	2269*	2270	2275*	2276	2283*
		2284	2560*	2575	2839	3167	3171	3184	3790	3797*	4675*	4676	4919	
RSMRR	021650	1687	4675*	4677										
RSREG	023544	4447	4856*											
RSVCPS	001142	1763*	4114*	4138*	4160*	4187*	4237*	4319	4320*					

TYPES = 104406	4915	4920	4925											
UNCMP 001162	1911	1927	4629*											
	1830*	1882*	1888	1915*	1922*	2442	2452	2474	2505	2518	2540	2583	2932	
	3731	3739	3777	3802										
UNITST 006334	2730*													
UNITSV 001160	1829*	1821*	1888	3629	4217									
UNNUM 001156	1828*	1883*	1890	1910	1917*	1923*	1926	1946	1961	2010	2023	2112	2360	
	2363	2400	2564	2624	2665	2700	2732	2739	2759	2812	2823	2864	2927	
	2994	3008	3023	3065	3090	3232	3285	3307	3345	3520	3539	3761	3793	
	3815	3863	3898	3993	4219	4227	4254	4265	4703*	4743*	4852			
UP = 000000	1781*	4018	4019	4020	4021									
WAITRY 024256	2610	2654	2698	2745	2785	2810	3337	3381	3415	3440	3584	3677	3708	
	3929	4144	4192	4929*										
WC = 000010	1796*	1997	2053	2157	2161	2165	2186	2376	2380	2435	2467	2498	2533	
	2588	2602	2620	2664	2714	2755	2888	2943	3004	3020	3038	3353	3770	
	3842	4264	4731	4894										
WCE = 040000	1814*													
WCEDNE 011644	3277	3319*												
WCEDON 011666	3272	3323*												
WCETST 011334	3260*													
WCETT 011342	3262*	3322												
WORK 001200	1850*	2261*	2262*	2263	2276*	2277*	2278	2284*	2285*	2286	2294*	2398*	2404*	
	2575*	2576*	2577	2838*	2841*	2864*	2868	2870*	3013*	3014*	3015	3028*	3029*	
	3032*	3033	3055*	3056	3079*	3080	3144*	3147*	3178*	3182*	3213	3223	3229*	
	3467*	3470*	3627*	3629	3632*	3655*	3975*	3977*	3981*	4117*	4118*	4167*	4168*	
	4216*	4217	4223*	4296*	4297	4301*	4313	4706*	4707*	4717	4929*	4932*	4933	
	4965*	4969*												
WORK1 001202	1851*	2967*	2968*	2988	2990	3188*	3190*	4212*	4225	4231*	4286	4714*	4715*	
	4716													
WORK2 001204	1852*	3494*	3497*	3498	3525	4711*	4732							
WRCKT 006146	2690*													
WRTLCK 022264	1670	4740*												
WRTST 005532	2595*													
WT 022204	4722*	4727												
WTDV 017440	4253*													
WTDV1 017762	4287	4312	4317*											
WTDV2 017642	4289	4292*	4315											
WTE 022214	4719	4725*												
WWP = 000004	1835*	3212												
ZERONE 004374	2395*													
SENDAD 020102	1663	4337*												
SEND1 020112	4335	4339*												
= 026150	1661*	1662*	1664*	1666*	1669*	1672*	1678*	1690*	1731*	1742*	1866	1886	1908	
	1929	1930*	1959	1963	1967	1972	1978	1984	1990	1996	2002	2021	2025	
	2030	2036	2042	2047	2052	2065	2069	2073	2081	2091	2101	2105	2110	
	2118	2122	2126	2137	2148	2156	2160	2164	2175	2185	2193	2197	2201	
	2212	2222	2230	2234	2241	2247	2254	2264	2271	2279	2287	2351	2355	
	2367	2375	2379	2387	2391	2410	2415	2428	2431	2434	2437	2440	2443	
	2453	2460	2463	2466	2469	2472	2475	2478	2491	2494	2497	2500	2503	
	2506	2509	2519	2526	2529	2532	2535	2538	2541	2544	2547	2567	2570	
	2573	2578	2581	2584	2587	2601	2604	2608	2613	2616	2619	2622	2628	
	2634	2637	2652	2657	2660	2663	2669	2675	2680	2696	2704	2707	2710	
	2713	2719	2748	2751	2754	2757	2763	2769	2790	2793	2797	2816	2819	
	2826	2851	2859	2862	2878	2881	2884	2887	2918	2921	2924	2930	2936	
	2939	2942	2945	2949	2952	2977	2980	2983	2986	2997	3000	3003	3011	
	3016	3019	3026	3034	3037	3041	3044	3063	3069	3072	3076	3088	3094	

600	4330	4952	4954	4956	4957	4959	4962	3302	3552	3554	4273	4329	4375	4378	4623
600	4936	4951	4953	4955	4958	4960	4961								
600	2140	2178	2215	3272	4224	4302									
600	1866	1878	1889	1900	1907	1959	1963	1967	1972	1978	1984	1990	1996	2002	2021
600	2025	2030	2036	2042	2047	2052	2065	2069	2073	2081	2091	2095	2101	2105	2110
600	2230	2233	2241	2247	2254	2264	2271	2279	2287	2301	2305	2315	2319	2329	2334
600	2443	2455	2463	2475	2479	2491	2498	2491	2494	2497	2500	2503	2506	2509	2526
600	2529	2532	2535	2544	2547	2547	2567	2570	2573	2578	2581	2584	2587	2601	2604
600	2612	2616	2619	2622	2628	2634	2637	2657	2660	2663	2669	2675	2680	2704	2707
600	2710	2713	2719	2748	2751	2754	2757	2763	2769	2790	2793	2797	2816	2819	2826
600	2847	2855	2857	2859	2862	2869	2873	2878	2881	2884	2887	2907	2918	2921	2924
600	2930	2936	2939	2942	2945	2949	2952	2963	2977	2980	2983	2986	2989	2991	2997
600	3000	3003	3011	3016	3019	3026	3034	3037	3041	3044	3063	3069	3072	3076	3088
600	3094	3097	3101	3104	3107	3119	3123	3126	3138	3148	3152	3168	3172	3181	3183
600	3185	3195	3234	3237	3245	3268	3288	3293	3298	3304	3311	3314	3317	3340	3343
600	3349	3355	3361	3364	3384	3390	3393	3397	3400	3418	3421	3424	3443	3446	3449
600	3474	3477	3480	3484	3530	3533	3536	3542	3547	3557	3561	3587	3606	3609	3612
600	3616	3630	3638	3643	3646	3649	3653	3680	3683	3690	3693	3711	3714	3721	3734
600	3737	3741	3745	3752	3757	3763	3766	3769	3772	3775	3781	3795	3799	3805	3808
600	3812	3818	3835	3838	3841	3859	3865	3868	3871	3875	3894	3901	3904	3907	3911
600	3914	3925	3932	3935	3942	3974	3986	3992	3997	4037	4053	4057	4075	4091	4095
600	4125	4174	4196	4218	4259	4263	4269	4275	4280	4283	4297	4306	4309	4312	4335
600	4361	4392	4394	4409	4421	4435	4487	4490	4560	4593	4685	4719	4756	4760	4765
600	4769	4774	4778	4783	4787	4792	4796	4801	4805	4810	4814	4826	4829	4833	4843
600	4848	4880	4885	4890	4895	4900	4907	4912	4917	4922	4934	4966			
600	4411	4562													
600	1673	1863	1887	1913	2262	2277	2285	2332	2563	2576	2911	2933	2968	2970	3146
600	3150	3187	3193	3323	3358	3376	3462	3511	3516	3732	3803	3899	3972	4229	4256
600	4379	4715	4905												
600	4597														
600	1691	1903	1938	2361	2364	2400	2565	2606	2625	2666	2701	2760	2783	2805	2813
600	2824	2915	2928	2974	2995	3009	3024	3066	3091	3232	3286	3308	3319	3346	3357
600	3501	3540	3641	3663	3761	3789	3793	3816	3826	3830	3849	3855	3863	3883	3890
600	3898	3960	3968	3994	4231	4266	4713	4926							
600	4491														
600	1865	1884	1888	1904	1906	1920	1924	1989	2024	2046	2270	2427	2442	2452	2459
600	2474	2490	2505	2518	2525	2540	2583	2796	2839	2846	2850	2856	3137	3167	3171
600	3184	3267	3292	3316	3468	3629	3652	3656	3660	3751	3777	3991	4217	4225	4286
600	4297	4331	4357	4391	4395	4406	4434	4438	4451	4478	4684	4718	4877	4879	4884
600	4889	4894	4899	4906	4911	4916	4921								
600	4564														
600	3217	3335	3413	3438	3522	3603	3686	3854	3889	4931					
600	1885	1892	1896	1902	1905	1921	1925	2405	2453	2475	2519	2541	2733	2840	2842
600	2851	3031	3170	3191	3352	3378	3387	3464	3469	3471	3496	3513	3518	3551	3657
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CLC
CLP

CLB
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CMPB
CCM
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EMT
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JSP

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