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IDENTIFICATION

PRODUCT CODE: AC-B1500-MC
PRODUCT TITLE: CVDVAO DLV11-E OFFLINE TEST
PRODUCT DATE: AUGUST 1984
AUTHOR: ODES CHOATE
MAINTAINER: DIAGNOSTIC ENGINEERING GROUP

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DEC	DECUS	DECTAPE	

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1.0 GENERAL PROGRAM INFORMATION.

1.1 PROGRAM PURPOSE (ABSTRACT).

THIS DIAGNOSTIC IS A LOGIC TEST TO VERIFY THE OPERATION OF THE DLV11-E SERIAL LINE INTERFACE. THE PROGRAM AS SET INITIALLY DEFAULTS TO ALL OPTIONS, EXCEPT PROGRAMMABLE BAUD RATE, ENABLED AND A WRAP CABLE CONNECTED. THE USER CAN SELECTIVELY ENABLE AND DISABLE TESTING OF THE OPTIONS BY ALTERING THE CONTENTS OF 'USER'. THE DIAGNOSTIC IS DESIGNED TO TEST AND DETECT FAULTS TO THE LOGIC LEVEL (NOT TO THE CHIP LEVEL). THIS TEST OPERATES ON UP TO SIXTEEN(16) IDENTICALLY CONFIGURED DLV11-E SERIAL LINE INTERFACES. THE DEFAULT ADDRESSES ARE:

175610 -FIRST SERIAL LINE ADDRESS OF 16 CONSECUTIVE SERIAL LINE DEVICES.

300 - VECTOR FOR FIRST OF 16 DEVICES.

THIS PROGRAM IS DESIGNED TO RUN ON ANY PDP-11 WITH 4K OF MEMORY AND A DLV11-E (LSI-BUS) MODULE. IT CAN RUN UNDER XXDP, APT, AND ACT MONITORS, AND ON PROCESSORS WITH NO HARDWARE SWITCH REGISTER. A POWER FAILURE WILL CAUSE THE DIAGNOSTIC TO RESTART.

1.2 SYSTEM REQUIREMENTS.

1. HARDWARE REQUIREMENTS:

ANY PDP-11 FAMILY PROCESSOR
4K MEMORY - MINIMUM
H315 - CABLE TURN AROUND PLUG (OR EQUIVALENT)
MODEM CABLE - BC01V-X OR BC05C X

SOFTWARE REQUIREMENTS:

THIS DIAGNOSTIC IS DESIGNED TO RUN IN ANY OF THE FOLLOWING WAYS:

STAND ALONE
WITH APT MONITOR
WITH ACT MONITOR
WITH XXDP MONITOR (CHAINABLE)

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1.3 RELATED DOCUMENTS AND STANDARDS.

DIAGNOSTIC ENGINEERING STANDARDS AND CONVENTIONS
APT
ACT
SYSMAC

175 003-009-02
MD-11-DZZMA
AUTOCAT 11-QZAUB
MD-11-DZQAC

1.4 DIAGNOSTIC HIERARCHY PREREQUISITES.
NO SPECIAL DIAGNOSTICS ARE REQUIRED TO RUN BEFORE THIS, BUT THE PROCESSOR, MEMORY, AND BUS ARE ASSUMED TO BE FULLY OPERATIONAL.

1.5 ASSUMPTIONS.

THIS DIAGNOSTIC ASSUMES THAT THE OPERATOR HAS INITIALIZED LOCATION 'IUSWR' AND 'IDEVN' TO THE PROPER VALUES. THE (H) JUMPER MUST BE REMOVED FROM ALL DLV11-E'S UNDER TEST.

2.0 OPERATING INSTRUCTIONS.

2.1 LOADING AND STARTING PROCEDURES.

USE STANDARD PROCEDURE FOR PDP-11 ABSOLUTE BINARY FORMATTED MEDIA.

THIS DIAGNOSTIC HAS ONLY ONE (1) STARTING ADDRESS. 200 FOR START AND RESTART.

THE USER CAN SELECT A SPECIFIC TEST TO BE EXECUTED BY SETTING SWITCH 8 IN THE SWITCH REGISTER AND THE TEST NUMBER (IN OCTAL) IN THE LOWER BYTE. (NOTE: ALL TESTS PREVIOUS TO THE SELECTED ONE ARE EXECUTED WITHOUT ITERATIONS.)

2.2 SPECIAL ENVIRONMENTS.

THIS DIAGNOSTIC FOLLOWS THE STANDARD PROCEDURE FOR RUNNING UOER APT,ACT,XXDP MONITORS, AS DESCRIBED IN THEIR RESPECTIVE PROCEDURES MANUAL AND SYSMAC PACKAGE.

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2.3 OPERATIONAL SWITCH SETTINGS

IF THE DIAGNOSTIC IS RUN ON A CPU WITHOUT A SWITCH REGISTER THEN A SOFTWARE SWITCH REGISTER IS USED WHICH ALLOWS THE USER THE SAME SWITCH OPTIONS AS THE HARDWARE SWITCH REGISTER. IF THE HARDWARE SWITCH REGISTER DOES NOT EXIST OR IF ONE DOES AND IT CONTAINS ALL ONES (177777) THEN THE SOFTWARE SWITCH REGISTER (LOC. 176) IS USED.

CONTROL:

THIS PROGRAM ALSO SUPPORTS THE DYNAMIC LOADING OF THE SOFTWARE SWITCH REGISTER (LOC. 176) FROM THE TTY. THIS CAN BE ACCOMPLISHED BY DOING THE FOLLOWING:

- 1) TYPE CONTROL G <↑G>; THIS WILL ALLOW THE TTY TO ENTER DATA INTO LOC. 176 AT SELECTED POINTS WITHIN THE PROGRAM.
- 2) THE MACHINE WILL THEN TYPE: 'SWR=XXXXXX NEW=' (XXXXXX IS THE OCTAL CONTENTS OF THE SOFTWARE SWITCH REGISTER.)
- 3) AFTER THE 'NEW=' HAS BEEN TYPED THEN THE OPERATOR CAN DO ONE OF THE FOLLOWING AT THE TTY:
 - A) TYPE A NUMBER TO BE LOADED INTO LOC. 176 FOLLOWED BY A <CR>. (ONLY NUMBERS BETWEEN 0-7 WILL BE ACCEPTED). LEADING ZEROS NEED NOT BE TYPED, AND IF MORE THAN 6 DIGITS ARE TYPED THE LAST 6 WILL BE USED. IF A <CR> IS THE FIRST KEY DEPRESSED THE SOFTWARE SWITCH REGISTER CONTENTS WILL NOT BE CHANGED.
 - B) IF A CONTROL U <↑U> IS DEPRESSED THEN THE PROGRAM WILL SEND YOU BACK TO STEP 3.
 - C) IF THE INPUT CHARACTER IS NOT ONE OF THE CHARACTERS MENTIONED ABOVE THEN A QUESTION MARK (?) WILL BE TYPED FOLLOWED BY A CARRAGE RETURN AND A LINE FEED THEN PROCEED FROM STEP 3 (ERASING ALL PREVIOUS INPUT.)

DYNAMIC SWITCH REGISTER

- BIT 15 - HALT ON ERROR
14 - LOOP ON TEST
13 - INHIBIT ERROR TYPEOUTS
12 - (UNUSED)
11 - INHIBIT ITERATIONS
10 - BELL ON ERROR
9 - LOOP ON ERROR
8 - LOOP ON TEST IN SWR<7:0>
7:0 - TEST NUMBER TO LOOP ON (USED WITH BIT 8)

2.4 PROGRAM OPTIONS.

THIS PROGRAM WILL SUPPORT TESTING OF MULTIPLE DLV11-E'S. IT
REQUIRES THE ADDRESS OF THE FIRST RCSR (STORED AT '#BASE') AND
ITS INTERRUPT VECTOR (STORED AT '#VECT1'); AND WILL BE ABLE
TO ADDRESS ANY DLV11-E STARTING AT THE SPECIFIED BASE ADDRESS
UP TO 16 CONSECUTIVE DEVICES.

EXAMPLES: #BASE: 175610
#VECT1: 300

THE PROGRAM WILL BE ABLE TO TEST ANY DLV11-E WITHIN THE
ADDRESS RANGE 175610 --> 176000

#BASE AND #VECT1 DEFAULT TO 175610 AND 300 RESPECTIVELY.
THE PROGRAM ASSOCIATES UNIT NUMBERS AS FOLLOWS: (NUMBERS IN
PARENTHESIS ARE OCTAL)

UNIT#0 -- BASE ADDRESS STORED AT '#BASE'
ASSOCIATED BASE VECTOR STORED AT '#VECT1'
UNIT#1 -- BASE ADDRESS + (10)
BASE VECTOR + (10)

⋮
UP TO

UNIT#15 -- BASE ADDRESS + (170)
BASE VECTOR + (170)

LOCATION '#DEVM' IS USED AS A BIT MAP TO INDICATE WHICH UNIT
NUMBERS ARE PRESENT AND WILL BE TESTED.

BIT 15	BIT 1	BIT 0
!UNIT!	!UNIT!	UNIT!
! 15 !	! #1 !	! #0 !

A BIT MAP CAN BE ENTERED AT '#DEVM' PRIOR TO STARTING THE
PROGRAM.

EXAMPLE:
#BASE: 175610
#VECTOR: 300
#DEVM: 13

THE PROGRAM WILL TEST-

UNIT#0	175610	300
UNIT#1	175620	310
UNIT#3	175640	330

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OPTIONS

LOCATION \$USWR CONTAINS ALL THE USER SELECTABLE OPTIONS. THE VALUES IN THIS WORD MUST CONFORM TO THE ACTUAL BOARD CONFIGURATION. THE DEFAULT VALUE OF \$USWR IS AS FOLLOWS:

BIT POSITION	DEFINITION	DEFAULT VALUE
0-3	#OF DATA BITS	10(8) = 8
4	PARITY ENABLED	0 = NO
5	EVEN ODD PARITY	0 = ODD
6	COMMON SPEED	1 = YES
7	PROGRAMMABLE BAUD RATE	0 = NO
8-11	BAUD RATE OFFSET (SEE FOLLOWING NOTE)	05(8) = 110 BAUD
12	BREAK GENERATION ENABLED	1 = YES
13	CABLE TERMINATED (H315)	1 = YES
14	(-FR) AND (-FD) JUMPERS IN	1 = YES
15	(NOT DEFINED)	

NOTE

THIS DIAGNOSTIC DOES NOT TEST THE PARITY LOGIC.

WHEN THE PROGRAMMABLE BAUD RATE OPTION IS ENABLED THE PROGRAMMABLE BAUD RATE TEST WILL EXIT WITH THE BAUD RATE SET TO THE SELECTED VALUE. TO CHANGE THE DEFAULT VALUE OF 110 BAUD REPLACE BITS <11:8> WITH THE OFFSET INDICATED IN THE TABLE AT THE END OF THE PBR TEST.

DLV11-E INDIVIDUAL TEST REQUIREMENTS

	TESTS NOT EXECUTED	IF BIT =
APT TEST (BIT 1 OF \$ENV)	T25, T37	1
PROGRAMMABLE BAUD RATE (BIT 7 OF \$USWR)	T32	1
BREAK GENERATION CIRCUIT (BIT 12 OF \$USWR)	T2, T40	0
CABLE TERMINATED (BIT 13 OF \$USWR)	T15, T16, T17, T20, T21 T22, T23, PARTS OF T34, T36	1
(-FR) & (-FD) JUMPERS IN (BIT 14 OF \$USWR)	T5, T6, T16, T17, T20, T22, T23	1

ALL OTHER TESTS WILL RUN REGARDLESS OF ANY BIT SETTINGS.

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2.5 EXECUTION TIMES.

EXECUTION TIMES ARE FOR AN LSI-11 PROCESSOR WITH ALL OPTIONS
ENABLED ON THE DLV11-E (EXCEPT FOR PROGRAMMABLE BAUD RATE), AT
110 BAUD.

FIRST PASS- 90 SECONDS
ADDITIONAL PASSES 95 SECONDS
ADDITIONAL DEVICES 95 SECONDS

THE TEST TIME IS BAUD RATE DEPENDANT; HIGHER BAUD GIVES
SHORTER PASS TIMES.

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3.0 ERROR INFORMATION.

3.1 ERROR REPORTING PROCEDURE.

SINCE THIS DIAGNOSTIC WAS DESIGNED TO FIT IN 4 K OF MEMORY THE
ERROR TYPEOUT IS VERY BRIEF. THE FORMAT OF THE ERROR TYPEOUT
IS AS FOLLOWS:

TEST#____,ERROR#____,PC=____,ADDRESS=____,VECTOR=_____

WHERE ALL VALUES TYPED ARE OCTAL.
THE ADDRESS AND VECTOR REFER TO THE FAILING DLV11-E.
FOR FURTHER INFORMATION THE LISTING MUST BE CONSULTED.
BITS 15,13,10 AND 9 OF THE SWITCH REGISTER CONTROL THE
SEQUENCE OF EVENTS AFTER AN ERROR IS CAUGHT.

BIT 15 - CAUSES THE PROGRAM TO HALT IN THE ERROR
ROUTINE. CONTINUEING THE PROGRAM CAUSES IT TO
PROCEED.

BIT 13 - DISABLES THE PRINTING OF THE ERROR MESSAGE.

BIT 10 - CAUSES THE BELL TO RING ON ERROR.

BIT 9 - CAUSES THE DIAGNOSTIC TO LOOP FROM BEGINNING
OF TEST TO ERROR.

THE ERROR ROUTINE SUPPORTS THE CONTROL G FUNCTION.

3.2 ERROR HALTS.

THE ONLY HALT IN THIS DIAGNOSTIC IS IN THE ERROR ROUTINE, AND
IS EXECUTED ONLY IF BIT 15 OF THE SWITCH REGISTER IS A ONE
WHEN AN ERROR OCCURS.

4.0 PERFORMANCE AND PROGRESS REPORTS.

4.1 PERFORMANCE REPORTS.

AS EACH DEVICE COMPLETES ONE PASS OF THE DIAGNOSTIC THE
FOLLOWING WILL BE TYPED:

CSR:____.VECTOR:____.ERRORS:_____

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WHERE. 'CSR:-----' IS THE DEVICE CSR UNDER TEST
'VECTOR:---' IS THE ASSOCIATED VECTOR
AND 'ERRORS:---' IS THE TOTAL NUMBER OF ERRORS ON THIS DEVICE
ON THIS PASS.

NOTE

THIS IS TYPED AFTER THE DEVICE HAS COMPLETED ITS PASS.
AFTER ALL DEVICES HAVE BEEN EXERCISED AN END PASS STATEMENT IS
TYPED.
"ENDPASS#-----"

5.0 DEVICE INFORMATION TABLES.

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	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
RCSR:	DATA	RING	CLR	CAR	RCVR	REC			RCVR	RCVR	DATA		SEC	REQ	DTR	
	INT		SEND	DET	ACT	REC			DONE	IE	IE		XMIT	SEND		
RBUF:	ERR	OR	FR	P												
	R	ERR	ERR	ERR												
TCSR:	PROGRAMMABLE BAUD			PBR					XMIT	XMIT				MAIN		BREA
		RATE	SELECT	ENAB					RDY	IE				T		K
TBUF:																

NOTE

BLANK BOXES INDICATE UNUSED AND RESERVED BIT POSITIONS. SEE THE LISTING FOR AN EXPLANATION OF THE BITS.

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6.0 SUMMARY OF TESTS AND SPECIAL SUBROUTINES.

TEST 1 ADDRESSABILITY

THIS TEST VERIFIES THAT THE ADDRESS AS PLACED IN THE
HARDWARE P-TABLE TO BE CORRECT AND THE DLV11-E
RESPONDS TO THAT ADDRESS SPACE.

THE FOLLOWING 8 TESTS TEST ALL 'READ WRITE' BITS

TEST 2 BREAK - TCSRO SET, CLEAR, RESET

TEST 3 MAINT - TCSR2 SET, CLEAR, RESET

TEST 4 XMITIE - TCSR6 SET, CLEAR, RESET

TEST 5 DTR - RCSR1 SET, CLEAR

NOTE

RESET DOES NOT CLEAR THIS BIT. WE CANNOT TEST
FOR AN INITIAL CONDITION AS THIS BIT IS
UNDEFINED UPON POWER UP AND INIT DOESN'T
AFFECT IT.

TEST 6 REQSEND - RCSR2 SET, CLEAR, RESET

THIS TEST ASSUMES THAT JUMPER FR IS IN.

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TEST 7 SECXMIT - RCSR3 SET, CLEAR, RESET

TEST 10 DATAIE - RCSR5 SET, CLEAR, RESET
---- --

TEST 11 RCVRIE - RCSR6 SET, CLEAR, RESET
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THE FOLLOWING 4 TESTS VERIFY THAT RESET (INIT) INITIALIZES
READ ONLY BITS.

TEST 12 RCVRDONE - RCSR 7 - IS CLEARED BY INIT
---- --

TEST 13 XMITRDY - TCSR 7 - IS SET BY INIT
---- --

TEST 14 DATAINT - RCSR 15 - IS CLEARED BY INIT.
---- --

TEST 15 RCVRACT - RCSR 11 - IS CLEARED BY INIT
---- --

THE FOLLOWING 4 TESTS VERIFY THAT THE EIA SIGNALS CAN BE
TRANSMITTED AND RECEIVED THROUGH THE CABLE.

TEST 16 CARDET SETS AND CLEARS AS DTR SETS AND CLEARS
---- --

TEST 17 CLRSEND SETS AND CLEARS AS DTR SETS AND CLEARS
---- --

TEST 20 RING SETS AND CLEARS AS REQSEND SETS AND CLEARS
---- --

TEST 21 SECREC SETS AND CLEARS AS SECXMIT SETS AND CLEARS
---- --

TEST 22 DATAINT (RCSR-15) SETS WHEN DTR CHANGES STATE AND THAT
---- --
DATAINT IS CLEARED AFTER READING RCSR

NOTE

DTR IS TIED TO BOTH CARDET AND CLRSEND BY THE
M315.

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TEST 23 DATAINT SETS WHEN RING SETS AND THAT DATAINT

DOES NOT SET WHEN RING CLEARS

TEST 24 DATAINT SETS WHEN SECREC CHANGES STAT:

02

541 TEST 25 XMIT RDY - TCSR 7 CLEARS WHEN TBUF IS LOADED
542 ---- --
543 WITH A CHARACTER AND THAT IT SETS WITHIN A
544 REASONABLE AMOUNT OF TIME.
545
546 TEST 26 OUTPUTTING A CHAR FROM TBUF (WITH MAINT SET)
547 ---- --
548 RESULTS IN RCVRDONE SETTING WITHIN A
549 REASONABLE AMOUNT OF TIME AND THAT RESET
550 CLEARS THE BIT.
551
552 TEST 27 RCVRDONE IS CLEARED BY READING RBUF
553 ---- --
554
555 TEST 30 RCVRACT - RCSR 11 - SETS WHEN A START BIT IS
556 ---- --
557 RECEIVED AND CLEARS WHEN RCVRDONE - RCSR 7 -
558 SETS
559
560 TEST 31 OVERRUN BIT RBUF 14
561 ---- --
562
563 TEST 32 PROGRAMMABLE BAUD RATE TEST TEST AT ALL SPEEDS
564 ---- --
565 AVAILABLE A COMPARISON WILL BE MADE TO SEE IF
566 NEW TIME IS LESS THAN PREVIOUS.
567
568 TEST 33 TRANSMITTER INTERRUPT LOGIC TEST
569 ---- --
570 LOGICALLY THIS IS 4 SEPARATE TESTS
571 A) DOES TRANSMITTER INTERRUPT LOGIC WORK
572 B) AT PRIORITY OF 0
573 C) AND ONLY ONCE
574 D) BUT NOT WITH INTERRUPT ENABLE CLEAR
575
576 TEST 34 RECEIVER INTERRUPT LOGIC TEST THIS TEST COVERS ALL
577 ---- --
578 OF THE RECEIVER SIDE OF THE INTERRUPT LOGIC, BOTH
579 DATASET AND CHARACTER MODES.
580
581 TEST 35 TEST ACTUAL DATA TRANSFERED NON-INTERRUPT
582 ---- --
583 MAINTENANCE BIT SET
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593 TEST 36 TEST DATA THROUGH CABLE
594 ----
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597 TEST 37 FULL DATA TRANSFER WITH INTERRUPTS AND MAINTENANCE
598 ----
599 MODE.
600
601
602 TEST 40 TEST BREAK GENERATION LOGIC TRANSMIT KNOWN CHAR
603 ----
604 WITH BREAK SET AND COMPARE RECEIVED WITH 0.
605
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607 TEST 41 NOT A TEST - SEND BACK TO LOOP
608 ----
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610

NOTE

FOR ALL OF THE FOLLOWING ROUTINES THE USE
OF (R5) IS PART OF THE LINKAGE MECHANISM
BETWEEN THE CALLER AND THE CALLED.

ROUTINE:TIMER

THIS ROUTINE IS USED TO TEST THE STATUS OF
ANY BIT IN ANY REGISTER.

INPUTS:

HOWLONG THE MAXIMUM AMOUNT OF TIME TO
SPEND IN THIS ROUTINE.
WHICHBIT A MASK WITH THE BIT(S) SET THAT
ARE TO BE CHECKED
REG A POINTER TO THE REGISTER TO BE
CHECKED
SFTCLR THE DESIRED RESULTS -- EITHER SET
OR CLEAR

OUTPUT:

THE 'C' BIT IS SET TO INDICATE AN ERROR BUT IT
IS TESTED BY THE IF.ERROR STATEMENT.

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CVDVAO.P11 12-JUL-84 05:04

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ROUTINE:DATLNG

THIS ROUTINE SETS UP A MASK FOR DATA, WITH
INPUT: NOTHING IS PASSED TO THIS ROUTINE BUT GLOBAL
INFORMATION IS ASSUMED TO EXIST:
\$USWR-- THE WORD FOR SOFTWARE PARAMETERS
DATA-- A MASK FOR THE LOCATION OF THE OCTAL
NUMBER OF DATA BITS
OUTPUT-----
MASK-- A MASK OF BINARY ZEROS RIGHT-JUSTIFIED
THE NUMBER OF WHICH IS DEFINED IN \$USWR WORD.

ROUTINE:WAIT

THIS ROUTINE IS USED TO DELAY EXECUTION OF THE
MAIN PROGRAM FOR A SPECIFIED AMOUNT OF TIME.
THIS IS ACCOMPLISHED BY INCREMENTING A
REGISTER UP TO A LIMIT. THE INNER LOOP IS SET
TO APPROXIMATE 1 MILLI SEC.

SERVICE ROUTINE: INTSRV

THIS GLOBAL ROUTINE DOES NOTHING BUT INCREMENT
'INTFLAG' EACH TIME IT IS CALLED. IT ASSUMES
THAT THE MAIN CALLING ROUTINE WILL KNOW WHAT
TO LOOK FOR.

ROUTINE:CYCLE

THIS ROUTINE CAUSES ADRS TO POINT TO THE
ADDRESS OF DLV11-E UNDER TEST, ADRS +2 TO
POINT TO THE VECTOR OF THE DLV11-E UNDER TEST.
IT KEEPS TRACK OF THE CURRENT DEVICE AND BIT
MASKS.

```

678      @
679      .TITLE MAINDEC-ZZ-CVDVA-D
680      ;*COPYRIGHT (C) 1977
681      ;*DIGITAL EQUIPMENT CORP.
682      ;*MAYNARD, MASS. 01754
683      ;*
684      ;*PROGRAM BY ODES CHOATE
685      ;*
686      ;*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
687      ;*PACKAGE (MAINDEC-11-DZQAC-C3), JAN 19, 1977.
688      ;*
689      .SBTTL OPERATIONAL SWITCH SETTINGS
690      ;*
691      ;*      SWITCH      USE
692      ;*      -----      -----
693      ;*      15      HALT ON ERROR
694      ;*      14      LOOP ON TEST
695      ;*      13      INHIBIT ERROR TYPEOUTS
696      ;*      11      INHIBIT ITERATIONS
697      ;*      10      BELL ON ERROR
698      ;*      9      LOOP ON ERROR
699      ;*      8      LOOP ON TEST IN SWR<7:0>
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701      .SBTTL BASIC DEFINITIONS
702
703      ;*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
704      001100      STACK= 1100
705      .EQUIV EMT,ERROR      ;;BASIC DEFINITION OF ERROR CALL
706      .EQUIV IOT,SCOPE      ;;BASIC DEFINITION OF SCOPE CALL
707
708      ;*MISCELLANEOUS DEFINITIONS
709      000011      HT= 11      ;;CODE FOR HORIZONTAL TAB
710      000012      LF= 12      ;;CODE FOR LINE FEED
711      000015      CR= 15      ;;CODE FOR CARRIAGE RETURN
712      000200      CRLF= 200      ;;CODE FOR CARRIAGE RETURN-LINE FEED
713      177776      PS= 177776      ;;PROCESSOR STATUS WORD
714      .EQUIV PS,PSW
715      177774      STKLMT= 177774      ;;STACK LIMIT REGISTER
716      177772      PIRQ= 177772      ;;PROGRAM INTERRUPT REQUEST REGISTER
717      177570      DSWR= 177570      ;;HARDWARE SWITCH REGISTER
718      177570      DDISP= 177570      ;;HARDWARE DISPLAY REGISTER
719
720      ;*GENERAL PURPOSE REGISTER DEFINITIONS
721      000000      R0= #0      ;;GENERAL REGISTER
722      000001      R1= #1      ;;GENERAL REGISTER
723      000002      R2= #2      ;;GENERAL REGISTER
724      000003      R3= #3      ;;GENERAL REGISTER
725      000004      R4= #4      ;;GENERAL REGISTER
726      000005      R5= #5      ;;GENERAL REGISTER
727      000006      R6= #6      ;;GENERAL REGISTER
728      000007      R7= #7      ;;GENERAL REGISTER
729      000006      SP= #6      ;;STACK POINTER
730      000007      PC= #7      ;;PROGRAM COUNTER
731
732      ;*PRIORITY LEVEL DEFINITIONS
733      000000      PRO= 0      ;;PRIORITY LEVEL 0

```

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734	000040	PR1=	40	::	PRIORITY LEVEL 1
735	000100	PR2=	100	::	PRIORITY LEVEL 2
736	000140	PR3=	140	::	PRIORITY LEVEL 3
737	000200	PR4=	200	::	PRIORITY LEVEL 4
738	000240	PR5=	240	::	PRIORITY LEVEL 5
739	000300	PR6=	300	::	PRIORITY LEVEL 6
740	000340	PR7=	340	::	PRIORITY LEVEL 7

741
 742 ;+ "SWITCH REGISTER" SWITCH DEFINITIONS

743	100000	SW15=	100000
744	040000	SW14=	40000
745	020000	SW13=	20000
746	010000	SW12=	10000
747	004000	SW11=	4000
748	002000	SW10=	2000
749	001000	SW09=	1000
750	000400	SW08=	400
751	000200	SW07=	200
752	000100	SW06=	100
753	000040	SW05=	40
754	000020	SW04=	20
755	000010	SW03=	10
756	000004	SW02=	4
757	000002	SW01=	2
758	000001	SW00=	1
759		.EQUIV	SW09,SW9
760		.EQUIV	SW08,SW8
761		.EQUIV	SW07,SW7
762		.EQUIV	SW06,SW6
763		.EQUIV	SW05,SW5
764		.EQUIV	SW04,SW4
765		.EQUIV	SW03,SW3
766		.EQUIV	SW02,SW2
767		.EQUIV	SW01,SW1
768		.EQUIV	SW00,SW0

770 ;+ DATA BIT DEFINITIONS (BIT00 TO BIT15)

771	100000	BIT15=	100000
772	040000	BIT14=	40000
773	020000	BIT13=	20000
774	010000	BIT12=	10000
775	004000	BIT11=	4000
776	002000	BIT10=	2000
777	001000	BIT09=	1000
778	000400	BIT08=	400
779	000200	BIT07=	200
780	000100	BIT06=	100
781	000040	BIT05=	40
782	000020	BIT04=	20
783	000010	BIT03=	10
784	000004	BIT02=	4
785	000002	BIT01=	2
786	000001	BIT00=	1
787		.EQUIV	BIT09,BIT9
788		.EQUIV	BIT08,BIT8
789		.EQUIV	BIT07,BIT7

```

790 .EQUIV BIT06,BIT6
791 .EQUIV BIT05,BIT5
792 .EQUIV BIT04,BIT4
793 .EQUIV BIT03,BIT3
794 .EQUIV BIT02,BIT2
795 .EQUIV BIT01,BIT1
796 .EQUIV BIT00,BIT0

```

;*BASIC "CPU" TRAP VECTOR ADDRESSES

```

797
798
799 000004 ERRVEC= 4 ;:TIME OUT AND OTHER ERRORS
800 000010 RESVEC= 10 ;:RESERVED AND ILLEGAL INSTRUCTIONS
801 000014 TBITVEC=14 ;:"T" BIT
802 000014 TRTVEC= 14 ;:TRACE TRAP
803 000014 BPTVEC= 14 ;:BREAKPOINT TRAP (BPT)
804 000020 IOTVEC= 20 ;:INPUT/OUTPUT TRAP (IOT) **SCOPE**
805 000024 PWRVEC= 24 ;:POWER FAIL
806 000030 EMTVEC= 30 ;:EMULATOR TRAP (EMT) **ERROR**
807 000034 TRAPVEC=34 ;:"TRAP" TRAP
808 000060 TKVEC= 60 ;:TTY KEYBOARD VECTOR
809 000064 TPVEC= 64 ;:TTY PRINTER VECTOR
810 000240 PIRQVEC=240 ;:PROGRAM INTERRUPT REQUEST VECTOR

```

```

ILLMEM= 4
ADRS= P1
GOOD= R2
BAD= R3
REGISTER=R1
BIT= R2
FUNCT= R3
LEAD= R2
FOLLOW= R4
DLADDR= 175610

```

; THE FOLLOWING DEFINITIONS APPLY TO THE GLOBAL SUBS

```

822
823 177777 SET= -1
824 000000 CLR= 0

```

```

;*****
; RCSR REGISTER BIT NAMES
;*****

```

```

829
830 100000 DATAINT= BIT15 ; DATASET INTERRUPT
831 040000 RING= BIT14 ; RINGING SIGNAL INDICATOR
832 020000 CLRSEND= BIT13 ; CLEAR TO SEND FROM DATASET
833 010000 CARDET= BIT12 ; CARRIER DETECT
834 004000 RCVRACT= BIT11 ; RECEIVER ACTIVE INDICATOR
835 002000 SECRC= BIT10 ; SECONDARY RECEIVE
836 ; UNUSED BIT09
837 ; UNUSED BIT08
838 000200 RCVRDONE= BIT07 ; RECEIVER DONE
839 000100 RCVRIE= BIT06 ; RECEIVER INTERRUPT ENABLE
840 000040 DATAIE= BIT05 ; DATASET INTERRUPT ENABLE
841 ; UNUSED BIT04
842 000010 SECXMIT= BIT03 ; SECONDARY TRANSMIT DATA
843 000004 REQSEND= BIT02 ; REQUEST TO SEND
844 000002 DTR= BIT01 ; DATA TERMINAL READY
845 ; UNUSED BIT00

```

```

846
847 ;*****
848 ; RBUF REGISTER BIT NAMES
849 ;*****
850 100000 ERROR= BIT15 ; ERROR INDICATOR
851 040000 ORERR= BIT14 ; OVERRUN ERROR
852 020000 FRERR= BIT13 ; FRAMING ERROR
853 010000 PERR= BIT12 ; PARITY ERROR
854 ; UNUSED BIT11
855 ; UNUSED BIT10
856 ; UNUSED BIT09
857 ; UNUSED BIT08
858 000200 RDATA7= BIT07 ; \
859 000100 RDATA6= BIT06 ; |
860 000040 RDATA5= BIT05 ; |
861 000020 RDATA4= BIT04 ; | RECEIVED DATA BITS
862 000010 RDATA3= BIT03 ; /
863 000004 RDATA2= BIT02 ; |
864 000002 RDATA1= BIT01 ; |
865 000001 RDATA0= BIT00 ; /
866
867 ;*****
868 ; TCSR REGISTER BIT NAMES
869 ;*****
870 100000 PBAUD3= BIT15 ; \
871 040000 PBAUD2= BIT14 ; | PROGRAMMABLE BAUD
872 020000 PBAUD1= BIT13 ; | RATE BITS
873 010000 PBAUD0= BIT12 ; /
874 004000 PBAUDSET= BIT11 ; ENABLE SETTING OF
; PROGRAMMABLE BAUDE RATE
875 ; UNUSED BIT10
876 ; UNUSED BIT09
877 ; UNUSED BIT08
878 ; UNUSED BIT07 ; TRANSMITTER READY
879 000200 XMTRDY= BIT07 ; TRANSMITTER INTERRUPT ENABLE
880 000100 XMTRIE= BIT06
881 ; UNUSED BIT05
882 ; UNUSED BIT04
883 ; UNUSED BIT03
884 000004 MAINT= BIT02 ; MAINTENANCE SET BIT
885 ; UNUSED BIT01
886 000001 BREAK= BIT00 ; SEND BREAK (CONTINUOUS SPACE)
887
888
889 ;*****
890 ; TBUF REGISTER BIT NAMES
891 ;*****
892 ; UNUSED BIT15

```

```

893 ; UNUSED BIT14
894 ; UNUSED BIT13
895 ; UNUSED BIT12
896 ; UNUSED BIT11
897 ; UNUSED BIT10
898 ; UNUSED BIT09
899 ; UNUSED BIT08
900 000200 TDATA7= BIT07 ; \
901 000100 TDATA6= BIT06 ; !
902 000040 TDATA5= BIT05 ; !
903 000020 TDATA4= BIT04 ; \ TRANSMITTER DATA BUFFER
904 000010 TDATA3= BIT03 ; /
905 000004 TDATA2= BIT02 ; !
906 000002 TDATA1= BIT01 ; !
907 000001 TDATA0= BIT00 ; /

```

```

;*****
; FLAG BITS TO BE USE OR CLEARED IN $USWR.

```

```

912 000017 DATA = 17
914 000020 PARITY = 20
915 000040 EVENODD = 40
916 000100 COMSPD = 100
917 000200 PBR = 200

; BAUDE MUST BE ON THE UPPER
; BYTE BOUNDARY OF $USWR.--4 BITS
921 007400 BAUD = 7400
922 010000 BRK = 10000
923 020000 CABLE = 20000
924 040000 FRFD = 40000

```

```

;*****
.SBTTL TRAP CATCHER

```

```

928 000000 .=0
929 ;*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
930 ;*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
931 ;*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS
932 .=174
933 000174 000000 DISPREG: .WORD 0 ;:SOFTWARE DISPLAY REGISTER
934 000176 000000 SWREG: .WORD 0 ;:SOFTWARE SWITCH REGISTER
935 .SBTTL STARTING ADDRESS(ES)
936 000200 000137 001336 JMP @START ;:JUMP TO STARTING ADDRESS OF PROGRAM
937 .SBTTL ACT11 HOOKS

```

```

;*****
;HOOKS REQUIRED BY ACT11

```

```

941 000204 $SVPC=. ;SAVE PC
942 000046 .=46
943 000046 012452 $ENDAD ;:1)SET LOC.46 TO ADDRESS OF $ENDAD IN .EOP
944 000052 .=52
945 000052 000000 .WORD 0 ;:2)SET LOC.52 TO ZERO
946 000204 .=$SVPC ;: RESTORE PC
947 001000 .=1000
948 .SBTTL APT PARAMETER BLOCK

```

```

949
950 ;*****
951 ;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
952 ;*****
953     .X=      ;;SAVE CURRENT LOCATION
954     =24     ;;SET POWER FAIL TO POINT TO START OF PROGRAM
955 000024    000200      200      ;;FOR APT START UP
956     =44     ;;POINT TO APT INDIRECT ADDRESS PNTR.
957 000044    001000    $APHDR    ;;POINT TO APT HEADER BLOCK
958     =.X     ;;RESET LOCATION COUNTER
959 ;*****
960 ;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
961 ;INTERFACE SPEC.
962
963 001000    $APTHD:
964 001000    000000    $HIBTS: .WORD 0      ;;TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
965 001002    001174    $MBADR: .WORD $MAIL  ;;ADDRESS OF APT MAILBOX (BITS 0-15)
966 001004    000005    $TSTM:  .WORD 5      ;;RUN TIM OF LONGEST TEST
967 001006    000055    $PASTM: .WORD 45.    ;;RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
968 001010    000036    $UNITM: .WORD 30.    ;;ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
969 001012    000030    .WORD $ETEND-$MAIL/2 ;;LENGTH MAILBOX-ETABLE(WORDS)

```


.SBTTL COMMON TAGS

;*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS

;*USED IN THE PROGRAM.

970
971
972
973
974
975
976 001100
977 001100
978 001100 000000
979 001102 000
980 001103 000
981 001104 000000
982 001106 000000
983 001110 000000
984 001112 000000
985 001114 000
986 001115 001
987 001116 000000
988 001120 000000
989 001122 000000
990 001124 000000
991 001126 000000
992 001130 000000
993 001132 000000
994 001134 000
995 001135 000
996 001136 000000
997 001140 177570
998 001142 177570
999 001144 177560
1000 001146 177562
1001 001150 177564
1002 001152 177566
1003 001154 000
1004 001155 002
1005 001156 012
1006 001157 000
1007 001160 000000
1008 001162 000000
1009 001164 177607 000377
1010 001170 077
1011 001171 015
1012 001172 000012
1013
1014
1015
1016
1017
1018 001174
1019 001174 000000
1020 001176 000000
1021 001200 000000
1022 001202 000000
1023 001204 000000
1024 001206 000000
1025 001210 000000

.-1100

\$CHTAG: ;:START OF COMMON TAGS

.WORD 0 ;:CONTAINS THE TEST NUMBER
\$TSTNM: .BYTE 0 ;:CONTAINS ERROR FLAG
\$ERFLG: .BYTE 0 ;:CONTAINS SUBTEST ITERATION COUNT
\$ICNT: .WORD 0 ;:CONTAINS SCOPE LOOP ADDRESS
\$LPADR: .WORD 0 ;:CONTAINS SCOPE RETURN FOR ERRORS
\$LPERR: .WORD 0 ;:CONTAINS TOTAL ERRORS DETECTED
\$ERTTL: .WORD 0 ;:CONTAINS ITEM CONTROL BYTE
\$ITEMB: .BYTE 0 ;:CONTAINS MAX. ERRORS PER TEST
\$ERMAX: .BYTE 1 ;:CONTAINS PC OF LAST ERROR INSTRUCTION
\$ERRPC: .WORD 0 ;:CONTAINS ADDRESS OF 'GOOD' DATA
\$GADR: .WORD 0 ;:CONTAINS ADDRESS OF 'BAD' DATA
\$BDADR: .WORD 0 ;:CONTAINS 'GOOD' DATA
\$GDDAT: .WORD 0 ;:CONTAINS 'BAD' DATA
\$BDDAT: .WORD 0 ;:RESERVED--NOT TO BE USED
;:RESERVED--NOT TO BE USED
\$AUTOB: .BYTE 0 ;:AUTOMATIC MODE INDICATOR
\$INTAG: .BYTE 0 ;:INTERRUPT MODE INDICATOR
;:RESERVED--NOT TO BE USED
\$SWR: .WORD DSWR ;:ADDRESS OF SWITCH REGISTER
\$DISPLAY: .WORD DDISP ;:ADDRESS OF DISPLAY REGISTER
\$TKS: 177560 ;:TTY KBD STATUS
\$TKB: 177562 ;:TTY KBD BUFFER
\$TPS: 177564 ;:TTY PRINTER STATUS REG. ADDRESS
\$TPB: 177566 ;:TTY PRINTER BUFFER REG. ADDRESS
\$NULL: .BYTE 0 ;:CONTAINS NULL CHARACTER FOR FILLS
\$FILLS: .BYTE 2 ;:CONTAINS # OF FILLER CHARACTERS REQUIRED
\$FILLC: .BYTE 12 ;:INSERT FILL CHARS. AFTER A "LINE FEED"
\$TPFLG: .BYTE 0 ;:"TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
\$TIMES: 0 ;:MAX. NUMBER OF ITERATIONS
\$ESCAPE: 0 ;:ESCAPE ON ERROR ADDRESS
\$BELL: .ASCIZ <207><377><377> ;:CODE FOR BELL
\$QUES: .ASCII /?/ ;:QUESTION MARK
\$CRLF: .ASCII <15> ;:CARRIAGE RETURN
\$LF: .ASCIZ <12> ;:LINE FEED

.SBTTL APT MAILBOX-ETABLE

.EVEN
\$MAIL: ;:APT MAILBOX
\$MSGTY: .WORD AMSGTY ;:MESSAGE TYPE CODE
\$FATAL: .WORD AFATAL ;:FATAL ERROR NUMBER
\$TESTN: .WORD ATESTN ;:TEST NUMBER
\$PASS: .WORD APASS ;:PASS COUNT
\$DEVCT: .WORD ADEVCT ;:DEVICE COUNT
\$UNIT: .WORD AUNIT ;:I/O UNIT NUMBER
\$MSGAD: .WORD AMSGAD ;:MESSAGE ADDRESS

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1026	001212	000000	\$MSGLG: .WORD	AMSGLG	::MESSAGE LENGTH
1027	001214		\$ETABLE:		::APT ENVIRONMENT TABLE
1028	001214	000	\$ENV: .BYTE	AENV	::ENVIRONMENT BYTE
1029	001215	000	\$ENVM: .BYTE	AENVM	::ENVIRONMENT MODE BITS
1030	001216	000000	\$SWREG: .WORD	ASWREG	::APT SWITCH REGISTER
1031	001220	071110	\$USWR: .WORD	AUSWR	::USER SWITCHES
1032	001222	000000	\$CPUOP: .WORD	ACPUOP	::CPU TYPE,OPTIONS
1033			::*		BITS 15-11-CPU TYPE
1034			::*		11/04-01,11/05-02,11/20-03,11/40-04,11/45-05
1035			::*		11/70-06,PDQ=07,Q=10
1036			::*		BIT 10-REAL TIME CLOCK
1037			::*		BIT 9-FLOATING POINT PROCESSOR
1038			::*		BIT 8-MEMORY MANAGEMENT
1039	001224	000	\$MAMS1: .BYTE	AMAMS1	::HIGH ADDRESS,M.S. BYTE
1040	001225	000	\$MTYP1: .BYTE	AMTYP1	::MEM. TYPE,BLK#1
1041			::*		MEM.TYPE P TE -- (HIGH BYTE)
1042			::*		900 NSEC CORE=001
1043			::*		300 NSEC BIPOLAR=002
1044			::*		500 NSEC MOS=003
1045	001226	000000	\$MADR1: .WORD	AMADR1	::HIGH ADDRESS,BLK#1
1046			::*		MEM.LAST ADDR.=3 BYTES,THIS WORD AND LOW OF "TYPE" ABOVE
1047	001230	000	\$MAMS2: .BYTE	AMAMS2	::HIGH ADDRESS,M.S. BYTE
1048	001231	000	\$MTYP2: .BYTE	AMTYP2	::MEM. TYPE,BLK#2
1049	001232	000000	\$MADR2: .WORD	AMADR2	::MEM.LAST ADDRESS,BLK#2
1050	001234	000	\$MAMS3: .BYTE	AMAMS3	::HIGH ADDRESS,M.S.BYTE
1051	001235	000	\$MTYP3: .BYTE	AMTYP3	::MEM. TYPE,BLK#3
1052	001236	000000	\$MADR3: .WORD	AMADR3	::MEM.LAST ADDRESS,BLK#3
1053	001240	000	\$MAMS4: .BYTE	AMAMS4	::HIGH ADDRESS,M.S.BYTE
1054	001241	000	\$MTYP4: .BYTE	AMTYP4	::MEM. TYPE,BLK#4
1055	001242	000000	\$MADR4: .WORD	AMADR4	::MEM.LAST ADDRESS,BLK#4
1056	001244	000300	\$VECT1: .WORD	AVECT1	::INTERRUPT VECTOR#1,BUS PRIORITY#1
1057	001246	000000	\$VECT2: .WORD	AVECT2	::INTERRUPT VECTOR#2BUS PRIORITY#2
1058	001250	175610	\$BASE: .WORD	ABASE	::BASE ADDRESS OF EQUIPMENT UNDER TEST
1059	001252	000001	\$DEVH: .WORD	ADEVH	::DEVICE MAP
1060	001254		\$ETEND:		
1061			.MEXIT		

```

1062
1063
1064
1065
1066
1067
1068
1069
1070
1071
1072
1073
1074
1075
1076 001254
1077
1078 001254 175610
1079 001256 000300
1080 001260 175610
1081 001262 175612
1082 001264 175614
1083 001266 175615
1084 001270 175616
1085 001272 000000
1086 001274 000020
1087 001334 000000
1088 001336
1089
1090
1091 001336 012706 001100
1092 001342 005026
1093 001344 022706 001140
1094 001350 001374
1095 001352 012706 001100
1096
1097 001356 012737 014374 000020
1098 001364 012737 000340 000022
1099 001372 012737 014174 000030
1100 001400 012737 000340 000032
1101 001406 012737 015326 000034
1102 001414 012737 000340 000036
1103 001422 012737 012506 000024
1104 001430 012737 000340 000026
1105 001436 016767 010756 010746
1106 001444 005067 177510
1107 001450 005067 177506
1108 001454 112767 000001 177433
1109 001462 012767 001462 177416
1110 001470 012767 001470 177412
1111
1112
1113 001476 013746 000004
1114 001502 012737 001536 000004
1115 001510 012767 177570 177422
1116 001516 012767 177570 177416
1117 001524 022777 177777 177406

```

.SBTTL ERROR POINTER TABLE

```

; THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
; THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
; LOCATION #ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
; NOTE1: IF #ITEMB IS 0 THE ONLY PERTINENT DATA IS (#ERRPC).
; NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

```

```

;# EM ;POINTS TO THE ERROR MESSAGE
;# DH ;POINTS TO THE DATA HEADER
;# DT ;POINTS TO THE DATA
;# DF ;POINTS TO THE DATA FORMAT

```

#ERRTB:

```

;; GLOBAL DATA
DLADD: DLADDR
DLVEC: 300
RCSR: DLADDR * 0
RBUF: DLADDR * 2
TCSR: DLADDR * 4
TCSRMI: DLADDR * 5
TBUF: DLADDR * 6
I: 0
.BLKW 20 ;FOR R5 STACK
RSSTACK: .WORD 0

```

START:

```

.SBTTL INITIALIZE THE COMMON TAGS
;; CLEAR THE COMMON TAGS (#CHTAG) AREA
MOV #CHTAG,R6 ;FIRST LOCATION TO BE CLEARED
CLR (R6) ;CLEAR MEMORY LOCATION
CMP #SWR,R6 ;DONE?
BNE --6 ;LOOP BACK IF NO
MOV #STACK,SP ;SETUP THE STACK POINTER
;; INITIALIZE A FEW VECTORS
MOV #SCOPE,#IOTVEC ;IOT VECTOR FOR SCOPE ROUTINE
MOV #340,#IOTVEC+2 ;LEVEL 7
MOV #ERROR,#EMTVEC ;EMT VECTOR FOR ERROR ROUTINE
MOV #340,#EMTVEC+2 ;LEVEL 7
MOV #TRAP,#TRAPVEC ;TRAP VECTOR FOR TRAP CALLS
MOV #340,#TRAPVEC+2 ;LEVEL 7
MOV #PWRON,#PWRVEC ;POWER FAILURE VECTOR
MOV #340,#PWRVEC+2 ;LEVEL 7
MOV #ENDCT,#EOPCT ;SETUP END-OF-PROGRAM COUNTER
CLR #TIMES ;INITIALIZE NUMBER OF ITERATIONS
CLR #ESCAPE ;CLEAR THE ESCAPE ON ERROR ADDRESS
MOV #1,#ERRMAX ;ALLOW ONE ERROR PER TEST
MOV #.,#LPADR ;INITIALIZE THE LOOP ADDRESS FOR SCOPE
MOV #.,#LPERR ;SETUP THE ERROR LOOP ADDRESS
;; SIZE FOR A HARDWARE SWITCH REGISTER, IF NOT FOUND OR IT IS
;; EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER.
MOV #ERRVEC,-(SP) ;SAVE ERROR VECTOR
MOV #64,#EPRVEC ;SET UP ERROR VECTOR
MOV #DSWR,#SWR ;SETUP FOR A HARDWARE SWICH REGISTER
MOV #DDISP,#DISPLAY ;AND A HARDWARE DISPLAY REGISTER
CMP #1,#BSWR ;TRY TO REFERENCE HARDWARE SWR

```

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```

1118 001532 001012          BNE      661          ;;BRANCH IF NO TIMEOUT TRAP OCCURRED
1119                                ;;AND THE HARDWARE SWR IS NOT = -1
1120 001534 000403          BR       651          ;;BRANCH IF NO TIMEOUT
1121 001536 012716 001544 641:  MOV      0651,(SP)  ;;SET UP FOR TRAP RETURN
1122 001542 000002          RTI
1123 001544 012767 000176 177366 651:  MOV      0$MREG,SWR  ;;POINT TO SOFTWARE SWR
1124 001552 012767 000174 177362  MOV      00DISPREG,DISPLAY
1125 001560 012637 000004 661:  MOV      (SP)+,0$ERRVEC  ;;RESTORE ERROR VECTOR
1126
1127 001564 005067 177412          CLR      1$PASS      ;;CLEAR PASS COUNT
1128 001570 132767 000200 177417  BITB     0$APTSIZE,1$ENVM  ;;TEST USER SIZE UNDER APT
1129 001576 001403          BEQ      671          ;;YES,USE NON-APT SWITCH
1130 001600 012767 001216 177332  MOV      01$MREG,SWR  ;;NO,USE APT SWITCH REGISTER
1131 001606
1132
1133 .SBTTL  TYPE PROGRAM NAME
1134 ;;TYPE THE NAME OF THE PROGRAM IF FIRST PASS
1134 001606 005227 177777          INC      0-1          ;;FIRST TIME?
1135 001612 001051          BNE      681          ;;BRANCH IF NO
1136 001614 022737 012452 000042  CMP      01$ENDAD,0042  ;;ACT-11?
1137 001622 001445          BEQ      681          ;;BRANCH IF YES
1138 001624 104401 001672          TYPE     .691          ;;TYPE ASCIZ STRING
1139 .SBTTL  GET VALUE FOR SOFTWARE SWITCH REGISTER
1140 001630 005737 000042          TST      0042          ;;ARE WE RUNNING UNDER XXDP/ACT?
1141 001634 001012          BNE      701          ;;BRANCH IF YES
1142 001636 126727 177352 000001  CMPB     1$ENV,01          ;;ARE WE RUNNING UNDER APT?
1143 001644 001406          BEQ      701          ;;BRANCH IF YES
1144 001646 026727 177266 000176  CMP      SWR,0$MREG      ;;SOFTWARE SWITCH REG SELECTED?
1145 001654 001005          BNE      711          ;;BRANCH IF NO
1146 001656 104406          GTSWR
1147 001660 000403          BR       711          ;;GET SOFT-SWR SETTINGS
1148 001662 112767 000001 177244 701:  MOVB     01,1$AUTOB      ;;SET AUTO-MODE INDICATOR
1149 001670          711:
1150 001670 000422          BR       681          ;;GET OVER THE ASCIZ
1151 ;;691: .ASCIZ <CRLF>*ZZ-CVDVA-D GLV11-E OFFLINE TEST*<CRLF>
1152 001736          681:
1153 001736          LET INITFLAG := 01
1154 001736 012767 000001 010410  MOV      01,INITFLAG
1155 001744          LOOP:
1156 001744          CALL    CYCLE          ; NO ARGUMENTS--ADRS -> NEXT ADDRESS
1157 001744 004767 010254          JSR      PC,CYCLE
1158
1159                                ;
1160 001750          LET      ADDRS+2 > NEXT VECTOR
1161 001750 012167 177300          MOV      (ADRS)+,DLADD  ;GET UNIT ADDRESS
1162                                ;
1163 001754          LET      DLVEC := (ADRS)  ;GET UNIT VECTOR
1164 001754 011167 177276          MOV      (ADRS),DLVEC
1165 001760          LET      ADRS := DLADD
1166 001760 016701 177270          MOV      DLADD,ADRS
1167                                ;
1168 001764          LET      RCSR = DLADD * 0
1169 001764 016767 177264 177266  MOV      DLADD,RCSR
1170 001772          LET      RBUF := DLADD * 02
1171 001772 016767 177256 177262  MOV      DLADD,RBUF
1172 002000 062767 000002 177254  ADD      02,RBUF
1173 002006          LET      TCSR := DLADD * 04

```

MAINDEC-22 CVDVA-D MACY11 30A(1052) 12-JUL-84 09:52 PAGE 31
CVDVAD.P11 12-JUL-84 05:04 GET VALUE FOR SOFTWARE SWITCH REGISTER

1174	002006	016767	177242	177250	MOV	DLADD,TCSR		
1175	002014	062767	000004	177242	ADD	#4,TCSR		
1176	002022						LET	TCSRMI := DLADD * #5
1177	002022	016767	177226	177236	MOV	DLADD,TCSRMI		
1178	002030	062767	000005	177230	ADD	#5,TCSRMI		
1179	002036						LET	TBUF := DLADD * #6
1180	002036	016767	177212	177224	MOV	DLADD,TBUF		
1181	002044	062767	000006	177216	ADD	#6,TBUF		
1182	002052						LET	R5 := #RSSTACK
1183	002052	012705	001334		MOV	#RSSTACK,R5		
1184								::BRESET
1185	002056	000005			RESET			

```

1186 ;*****
1187 ;*TEST 1 ADDRESSABILITY
1188 ;* THIS TEST VERIFIES THAT THE ADDRESS AS PLACED IN
1189 ;* THE HARDWARE P-TABLE TO BE CORRECT AND THE DLV11-E RESPONDS
1190 ;* TO THAT ADDRESS SPACE
1191 ;*****
1192 002060 000004 TST1: SCOPE
1193 002062 012767 000002 177070 MOV #2,#TIMES ;DO 2 ITERATIONS
1194 002070 012767 000001 177102 MOV #1,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
1195 002076 LET ADRS := DLADD
1196 002076 016701 177152 MOV DLADD,ADRS
1197 ; SET UP INTERRUPT
1198 002102 SETVEC #ILLMEM,#INTSRV,#PR7
1199 002102 010146 MOV R1,-(SP)
1200 002104 012701 000004 MOV #ILLMEM,R1
1201 002110 012721 012032 MOV #INTSRV,(R1)+
1202 002114 012711 000340 MOV #PR7,(R1)
1203 002120 012601 MOV (SP)+,R1
1204 002122 LET I := #0
1205 002122 005067 177144 CLR I
1206 002126 REPEAT
1207 002126 #1: BGNSUB
1208 002126 MOV #64#,#LPERR ;CLEAR FLAG
1209 002126 012767 002134 176754 ;LET INTFLAG := #0
1210 1211 002134 CLR INTFLAG
1212 002134 005067 007700 ;READ FLAG
1213 1214 ;IF INTFLAG NE #0 THEN
1215 002140 005711 TST #ADRS
1216 002142 IF INTFLAG NE #0 THEN
1217 002142 005767 007672 TST INTFLAG
1218 002146 001401 BEQ #2 ; FATAL ERROR
1219 ; ERRDF 1,,NOOL
1220 002150 ERROR 1
1221 002150 104001
1222 002152 ENDF
1223 002152 #2: ENDSUB
1224 002152 LET I := I + #2
1225 002152 LET ADRS := DLADD + I
1226 002152 062767 000002 177112 ADD #2,I
1227 002160 UNTIL I EQ #8.
1228 002160 016701 177070 MOV DLADD,ADRS
1229 002164 066701 177102 ADD I,ADRS
1230 002170
1231 002170 026727 177076 000010 CMP I,#8.
1232 002176 001353 BNE #1
1233 002200 CLRVEC ILLMEM
1234 002200 010146 MOV R1,-(SP) ;PUSH R1 ON STACK
1235 002202 010246 MOV R2,-(SP) ;PUSH R2 ON STACK
1236 002204 012701 000004 MOV #ILLMEM,R1
1237 002210 010102 MOV R1,R2
1238 002212 062702 000002 ADD #2,R2
1239 002216 010221 MOV R2,(R1)+
1240 002220 005011 CLR (R1)
1241 002222 012602 MOV (SP)+,R2 ;POP STACK INTO R2

```

MAINDEC-ZZ-CVDVA-D MACY11 30A(1052) 12-JUL-84 09:52 PAGE 33
CVDVAD.P11 12-JUL-84 05:04 T1 ADDRESSABILITY

1242 002224 012601
1243
1244 002226
1245
1246
1247
1248
1249

MOV (SP),R1 ;;POP STACK INTO R1

;END OF TEST

ENDTST

;;*****
;* THE FOLLOWING 8 TESTS TEST ALL 'READ WRITE' BITS
;;*****

MAINDEC ZZ-CVOVA-D MACY11 30A(1052) 12-JUL-84 09:52 PAGE 34
CVOVAD.P11 12-JUL-84 05:04 T2 BREAK - TCSRO SET, CLEAR, RESET

```

1250 ;*****
1251 ;*TEST 2 BREAK - TCSRO SET, CLEAR, RESET
1252 ;* NOTE: THE (H) JUMPER MUST BE REMOVED FOR THIS
1253 ;* TEST TO FUNCTION PROPERLY.
1254 ;*****
1255 002226 000004 TST2: SCOPE
1256 002230 012767 000010 176722 MOV #10,#TIMES ;DO 10 ITERATIONS
1257 002236 012767 000002 176734 MOV #2,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
1258
1259 002244 IF #BRK NOTSETIN #USWR THEN
1260 002244 032767 010000 176746 BIT #BRK,#USWR
1261 002252 001004 BNE #3
1262 002254 EXIT TST
1263 002254 012767 000001 176676 MOV #1,#TIMES
1264 002262 000452 BR TST3 ;;;EXIT THIS TEST
1265 002264 ENDF
1266 002264
1267 #3: ; SEE IF IT IS CLEAR
1268 002264 BGNSUB
1269 002264 012767 002272 176616 MOV #64,#LPERR
1270
1271 002272 IF #BREAK SETIN @TCSR THEN
1272 002272 032777 000001 176764 BIT #BREAK,@TCSR
1273 002300 001401 BEQ #4
1274 ; BREAK DID NOT RESET IN TCSR
1275 002302 ERROR 2 ERRHRD 2.,DIDNOT
1276 002302 104002
1277 002304 ENDF
1278 002304 #4:
1279 002304 ENDSUB
1280
1281 ; TRY TO SET BREAK BIT
1282 002304 BGNSUB
1283 002304 012767 002312 176576 MOV #64,#LPERR
1284 002312 LET @TCSR := @TCSR SET.BY #BREAK
1285 002312 052777 000001 176744 BIS #BREAK,@TCSR
1286 ; STUCK TO 0
1287 002320 IF #BREAK NOTSETIN @TCSR THEN
1288 002320 032777 000001 176736 BIT #BREAK,@TCSR
1289 002326 001001 BNE #5
1290 ; BREAK DID NOT SET IN TCSR
1291 002330 ERROR 3 ERRHRD 3.,DIDNOT
1292 002330 104003
1293 002332 ENDF
1294 002332 #5:
1295 002332 ENDSUB
1296
1297 ; TRY TO CLEAR A SET BIT
1298 002332 BGNSUB
1299 002332 012767 002340 176550 MOV #64,#LPERR
1300
1301 002340 LET @TCSR := @TCSR CLR.BY #BREAK
1302 002340 042777 000001 176716 BIC #BREAK,@TCSR
1303 ; SHOULD HAVE CLEARED
1304 002346 IF #BREAK SETIN @TCSR THEN
1305 002346 032777 000001 176710 BIT #BREAK,@TCSR

```


MAINDEC-ZZ CVDVA D MACY11 30A(1052) 12 JUL-84 09.52 PAGE 36
 CVDVAD.P11 12-JUL-84 05:04 T3 MAINT - TCSR2 SET, CLEAR, RESET

```

1336 ;*****
1337 ;*TEST 3 MAINT - TCSR2 SET, CLEAR, RESET
1338 ;*****
1339 002410 000004 TST3: SCOPE
1340 002412 012767 000010 176540 MOV #10,#TIMES ;DO 10 ITERATIONS
1341 002420 012767 000003 176552 MOV #3,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
1342
1343 ; SEE IF IT IS CLEAR
1344 002426 ; BGNSUB
1345 002426 012767 002434 176454 MOV #64#,#LPERR
1346
1347 002434 IF #MAINT SETIN @TCSR THEN
1348 002434 032777 000004 176622 BIT #MAINT,@TCSR
1349 002442 001401 BEQ #10
1350 ; MAINT DID NOT RESET IN TCSR
1351 002444 ERROR 6 ERRHRD 6.,DIDNOT
1352 002444 104006
1353 002446 ; ENDIF
1354 002446 #10:
1355 002446 ENDSUB
1356
1357 ; TRY TO SET MAINT BIT
1358 002446 ; BGNSUB
1359 002446 012767 002454 176434 MOV #64#,#LPERR
1360 002454 LET @TCSR := @TCSR SET.BY #MAINT
1361 002454 052777 000004 176602 BIS #MAINT,@TCSR
1362 ; STUCK TO 0
1363 002462 IF #MAINT NOTSETIN @TCSR THEN
1364 002462 032777 000004 176574 BIT #MAINT,@TCSR
1365 002470 001001 BNE #11
1366 ; MAINT DID NOT SET IN TCSR
1367 002472 ERROR 7 ERRHRD 7.,DIDNOT
1368 002472 104007
1369 002474 ; ENDIF
1370 002474 #11:
1371 002474 ENDSUB
1372
1373 ; TRY TO CLEAR A SET BIT
1374 002474 ; BGNSUB
1375 002474 012767 002502 176406 MOV #64#,#LPERR
1376
1377 002502 LET @TCSR := @TCSR CLR.BY #MAINT
1378 002502 032777 000004 176554 BIC #MAINT,@TCSR
1379 ; SHOULD HAVE CLEARED
1380 002510 IF #MAINT SETIN @TCSR THEN
1381 002510 032777 000004 176546 BIT #MAINT,@TCSR
1382 002516 001401 BEQ #12
1383 ; MAIN DID NOT CLEAR INTCSR
1384 002520 ERROR 10 ERRHRD 10.,DIDNOT
1385 002520 104010
1386 002522 ; ENDIF
1387 002522 #12:
1388 002522 ENDSUB
1389
1390 ; NOW SEE IF RESET CLEARS IT
1391 002522 ; BGNSUB

```

MAINDEC-ZZ-CVDVA-D MACY11 30A(1052) 12-JUL-84 09:52 PAGE 37
CVDVAD.P11 12-JUL-84 05:04 T3 MAINT - TCSR2 SET, CLEAR, RESET

1392 002522 012767 002530 176360
1393
1394 002530
1395 002530 052777 000004 176526
1396
1397 002536
1398 002536 000005
1399 002540
1400 002540 032777 000004 176516
1401 002546 001401
1402
1403 002550
1404 002550 104011
1405 002552
1406 002552
1407 002552
1408 002552
1409
1410
1411
1412

MOV #64,\$LPERR
BIS #MAINT,@TCSR
RESET
BIT #MAINT,@TCSR
BEQ #13
ERROR 11
#13:

LET @TCSR := @TCSR SET.BY #MAINT
; ISSUE BUS RESET
BRESET
IF #MAINT SET IN @TCSR THEN
; MAINT DID NOT RESET IN TCSR
ERRHRD 11,,DIDNOT
ENDIF
ENDSUB
ENDTST

;;*****

```

1413 ;*****
1414 ;*TEST 4 XMITIE - TCSR6 SET, CLEAR, RESET
1415 ;*****
1416 002552 000004 TST4: SCOPE
1417 002554 012767 000010 176376 MOV #10,#TIMES ;DO 10 ITERATIONS
1418 002562 012767 000004 176410 MOV #4,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
1419 ; USE PRIORITY OF 7
1420 002570 012746 000340 MOV #PR7,-(SP) ;PUT NEW PS ON STACK
1421 002574 012746 002602 MOV #64#,-(SP) ;PUT NEW PC ON STACK
1422 002600 000002 RTI ;POP NEW PC AND PS
1423 002602 64#:
1424
1425 ; SEE IF IT IS CLEAR
1426 002602 ; BGNSUB
1427 002602 012767 002610 176300 MOV #65#,#LPERR
1428
1429 002610 IF #XMITIE SETIN @TCSR THEN
1430 002610 032777 000100 176446 BIT #XMITIE,@TCSR
1431 002616 001401 BEQ #14
1432 ; XMITIE DID NOT RESET IN TCSR
1433 002620 ERRHRD 12,,DIDNOT
1434 002620 104012 ERROR 12
1435 002622 ENDF
1436 002622 $14:
1437 002622 ENDSUB
1438
1439 ; TRY TO SET XMITIE BIT
1440 002622 ; BGNSUB
1441 002622 012767 002630 176260 MOV #64#,#LPERR
1442 002630 LET @TCSR := @TCSR SET.BY #XMITIE
1443 002630 052777 000100 176426 BIS #XMITIE,@TCSR
1444 ; STUCK TO 0
1445 002636 IF #XMITIE NOTSETIN @TCSR THEN
1446 002636 032777 000100 176420 BIT #XMITIE,@TCSR
1447 002644 001001 BNE #15
1448 ; XMIT DID NOT RESET IN TCSR
1449 002646 ERRHRD 13,,DIDNOT
1450 002646 104013 ERROR 13
1451 002650 ENDF
1452 002650 $15:
1453 002650 ENDSUB
1454
1455 ; TRY TO CLEAR A SET BIT
1456 002650 ; BGNSUB
1457 002650 012767 002656 176232 MOV #64#,#LPERR
1458
1459 002656 LET @TCSR := @TCSR CLR.BY #XMITIE
1460 002656 042777 000100 176400 BIC #XMITIE,@TCSR
1461 ; SHOULD HAVE CLEARED
1462 002664 IF #XMITIE SETIN @TCSR THEN
1463 002664 032777 000100 176372 BIT #XMITIE,@TCSR
1464 002672 001401 BEQ #16
1465 ; XMIT DID NOT CLEAR IN TCSR
1466 002674 ERRHRD 14,,DIDNOT
1467 002674 104014 ERROR 14
1468 002676 ENDF
    
```

MAINDEC-ZZ-CVDVA-D MACY11 30A(1052) 12-JUL-84 09:52 PAGE 39
CVDVAD.P11 12-JUL-84 05:04 T4 XMITIE - TCSR6 SET, CLEAR, RESET

```

1469 002676          $16:
1470 002676                                ENDSUB
1471
1472                                ; NOW SEE IF RESET CLEARS IT
1473 002676                                BGNSUB
1474 002676 012767 002704 176204      MOV    #64$, $LPERR
1475
1476 002704                                LET    @TCSR := @TCSR SET.BY #XMITIE
1477 002704 052777 000100 176352      BIS    #XMITIE, @TCSR
1478                                ; ISSUE BUS RESET
1479 002712                                BRESET
1480 002712 000005      RESET
1481 002714                                IF    #XMITIE SETIN @TCSR THEN
1482 002714 032777 000100 176342      BIT    #XMITIE, @TCSR
1483 002722 001401      BEQ    $17
1484                                ; XMIT DID NOT RESET IN TCSR
1485 002724                                ERRMRD 15..DIDNOT
1486 002724 104015      ERROR 15
1487 002726                                ENDIF
1488 002726          $17:
1489 002726                                ENDSUB
1490 002726                                ENDTST
1491
1492
1493
1494          ;;*****

```

MAINDEC-ZZ CVDVA-D MACY11 30A(1052) 12-JUL-84 09:52 PAGE 40
CVDVAD.P11 12 JUL -84 05:04 T5 DTR - RCSR1 SET, CLEAR

```

1495 ;*****
1496 ;*TEST 5      DTR  RCSR1  SET, CLEAR
1497 ;*
1498 ;*      NOTE:  RESET DOES NOT CLEAR THIS BIT
1499 ;*      WE CANNOT TEST FOR AN INITIAL CONDITION
1500 ;*      AS THIS BIT IS UNDEFINED UPON POWER UP AND
1501 ;*      INIT DOESN'T AFFECT IT.
1502 ;*      THE (-FD) JUMPER MUST BE IN FOR THIS TEST TO WORK.
1503 ;*****
1503 002726 000004 TST5:  SCOPE
1504 002730 012767 000010 176222      MOV      #10,#TIMES      ;;DO 10 ITERATIONS
1505 002736 012767 000005 176234      MOV      #5,#TESTN      ;;SET TEST NUMBER IN APT MAIL BOX
1506 002744                                     IF #FRFD NOTSETIN #USMR THEN
1507 002744 032767 040000 176246      BIT      #FRFD,#USMR
1508 002752 001004      BNE      #20
1509 002754                                     EXIT TST
1510 002754 012767 000001 176176      MOV      #1,#TIMES
1511 002762 000441      BR       TST6          ;;EXIT THIS TEST
1512 002764                                     ENDIF
1513 002764      #20:
1514                                     ; TRY TO CLEAR DTR BIT
1515 002764                                     BGNSUB
1516 002764 012767 002772 176116      MOV      #64#,#LPERR
1517 002772                                     LET      @RCSR := @RCSR CLR.BY #DTR
1518 002772 042777 000002 176260      BIC      #DTR,@RCSR
1519                                     ; STUCK TO 0
1520 003000                                     IF      #DTR SETIN @RCSR THEN
1521 003000 032777 000002 176252      BIT      #DTR,@RCSR
1522 003006 001401      BEQ      #21
1523                                     ; DTR DID NOT CLEAR IN RCSR
1524 003010                                     ERRHRD 16.,DIDNOT
1525 003010 104016      ERROR  16
1526 003012                                     ENDIF
1527 003012      #21:
1528 003012                                     ENDSUB
1529
1530                                     ; TRY TO SET  DTR
1531 003012                                     BGNSUB
1532 003012 012767 003020 176070      MOV      #64#,#LPERR
1533
1534 003020                                     LET      @RCSR := @RCSR SET.BY #DTR
1535 003020 052777 000002 176232      BIS      #DTR,@RCSR
1536 003026                                     IF      #DTR NOTSETIN @RCSR THEN
1537 003026 032777 000002 176224      BIT      #DTR,@RCSR
1538 003034 001001      BNE      #22
1539                                     ; DTR DID NOT SET IN RCSR
1540 003036                                     ERRHRD 17.,DIDNOT
1541 003036 104017      ERROR  17
1542 003040                                     ENDIF
1543 003040      #22:
1544 003040                                     ENDSUB
1545
1546                                     ; TRY TO CLEAR IT AGAIN
1547 003040                                     BGNSUB
1548 003040 012767 003046 176042      MOV      #64#,#LPERR
1549 003046                                     LET      @RCSR := @RCSR CLR.BY #DTR
1550 003046 042777 000002 176204      BIC      #DTR,@RCSR

```

MAINDEC-ZZ CVDVA-D MACY11 30A(1052) 12-JUL-84 09:52 PAGE 41
CVDVAD.P11 12-JUL-84 05:04 T5 DTR - RCSR1 SET, CLEAR

1551
1552 003054
1553 003054 032777 000002 176176
1554 003062 001401
1555
1556 003064
1557 003064 104020
1558 003066
1559 003066 \$23:
1560 003066
1561 003066
1562
1563
1564
1565

BIT #DTR,@RCSR
BEQ \$23

ERROR 20

IF ; SHOULD HAVE CLEARED IT
#DTR SET IN @RCSR THEN

; DTR DID NOT CLEAR IN RCSR
ERRHRD 20,,DIDNOT

ENDIF

ENDSUB
ENDTST

MAINDEC-22 CVDVA D MACY11 30A(1052) 12-JUL 84 09:52 PAGE 42
CVDVAD.P11 12-JUL-84 05:04 T6 REQSEND - RCSR2 SET, CLEAR, RESET

```

1566 ;*****
1567 ;*TEST 6          REQSEND - RCSR2      SET, CLEAR, RESET
1568 ;*              THIS TEST ASSUMES THAT JUMPER -(FR) IS IN
1569 ;*****
1570 TST6:  SCOPE
1571 003066 000004      MOV      #10,#TIMES      ;;DO 10 ITERATIONS
1572 003070 012767 000010 176062      MOV      #6,#TESTN      ;;SET TEST NUMBER IN APT MAIL BOX
1573 003104 032767 040000 176106      BIT      #FRFD,#USWR      IF #FRFD NOTSETIN #USWR THEN
1574 003112 001004      BNE      #24
1575 003114      EXIT TST
1576 003114 012767 000001 176036      MOV      #1,#TIMES
1577 003122 000452      BR       TST7          ;;EXIT THIS TEST
1578 003124      ENDIF
1579 003124
1580 #24:
1581
1582 ; SEE IF IT IS CLEAR
1583 003124      BGNSUB
1584 003124 012767 003132 175756      MOV      #641,#LPERR
1585
1586 003132      IF      #REQSEND SETIN #RCSR THEN
1587 003132 032777 000004 176120      BIT      #REQSEND,#RCSR
1588 003140 001401      BEQ
1589
1590 003142      ; REQSEND DID NOT RESET IN RCSR
1591 003142 104021      ERROR 21      ERRNRD 21.,DIDNOT
1592 003144      ENDIF
1593 003144      #25:
1594 003144      ENDSUB
1595
1596 ; TRY TO SET REQSEND BIT
1597 003144      BGNSUB
1598 003144 012767 003152 175736      MOV      #641,#LPERR
1599 003152      LET      #RCSR := #RCSR SET.BY #REQSEND
1600 003152 052777 000004 176100      BIS      #REQSEND,#RCSR
1601
1602 003160      IF      #REQSEND NOTSETIN #RCSR THEN
1603 003160 032777 000004 176072      BIT      #REQSEND,#RCSR
1604 003166 001001      BNE      #26
1605
1606 003170      ; REQSEND DID NOT SET IN RCSR
1607 003170 104022      ERROR 22      ERRNRD 22.,DIDNOT
1608 003172      ENDIF
1609 003172      #26:
1610 003172      ENDSUB
1611
1612 ; TRY TO CLEAR A SET BIT
1613 003172      BGNSUB
1614 003172 012767 003200 175710      MOV      #641,#LPERR
1615
1616 003200      LET      #RCSR := #RCSR CLR.BY #REQSEND
1617 003200 042777 000004 176052      BIC      #REQSEND,#RCSR
1618
1619 003206      IF      #REQSEND SETIN #RCSR THEN
1620 003206 032777 000004 176044      BIT      #REQSEND,#RCSR
1621 003214 001401      BEQ      #27

```


SET, CLEAR, RESET

```
1622 ; REQSEND DID NOT CLEAR IN RCSR
1623 003216 ERRMRD 23,,DIDNOT
1624 003216 104023 ERROR 23
1625 003220
1626 003220 ;27: ENDF
1627 003220 ENDSUB
1628
1629 ; NOW SEE IF RESET CLEARS IT
1630 003220 BGNSUB
1631 003220 012767 003226 175662 MOV #641,ILPERR
1632
1633 003226 LET BRCSR := BRCSR SET BY #REQSEND
1634 003226 052777 000004 176024 BIS #REQSEND,BRCSR
1635 ; ISSUE BUS RESET
1636 003234 BRESET
1637 003234 000005 RESET
1638 003236 IF #REQSEND SET IN BRCSR THEN
1639 003236 032777 000004 176014 BIT #REQSEND,BRCSR
1640 003244 001401 BEQ #30
1641 ; REQSEND DID NOT RESET IN RCSR
1642 003246 ERRMRD 24,,DIDNOT
1643 003246 104024 ERROR 24
1644 003250 ENDF
1645 003250 ;30: ENDSUB
1646 003250 ENDTST
1647 003250
1648
1649
1650
1651 ;:.....
```

SET, CLEAR, RESET

```
1652 ;*****  
1653 ;*TEST 7 SECXMIT - RCSR3 SET, CLEAR, RESET  
1654 ;*****  
1655 TST7: SCOPE  
1656 003250 000004 MOV #10,#TIMES ;DO 10 ITERATIONS  
1657 003252 012767 000010 175700 MOV #7,#TESTN ;SET TEST NUMBER IN APT MAIL BOX  
1658 003260 012767 000007 175712 ; SEE IF IT IS CLEAR  
1659 003266 BGNSUB  
1660 003266 012767 003274 175614 MOV #64#,#LPERR  
1661  
1662 IF #SECXMIT SETIN @RCSR THEN  
1663 003274 032777 000010 175756 BIT #SECXMIT,@RCSR  
1664 003302 001401 BEQ #31  
1665 ; SECXMIT DID NOT RESET IN RCSR  
1666 003304 ERRHRD 25,,DIDNOT  
1667 003304 104025 ERROR 25  
1668 003306 ENDIF  
1669 003306 #31:  
1670 003306 ENDSUB  
1671  
1672 ; TRY TO SET SECXMIT BIT  
1673 003306 BGNSUB  
1674 003306 012767 003314 175574 MOV #64#,#LPERR  
1675 003314 LET @RCSR := @RCSR SET.BY #SECXMIT  
1676 003314 052777 000010 175736 BIS #SECXMIT,@RCSR  
1677 ; STUCK TO 0  
1678 003322 IF #SECXMIT NOTSETIN @RCSR THEN  
1679 003322 032777 000010 175730 BIT #SECXMIT,@RCSR  
1680 003330 001001 BNE #32  
1681 ; SECXMIT DID NOT SET IN RCSR  
1682 003332 ERRHRD 26,,DIDNOT  
1683 003332 104026 ERROR 26  
1684 003334 ENDIF  
1685 003334 #32:  
1686 003334 ENDSUB  
1687  
1688 ; TRY TO CLEAR A SET BIT  
1689 003334 BGNSUB  
1690 003334 012767 003342 175546 MOV #64#,#LPERR  
1691  
1692 003342 LET @RCSR := @RCSR CLR.BY #SECXMIT  
1693 003342 042777 000010 175710 BIC #SECXMIT,@RCSR  
1694 ; SHOULD HAVE CLEARED  
1695 003350 IF #SECXMIT SETIN @RCSR THEN  
1696 003350 032777 000010 175702 BIT #SECXMIT,@RCSR  
1697 003356 001401 BEQ #33  
1698 ; SECXMIT DID NOT CLEAR IN RCSR  
1699 003360 ERRHRD 27,,DIDNOT  
1700 003360 104027 ERROR 27  
1701 003362 ENDIF  
1702 003362 #33:  
1703 003362 ENDSUB  
1704  
1705 003362 BGNSUB  
1706 003362 012767 003370 175520 MOV #64#,#LPERR  
1707 ; NOW SEE IF RESET CLEARS IT
```

MAINDEC-22-CVDVA D MACY11 30A(1052) 12 JUL 84 09:52 PAGE 45
CVDVAD.P11 12-JUL-84 05:04 T7 SECXMIT - RCSR3

SET, CLEAR, RESET

```

1708
1709 003370 LET @RCSR := @RCSR SET.BY @SECXMIT
1710 003370 052777 000010 175662 BIS @SECXMIT,@RCSR ; ISSUE BUS RESET
1711 ; BRESET
1712 003376 RESET
1713 003376 000005
1714 003400 IF @SECXMIT SETIN @RCSR THEN
1715 003400 032777 000010 175652 BIT @SECXMIT,@RCSR
1716 003406 001401 BEQ $34 ; SECXMIT DID NOT RESET IN RCSR
1717 ; ERRHRD 30,,DIDNOT
1718 003410 ERROR 30
1719 003410 104030
1720 003412 ENDIF
1721 003412 $34:
1722 003412 ENDSUB
1723 003412 ENDTST
1724
1725
1726
1727 ;*****

```

MAINDEC-22 CVDVA-D MACY11 30A(1052) 12-JUL-84 09:52 PAGE 46
 CVDVAD.P11 12-JUL-84 05:04 T10 DATAIE - RCSR5 SET, CLEAR, RESET

```

1728 ;*****
1729 ;*TEST 10      DATAIE - RCSR5 SET, CLEAR, RESET
1730 ;*****
1731 003412 000004 TST10: SCOPE
1732 003414 012767 000010 175536      MOV      #10,#TIMES      ;;DO 10 ITERATIONS
1733 003422 012767 000010 175550      MOV      #10,#TESTN    ;;SET TEST NUMBER IN APT MAIL BOX
1734                                ; SEE IF IT IS CLEAR
1735 003430                                BGNSUB
1736 003430 012767 003436 175452      MOV      #64#,#LPERR
1737
1738 003436                                IF      #DATAIE SETIN @RCSR THEN
1739 003436 032777 000040 175614      BIT      #DATAIE,@RCSR
1740 003444 001401                                BEQ
1741                                ; DATAIE DID NOT RESET IN RCSR
1742 003446                                ERRHRD 31,,DIDNOT
1743 003446 104031                                ERROR 31
1744 003450                                ENDF
1745 003450                                #35:
1746 003450                                ENDSUB
1747
1748                                ; TRY TO SET DATAIE BIT
1749 003450                                BGNSUB
1750 003450 012767 003456 175432      MOV      #64#,#LPERR
1751 003456                                LET      @RCSR := @RCSR SET.BY #DATAIE
1752 003456 052777 000040 175574      BIS      #DATAIE,@RCSR
1753                                ; STUCK TO 0
1754 003464                                IF      #DATAIE NOTSETIN @RCSR THEN
1755 003464 032777 000040 175566      BIT      #DATAIE,@RCSR
1756 003472 001001                                BNE      #36
1757                                ; DATAIE DID NOT SET IN RCSR
1758 003474                                ERRHRD 32,,DIDNOT
1759 003474 104032                                ERROR 32
1760 003476                                ENDF
1761 003476                                #36:
1762 003476                                ENDSUB
1763
1764                                ; TRY TO CLEAR A SET BIT
1765 003476                                BGNSUB
1766 003476 012767 003504 175404      MOV      #64#,#LPERR
1767
1768 003504                                LET      @RCSR := @RCSR CLR.BY #DATAIE
1769 003504 042777 000040 175546      BIC      #DATAIE,@RCSR
1770                                ; SHOULD HAVE CLEARED
1771 003512                                IF      #DATAIE SETIN @RCSR THEN
1772 003512 032777 000040 175540      BIT      #DATAIE,@RCSR
1773 003520 001401                                BEQ      #37
1774                                ; DATAIE DID NOT CLEAR IN RCSR
1775 003522                                ERRHRD 33,,DIDNOT
1776 003522 104033                                ERROR 33
1777 003524                                ENDF
1778 003524                                #37:
1779 003524                                ENDSUB
1780
1781                                ; NOW SEE IF RESET CLEARS IT
1782 003524                                BGNSUB
1783 003524 012767 003532 175356      MOV      #64#,#LPERR
    
```

MAINDEC ZZ CVDVA-D MACY11 30A(1052) 12-JUL-84 09:52 PAGE 47
CVDVAD.P11 12-JUL-84 05:04 T10 DATAIE - RCSR5 SET, CLEAR, RESET

```

1784
1785 003532
1786 003532 052777 000040 175520      BIS      #DATAIE,#RCSR      LET      #RCSR := #RCSR SET.BY #DATAIF
1787
1788 003540
1789 003540 000005      RESET      ; ISSUE BUS RESET
1790 003542
1791 003542 032777 000040 175510      BIT      #DATAIE,#RCSR      BRESET
1792 003550 001401      BEQ      #40      IF      #DATAIE SETIN #RCSR THEN
1793
1794 003552
1795 003552 104034      ERROR   34      ; DATAIE DID NOT RESET IN RCSR
1796 003554
1797 003554      #40:      ERRPRD 34,,DIDNOT
1798 003554
1799 003554
1800
1801
1802
1803
;*****

```

ENDSUB
ENDTST

MAINDEC-ZZ CVDVA D MACY11 30A(1052) 12-JUL-84 09:52 PAGE 48
CVDVAD.P11 12-JUL-84 05:04 T11 RCVRIE - RCSR6 SET, CLEAR, RESET

```

1804 ;*****
1805 ;*TEST 11 RCVRIE - RCSR6 SET, CLEAR, RESET
1806 ;*****
1807 003554 000004 TST11: SCOPE
1808 003556 012767 000010 175374 MOV #10,#TIMES ;DO 10 ITERATIONS
1809 003564 012767 000011 175406 MOV #11,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
1810 ; SEE IF IT IS CLEAR
1811 003572 BGNSUB
1812 003572 012767 003600 175310 MOV #64#,#LPERR
1813
1814 003600 IF #RCVRIE SETIN #RCSR THEN
1815 003600 032777 000100 175452 BIT #RCVRIE,#RCSR
1816 003606 001401 BEQ #41
1817 ; RCVRIE DID NOT RESET IN RCSR
1818 003610 ERRHRD 35,,DIDNOT
1819 003610 104035 ERROR 35
1820 003612 ENDF
1821 003612 #41:
1822 003612 ENDSUB
1823
1824 ; TRY TO SET RCVRIE BIT
1825 003612 BGNSUB
1826 003612 012767 003620 175270 MOV #64#,#LPERR
1827 003620 LET #RCSR := #RCSR SET.BY #RCVRIE
1828 003620 052777 000100 175432 BIS #RCVRIE,#RCSR
1829 ; STUCK TO 0
1830 003626 IF #RCVRIE NOTSETIN #RCSR THEN
1831 003626 032777 000100 175424 BIT #RCVRIE,#RCSR
1832 003634 001001 BNE #42
1833 ; RCVRIE DID NOT SET IN RCSR
1834 003636 ERRHRD 36,,DIDNOT
1835 003636 104036 ERROR 36
1836 003640 ENDF
1837 003640 #42:
1838 003640 ENDSUB
1839
1840 ; TRY TO CLEAR A SET BIT
1841 003640 BGNSUB
1842 003640 012767 003646 175242 MOV #64#,#LPERR
1843
1844 003646 LET #RCSR := #RCSR CLR.BY #RCVRIE
1845 003646 042777 000100 175404 BIC #RCVRIE,#RCSR
1846 ; SHOULD HAVE CLARED
1847 003654 IF #RCVRIE SETIN #RCSR THEN
1848 003654 032777 000100 175376 BIT #RCVRIE,#RCSR
1849 003662 001401 BEQ #43
1850 ; RCVRIE DID NOT CLEAR IN RCSR
1851 003664 ERRHRD 37,,DIDNOT
1852 003664 104037 ERROR 37
1853 003666 ENDF
1854 003666 #43:
1855 003666 ENDSUB
1856
1857 ; NOW SEE IF RESET CLEARS IT
1858 003666 BGNSUB
1859 003666 012767 003674 175214 MOV #64#,#LPERR

```

MAINDEC-ZZ-CVDVA-D MACY11 30A(1052) 12-JUL-84 09:52 PAGE 49
CVDVAO.P11 12-JUL-84 05:04 T11 RCVRIE - RCSR6 SET, CLEAR, RESET

```

1860
1861 003674
1862 003674 052777 000100 175356 BIS #RCVRIE, @RCSR LET @RCSR := @RCSR SET.BY @RCVRIE
1863 ; ISSUE BUS RESET
1864 003702 BRESET
1865 003702 000005 RESET
1866 003704 IF @RCVRIE SETIN @RCSR THEN
1867 003704 032777 000100 175346 SIT #RCVRIE, @RCSR
1868 003712 001401 BEQ $44 ; RCVRIE DID NOT RESET IN RCSR
1869 ; ERRHRD 40, .DIDNOT
1870 003714
1871 003714 104040 ERROR 40
1872 003716
1873 003716 $44:
1874 003716 CKLOOP
1875 003716 ENDSUB
1876 003716 ENDTST
1877
1878
1879
1880
1881
1882
1883
1884
1885
1886

```

```

;*****
;* THE FOLLOWING 4 TESTS VERIFY
;* THAT RESET (INIT) INITIALIZES READ ONLY BITS.
;*****

```

MAINDEC-ZZ-CVDVA D MACY11 30A(1052) 12-JUL-84 09:52 PAGE 50
CVDVAD.P11 12-JUL-84 05:04 T12 TEST THAT RCVRDONE - RCSR 7 - IS CLEARED BY INIT

```

1887 ;*****
1888 ;*TEST 12 TEST THAT RCVRDONE - RCSR 7 - IS CLEARED BY INIT
1889 ;*****
1890 003716 000004 TST12: SCOPE
1891 003720 012767 000010 175232 MOV #10,#TIMES ;DO 10 ITERATIONS
1892 003726 012767 000012 175244 MOV #12,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
1893
1894
1895
1896
1897
1898 003734 BGNSUB
1899 003734 012767 003742 175146 MOV #64,,$LPERR IF #RCVRDONE SETIN #RCSR THEN
1900 003742
1901 003742 032777 000200 175310 BIT #RCVRDONE,#RCSR
1902 003750 001402 BEQ #45
1903
1904 ;RCVRDONE SHOULD HAVE CLEARED BY INIT
1905 ; RCVRDONE DID NOT CLEAR IN RCSR
1906 003752 ERRHRD 41,#RESET, DIDNOT
1907 003752 104041 ERROR 41
1908 ;REISSUE RESET
1909 003754 BRESET
1910 003754 000005 RESET
1911 003756 ENDF
1912 003756 ;45: ;ALLOW LOOPING AFTER ERROR
1913 CKLOOP
1914 003756 ENDSUB
1915 003756 ENDTST
1916 003756
1917
1918
1919

```


MAINDEC-27-CVDVA-D MACY11 30A(1052) 12-JUL-84 09:52 PAGE 51
CVDVAD.P11 12-JUL-84 05:04 T13 TEST THAT XMITRDY - TCSR 7 - IS SET BY INIT

```

1920 ;*****
1921 ;*TEST 13 TEST THAT XMITRDY - TCSR 7 - IS SET BY INIT
1922 ;*****
1923 003756 C00004 TST13: SCOPE
1924 003760 012767 000010 175172 MOV #10,#TIMES ;DO 10 ITERATIONS
1925 003766 012767 000013 175204 MOV #13,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
1926
1927
1928
1929
1930 003774 BGNSUB
1931 003774 012767 004002 175106 MOV #64#,$LPERR
1932
1933 004002 IF #XMITRDY NOTSETIN @TCSR THEN
1934 004002 032777 000200 175254 BIT #XMITRDY,@TCSR
1935 004010 001002 BNE #46
1936
1937 ;RESET SHOULD HAVE SET BIT.
1938 ;XMITRDY DID NOT SET IN TCSR (AFTER RESE
1939 004012 ERRMRD 42,#RESET,DIDNOT
1940 004012 104042 ERROR 42
1941 ;ISSUE ANOTHER RESET
1942 004014 BRESET
1943 004014 000005 RESET
1944 004016
1945 004016 ;46: ENDF
1946 ;ALLOW LOOPING ON ERROR
1947 004016 CKLOOP
1948 004016 ENDSUB
1949 004016 ENDTST
1950
1951
1952

```

MAINDEC-ZZ-CVDVA-D MACY11 30A(1052) 12-JUL-84 09:52 PAGE 52
CVDVAD.P11 12-JUL-84 05:04

T14 TEST THAT DATAINT - RCSR 15 - IS CLEARED BY INIT.

```

1953 ;:*****
1954 ;*TEST 14 TEST THAT DATAINT - RCSR 15 - IS CLEARED BY INIT.
1955 ;:*****
1956 004016 000004 TST14: SCOPE
1957 004020 012767 000010 175132 MOV #10,#TIMES ;;DO 10 ITERATIONS
1958 004026 012767 000014 175144 MOV #14,#TESTN ;;SET TEST NUMBER IN APT MAIL BOX
1959
1960
1961
1962
1963 004034 BGNSUB
1964 004034 012767 004042 175046 MOV #64,#LPERR
1965 004042 IF #DATAINT SETIN @RCSR THEN
1966 004042 032777 100000 175210 BIT #DATAINT,@RCSR
1967 004050 001402 BEQ #47
1968
1969 004052 ERRHRD 43, HRESET, DIDNOT
1970 004052 104043 ERROR 43
1971
1972 ; TESTING EFFECT OF RESET ON BIT
1973
1974 ; DATAINT DID NOT CLEAR IN RCSR
1975 ; ALLOW A FRESH START
1976 004054 BRESET
1977 004054 000005 RESET
1978 004056 ENDF
1979 004056 #47:
1980 004056 CKLOOP
1981 004056 ENDSUB
1982 004056 ENDTST
1983
1984
1985
1986 ;:*****

```

MAINDEC 77-CVDVA D MACY11 30A(1052) 12-JUL-84 09:52 PAGE 53
CVDVAD.P11 12-JUL-84 05:04 T15 TEST THAT RCVRACT - RCSR 11 - 15 CLEARED BY INIT

```

1987 ;*****
1988 ;*TEST 15 TEST THAT RCVRACT - RCSR 11 - 15 CLEARED BY INIT
1989 ;*****
1990 004056 000004 TST15: SCOPE
1991 004060 012767 000010 175072 MOV #10,#TIMES ;DO 10 ITERATIONS
1992 004066 012767 000015 175104 MOV #15,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
1993
1994
1995 004074 IF #CABLE NOTSETIN #USWR THEN
1996 004074 032767 020000 175116 BIT #CABLE,#USWR
1997 004102 001004 BNE #50
1998 ; CAN'T TEST WITHOUT BERG OR M315.
1999 004104 EXIT TST
2000 004104 012767 000001 175046 MOV #1,#TIMES
2001 004112 000411 BR TST16 ;EXIT THIS TEST
2002 004114 ENDF
2003 $50:
2004
2005
2006
2007 004114 BGNSUB
2008 004114 012767 004122 174766 MOV #64#,#LPERR
2009
2010 004122 IF #RCVRACT SETIN #RCSR THEN
2011 004122 032777 004000 175130 BIT #RCVRACT,#RCSR
2012 004130 001402 BEQ #51
2013
2014 ;RESET SHOULD HAVE CLEARED RCVRACT
2015 004132 ERROR 44 ERRHRD 44, HRESET, DIDNOT
2016 004132 104044
2017
2018 ;TESTING EFFECT OF RESET ON BIT
2019
2020 ;RCVRACT DID NOT CLEAR IN RCSR
2021
2022 ;ALLOW ANOTHER TRY
2023 004134 BRESET
2024 004134 000005 RESET
2025 004136 ENDF
2026 004136 $51:
2027
2028 004136 ;ALLOW LOOPING ON ERROR
2029 004136 CKLOOP
2030 004136 ENDSUB
2031 004136 ENDTST

```

MAINDEC-ZZ-CVDVA-D MACY11 30A(1052) 12-JUL-84 09:52 PAGE 54
 CVDVAD.P1' 12-JUL-84 05:04 T15 TEST THAT RCVRCT - RCSR 11 - 15 CLEARED BY INIT

```

2032 ;*****
2033 ;* THE FOLLOWING 4 TESTS VERIFY
2034 ;* THAT THE EIA SIGNALS CAN BE TRANSMITTED
2035 ;* AND RECEIVED THROUGH THE CABLE
2036 ;*****
2037
2038
2039
2040 ;*****
2041 ;*TEST 16 TEST THAT CARDET SETS AND CLEARS
2042 ;* AS DTR SETS AND CLEARS
2043 ;* THE (-FD) JUMPER MUST BE IN FOR THIS TEST.
2044 ;*****
2045 004136 000004 TST16: SCOPE
2046 004140 012767 000010 175012 MOV #10,#TIMES ;;DO 10 ITERATIONS
2047 004146 012767 000016 175024 MOV #16,#TESTN ;;SET TEST NUMBER IN APT MAIL BOX
2048 ; CAN WE USE THE WRAPAROUND??
2049 004154 IF #CABLE+FRFD NOTSETIN #USMR THEN
2050 004154 032767 060000 175036 BIT #CABLE+FRFD,#USMR
2051 004162 001004 BNE #52
2052 ; CAN'T TEST WITHOUT BERG OR H315
2053 ; OR WITH (-FD) JUMPER OUT.
2054 ; OR WITH (-FR) JUMPER OUT.
2055 004164 EXIT TST
2056 004164 012767 006001 174766 MOV #1,#TIMES
2057 004172 000441 BR TST17 ;;EXIT THIS TEST
2058 004174 ENDF
2059 004174 #52:
2060
2061 ; DTR AND
2062 ; CARDET ARE CONNECTED
2063
2064 ; BY THE #315 OR EQUIV.
2065
2066 ; CLEAR
2067 004174 ; CLEAR BGNSUB
2068 004174 012767 004202 174706 MOV #64#,#LPERR
2069
2070 ; CLEAR DTR
2071 004202 LET @RCSR := @RCSR CLR.BY #DTR
2072 004202 042777 000002 175050 BIC #DTR,@RCSR
2073 ; CARDET SHOULD FOLLOW
2074 004210 IF #CARDET SETIN @RCSR THEN
2075 004210 032777 010000 175042 BIT #CARDET,@RCSR
2076 004216 001401 BEQ #53
2077 ; CARDET DID NOT
2078 004220 ERROR 45 ERRHRD 45,,FORCE
2079 004220 104045
2080 ; CLEAR WITH DTR
2081 ENDF
2082 004222 #53:
2083 004222 ENDSUB
2084 004222
2085 ; SET
2086 004222
2087 004222 BGNSUB
    
```

```
2088 004222 012767 004230 174660      MOV      #64#,#LPERR
2089
2090
2091 004230
2092 004230 052777 000002 175022      BIS      #DTR,#RCSR
2093
2094 004236
2095 004236 032777 010000 175014      BIT      #CARDET,#RCSR
2096 004244 001001      BNE      #54
2097
2098 004246
2099 004246 104046      ERROR   46
2100
2101
2102 004250
2103 004250
2104 004250
2105
2106
2107 004250
2108 004250 012767 004256 174632      MOV      #64#,#LPERR
2109
2110
2111 004256
2112 004256 042777 000002 174774      BIC      #DTR,#RCSR
2113
2114 004264
2115 004264 032777 010000 174766      BIT      #CARDET,#RCSR
2116 004272 001401      BEQ      #55
2117
2118 004274
2119 004274 104047      ERROR   47
2120
2121
2122 004276
2123 004276
2124 004276
2125 004276
2126
2127
2128
2129
```

; SET DTR
LET #RCSR := #RCSR SET.BY #DTR
; CARDET SHOULD FOLLOW
IF #CARDET NOTSETIN #RCSR THEN
; CARDET DID NOT SET
ERRHRD 46,,FORCE
; WITH DTR
ENDIF
ENDSUB
; CLEAR
BGNSUB
; CLEAR DTR
LET #RCSR := #RCSR CLR.BY #DTR
; CARDET SHOULD FOLLOW
IF #CARDET SETIN #RCSR THEN
; CARDET DID NOT
ERRHRD 47,,FORCE
; CLEAR WITH DTR
ENDIF
ENDSUB
ENDTST

#54:
#55:

```
2130 ; ;
2131 ; ;
2132 ; *TEST 17 TEST THAT CLREND SETS AND CLEARS
2133 ; * AS DTR SETS AND CLEARS
2134 ; * (-FD) JUMPER MUST BE IN FOR THIS TEST TO WORK
2135 ; ;
2136 004276 000004 TST17: SCOPE
2137 004300 012767 000010 174652 MOV #10, #TIMES ; DO 10 ITERATIONS
2138 004306 012767 000017 174664 MOV #17, #TESTN ; SET TEST NUMBER IN APT MAIL BOX
2139 ; ; ; CAN WE USE THE WRAPAROUND??
2140 004314 ; ; ; CAN WE USE THE WRAPAROUND??
2141 004314 032767 060000 174676 BIT @CABLE, #FRFD, #USMR IF @CABLE, #FRFD NOTSETIN #USMR THEN
2142 004322 001004 BNE #56 ;
2143 ; ; ; CAN'T TEST WITHOUT BERG OR M315
2144 004324 ; ; ; CAN'T TEST WITHOUT BERG OR M315
2145 004324 012767 000001 174626 MOV #1, #TIMES EXIT TST
2146 004332 000441 BR TST20 ; ; EXIT THIS TEST
2147 004334 ; ; ;
2148 004334 ; ; ;
2149 ; ; ;
2150 ; ; ;
2151 ; ; ; DTR AND
2152 ; ; ; CLREND ARE CONNECTED
2153 ; ; ;
2154 ; ; ; BY THE M315 OR EQUIV.
2155 ; ; ;
2156 004334 ; CLEAR ;
2157 004334 012767 004342 174546 MOV #64, #LPERR BGNSUB
2158 ; ; ;
2159 ; ; ;
2160 004342 ; CLEAR DTR
2161 004342 042777 000002 174710 BIC #DTR, #RCSR LET @RCSR := @RCSR CLR.BY #DTR
2162 ; ; ;
2163 004350 ; CLREND SHOULD FOLLOW
2164 004350 032777 020000 174702 BIT @CLREND, #RCSR IF @CLREND SETIN @RCSR THEN
2165 004356 001401 BEQ #57 ;
2166 ; ; ; CLREND DID NOT
2167 004360 ; ; ; ERRORD 50., FORCE
2168 004360 104050 ERROR 50 ;
2169 ; ; ;
2170 ; ; ;
2171 004362 ; CLEAR WITH DTR
2172 004362 ; ; ;
2173 004362 ; ; ;
2174 ; ; ;
2175 ; ; ;
2176 004362 ; SET ;
2177 004362 012767 004370 174520 MOV #64, #LPERR BGNSUB
2178 ; ; ;
2179 ; ; ;
2180 004370 ; SET DTR
2181 004370 052777 000002 174662 BIS #DTR, #RCSR LET @RCSR := @RCSR SET.BY #DTR
2182 ; ; ;
2183 004376 ; CLREND SHOULD FOLLOW
2184 004376 032777 020000 174654 BIT @CLREND, #RCSR IF @CLREND NOTSETIN @RCSR THEN
2185 004404 001001 BNE #160 ;
```

```
2186 ; CLRSEND DID NOT SET  
2187 004406 ERRHRD 51,,FORCE  
2188 004406 104051 ERROR 51  
2189  
2190 ; WITH DTR  
2191 004410 ENDIF  
2192 004410 #60:  
2193 004410 ENDSUB  
2194  
2195 ; CLEAR  
2196 004410 BGNSUB  
2197 004410 012767 004416 174472 MOV #64,,#LPERR  
2198  
2199 ; CLEAR DTR  
2200 004416 LET #RCSR := #RCSR CLR.BY #DTR  
2201 004416 042777 000002 174634 BIC #DTR,#RCSR  
2202  
2203 004424 IF ; CLRSEND SHOULD FOLLOW  
2204 004424 032777 020000 174626 BIT #CLRSEND,#RCSR  
2205 004432 001401 BEQ #61  
2206 ; CLRSEND DID NOT  
2207 004434 ERRHRD 52,,FORCE  
2208 004434 104052 ERROR 52  
2209  
2210 ; CLEAR WITH DTR  
2211 004436 ENDIF  
2212 004436 #61:  
2213 004436 ENDSUB  
2214 004436 ENDTST  
2215  
2216  
2217  
2218
```

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CVDVAD.P11 12-JUL-84 05:04 T17 TEST THAT CLRSEND SETS AND CLEARS

```

2219 ;|*****
2220 ;|*****
2221 ;|TEST 20 TEST THAT RING SETS AND CLEARS
2222 ;| AS REQSEND SETS AND CLEARS
2223 ;| THE (-FR) JUMPER MUST BE IN FOR THIS TEST.
2224 ;|*****
2225 004436 000004 TST20: SCOPE
2226 004440 012767 000010 174512 MOV #10,#TIMES ;DO 10 ITERATIONS
2227 004446 012767 000020 174524 MOV #20,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
2228 ;| ; CAN WE USE THE WRAPAROUND??
2229 004454 ;| IF #CABLE+FRFD NOTSETIN #USMR THEN
2230 004454 032767 060000 174536 BIT #CABLE+FRFD,#USMR
2231 004462 001004 BNE #62
2232 ;| ; CAN'T TEST WITHOUT BERG OR H315
2233 ;| ; OR WITH (-FR) JUMPER OUT.
2234 004464 ;| EXIT TST
2235 004464 012767 000001 174466 MOV #1,#TIMES
2236 004472 000441 BR TST21 ;EXIT THIS TEST
2237 004474 ;| ENDIF
2238 004474 ;|62:
2239 ;|
2240 ;| ; REQSEND AND
2241 ;| ; RING ARE CONNECTED
2242 ;|
2243 ;| ; BY THE H315 OR EQUIV.
2244 ;|
2245 ;| ; CLEAR
2246 004474 ;| BGNSUB
2247 004474 012767 004502 174406 MOV #64,#LPERR
2248 ;|
2249 ;| ; CLEAR REQSEND
2250 004502 ;| LET #RCSR := #RCSR CLR.BY #REQSEND
2251 004502 042777 000004 174550 BIC #REQSEND,#RCSR
2252 ;| ; RING SHOULD FOLLOW
2253 004510 ;| IF #RING SETIN #RCSR THEN
2254 004510 032777 040000 174542 BIT #RING,#RCSR
2255 004516 001401 BEQ #63
2256 ;| ; RING DID NOT
2257 004520 ;| ERRHRD 53.,FORCE
2258 004520 104053 ERROR 53
2259 ;|
2260 ;| ; CLEAR WITH REQSEND
2261 004522 ;| ENDIF
2262 004522 ;|63:
2263 004522 ;| ENDSUB
2264 ;|
2265 ;| ; SET
2266 004522 ;| BGNSUB
2267 004522 012767 004530 174360 MOV #64,#LPERR
2268 ;|
2269 ;| ; SET REQSEND
2270 004530 ;| LET #RCSR := #RCSR SET.BY #REQSEND
2271 004530 052777 000004 174522 BIS #REQSEND,#RCSR
2272 ;| ; RING SHOULD FOLLOW
2273 004536 ;| IF #RING NOTSETIN #RCSR THEN
2274 004536 032777 040000 174514 BIT #RING,#RCSR

```


MAINDEC-ZZ-CVDVA-D MACY11 30A(1052) 12-JUL-84 09:52 PAGE 60
CVDVAD.P11 12-JUL-84 05:04 T20 TEST THAT RING SETS AND CLEARS

```

2309 ;*****3*****
2310 ;*****
2311 ;*TEST 21 TEST THAT SECRC SETS AND CLEARS
2312 ;* AS SECXMIT SETS AND CLEARS
2313 ;*****
2314 004576 000004 TST21: SCOPE
2315 004600 012767 000010 174352 MOV #10,#TIMES ;DO 10 ITERATIONS
2316 004606 012767 000021 174364 MOV #21,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
2317 ; ; CAN WE USE THE WRAPAROUND??
2318 004614 ; IF #CABLE NOTSETIN #USWR THEN
2319 004614 032767 020000 174376 BIT #CABLE,#USWR
2320 004622 001004 BNE #66
2321 ; CAN'T TEST WITHOUT BERG OR H315.
2322 ; EXIT TST
2323 004624 012767 000001 174326 MOV #1,#TIMES
2324 004632 000441 BR TST22 ;EXIT THIS TEST
2325 004634 ; ENDF
2326 004634 #66:
2327
2328 ; SECXMIT AND
2329 ; SECRC ARE CONNECTED
2330
2331 ; BY THE H315 OR EQUIV.
2332
2333 ; CLEAR
2334 004634 ; BGNSUB
2335 004634 012767 004642 174246 MOV #64,#LPERR
2336
2337 ; CLEAR SECXMIT
2338 004642 LET #RCSR := #RCSR CLR.BY #SECXMIT
2339 004642 042777 000010 174410 BIC #SECXMIT,#RCSR
2340 ; SECRC SHOULD FOLLOW
2341 004650 IF #SECRC SETIN #RCSR THEN
2342 004650 032777 002000 174402 BIT #SECRC,#RCSR
2343 004656 001401 BEQ #67
2344 ; SECRC DID NOT
2345 004660 ERRHRD 56,.FORCE
2346 004660 104056 ERROR 56
2347
2348 ; CLEAR WITH SECXMIT
2349 004662 ENDF
2350 004662 #67:
2351 004662 ; ENDSUB
2352
2353 ; SET
2354 004662 ; BGNSUB
2355 004662 012767 004670 174220 MOV #64,#LPERR
2356
2357 ; SET SECXMIT
2358 004670 LET #RCSR := #RCSR SET.BY #SECXMIT
2359 004670 052777 000010 174362 BIS #SECXMIT,#RCSR
2360 ; SECRC SHOULD FOLLOW
2361 004676 IF #SECRC NOTSETIN #RCSR THEN
2362 004676 032777 002000 174354 BIT #SECRC,#RCSR
2363 004704 001001 BNE #70
2364 ; SECRC DID NOT SET

```

MAINDEC-ZZ-CVDVA D MACY11 30A(1052) 12 JUL-84 09:52 PAGE 61
CVDVAD.P11 12-JUL-84 05:04 T21 TEST THAT SECURE SETS AND CLEARS

```

2365 004706 ERRHRD 57.,FORCE
2366 004706 104057 ERROR 57
2367
2368 ; WITH SECXMIT
2369 004710 ENDIF
2370 004710 $70:
2371 004710 ENDSUB
2372
2373 ; CLEAR
2374 004710 BGNSUB
2375 004710 012767 004716 174172 MOV #64,LPERR
2376
2377 ; CLEAR SECXMIT
2378 004716 LET BRCSR := BRCSR CLR.BY #SECXMIT
2379 004716 042777 000010 174334 BIC #SECXMIT,BRCSR
2380
2381 004724 ; SECURE SHOULD FOLLOW
2382 004724 032777 002000 174326 BIT #SECURE,BRCSR
2383 004732 001401 BEQ #71
2384
2385 004734 ; SECURE DID NOT
2386 004734 104060 ERROR 60 ERRHRD 60.,FORCE
2387
2388 ; CLEAR WITH SECXMIT
2389 004736 ENDIF
2390 004736 $71:
2391 004736 ENDSUB
2392 004736 ENDTST
2393
2394
2395
2396

```

```

2397 ;*****
2398 ;*****
2399 ;TEST 22 TEST THAT DATAINT (RCSR-15) SETS
2400 ;* WHEN DTR CHANGES STATE
2401 ;* AND THAT DATAINT IS CLEARED AFTER READING RCSR
2402 ;* NOTE DTR IS TIED TO BOTH CARDET AND CLRSEND BY THE H315
2403 ;* THE (-FD) JUMPER MUST BE IN FOR THIS TEST.
2404 ;*****
2405 004736 000004 TST22: SCOPE
2406 004740 012767 000010 174212 MOV #10,#TIMES ;DO 10 ITERATIONS
2407 004746 012767 000022 174224 MOV #22,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
2408 ; CAN WE USE THE WRAPAROUND??
2409 004754 IF #CABLE+FRFD NOTSETIN #USWR THEN
2410 004754 032767 060000 174236 BIT #CABLE+FRFD,#USWR
2411 004762 001004 BNE $72
2412 ; CAN'T TEST WITHOUT BERG OR H315
2413 ; OR WITH (-FD) JUMPER OUT.
2414 004764 EXIT TST
2415 004764 012767 000001 174166 MOV #1,#TIMES
2416 004772 000463 BR TST23 ;EXIT THIS TEST
2417 004774 ENDF
2418 004774 $72:
2419
2420 ; MAKE SURE NOTHING UNEXPECTED HAPPENS
2421 004774 012746 000340 MOV #PR7,-(SP) ;PUT NEW PS ON STACK
2422 005000 012746 005006 MOV #64,-(SP) ;PUT NEW PC ON STACK
2423 005004 000002 RTI ;POP NEW PC AND PS
2424 005006 64:
2425
2426 ;READ TWICE - CLEARS
2427 005006 BGNSUB
2428 005006 012767 005014 174074 MOV #65,#LPERR
2429
2430 ; CLEAR DTR
2431 005014 042777 000002 174236 BIC #DTR,#RCSR LET #RCSR := #RCSR CLR.BY #DTR
2432 ;WAIT 1 MILLI-SEC FOR CABLE
2433 005022 WAITMS 1
2434 005022 010546 MOV R5,-(SP)
2435 005024 012745 000001 MOV #1,-(R5)
2436 005030 004767 004716 JSR PC,WAIT
2437 005034 012605 MOV (SP)+,R5
2438
2439 ; READ RCSR - TO CLEAR DATAINT
2440 005036 017703 174216 MOV #RCSR,R3 LET R3 := #RCSR
2441
2442 ; READ RCSR AGAIN
2443 005042 032777 100000 174210 BIT #DATAINT,#RCSR IF #DATAINT SETIN #RCSR THEN
2444 005050 001401 BEQ $73
2445 ; READING RCSR DID NOT CLEAR DATAINT
2446 005052 ERRHRD 61,EDATAINT
2447 005052 104061 ERROR 61
2448 005054 ENDF
2449 005054 $73:
2450
2451 005054 ENDSUB
2452

```



```

2499 ;*****
2500 ;*****
2501 ;*TFST 23 TEST THAT DATAINT SETS WHEN RING SETS
2502 ;* AND THAT DATAINT DOES NOT SET WHEN RING CLEARS
2503 ;* THE (-FR) JUMPER MUST BE IN FOR THIS TEST.
2504 ;*****
2505 005142 000004 TST23: SCOPE
2506 005144 012767 000010 174006 MOV #10,#TIMES ;DO 10 ITERATIONS
2507 005152 012767 000023 174020 MOV #23,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
2508 ; CAN WE USE THE WRAPAROUND??
2509 005160 IF ; CAN WE USE THE WRAPAROUND??
2510 005160 032767 060000 174032 BIT #CABLE*FRFD,#USMR ;CAN'T TEST WITHOUT BERG OR H315
2511 005166 001004 BNE $77 ; OR WITH (-FR) JUMPER OUT.
2512 ;
2513 ;
2514 005170 EXIT TST
2515 005170 012767 000001 173762 MOV #1,#TIMES
2516 005176 000473 BR TST24 ;EXIT THIS TEST
2517 005200 ENDIF
2518 005200 $77:
2519 ;
2520 ; NO INTERRUPTS
2521 005200 012746 000340 MOV #PR7,-(SP) ;PUT NEW PS ON STACK
2522 005204 012746 005212 MOV #64,-(SP) ;PUT NEW PC ON STACK
2523 005210 000002 RTI ;POP NEW PC AND PS
2524 005212 64:
2525 ;
2526 ; START OFF WITH EVERYTHING CLEAR
2527 005212 BGNSUB
2528 005212 012767 005220 173670 MOV #65,#LPERR
2529 ;
2530 ; CLEAR RING
2531 005220 LET @RCSR := @RCSR CLR.BY @REQSEND
2532 005220 042777 000004 174032 BIC @REQSEND,@RCSR
2533 ; WAIT 1 MILLI-SEC FOR CABLE
2534 005226 WAITMS 1
2535 005226 010546 MOV R5,-(SP)
2536 005230 012745 000001 MOV #1,-(R5)
2537 005234 004767 004512 JSR PC,WAIT
2538 005240 012605 MOV (SP)+,R5
2539 ;
2540 005242 ; READ ONCE
2541 005242 017703 174012 MOV @RCSR,R3 LET R3 := @RCSR
2542 ;
2543 005246 ; READ TWICE
2544 005246 032777 100000 174004 BIT @DATAINT,@RCSR IF @DATAINT SETIN @RCSR THEN
2545 005254 001401 BEQ $100
2546 ;
2547 005256 ; READING RCSR DID NOT CLEAR DATAINT
2548 005256 104065 ERROR 65 ERRHRD 65, EDATAINT
2549 005260 ENDIF
2550 005260 $100:
2551 005260 ENDSUB
2552 ;
2553 ; SET RING --> SET DATAINT
2554 005260 ; BGNSUB

```


MS

```

2605      ;*****
2606      ;*****
2607      ;*****
2608      ;TEST 24      TEST THAT DATAINT SETS WHEN SECRC CHANGES STATE
2609      ;*****
2609      005366 000004      TST24: SCOPE
2610      005370 012767 000010 173562      MOV      #10,#TIMES      ;;DO 10 ITERATIONS
2611      005376 012767 000024 173574      MOV      #24,#TESTN      ;;SET TEST NUMBER IN APT MAIL BOX
2612      ;*****
2613      005404      ;*****
2614      005404 032767 020000 173606      BIT      #CABLE,#USMR      ; CAN WE USE THE WRAPAROUND??
2615      005412 001004      BNE      #103      ;*****
2616      ;*****
2617      005414      ;*****
2618      005414 012767 000001 173536      MOV      #1,#TIMES      ; CAN'T TEST WITHOUT BERG OR H315.
2619      005422 000454      BR       TST25      EXIT TST
2620      005424      ;*****
2621      005424      #103:      ;*****
2622      ;*****
2623      ;*****
2624      005424 012746 000340      MOV      #PR7,-(SP)      ;NO INTERRUPTS
2625      005430 012746 005436      MOV      #64,-(SP)      ;;PUT NEW PS ON STACK
2626      005434 000002      RTI      ;;PUT NEW PC ON STACK
2627      005436      64:      ;*****
2628      ;*****
2629      ;*****
2630      ;*****
2631      ;*****
2632      005436      ;START FRESH
2633      005436 042777 000010 173614      BIC      #SECXMIT,#RCSR      ;CLEAR SECRC
2634      005444      LET      #RCSR := #RCSR CLR.BY #SECXMIT
2635      005444 017703 173610      MOV      #RCSR,R3      LET      R3 := #RCSR
2636      ;*****
2637      ;*****
2638      005450      ;SET SECRC --> DATAINT SET
2639      005450 012767 005456 173432      MOV      #65,#LPERR      BGNSUB
2640      ;*****
2641      ;*****
2642      005456      ;SET SECRC
2643      005456 052777 000010 173574      BIS      #SECXMIT,#RCSR      LET      #RCSR := #RCSR SET.BY #SECXMIT
2644      ;*****
2645      005464      ;WAIT 1 MILLI-SEC FOR CABLE
2646      005464 010546      MOV      #R5,-(SP)      WAITMS 1
2647      005466 012745 000001      MOV      #1,-(R5)
2648      005472 004767 004254      JSR      PC,WAIT
2649      005476 012605      MOV      (SP)+,R5
2650      ;*****
2651      005500 032777 100000 173552      BIT      #DATAINT,#RCSR      IF      #DATAINT NOTSETIN #RCSR THEN
2652      005506 001001      BNE      #104
2653      ;*****
2654      005510      ;SETTING SECRC DID NOT SET DATAINT
2655      005510 104124      ERROR 124      ERRMRD 124,, E2DATA
2656      005512      ;*****
2657      005512      #104:      ;*****
2658      005512      ;*****
2659      ;*****
2660      ;*****
;CLEAR SECRC --> DATAINT SET

```



```

2661 005512
2662 005512 012767 005520 173370      MOV      #64#, #LPERR
2663
2664 005520
2665 005520 042777 000010 173532      BIC      #SECMIT, @RCSR
2666
2667 005526
2668 005526 010546
2669 005530 012745 000001
2670 005534 004767 004212
2671 005540 012605
2672 005542
2673 005542 032777 100000 173510      BIT      @DATAINT, @RCSR
2674 005550 001001
2675
2676 005552
2677 005552 104125      ERROR   125
2678 005554
2679 005554
2680 005554
2681 005554
2682
2683
2684

```

```

BGNSUB
;CLEAR SECRC
LET @RCSR := @RCSR CLR.BY #SECMIT
;WAIT 1 MILLI-SEC FOR CABLE
WAITMS 1
IF @DATAINT NOTSETIN @RCSR THEN
;CLEARING SECRC DID NOT SET DATAINT
ERRHRD 125,, E2DATA
ENDIF
ENDSUB
ENDTST

```

#105:

```
2685 ; ;
2686 ; ;
2687 ; *TEST 25 TEST THAT XMIT RDY - TCSR 7 - CLEARS
2688 ; * WHEN TBUF IS LOADED WITH A CHARACTER
2689 ; * AND THAT IT SETS WITHIN A REASONABLE AMOUNT OF TIME.
2690 ; ;
2691 005554 000004 TST25: SCOPE
2692 005556 012767 000001 173374 MOV #1, #TIMES ; DO 1 ITERATION
2693 005564 012767 000025 173406 MOV #25, #TESTN ; SET TEST NUMBER IN APT MAIL BOX
2694 ; ;
2695 ; THIS TEST IS 'BREAK OR HALT' SENSITIVE.
2696 005572 ; IF #APTENV SET IN #ENV THEN
2697 005572 032767 000001 173414 BIT #APTENV, #ENV
2698 005600 001404 BEQ #106
2699 005602 ; EXIT TEST
2700 005602 012767 000001 173350 MOV #1, #TIMES
2701 005610 000454 BR TST26 ; EXIT THIS TEST
2702 005612 ; ENDF
2703 005612 ; ;
2704 005612 ; ;
2705 005612 012767 005620 173270 MOV #64, #LPERR
2706 ; ;
2707 ; LOAD TBUF WITH ONE CHARACTER
2708 ; WAIT FOR READY TO SET
2709 ; (SHOULD BE VERY SHORT WAIT
2710 ; SINCE UART DOUBLE BUFFERS ITS INPUT)
2711 ; ;
2712 005620 ; SEND A CHARACTER
2713 005620 105077 173444 CLR #BTBUF LET #BTBUF :B= #0
2714 ; ;
2715 ; WAIT A MAXIMUM
2716 ; OF 50 MSEC FOR
2717 005624 ; XMIT RDY TO SET IN TCSR
2718 005624 010546 MOV #R5, -(#SP) CALL TIMER IN (<#1000, #XMITRDY, TCSR, #SET>)
2719 005626 012745 177777 MOV #SET, -(#R5)
2720 005632 016745 173426 MOV #TCSR, -(#R5)
2721 005636 012745 000200 MOV #XMITRDY, -(#R5)
2722 005642 012745 001000 MOV #1000, -(#R5)
2723 005646 004767 003622 JSR #PC, #TIMER
2724 005652 012605 MOV (#SP), #R5
2725 ; ;
2726 ; TIMER RETURNS AN ERROR IF BIT DID
2727 005654 ; NOT MEET CONDITION WITHIN TIME LIMIT
2728 005654 103001 BCC #107 IF .ERROR THEN
2729 ; ;
2730 005656 ; XMIT RDY DID NOT SET IN TCSR
2731 005656 104066 ERROR #66 ERRORD 66, .DIDNOT
2732 005660 ; ;
2733 005660 ; ;
2734 005660 ; ;
2735 ; ;
2736 005660 ; ;
2737 005660 012767 005654 173222 MOV #64, #LPERR
2738 ; ;
2739 ; LOAD TBUF WITH A SECOND CHARACTER
2740 ; CHECK IMMEDIATELY THAT XMITRDY IS CLEAR
; AND THEN WAIT FOR IT TO SET
```

```

2741
2742
2743 005666
2744 005666 105077 173376 CLR8 @TBUF
2745 005672 000240 NOP
2746
2747
2748 005674
2749 005674 032777 000200 173362 BIT @XMITRDY,@TCSR
2750 005702 001401 BEQ #110
2751
2752 005704
2753 005704 104067 ERROR 67
2754 005706
2755 005706 #110:
2756
2757
2758
2759
2760 005706
2761 005706 010546 MOV R5,-(SP)
2762 005710 012745 177777 MOV @SET,-(R5)
2763 005714 016745 173344 MOV TCSR,-(R5)
2764 005720 012745 000200 MOV @XMITRDY,-(R5)
2765 005724 012745 001000 MOV #1000,-(R5)
2766 005730 004767 003540 JSR PC,TIMER
2767 005734 012605 MOV (SP),R5
2768 005786
2769 005736 103001 BCC #111
2770
2771 005740
2772 005740 104070 ERROR 70
2773 005742
2774 005742 #111:
2775 005742
2776 005742

;SEND SECOND CHARACTER
LET @TBUF :B= #0
; GIVE IT TIME TO CLEAR
; XMITRDY SHOULD HAVE CLEARED UPON
; RECEIPT OF A CHARACTER
IF @XMITRDY SET IN @TCSR THEN
; XMITRDY DID NOT CLEAR IN TCSR
ERRHRD 67,,DIDNOT
ENDIF

;WAIT A MAXIMUM
;OF 50 MSEC FOR
;XMIT RDY TO SET IN TCSR
CALL TIMER IN <#1000,@XMITRDY,TCSR,@SET>

IF .ERROR THEN
;XMIT RDY DID NOT SET IN TCSR
ERRHRD 70,,DIDNOT
ENDIF

ENDSUB
ENDTST
  
```

```

2777
2778
2779
2780
2781
2782
2783 005742 000004
2784 005744 012767 000010 173206
2785 005752 012767 000026 173220
2786
2787
2788 005760
2789 005760 052777 000004 173276
2790
2791 005766
2792 005766 012767 005774 173114
2793
2794
2795 005774
2796 005774 105077 173270
2797
2798
2799
2800
2801 006000
2802 006000 010546
2803 006002 012745 177777
2804 006006 016745 173246
2805 006012 012745 000200
2806 006016 012745 001000
2807 006022 004767 003446
2808 006026 012605
2809
2810
2811 006030
2812 006030 103001
2813
2814 006032
2815 006032 104071
2816 006034
2817 006034
2818
2819 006034
2820
2821 006034
2822 006034 012767 006042 173046
2823
2824
2825 006042
2826 006042 000005
2827
2828 006044

```

```

;*****
;*****
;TEST 26 TEST THAT OUTPUTTING A CHAR FROM TBUF (WITH MAINT SET)
;* RESULTS IN RCVRDONE SETTING WITHIN A REASONABLE AMOUNT OF TIME
;* AND THAT RESET CLEARS THE BIT.
;*****
TST26: SCOPE
MOV #10,#TIMES ;DO 10 ITERATIONS
MOV #26,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
; SET THE MAINTENANCE BIT
LET @TCSR := @TCSR SET.BY @MAINT
BIS @MAINT,@TCSR
BGNSUB
MOV #64,#LPERR
; SEND A CHARACTER AND LET IT WRAP AROUND
LET @TBUF :B= #0
CLR @TBUF
; WAIT A MAXIMUM OF 50 MSEC
; FOR RCVR DONE TO SET IN
; RCSR
CALL TIMER IN <#1000,@RCVRDONE,RCSR,#SET>
MOV RS,-(SP)
MOV @SET,-(RS)
MOV RCSR,-(RS)
MOV @RCVRDONE,-(RS)
MOV #1000,-(RS)
JSR PC,TIMER
MOV (SP),RS
;DIDN'T SET IN TIME
IF.ERROR THEN
; RCVRDONE DID NOT SET IN RCSR
ERRHRD 71,,DIDNOT
ENDIF
@112:
ENDSUB
BGNSUB
MOV #64,#LPERR
; NOW THAT IT IS SET SEE IF IT CAN BE RESET
; THIS ALSO WILL CLEAR THE MAINT. BIT
BRESET
RESET
IF @RCVRDONE SETIN @RCSR THEN

```

2829 006044 032777 000200 173206
2830 006052 001401
2831
2832 006054
2833 006054 104072
2834 006056
2835 006056
2836 006056
2837 006056

BIT #RCVRDONE, @RCSR
BEQ \$113

; RCVRDONE DID NOT RESET IN RCSR.
ERRHRD 72,,DIDNOT

ERROR 72

\$113:

ENDIF

ENDSUB

ENDTST

```

2838 ;*****
2839 ;*****
2840 ;*TEST 27 TEST THAT RCVRDONE IS CLEARED BY READING RBUF
2841 ;*****
2842 006056 000004 TST27: SCOPE
2843 006060 012767 000010 173072 MOV #10,#TIMES ;DO 10 ITERATIONS
2844 006066 012767 000027 173104 MOV #27,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
2845
2846 ; SET MAINT. BIT
2847 006074 LET @TCSR := @TCSR SET.BY #MAINT
2848 006074 052777 000004 173162 BIS #MAINT,@TCSR
2849 006102 BGNSUB
2850 006102 012767 006110 173000 MOV #64,#LPERR
2851 ; OUTPUT A CHARACTER WITH MAINTENANCE
2852 ; SET, AND WAIT FOR XMITRDY TO SET.
2853
2854 ; OUTPUT A CHARACTER
2855 006110 LET @TBUF :B= #0
2856 006110 105077 173154 CLR @TBUF
2857 ; WAIT MAXIMUM OF 1000 MSEC
2858 ; FOR RCVRDONE TO SET IN
2859 ; RCSR
2860 006114 CALL TIMER IN <#1000,@RCVRDONE,RCSR,#SET
2861 006114 010546 MOV R5,-(SP)
2862 006116 012745 177777 MOV #SET,-(R5)
2863 006122 016745 173132 MOV RCSR,-(R5)
2864 006126 012745 000200 MOV #RCVRDONE,-(R5)
2865 006132 012745 001000 MOV #1000,-(R5)
2866 006136 004767 003332 JSR PC,TIMER
2867 006142 012605 MOV (SP)+,R5
2868
2869 ; DID IT BECAME READY?
2870 006144 IF.ERROR THEN
2871 103001 BCC #114
2872 ;RCVRDONE DID NOT SET IN RCSR
2873 006146 104073 ERROR 73 ERRHRD 73.. DIDNOT
2874 006150
2875 006150 #114: ENDF
2876 006150 ENDSUB
2877
2878 ; NOW THAT IT IS SET LETS SEE IF READING THE
2879 ; BUFFER CLEARS RCVRDONE.
2880
2881 ; READ BUFFER
2882 006150 LET R0 :B= @RBUF
2883 006150 117700 173106 MOVB @RBUF,R0
2884
2885 ; IF #RCVRDONE SET IN @RCSR THEN
2886 006154 032777 000200 173076 BIT #RCVRDONE,@RCSR
2887 006162 001401 BEQ #115
2888
2889 ; RCVRDONE DID NOT CLEAR IN RCSR
2890 006164 104074 ERROR 74 ERRHRD 74,DIDNOT
2891 006166
2892 006166 #115: ENDF
2893 006166 ENDTST

```

```

2894 ;*****
2895 ;*****
2896 ;*TEST 30      TEST THAT RCVRACT - RCSR 11 - SETS
2897 ;*              WHEN A START BIT IS RECEIVED AND
2898 ;*              CLEARS WHEN RCVRDONE - RCSR 7 - SETS
2899 ;*****
2900 006166 000004 TST30: SCOPE
2901 006170 012767 000010 172762     MOV     #10,#TIMES      ;;DO 10 ITERATIONS
2902 006176 012767 000030 172774     MOV     #30,#TESTN    ;;SET TEST NUMBER IN APT MAIL BOX
2903
2904 ; THIS TEST IS 'BREAK OR HALT' SENSITIVE.
2905 006204 ; IF #APTENV SET IN #ENV THEN
2906 006204 032767 000001 173002     BIT     #APTENV,#ENV
2907 006212 001404                      BEQ     $116
2908 006214                      EXIT TEST
2909 006214 012767 000001 172736     MOV     #1,#TIMES
2910 006222 000500                      BR      TST31          ;;;EXIT THIS TEST
2911 006224                      ENDIF
2912 006224 $116:
2913 006224                      LET #TCSR := #TCSR SET BY #MAINT
2914 006224 052777 000004 173032     BIS     #MAINT,#TCSR
2915 006232                      LET R0 := #CLR
2916 006232 012700 000000     MOV     #CLR,R0
2917 006236                      LET R1 := #0
2918 006236 005001     CLR     R1
2919 ;LOAD A CHARACTER INTO TBUF
2920 ;WAIT FOR RCVRACT TO SET
2921
2922 ;SEND A CHARACTER
2923 006240     LET #TBUF := #0
2924 006240 105077 173024     CLRB   #TBUF
2925 ;REPEAT
2926 006244 $117:
2927 006244                      IF #RCVRACT SET IN #RCSR THEN
2928 006244 032777 004000 173006     BIT     #RCVRACT,#RCSR
2929 006252 001403                      BEQ     $120
2930 006254                      LET R0 := #SET
2931 006254 012700 177777     MOV     #SET,R0
2932 006260                      ELSE
2933 006260 000401                      BR      $121
2934 006262 $120:
2935 006262                      LET R1 := R1 + #1
2936 006262 005201     INC     R1
2937 006264                      ENDIF
2938 006264 $121:
2939 006264                      UNTIL R0 EQ #SET OR R1 HI MAX
2940 006264 020027 177777     CMP     R0,#SET
2941 006270 001403                      BEQ     $122
2942 006272 020167 000124     CMP     R1,MAX
2943 006276 101762                      BLOS   $117
2944 006300 $122:
2945 006300                      IF R1 HI MAX THEN
2946 006300 020167 000116     CMP     R1,MAX
2947 006304 101407                      BLOS   $123
2948 ;IT NEVER SET
2949 ;RCVRACT DID NOT SET IN RCSR.

```

```

2950 006306
2951 006306 104075          ERROR 75          ERRHRD 75,, DIDNOT
2952 006310
2953 006310 017700 172746    MOV    @RBUF,RO      LET RO := @RBUF ; CLEAR BUFFER
2954 006314
2955 006314 012767 000001 172636    MOV    @1,@TIMES    EXIT TEST
2956 006322 000440          BR      TST31        ;;;EXIT THIS TEST
2957 006324
2958 006324          #123:              ENDF
2959
2960
2961
2962
2963
2964
2965 006324
2966 006324          #124:              WHILE @RCVRACT SETIN @RCSR DO
2967 006324 032777 004000 172726    BIT    @RCVRACT,@RCSR
2968 006332 001416          BEQ    #125
2969
2970 006334
2971 006334 032777 000200 172716    BIT    @RCVRDONE,@RCSR    IF @RCVRDONE SETIN @RCSR THEN
2972 006342 001411          BEQ    #126
2973 006344
2974 006344 032777 004000 172706    BIT    @RCVRACT,@RCSR    IF @RCVRACT SETIN @RCSR THEN
2975 006352 001405          BEQ    #127
2976
2977
2978 006354
2979 006354 104076          ERROR 76          ;RCVRDONE AND RCVRACT
;BOTH SET
ERRHRD 76, DONEACT
2980
2981 006356
2982 006356 012767 000001 172574    MOV    @1,@TIMES    ;NO USE CONTINUING
2983 006364 000417          BR      TST31        EXIT TST
2984 006366
2985 006366          #127:              ;;;EXIT THIS TEST
2986 006366          #126:              ENDF
2987 006366          #126:              ENDF
2988 006366
2989 006366 000756          BR      #124        ENDDO
2990 006370          #125:
2991
2992
2993 006370
2994 006370 032777 000200 172662    BIT    @RCVRDONE,@RCSR    ;RCVRACT = 0 NOW.
2995 006376 001001          BNE    #130          IF @RCVRDONE NOTSETIN @RCSR THEN
2996
2997 006400
2998 006400 104077          ERROR 77          ;RCVRDONE DID NOT SET IN RCSR
ERRHRD 77,,DIDNOT
2999
3000 006402
3001 006402          #130:              ;SET IT BACK.
3002
3003
3004
3005
;TEST THAT READING THE RECEIVER
;BUFFER CLEARS RCVRDONE
    
```



```
3006                                     ;READ CHAR.  
3007 006402                             LET RO := BRBUF  
3008 006402 017700 172654             MOV     BRBUF,RO  
3009  
3010 006406                             IF #RCVRDONE SETIN BRCSR THEN  
3011 006406 032777 000200 172644     BIT     #RCVRDONE,BRCSR  
3012 006414 001401                     BEQ     #131  
3013  
3014 006416                             ;RCVRDONE DID NOT CLEAR IN RCSR  
3015 006416 104100                     ERRHRD 100.,DIDNOT  
3016 006420                             ENDIF  
3017 006420                             #131:  
3018  
3019 006420                             EXIT  
3020 006420 000401                     :::EXIT THIS TEST  
3021 006422 070000                     BR      TST31  
3022                                     MAX:70000  
3023 006424                             ENDTST  
3024
```

```

3025
3026 ;*****
3027 ;*****
3028 ;*TEST 31 TEST THE OVERRUN BIT - RBUF 14
3029 ;*****
3030 006424 000004 TST31: SCOPE
3031 006426 012767 000010 172524 MOV #10,#TIMES ;DO 10 ITERATIONS
3032 006434 012767 000031 172536 MOV #31,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
3033
3034 006442 BGNSUB
3035 006442 012767 006450 172440 MOV #64,#LPERR
3036 ;OUTPUT 2 CHARACTERS WITH
3037 ;AMPLE DELAYS BETWEEN FOR RECEPTION.
3038 ;THIS SHOULD AN CAUSE OVERRUN ERROR.
3039
3040 ;OUTPUT 1 CHARACTER
3041 006450 LET @TBUF :B= #0
3042 006450 105077 172614 CLRB @TBUF
3043
3044 006454 ;GO AWAY FOR 1024. M SEC
3045 006454 010546 MOV R5,-(SP) WAITMS 1024.
3046 006456 012745 002000 MOV #1024,-(R5)
3047 006462 004767 003264 JSR PC,WAIT
3048 006466 012605 MOV (SP)+,R5
3049
3050 ;OUTPUT 2ND CHARACTER
3051 006470 LET @TBUF :B= #0
3052 006470 105077 172574 CLRB @TBUF
3053
3054 006474 ;LET OVERRUN HAPPEN
3055 006474 010546 MOV R5,-(SP) WAITMS 1024.
3056 006476 012745 002000 MOV #1024,-(R5)
3057 006502 004767 003244 JSR PC,WAIT
3058 006506 012605 MOV (SP)+,R5
3059
3060 ;READ BUFFER AND ERROR BITS
3061 006510 LET R4 := @RBUF
3062 006510 017704 172546 MOV @RBUF,R4
3063
3064 ;IT DIDN'T SET
3065 006514 IF #ORERR NOTSET IN R4 THEN
3066 006514 032704 040000 BIT #ORERR,R4
3067 006520 001005 BNE #132
3068
3069 ;ORERR DID NOT SET IN RBUF
3070 006522 104101 ERROR ERRHRD 101,,UIDNOT
3071
3072 ;NO USE COMPOUNDING ERRORS
3073 006524 EXIT TST
3074 006524 012767 000001 172426 MOV #1,#TIMES
3075 006532 000456 BR TST32 ;;;EXIT THIS TEST
3076 006534 ENDF
3077 006534 #132:
3078 006534 ENDSUB
3079
3080 ;NOW SEE IF ERROR BIT SET WITH OVERRUN ERROR:
    
```

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```

3081 006534                                BGNSUB
3082 006534 012767 006542 172346          MOV    #64,LPERR
3083 006542                                IF #ERROR NOTSET IN R4 THEN
3084 006542 032704 100000                  BIT    #ERROR,R4
3085 006546 001005                          BNE    #133
3086
3087
3088 006550                                ;ERROR DID NOT SET IN RBUF
3089 006550 104102                          ERROR  102
3090
3091
3092
3093 006552                                ;-WHEN ORERR SET.
3094 006552 012767 000001 172400          MOV    #1,TIMES
3095 006560 000443                          BR     TST32
3096 006562                                ;EXIT THIS TEST
3097 006562                                ENDIF
3098 006562                                #133:
3099
3100
3101 006562                                BGNSUB
3102 006562 012767 006570 172320          MOV    #64,LPERR
3103
3104
3105
3106
3107
3108
3109
3110
3111
3112
3113
3114
3115
3116
3117
3118
3119 006604                                ;CHECK REAL RBUF TO SEE IF ORERR IS STILL SET.
3120 006604 012767 006570 172320          MOV    #64,LPERR
3121
3122
3123
3124
3125
3126
3127
3128
3129
3130
3131
3132
3133
3134
3135
3136

```

IF #ERROR NOTSET IN R4 THEN

;ERROR DID NOT SET IN RBUF
 ERRHRD 102.,DIDNOT

;WHEN ORERR SET.
 ;GET OUT NOW.
 EXIT TST

;;EXIT THIS TEST
 ENDIF

ENDSUB

BGNSUB

;CHECK REAL RBUF TO SEE IF ORERR IS STILL SET.

IF #ORERR NOTSET IN #RBUF THEN

;READING RBUF CLEARED ORERR.
 ERRHRD 103,ITCLRED

;SKIP REST OF TEST
 EXIT

;;EXIT THIS TEST
 ENDIF

ENDSUB

BGNSUB

;NOW SEE IF THEY CLEAR WHEN ANOTHER CHAR. IS RECEIVED

;SEND A CHARACTER AROUND.
 LET #TBUF :B= #0

;LET IT CIRCULATE
 WAITMS 1024.

IF #ORERR SET IN #RBUF THEN

;ORERR DID NOT CLEAR IN #RBUF
 ERRHRD 104.,DIDNOT

```

3137 006642 104104          ERROR 104
3138
3139
3140
3141 006644
3142 006644 012767 000001 172306  MOV  #1,#TIMES
3143 006652 000406          BR    TST32          ;;;EXIT THIS TEST
3144 006654
3145 006654          $155:          ENDF
3146
3147 006654
3148 006654 032777 100000 172400  BIT  #ERROR,#RBUF          IF #ERROR SETIN #RBUF THEN
3149 006662 001401          BEQ  #136
3150
3151 006664
3152 006664 104105          ERROR 105          ;ERROR DID NOT CLEAR IN RBUF
3153
3154 006666          ENDF          ERRHRD 105.,DIDNOT
3155 006666          $136:
3156 006666
3157 006666          ENDSUB
3158 006666 000400          BR    TST32          EXIT
3159
3160 006670          ;;;EXIT THIS TEST
3161
          .EVEN
          ENDTST
  
```

```

3162
3163
3164
3165
3166
3167
3168
3169
3170 006670 000004
3171 006672 012767 000010 172260
3172 006700 012767 000032 172272
3173 006706
3174 006706 032767 000200 172304
3175 006714 001004
3176 006716
3177 006716 012767 000001 172234
3178 006724 000552
3179 006726
3180 006726
3181
3182 006726
3183 006726 032767 000001 172260
3184 006734 001404
3185 006736
3186 006736 012767 000001 172214
3187 006744 000542
3188 006746
3189 006746
3190
3191 006746
3192 006746 012767 177777 000272
3193 006754
3194 006754 012767 177777 000266
3195 006762
3196 006762 052777 000004 172274
3197
3198 006770
3199 006770 005003
3200 006772 000401
3201 006774
3202 006774 005203
3203 006776
3204 006776 020327 000017
3205 007002 003060
3206 007004
3207 007004 017700 172252
3208
3209 007010
3210 007010 116377 007170 172250
3211
3212 007016
3213 007016 005002
3214
3215 007020
3216 007020 005077 172244
3217

```

```

;*****
;*****
;TEST 32 PROGRAMMABLE BAUD RATE TEST
; TEST AT ALL SPEEDS AVAILABLE
; A COMPARISON WILL BE MADE TO SEE
; IF NEW TIME IS LESS THAN PREVIOUS.
;*****

```

```

TST32: SCOPE
MOV #10,#TIMES ;DO 10 ITERATIONS
MOV #32,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
; IF #PBR NOTSETIN #USWR THEN
BIT #PBR,#USWR
BNE #137
EXIT TST
MOV #1,#TIMES
BR TST33 ;EXIT THIS TEST
ENDIF

```

```

#137:
; THIS TEST IS 'BREAK OR HALT' SENSITIVE.
; IF #APTENV SETIN #ENV THEN

```

```

BIT #APTENV,#ENV
BEQ #140
EXIT TEST
MOV #1,#TIMES
BR TST33 ;EXIT THIS TEST
ENDIF

```

```

#140:
LET OLD := #-1
LET OLD+2 := #-1
LET #TCSR := #TCSR SET.BY #MAINT
;EACH BAUD RATE
INCR R3 FROM #0 TO #15. BY #1

```

```

CLR R3
BR #141
#142:
INC R3
#141:
CMP R3,#15.
BGT #143
LET R0 := #RBUF

```

```

;CHANGE BAUDE RATE
LET #TCSRHI := #RATES(R3)
;FLAG
LET BIT := #0
;OUTPUT THE CHARACTER
LET #TBUF := #0
;INITIALIZE COUNTER

```

3218	007024									LET NEW := #0
3219	007024	005067	000212		CLP	NEW				LET NEW+2 := #0
3220	007030									
3221	007030	005067	000210		CLR	NEW+2				WHILE BIT EQ #0 DO
3222	007034									
3223	007034			\$144:						
3224	007034	005702			TST	BIT				
3225	007036	001014			BNE	\$145				
3226	007040									IF #RCVRDONE SETIN #RCSR THEN
3227	007040	032777	000200	172212	BIT	#RCVRDONE,#RCSR				
3228	007046	001403			BEQ	\$146				;DONE - ITS READY
3229										LET BIT := #1
3230	007050									
3231	007050	012702	000001		MOV	#1,BIT				ELSE
3232	007054									
3233	007054	000404			BR	\$147				
3234	007056			\$146:						
3235										;OTHERWISE-INCREMENT TIME
3236	007056									LET NEW := NEW + #1
3237	007056	005267	000160		INC	NEW				
3238	007062									LET NEW+2 := NEW+2 + CARRY
3239	007062	005567	000156		ADC	NEW+2				
3240	007066									ENDIF
3241	007066			\$147:						
3242										;SIGNALS DONE
3243	007066									ENDDO
3244	007066	000762			BR	\$144				
3245	007070			\$145:						
3246										
3247	007070									IF NEW+2 LO OLD+2 THEN
3248	007070	026767	000150	000152	CMP	NEW+2,OLD+2				
3249	007076	103001			BHIS	\$150				
3250										; OK
3251	007100									ELSE
3252	007100	000412			BR	\$151				
3253	007102			\$150:						
3254										
3255	007102									; NEW+2 >= OLD+2
3256	007102	026767	000136	000140	CMP	NEW+2,OLD+2				IF NEW+2 EQ OLD+2 AND NEW LO OLD THEN
3257	007110	001005			BNE	\$152				
3258	007112	026767	000124	000126	CMP	NEW,OLD				
3259	007120	103001			BHIS	\$152				
3260										;OK
3261	007122									ELSE
3262	007122	000401			BR	\$153				
3263	007124			\$152:						
3264										
3265										;NEW+2 > OLD+2 OR
3266										;(NEW+2 = OLD+2 AND
3267										; NEW >= OLD)
3268	007124									;BAUD RATE DIDN'T CHANGE
3269	007124	104126			ERROR	126				ERRHRD 126, BAUDRATE
3270	007126									ENDIF
3271	007126			\$153:						
3272	007126									ENDIF
3273	007126			\$151:						

```

3274                                     ;UPDATE OLD TIME
3275 007126                               LET OLD := NEW
3276 007126 016767 000110 000112      MOV  NEW,OLD
3277 007134                               LET OLD*2 := NEW*2
3278 007134 016767 000104 000106      MOV  NEW*2,OLD*2
3279
3280 007142                               ENDINC ;BAUD RATE
3281 007142 000714                       BR   #142
3282 007144                               #143:
3283 007144                               LET R3 := #USMR*1 AND #17 ; PUT BAUD BACK
3284 007144 116703 172051               MOVB  #USMR*1,R3
3285 007150 110346                       MOVB  R3,-(SP)
3286 007152 142716 000017               BICB  #17,(SP)
3287 007156 142603                       BICB  (SP)+,R3
3288 007160                               LET @TCSRHI := RATES(R3) ; LIKE HE WANTED IT
3289 007160 116377 007170 172100      MOVB  RATES(R3),@TCSRHI
3290
3291 007166                               EXIT ;SKIP TABLE
3292 007166 000431                       BR   TST33 ;;;EXIT THIS TEST
3293
3294 007170

```

RATES: ; A TABLE OF THE ACTUAL BYTES TO MOVE INTO THE
 ; UPPER BYTE OF XCSR FOR EACH BAUD RATE
 ; ** NOTE: : THE VALUE INDICATED IN THE COLUMN 'OFFSET
 ; ** INTO TABLE' CAN BE PLACED INTO BITS<11:8>
 ; ** OF LOCATION 'USMR' TO CAUSE THE CORROSPONDING
 ; ** BAUD TO BE SELECTED IN THE DLV11-E UPON
 ; ** COMPLETION OF THIS TEST.

				BAUD	OFFSET INTO TABLE
3303	007170	010	R0050: .BYTE	010	0
3304	007171	030	R0070: .BYTE	030	1
3305	007172	050	R0110: .BYTE	050	2
3306	007173	070	R0135: .BYTE	070	3
3307	007174	110	R0150: .BYTE	110	4
3308	007175	130	R0300: .BYTE	130	5
3309	007176	150	R0600: .BYTE	150	6
3310	007177	170	R0200: .BYTE	170	7
3311	007200	210	R1800: .BYTE	1800	10
3312	007201	230	R2000: .BYTE	2000	11
3313	007202	250	R2400: .BYTE	2400	12
3314	007203	270	R3600: .BYTE	3600	13
3315	007204	310	R4800: .BYTE	4800	14
3316	007205	330	R7200: .BYTE	7200	15
3317	007206	350	R9600: .BYTE	9600	16
3318	007207	370	R10000: .BYTE	370	17
3319					

3320	007210	040502	042125	051040	BAUDRATE:	.ASCII /BAUD RATE DIDN'T CHANGE./
3321	007216	052101	020105	044504		
3322	007224	047104	052047	041440		
3323	007232	040510	043516	027105		
3324	007240	000				
3325		007242			.EVEN	
3326	007242	000000	000000		NEW: 0.0	
3327	007246	000000	000000		OLD: 0.0	
3328	007252					ENDTST
3329						
3330						
3331						


```

3332
3333
3334
3335
3336
3337
3338
3339
3340
3341 007252 000004
3342 007254 012767 000010 171676
3343 007262 012767 000033 171710
3344
3345 007270
3346 007270 005067 002544
3347
3348
3349 007274
3350 007274 016703 171756
3351
3352 007300
3353 007300 062703 000004
3354
3355 007304
3356 007304 010146
3357 007306 010301
3358 007310 012721 012032
3359 007314 012711 000340
3360 007320 012601
3361 007322
3362 007322 012767 007330 171560
3363
3364 007330
3365 007330 042777 000100 171726
3366
3367
3368 007336 012746 000000
3369 007342 012746 007350
3370 007346 000002
3371 007350
3372
3373
3374 007350
3375 007350 052777 000100 171706
3376
3377
3378 007356
3379 007356 010546
3380 007360 012745 002000
3381 007364 004767 002362
3382 007370 012605
3383
3384
3385 007372
3386 007372 026727 002442 000001
3387 007400 001406

```

```

;*****
;*****
;TEST 33 TRANSMITTER INTERRUPT LOGIC TEST
; LOGICALLY THIS IS 4 SEPARATE TESTS
; A) DOES TRANSMITTER INTERRUPT LOGIC WORK
; B) AT PRIORITY OF 0
; C) AND ONLY ONCE
; D) BUT NOT WITH INTERRUPT ENABLE CLEAR
;*****
TST33: SCOPE
MOV #10,#TIMES ;DO 10 ITERATIONS
MOV #33,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
;CLEAR INTERRUPT OCCURED FLAG
LET INTFLAG := #0
;GET VECTOR ADDRESS
LET R3 := DLVEC
;FOR THE TRANSMITTER
LET R3 := R3 * #4
;SET VECTOR TO POINT TO TRANS.SRV AT PRI
SETVEC R3, #INTSRV, #PR7
MOV R1,-(SP)
MOV R3,R1
MOV #INTSRV,(R1)
MOV #PR7,(R1)
MOV (SP),R1
BGNSUB
;CLEAR INTERRUPT ENABLE
LET #TCSR := #TCSR CLR.BY #XMITIE
;SET IT TO 0
MOV #PRO,-(SP) ;PUT NEW PS ON STACK
MOV #65,-(SP) ;PUT NEW PC ON STACK
RTI ;POP NEW PC AND PS
65:
;NOW SET I.E. BIT
LET #TCSR := #TCSR SET.BY #XMITIE
;LET INTERRUPT HAVE TIME TO OCCUR
WAITMS 1024.
MOV R5,-(SP)
MOV #1024,-(R5)
JSR PC,WAIT
MOV (SP),R5
;DID EXACTLY 1 INTERRUPT OCCUR
IF INTFLAG NE #1 THEN

```

```

3388                                     ;NO - WAS IT 0 OR MORE THAN ONCE
3389 007402                                     IF INTFLAG EQ #0 THEN
3390 007402 005767 002432                     TST     INTFLAG
3391 007406 001002                             BNE     #155
3392                                     ;TRANSMITTER DID NOT INTERRUPT IN TIME
3393 007410                                     ERRHRD 106.,DIDNOT
3394 007410 104106                             ERROR   106
3395 007412                                     ELSE
3396 007412 000401                             BR      #156
3397 007414                                     #155:
3398                                     ;TWICE
3399                                     ;TRANSMITTER INTERRUPTED TWICE
3400 007414                                     ERRHRD 107.,TWICE
3401 007414 104107                             ERROR   107
3402 007416                                     ENDF
3403 007416                                     #156:
3404 007416                                     ENDF
3405 007416                                     #154:
3406 007416                                     ENDSUB
3407                                     ;INTERRUPT WITHOUT INTERRUPT ENABLE SET
3408 007416                                     BGNSUB
3409 007416 012767 007424 171464             MOV     #64#,#LPERR
3410                                     ;CLEAR 'INTERRUPT OCCURED' FLAG
3411 007424                                     LET INTFLAG := #0
3412 007424 005067 002410                     CLR     INTFLAG
3413                                     ;CLEAR INTERRUPT ENABLE
3414 007430                                     LET #TCSR := #TCSR CLR.BY #XMITIE
3415 007430 042777 000100 171626             BIC     #XMITIE,#TCSR
3416                                     ;NO INTERRUPTS SHOULD OCCUR.
3417 007436 012746 000000                     MOV     #PRO,-(SP) ;;PUT NEW PS ON STACK
3418 007442 012746 007450                     MOV     #65#,-(SP) ;;PUT NEW PC ON STACK
3419 007446 000002                                     ;;POP NEW PC AND PS
3420 007450                                     #65:
3421                                     ;DARE IT TO HAPPEN
3422 007450                                     WAITMS 2
3423 007450 010546                             MOV     R5,-(SP)
3424 007452 012745 000002                     MOV     #2,-(R5)
3425 007456 004767 002270                     JSR     PC,WAIT
3426 007462 012605                             MOV     (SP),R5
3427 007464                                     IF INTFLAG NE #0 THEN
3428 007464 005767 002350                     TST     INTFLAG
3429 007470 001401                             BEQ     #157
3430                                     ;INTERRUPT OCCURED WITH I E CLEARED
3431 007472                                     ERRHRD 110.,NOTENAB
3432 007472 104110                             ERROR   110
3433 007474                                     ENDF
3434 007474                                     #157:
3435 007474                                     BRESET
3436 007474 000005                             RESET
3437 007476                                     ENDSUB
3438                                     ;RESTORE VECTOR AREA
3439 007476                                     LET R4 := #DLVEC
3440 007476 017704 171554                     MOV     #DLVEC,R4
3441 007502                                     CLRVEC R4
3442 007502 010146                             MOV     R1,-(SP) ;;PUSH R1 ON STACK
3443 007504 010246                             MOV     R2,-(SP) ;;PUSH R2 ON STACK
    
```



```
3458 ;*****
3459 ;*****
3460 ;*TEST 34 RECEIVER INTERRUPT LOGIC TEST
3461 ;* THIS TEST COVERS ALL OF THE RECEIVER
3462 ;* SIDE OF THE INTERRUPT LOGIC, BOTH DATASET
3463 ;* AND CHARACTER MODES.
3464 ;*****
3465 007530 000004 TST34: SCOPE
3466 007532 012767 000010 171420 MOV #10,#TIMES ;DO 10 ITERATIONS
3467 007540 012767 000034 171432 MOV #34,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
3468 ;CLEAR INTERRUPT OCCURED FLAG
3469 ;SET UP RECEIVER INTER.VECTOR
3470 007546 SETVEC DLVEC,#INTSRV,#PR7
3471 007546 010146 MOV R1,-(SP)
3472 007550 016701 171502 MOV DLVEC,R1
3473 007554 012721 012032 MOV #INTSRV,(R1)+
3474 007560 012711 000340 MOV #PR7,(R1)
3475 007564 012601 MOV (SP)+,R1
3476 ;PRIORITY 0 AND MULTIPLE INTERRUPT TEST.-RCVRIE
3477 007566 ;BGNSUB
3478 007566 012767 007574 171314 MOV #64,#LFERR
3479 007574 LET INTFLAG := #0
3480 007574 005067 002240 CLR INTFLAG
3481 ;SET MAINT. BIT
3482 007600 LET #TCSR := #TCSR SET.BY #MAINT
3483 007600 052777 000004 171456 BIS #MAINT,#TCSR
3484 ;CLEAR INTERRUPTS
3485 007606 LET #RCSR := #RCSR CLR.BY #RCVRIE
3486 007606 042777 000100 171444 BIC #RCVRIE,#RCSR
3487 ;CHANGE PRIORITY
3488 ;..TO 0
3489 007614 012746 000000 MOV #PRO,-(SP) ;PUT NEW PS ON STACK
3490 007620 012746 007626 MOV #65,-(SP) ;PUT NEW PC ON STACK
3491 007624 000002 RTI ;POP NEW PC AND PS
3492 007626 654:
3493
3494 ;SEND A CHARACTER
3495 007626 LET #TBUF := #0
3496 007626 105077 171436 CLRB #TBUF
3497 ;WAIT A MAXIMUM
3498 ;OF 1000 MSEC FOR
3499 ;RCVR RDY TO SET IN RCSR
3500 CALL TIMER IN <#1000,#RCVRDONE,RCSR,#SFT>
3501 007632 010546 MOV R5,-(SP)
3502 007634 012745 177777 MOV #SET,-(R5)
3503 007640 016745 171414 MOV RCSR,-(R5)
3504 007644 012745 000200 MOV #RCVRDONE,-(R5)
3505 007650 012745 001000 MOV #1000,-(R5)
3506 007654 004767 001614 JSR PC,TIMER
3507 007660 012605 MOV (SP)+,R5
3508 ;SET INTERRUPT ENABLE
3509 007662 LET #RCSR := #RCSR SET.BY #RCVRIE
3510 007662 052777 000100 171370 BIS #RCVRIE,#RCSR
3511 ;LET IT COME IN.
3512 007670 WAITMS 1
3513 007670 010546 MOV R5,-(SP)
```

```

3514 007672 012745 000001      MOV    #1,-(R5)
3515 007676 004767 002050      JSR    PC,WAIT
3516 007702 012605              MOV    (SP)+,R5
3517 007704                      LET R0 := @RBUF ; CLEAR RCVRDONE
3518 007704 017700 171352      MOV    @RBUF,R0
3519
3520
3521 007710                      ;DID HE DO IT RIGHT?
3522 007710 026727 002124 000001  CMP    INTFLAG,#1
3523 007716 001406              BEQ    #160
3524
3525 007720                      ;NONE OCCURED
3526 007720 005767 002114      TST    INTFLAG
3527 007724 001002              BNE    #161
3528
3529 007726                      ;RECEIVER DID NOT INTERRUPT IN TIME
3530 007726 104111      ERROR  111
3531
3532 007730                      ;TWICE OR MORE
3533 007730 000401      BR     #162
3534 007732                      ELSE
3535
3536 007732                      ;RECEIVER INTERRUPTED TWICE
3537 007732 104112      ERROR  112
3538 007734                      ;RECEIVER INTERRUPTED TWICE
3539 007734                      ERRMRD 112,,TWICE
3540 007734                      ENDIF
3541 007734                      ENDIF
3542
3543 007734                      ;RESET MAINT. BIT.
3544 007734 042777 000004 171322  BIC    @MAINT,@TCSR
3545
3546 007742                      ; CLEAR INTERRUPT ENABLE
3547 007742 042777 000100 171310  BIC    @RCVRIE,@RCSR
3548 007750                      ENDSUB
3549
3550
3551
3552
3553
3554
3555
3556
3557 007750                      ;PRIORITY 0 AND MULTIPLE INTERRUPT TEST.-DATAIE
3558 007750 012767 007756 171132  MOV    #64,,$LPERR
3559 007756                      BGNSUB
3560 007756 032767 020000 171234  BIT    @CABLE,$USWR
3561 007764 001004                      IF @CABLE NOTSETIN $USWR THEN
3562
3563 007766                      ;CAN'T TEST WITHOUT A CABLE
3564 007766 012767 000001 171164  MOV    #1,$TIMES
3565 007774 000466      BR     TST35
3566 007776                      ;:EXIT THIS TEST
3567 007776                      ENDIF
3568
3569 007776                      ; CLEAR 'INTFLAG'
                      LET INTFLAG := #0
    
```

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```

3570 007776 005067 002036          CLR      INTFLAG
3571                                ;CLEAR INTERRUPTS
3572 010002                                LET BRCSR := BRCSR CLR.BY #DATAIE
3573 010002 042777 000040 171250      BIC      #DATAIE,BRCSR
3574                                ;CHANGE PRIORITY
3575                                ;..TO 0
3576 010010 012746 000000          MOV      #PRO,-(SP)      ;;PUT NEW PS ON STACK
3577 010014 012746 010022          MOV      #64,-(SP)      ;;PUT NEW PC ON STACK
3578 010020 000002          RTI      ;;POP NEW PC AND PS
3579 010022                                64:
3580 010022                                LET BRCSR := BRCSR CLR.BY #REQSEND
3581 010022 042777 000004 171230      BIC      #REQSEND,BRCSR
3582                                ;SET INTERRUPT ENABLE
3583 010030                                LET BRCSR := BRCSR SET.BY #DATAIE
3584 010030 052777 000040 171222      BIS      #DATAIE,BRCSR
3585 010036                                LET BRCSR := BRCSR SET.BY #REQSEND
3586 010036 052777 000004 171214      BIS      #REQSEND,BRCSR
3587                                ;LET IT COME IN.
3588 010044                                WAITMS 1
3589 010044 010546          MOV      R5,-(SP)
3590 010046 012745 000001          MOV      #1,-(R5)
3591 010052 004767 001674          JSR      PC,WAIT
3592 010056 012605          MOV      (SP)+,R5
3593
3594                                ; DID IT DO IT RIGHT?
3595 010060                                IF INTFLAG NE #1 THEN
3596 010060 026727 001754 000001      CMP      INTFLAG,#1
3597 010066 001406          BEQ      #164
3598                                ;NONE OCCURED
3599 010070                                IF INTFLAG EQ #0 THEN
3600 010070 005767 001744          TST      INTFLAG
3601 010074 001002          BNE      #165
3602                                ;DATAINT DID NOT INTERRUPT IN TIME
3603 010076                                ERRHRD 113,,DIDNOT
3604 010076 104113          ERROR    113
3605                                ;TWICE OR MORE
3606 010100                                ELSE
3607 010100 000401          BR      #166
3608 010102                                #165:
3609                                ; DATAINT INTERRUPTED TWICE
3610 010102                                ERRHRD 114,,TWICE
3611 010102 104114          ERROR    114
3612 010104                                ENDF
3613 010104                                #166:
3614 010104                                ENDF
3615 010104                                #164:
3616 010104                                LET BRCSR := BRCSR CLR.BY #DATAIE
3617 010104 042777 000040 171146      BIC      #DATAIE,BRCSR
3618 010112                                LET BRCSR := BRCSR CLR.BY #REQSEND
3619 010112 042777 000004 171140      BIC      #REQSEND,BRCSR
3620 010120                                ENDSUB
3621                                LET R4 := @DLVEC
3622 010120                                MOV      @DLVEC,R4
3623 010120 017704 171132          MOV      @DLVEC,R4
3624 010124                                CLRVEC R4
3625 010124 010146          MOV      R1,-(SP)      ;;PUSH R1 ON STACK

```

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CVDVAD.P11 12-JUL-84 05:04 T34 RECEIVER INTERRUPT LOGIC TEST

3626	010126	010246		MOV	R2,-(SP)	::PUSH R2 ON STACK
3627	010130	012701	000004	MOV	#R4,R1	
3628	010134	010102		MOV	R1,R2	
3629	010136	062702	000002	ADD	#2,R2	
3630	010142	010221		MOV	R2,(R1)+	
3631	010144	005011		CLR	(R1)	
3632	010146	012602		MOV	(SP)+,R2	::POP STACK INTO R2
3633	010150	012601		MOV	(SP)+,R1	::POP STACK INTO R1
3634	010152					ENDTST

```

3635 ;*****
3636 ;*****
3637 ;*TEST 35 TEST ACTUAL DATA TRANSFERED
3638 ;* NON-INTERRUPT MAINTENANCE BIT SET
3639 ;*****
3640 010152 000004 TST35: SCOPE
3641 010154 012767 000001 170776 MOV #1,#TIMES ;DO 1 ITERATION
3642 010162 012767 000035 171010 MOV #35,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
3643 ;SET MAINT. BIT
3644 010170 ;LET @TCSR := @TCSR SET.BY @MAINT
3645 010170 052777 000004 171066 BIS #MAINT,@TCSR
3646 ;CHANGE PRIORITY
3647 ;..TO 0
3648 ;PUT NEW PS ON STACK
3649 010176 012746 000000 MOV #PRO,-(SP) ;PUT NEW PC ON STACK
3650 010202 012746 010210 MOV #64,-(SP) ;POP NEW PC AND PS
3651 010206 000002 RTI
3652 010210 64:
3653 ;GET DATA MASK.
3654 010210 CALL DATLNG OUT <R1>
3655 010210 162705 000002 SUB #1*2,R5
3656 010214 004767 001432 JSR PC,DATLNG
3657 010220 012501 MOV (R5),R1
3658 ; START CLEAN
3659 010222 LET R0 := @RBUF
3660 010222 017700 171034 MOV @RBUF,R0
3661 ;ALL BINARY CHAR.
3662 INCR R2 FROM #0 TO #377 BY #1
3663 010226
3664 010226 005002 CLR R2
3665 010230 000401 BR #167
3666 010232 $170:
3667 010232 005202 INC R2
3668 010234 $167:
3669 010234 020227 000377 CMP R2,#377
3670 010240 003047 BGT #171
3671
3672 ;TRANSMIT CHAR IN R2
3673 CALL TIMER IN <#1000,@XMITRDY,TCSR,#SET>
3674
3675 010242
3676 010242 010546 MOV R5,-(SP)
3677 010244 012745 177777 MOV #SET,-(R5)
3678 010250 016745 171010 MOV TCSR,-(R5)
3679 010254 012745 000200 MOV @XMITRDY,-(R5)
3680 010260 012745 001000 MOV #1000,-(R5)
3681 010264 004767 001204 JSR PC,TIMER
3682 010270 012605 MOV (SP),R5
3683
3684 ;TRANSMIT IT
3685 010272 LET @TBUF := R2
3686 010272 110277 170772 MOVB R2,@TBUF
3687
3688 010276 CALL TIMER IN <#1000,@RCVRDONE,RCSR,#SET>
3689 010276 010546 MOV R5,-(SP)
3690 010300 012745 177777 MOV #SET,-(R5)
    
```


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 CVDVAD.P11 12 JUL-84 05:04 T35 TEST ACTUAL DATA TRANSFERED

```

3691 010304 016745 170750      MOV      RCSR,-(R5)
3692 010310 012745 000200      MOV      @R5,R5
3693 010314 012745 001000      MOV      @1000,-(R5)
3694 010320 004767 001150      JSR      PC,TIMER
3695 010324 012605      MOV      (SP)+,R5
3696                                     ;AND SAVE IT
3697 010326      MOV      BRBUF,R3      LET R3 := BRBUF
3698 010326 017703 170730
3699
3700                                     ;COMPARE TO SEE IF WE RECEIVED IT ALL
3701
3702                                     ;CLEAN OFF NON-DATA BITS
3703                                     ;ON BOTH TRANSMITTED AND
3704      LET R4 := R2 CLR.BY R1
3705 010332
3706 010332 010204      MOV      R2,R4
3707 010334 040104      BIC      R1,R4
3708 010336      LET R3 := R3 CLR.BY R1
3709 010336 040103      BIC      R1,R3
3710
3711                                     ;RECEIVED DATA
3712                                     IF R4 NE R3 THEN
3713 010340      CMP      R4,R3
3714 010342 001405      BEQ      $172
3715
3716 010344                                     ;DATA COMPARE ERROR
3717 010344 104116      ERROR    116      ERRNRD 116,COMP,SBMAS
3718 010346      EXIT TST ; ON ERROR
3719 010346 012767 000001 170604      MOV      @1,$TIMES
3720 010354 000404      BR       TST36      ;;;EXIT THIS TEST
3721 010356                                     ENDIF
3722 010356      $172:
3723 010356
3724 010356 000725      BR       $170
3725 010360      $171:
3726
3727                                     ;RESET MAINT. BIT.
3728 010360      LET @TCSR := @TCSR CLR.BY @MAINT
3729 010360 042777 000004 170676      BIC      @MAINT,@TCSR
3730 010366      ENDTST
3731
3732
3733

```

```

3734
3735
3736
3737
3738 010366 000004
3739 010370 012767 000001 170562
3740 010376 012767 000036 170574
3741 010404
3742 010404 032767 020000 170606
3743 010412 001004
3744
3745 010414
3746 010414 012767 000001 170536
3747 010422 000474
3748 010424
3749 010424
3750
3751 010424
3752 010424 042777 000004 170632
3753
3754
3755 010432 012746 000000
3756 010436 012746 010444
3757 010442 000002
3758 010444
3759
3760 010444
3761 010444 162705 000002
3762 010450 004767 001176
3763 010454 012501
3764 010456
3765 010456 017700 170600
3766
3767 010462
3768 010462 005002
3769 010464 000401
3770 010466
3771 010466 005202
3772 010470
3773 010470 020227 000377
3774 010474 003047
3775
3776
3777
3778
3779 010476
3780 010476 010546
3781 010500 012745 177777
3782 010504 016745 170554
3783 010510 012745 000200
3784 010514 012745 001000
3785 010520 004767 000750
3786 010524 012605
3787
3788
3789 010526
    
```

```

;*****
;*****
;+TEST 36 TEST DATA THROUGH CABLE
;*****
TST36: SCOPE
MOV #1,#TIMES ;;DO 1 ITERATION
MOV #36,#TESTN ;;SET TEST NUMBER IN APT MAIL BOX
IF #CABLE NOTSETIN #USWR THEN
;CAN'T TEST WITHOUT A CABLE
EXIT TST
MOV #1,#TIMES
BR TST37 ;;EXIT THIS TEST
ENDIF
#173:
;DON'T USE MAINT.
LET #TCSR := #TCSR CLR.BY #MAINT
;CHANGE PRIORITY
;..TO 0
MOV #PRO,-(SP) ;;PUT NEW PS ON STACK
MOV #64,-(SP) ;;PUT NEW PC ON STACK
RTI ;;POP NEW PC AND PS
64:
;GET DATA MASK
CALL DATLNG OUT <R1>
SUB #1*2,R5
JSR PC,DATLNG
MOV (R5),R1
LET R0 := #RBUF ; START CLEAN
;BINARY COUNT PATTERN
INCR R2 FROM #0 TO #377 BY #1
#175:
INC R2
#174:
CMP R2,#377
BGT #176
;TRANSMIT THE CHAR. IN R2.
CALL TIMER IN <#1000,#XMITRDY,TCSR,#SET>
MOV R5,-(SP)
MOV #SET,-(R5)
MOV TCSR,-(R5)
MOV #XMITRDY,-(R5)
MOV #1000,-(R5)
JSR PC,TIMER
MOV (SP),R5
;START IT ON ITS WAY
LET #RBUF := R2
    
```

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```

3790 010526 110277 170536      MOVB   R2,@TBUF
3791 010532
3792 010532 010546
3793 010534 012745 177777      MOV    R5,-(SP)
3794 010540 016745 170514      MOV    #SET,-(R5)
3795 010544 012745 000200      MOV    RCSR,-(R5)
3796 010550 012745 001000      MOV    #RCVRDONE,-(R5)
3797 010554 004767 000714      MOV    #1000,-(R5)
3798 010560 012605
3799
3800
3801 010562
3802 010562 017703 170474      MOV    @RBUF,R3
3803
3804
3805 010566
3806 010566 010204      MOV    R2,R4
3807 010570 040104      BIC   R1,R4
3808 010572
3809 010572 040103      BIC   R1,R3
3810
3811
3812 010574
3813 010574 020403      CMP   R4,R3
3814 010576 001405      BEQ   #177
3815
3816 010600
3817 010600 104117      ERROR 117
3818 010602
3819 010602 012767 000001 170350      MOV   #1,#TIMES
3820 010610 000401
3821 010612
3822 010612      #177:
3823
3824 010612
3825 010612 000725      BR    #175
3826 010614      #176:
3827
3828
3829
3830 010614
3831
3832
3833
3834

;RETRIEVE
LET R3 := @RBUF

;STRIP OFF JUNK ON BOTH
LET R4 := R2 CLR.BY R1

LET R3 := R3 CLR.BY R1

;WE HAVE TROUBLE
IF R4 NE R3 THEN

;DATA COMPARE ERROR
ERRMRD 117,COMP.SBWAS

EXIT TST ; ON ERROR

;;;EXIT THIS TEST
ENDIF

ENDINC ; R2

ENDTST

```

```

3835 ;
3836 ;
3837 ;
3838 ;*TEST 37 FULL DATA TRANSFER WITH INTERRUPTS
3839 ;* AND MAINTENANCE MODE.
3840 010614 000004 TST37: SCOPE
3841 010616 012767 000001 170334 MOV #1,#TIMES ;DO 1 ITERATION
3842 010624 012767 000037 170346 MOV #37,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
3843
3844
3845 ; THIS TEST IS 'BREAK OR HALT' SENSITIVE.
3846 010632 ; IF #APTENV SET IN #ENV THEN
3847 010632 032767 000001 170354 BIT #APTENV,#ENV
3848 010640 001404 BEQ $200
3849 010642 EXIT TEST
3850 010642 012767 000001 170310 MOV #1,#TIMES
3851 010650 000550 BR TST40 ;EXIT THIS TEST
3852 010652 ;
3853 010652 ;$200: ;
3854 ;
3855 010652 ;GET DATA MASK
3856 010652 162705 000002 SUB #1*2,R5 CALL DATLNG OUT <R3>
3857 010656 004767 000770 JSR PC,DATLNG
3858 010662 012503 MOV (R5),R3
3859
3860
3861 ; THIS TEST WILL RUN BOTH TRANSMITTER AND
3862 ; RECEIVER AT FULL SPEED TESTING
3863 ; THE ABILITY OF THE MODULE
3864 ; TO HANDLE INTERRUPTS FROM BOTH SIDES
3865 ; AT ONCE. ALSO, THE DOUBLE BUFFERING LOGIC
3866 ; OF THE UART WILL BE FULLY TESTED.
3867 ; THIS TEST WILL TRANSFER A MAXIMUM OF 400(B)
3868 ; CHARACTERS THROUGH THE MODULE, BUT IF AN ERROR
3869 ; IS DETECTED BY THE TEST A PREMATURE SHUTDOWN OCCURS.
3870
3871
3872 ;
3873 ;CHANGE PRIORITY
3874 010664 012746 000000 MOV #PRO,-(SP) ;..TO 0
3875 010670 012746 010676 MOV #64,-(SP) ;PUT NEW PS ON STACK
3876 010674 000002 RTI ;PUT NEW PC ON STACK
3877 010676 ;POP NEW PC AND PS
3878 64:
3879 010676 ;GET VECTOR ADDRESS
3880 010676 016701 170354 MOV DLVEC,R1 LET R1 := DLVEC
3881 ;RCVR VECTOR
3882 010702 LET (R1) := #REC
3883 010702 012721 011076 MOV #REC,(R1)
3884 010706 LET (R1) := #PR7
3885 010706 012721 000340 MOV #PR7,(R1)
3886 ;POINT TO TRANSMITTER VECTOR
3887 ;AND SET IT UP ALSO
3888 010712 LET (R1) := #TRAN
3889 010712 012721 011034 MOV #TRAN,(R1)
3890 010716 LET (R1) := #PR7
    
```

3891	010716	012711	000340	MOV	#PR7,(R1)	
3892						
3893						; INITIALIZE COUNTERS
3894	010722			LET R1 :=	#-1	
3895	010722	012701	177777	MOV	# 1,R1	
3896						; RECEIVER STORAGE
3897	010726			LET R2 :=	#0	
3898	010726	005002		CLR	R2	
3899						; # OF RECEIVED CHAR. COUNT.
3900	010730			LET R4 :=	#-1	
3901	010730	012704	177777	MOV	#-1,R4	
3902						; CLEAR ERROR COUNT.
3903				LET ERRCNT :=	#0	
3904	010734					
3905	010734	005067	000066	CLR	ERRCNT	
3906						
3907	010740			BRESET		; SET UP ALL REGISTERS
3908	010740	000005				
3909						; SET UP MAINTENANCE
3910	010742			LET #TCSR :=	#TCSR SET.BY #MAINT	
3911	010742	052777	000004 170314	BIS	#MAINT,#TCSR	
3912						; SET I.E. IN TRANSMITTER
3913				LET #TCSR :=	#TCSR SET.BY #XMITIE	
3914	010750					; AND RECEIVER
3915	010750	052777	000100 170306	BIS	#XMITIE,#TCSR	
3916						
3917	010756			LET #RCSR :=	#RCSR SET.BY #RCVRIE	
3918	010756	052777	000100 170274	BIS	#RCVRIE,#RCSR	
3919						
3920						; NOW WE WAIT UNTIL R4 COUNT (RECEIVED) IS EQUAL
3921						REPEAT
3922	010764					
3923	010764		\$201:			
3924	010764					UNTIL R4 EQ NUMBER OR ERRCNT GT #0
3925	010764	020467	000040	CMP	R4,NUMBER	
3926	010770	001403		BEG	\$202	
3927	010772	005767	000030	TST	ERRCNT	
3928	010776	003772		BLE	\$201	
3929	011000		\$202:			
3930						
3931						; DATA COMPARE ERRORS.
3932	011000			IF ERRCNT NE	#0 THEN	
3933	011000	005767	000022	TST	ERRCNT	
3934	011004	001401		BEG	\$203	
3935						; DATA COMPARE ERROR
3936	011006			ERRHRD	120,COMP,FIRST	
3937	011006	104120		ERROR	120	
3938	011010					ENDIF
3939	011010		\$203:			
3940						
3941	011010			LET #TCSR :=	#TCSR CLR.BY #XMITIE	
3942	011010	042777	000100 170246	BIC	#XMITIE,#TCSR	
3943	011016			LET #RCSR :=	#RCSR CLR.BY #XMITIE	
3944	011016	042777	000100 170234	BIC	#XMITIE,#RCSR	
3945						
3946	011024			EXIT		; SKIP OVER SUPPORT ROUTINES & STORAGE

3947 011024 000462
3948
3949 011026 000000
3950 011030 000400
3951 011032 000
3952 011033 000
3953
3954
3955
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3959 011034
3960 011034
3961
3962
3963
3964 011034
3965 011034 005201
3966
3967 011036
3968 011036 010167 000030
3969 011042 040367 000024
3970
3971 011046
3972 011046 016777 000020 170214
3973
3974 011054
3975 011054 020167 177750
3976 011060 001003
3977
3978 011062
3979 011062 042777 000100 170174
3980 011070
3981 011070
3982
3983 011070 000401
3984
3985 011072 000000
3986
3987 011074
3988 J11074 000002
3989
3990
3991
3992
3993
3994 011076
3995 011076
3996
3997
3998
3999 011076
4000 011076 005204
4001
4002 011100

BR TST40 ;;;EXIT THIS TEST
ERRCNT: 0
NUMBER: 400
SB: .BYTE 0
WAS: .BYTE 0
;.....
;TRANSMIT INTERRUPT HANDLER
BGNSRV TRAN
TRAN:
;.....
;INCREMENT CHAR COUNT
LET R1 := R1 + #1
INC R1
;SET UP FOR TRANSFER
LET HOLD := R1 CLR.BY R3
MOV R1,HOLD
BIC R3,HOLD
;AND SEND.
LET @TBUF := HOLD
;ALL DONE
IF R1 EQ NUMBER THEN
;STOP INTERRUPT PROCESSING
LET @TCSR := @TCSR CLR.BY @XMITIE
ENDIF
\$204:
BR ZZZ ; EXIT SRV
HOLD:0
ZZZ: ENDSRV
RTI
;.....
;RECEIVER INTERRUPT HANDLER
BGNSRV REC
REC:
;.....
;COUNT THIS CHAR.
LET R4 := R4 + #1
;GET CHAR IN + MASK IT
LET R2 := @RBUF CLR.BY R3

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 CVDVAD.P11 12-JUL-84 05:04 T37 FULL DATA TRANSFER WITH INTERRUPTS

4003	011100	017702	170156	MOV	BRBUF,R2	
4004	011104	040302		BIC	R3,R2	
4005						
4006	011106					;RMLD WILL CONTAIN EXPECTED INPUT
4007	011106	010467	000054	MOV	R4,RMLD	LET RMLD := R4 CLR BY R3
4008	011112	040367	000050	BIC	R3,RMLJ	
4009						
4010						
4011	011116					;DO THEY COMPARE
4012	011116	020267	000044	CMP	R2,RMLD	IF R2 NE RMLD THEN
4013	011122	001412		BEQ	1205	
4014						
4015	011124					;FIRST ERROR
4016	011124	005767	177676	TST	ERRCNT	IF ERRCNT EQ #0 THEN
4017	011130	001005		BNE	1206	
4018						
4019	011132					;SAVE RECORD OF FIRST MISS
4020	011132	116767	000030 177672	MOVB	RMLD,SB	LET SB :B= RMLD
4021	011140					LET WAS :B= R2
4022	011140	110267	177667	MOVB	R2,WAS	
4023	011144					ENDIF
4024	011144					
4025						
4026	011144					;COUNT IT.
4027	011144	005267	177656	INC	ERRCNT	LET ERRCNT := ERRCNT + #1
4028	011150					ENDIF
4029	011150					
4030						
4031						
4032	011150					;ALL DONE?
4033	011150	020467	177654	CMP	R4,NUMBER	IF R4 EQ NUMBER THEN
4034	011154	001003		BNE	1207	
4035						
4036	011156					;STOP RECEIVER INTERRUPTS
4037	011156	042777	000100 170074	BIC	#RCVRIE,#RCSR	LET #RCSR := #RCSR CLR BY #RCVRIE
4038						
4039	011164					;MAIN REPEAT LOOP IS CHECKING
4040	011164					ENDIF
4041						
4042						
4043	011164	000401		BR	ZZZZ	;FOR 'R4 = NUMBER' ALSO
4044						; EXIT SRV
4045	011166	000000				RMLD:0
4046	011170					
4047	011170					ENDSRV
4048	011170	000002		RTI		
4049						
4050	011172					ENDTST
4051						
4052						
4053						

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4054
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4060 011172 000004
4061 011174 012767 000010 167756
4062 011202 012767 000040 167770
4063 011210
4064 011210 032767 010000 170002
4065 011216 001004
4066 011220
4067 011220 012767 000001 167732
4068 011226 000452
4069 011230
4070 011230
4071
4072 011230
4073 011230 052777 000004 170026
4074
4075
4076 011236
4077 011236 017700 170020
4078
4079
4080
4081 011242
4082 011242 052777 000001 170014
4083
4084 011250
4085 011250 012777 000252 170012
4086 011256
4087 011256 010546
4088 011260 012745 177777
4089 011264 016745 167770
4090 011270 012745 000200
4091 011274 012745 001000
4092 011300 004767 000170
4093 011304 012605
4094 011306
4095 011306 103001
4096
4097 011310
4098 011310 104115
4099 011312
4100 011312
4101
4102 011312
4103 011312 105777 167744
4104 011316 001401
4105
4106 011320
4107 011320 104121
4108 011322
4109 011322

;*****
;*****
;TEST 40 TEST BREAK GENERATION LOGIC
;* TRANSMIT KNOWN CHAR WITH BREAK SET
;* AND COMPARE RECEIVED WITH 0.
;*****
TST40: SCOPE
MOV #10,#TIMES ;DO 10 ITERATIONS
MOV #40,#TESTN ;SET TEST NUMBER IN APT MAIL BOX
; IF #BRK NOTSETIN #USWR THEN
BIT #BRK,#USWR
RNE #210
; EXIT TST
MOV #1,#TIMES
BR TST41 ;;;EXIT THIS TEST
ENDIF

$210:
;SET MAINTENANCE BIT
LET @TCSR := @TCSR SET.BY #MAINT

; CLEAR RCVRDONE JUST IN CASE
LET RO := @RBUF

;SET BREAK BIT
LET @TCSR := @TCSR SET.BY #BREAK

;NON-ZERO CHAR. '*'
LET @TBUF := #252
CALL TIMER IN <#1000,@RCVRDONE,RCSR,#SET>

MOV R5,-(SP)
MOV #SET,-(R5)
MOV RCSR,-(R5)
MOV #RCVRDONE,-(R5)
MOV #1000,-(R5)
JSR PC,TIMER
MOV (SP)+,R5

IF.ERROR THEN
; RECIEVER DONE DID NOT SET
ERRHRD 115
ENDIF

$211:
IFB @RBUF NE #0 THEN
TSTB @RBUF
BEQ $212

; BREAK DID NOT EQUAL 0
ERRHRD 121 .BADBRK
ENDIF

$212:

```


4110 011322
4111 011322 000005
4112 011324
4113 011324 000413
4114 011326 051102 040505 020113
4115 011334 044504 020104 047516
4116 011342 020124 050505 040525
4117 011350 020114 000060
4118
4119 011354

RESET

BR TST41

BRESE1 ;CLEAN UP

EXIT
EXIT THIS TEST

!!!EXIT THIS TEST
BADBRK: .ASCIZ /BREAK DID NOT EQUAL 0/

ENDTST

```
4120
4121 ;*****
4122 ;*TEST 41      NOT A TEST - SEND BACK TO LOOP
4123 ;*****
4124 011354 000004
4125 011356 012767 000001 167574 TST41: SCOPE
4126 011364 104401 011372      MOV     @1,%TIMES      ;;DO 1 ITERATION
4127 011370 000404      TYPE   .65%           ;;TYPE ASCIZ STRING
4128      BR     64%           ;;GET OVER THE ASCIZ
4129 011402      ;;65%: .ASCIZ <CRLF>*(LSR: *
4130 011402 016746 167646      64%:      MOV     DLADD,-(SP)      ;;SAVE DLADD FOR TYPEOUT
4131 011406 104402      TYPOC      ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
4132 011410 104401 011416      TYPE   .67%           ;;TYPE ASCIZ STRING
4133 011414 000405      BR     66%           ;;GET OVER THE ASCIZ
4134      ;;67%: .ASCIZ *,VECTOR: *
4135 011430      66%:      MOV     DLVEC,-(SP)      ;;SAVE DLVEC FOR TYPEOUT
4136 011430 016746 167622      TYPOC      ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
4137 011434 104402      TYPE   .69%           ;;TYPE ASCIZ STRING
4138 011436 104401 011444      BR     68%           ;;GET OVER THE ASCIZ
4139 011442 000405      ;;69%: .ASCIZ *,ERRORS: *
4140      68%:
4141 011456      MOV     %ERTTL,-(SP)      ;;SAVE %ERTTL FOR TYPEOUT
4142 011456 016746 167430      TYPOS      ;;GO TYPE--DECIMAL ASCII WITH SIGN
4143 011462 104405      CLR     %ERTTL        ; RESET FOR NEXT DEVICE/PASS
4144 011464 005067 167422      JMP     LOOP          ; BACK UP TO THE BEGINNING
4145 011470 000167 170250
```

4146
4147
4148
4149 011474
4150 011474
4151
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4172
4173
4174 011474
4175 011474 016567 000004 000136
4176 011502
4177 011502 016567 000000 000132
4178 011510
4179 011510 112767 000000 000126
4180
4181
4182
4183
4184 011516
4185 011516
4186
4187 011516
4188 011516 036577 000002 000114
4189 011524 001004
4190 011526
4191 011526 112767 000000 000111
4192 011534
4193 011534 000403
4194 011536
4195 011536
4196 011536 112767 177777 000101
4197 011544
4198 011544
4199
4200
4201 011544

```

;:*****
;:BGNMOD          SUBS
;:*****
ROUTINE TIMER <HOWLONG,WHICHBIT,REG,SETCLR>
TIMER:
;: ROUTINE:TIMER
;: THIS ROUTINE IS USED TO TEST THE STATUS OF ANY BIT
;: IN ANY REGISTER.
;: INPUTS:
;:   HOWLONG      THE MAXIMUM AMOUNT OF TIME TO SPEND IN
;:                 THIS ROUTINE.
;:   WHICHBIT     A MASK WITH THE BIT(S) SET THAT ARE
;:                 TO BE CHECKED.
;:   REG          A POINTER TO THE REGISTER TO BE CHECKED
;:   SETCLR      THE DESIRED RESULTS
;:                 EITHER #SET OR #CLEAR
;: OUTPUT:
;:   THE 'C' BIT IS SET TO INDICATE AN ERROR
;:   BUT IT IS TESTED BY THE IF.ERROR STATEMENT
;:
;: NOTE:: THE USE OF (R5) IS PART OF THE LINKAGE
;:         MECHANISM BETWEEN THE CALLER AND THE CALLED
;:*****

```

```

TRUE= 1
FALSE= 0

LET REGSAV := REG(R5) ; GET POINTER TO REGIST
LET TIMSAV := HOWLONG(R5) ; SAVE HOWLONG FOR
LET FLAG :B= #FALSE ; INITIALIZE THE EXIT FLA

; START OF AN INFINITE LOOP
LOOP
; TEST TO SEE IF WHICHBIT IS SET
IF WHICHBIT(R5) NOTSETIN @REGSAV THEN
LET HOLDSC :B= #CLR
ELSE
LET HOLDSC :B= #SET ; REMEMBER THIS
ENDIF
; NOW SEE IF THAT WAS WHAT WE WANTED
IFB HOLDSC EQ SETCLR(R5) THEN

```

```

000001
000000

MOV REG(R5),REGSAV
MOV HOWLONG(R5),TIMSAV
MOVB #FALSE,FLAG

$215:
BIT WHICHBIT(R5),@REGSAV
BNE $217

MOVB #CLR,HOLDSC

BR $220

$217:
MOVB #SET,HOLDSC

$220:

```

```

4202 011544 126765 000075 000006      CMPB   HOLDSC,SETCLR(R5)
4203 011552 001003                    BNE    $221
4204                                     ; JUST THE THING WE NEEDED
4205 011554                                LET    FLAG :B= #TRUE
4206 011534 112767 000001 000062      MOVB   #TRUE,FLAG
4207 011562                                ENDIF
4208 011562                                $221:
4209
4210 011562                                EXIFB  FLAG EQ #TRUE OR TIMSAV LE #0
4211 011562 126727 000056 000001      CMPB   FLAG,#TRUE
4212 011570 001414                    BEQ    $216
4213 011572 005767 000044                    TST   TIMSAV
4214 011576 003411                    BLE    $216
4215
4216                                     ; ONE WAY OR THE OTHER, WE ARE DONE
4217                                     ; IF WE ARE STILL HERE THEN HANG AROUND A WHILE
4218 011600                                WAITMS 1          ;WAIT FOR 10 MILLI-SECONDS
4219 011600 010546                    MOV    R5,-(SP)
4220 011602 012745 000001                    MOV    #1,-(R5)
4221 011606 004767 000140                    JSR   PC,WAIT
4222 011612 012605                    MOV    (SP)+,R5
4223 011614                                LET    TIMSAV := TIMSAV - #1 ; COUNTING DOWN
4224 011614 005367 000022                    DEC   TIMSAV
4225 011620                                ENDL0OP          ; CONTINUED AT THE TOP
4226 011620 000736                    BR    $215
4227 011622                                $216:
4228
4229                                     ; ONLY 2 WAYS TO GET HERE
4230                                     ; 1). WE RAN OUT OF TIME---ERROR !!
4231                                     ; 2). THE BIT IS IN THE CORRECT CONDITION--GOOD !!
4232
4233 011622                                IFB   FLAG EQ #TRUE THEN
4234 011622 126727 000016 000001      CMPB   FLAG,#TRUE
4235 011630 001001                    BNE    $222
4236 011632                                RETURN NO.ERROR ; GOOD
4237 011632 000405                    BR    $213
4238 011634                                ENDF
4239 011634                                $222:
4240 011634                                RETURN ERROR ; BAD
4241 011634 000261                    SEC
4242 011636 000404                    BR    $214
4243
4244 011640 000000                                REGSAV: .WORD 0
4245 011642 000000                                TIMSAV: .WORD 0
4246 011644 000000                                FLAG: .BYTE 0
4247 011645 000000                                HOLDSC: .BYTE 0
4248                                     ; WE ARE DONE GO BACK HOME
4249 011646                                ENDRTN
4250 011646                                $213:
4251 011646 000241                    CLC
4252 011650                                $214:
4253 011650 000207                    RTS   PC
    
```

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4254
4255
4256 011652
4257 011652
4258
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4260
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4264
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4266
4267
4268
4269
4270
4271 011652
4272 011652 005065 000000
4273 011656
4274 011656 016767 167336 000062
4275 011664 016746 000056
4276 011670 042716 000017
4277 011674 042667 000046
4278
4279 011700
4280 011700 012767 000001 167364
4281 011706 000402
4282 011710
4283 011710 005267 167356
4284 011714
4285 011714 026767 167352 000024
4286 011722 003006
4287 011724
4288 011724 006365 000000
4289 011730
4290 011730 052765 000001 000000
4291 011736
4292 011736 000764
4293 011740
4294 011740
4295 011740 005165 000000
4296 011744
4297 011744 000401
4298 011746 000000
4299 011750
4300 011750
4301 011750
4302 011750 000207

```

```

;*****
ROUTINE DATLNG <MASK>
DATLNG:
;* ROUTINE:DATLNG
;* THIS ROUTINE SETS UP A MASK FOR DATA, WITH
;* INPUT - NOTHING IS PASSED TO THIS ROUTINE
;* BUT GLOBAL INFORMATION IS ASSUMED TO EXIST:
;* $USMR-- THE WORD FOR SOFTWARE PARAMETERS
;* DA'A-- A MASK FOR THE LOCATION OF THE OCTAL
;* NUMBER OF DATA BITS
;* OUTPUT----
;* MASK-- A MASK OF BINARY ONES RIGHT-JUSTIFIED
;* THE NUMBER OF WHICH IS DEFINED IN $USMR WORD.
;*****

```

```

LET MASK(R5) := #0 ; START
LET NUMBR := $USMR AND #DATA
MOV $USMR,NUMBR
MOV NUMBR,-(SP)
BIC #DATA,(SP)
BIC (SP)+,NUMBR
INCR I FROM #1 TO NUMBR BY #1
MOV #1,I
BR $225
$226: INC I
$225: CMP I,NUMBR
BGT $227
LET MASK(R5) := MASK(R5) SHIFT #1
LET MASK(R5) := MASK(R5) SET.BY #1
BIS #1,MASK(R5)
ENDINC
BR $226
$227: LET MASK(R5) := COMP MASK(R5)
RETURN
BR $223
NUMBR:0
ENDRTN
$223:
$224:
RTS PC

```

4303					
4304					
4305	011752				
4306	011752				
4307					
4308					
4309					
4310					
4311					
4312					
4313					
4314	011752	010146			
4315	011754	010246			
4316	011756	010346			
4317	011760				
4318	011760	016501	000000		
4319	011764				
4320	011764	012702	000001		
4321	011770	000402			
4322	011772				
4323	011772	062702	000000		
4324	011776				
4325	011776	020201			
4326	012000	101010			
4327	012002				
4328	012002	005003			
4329	012004	000401			
4330	012006				
4331	012006	005203			
4332	012010				
4333	012010	020327	000100		
4334	012014	003001			
4335	012016				
4336	012016	000773			
4337	012020				
4338	012020				
4339	012020	000764			
4340	012022				
4341	012022	012603			
4342	012024	012602			
4343	012026	012601			
4344	012030				
4345	012030				
4346	012030				
4347	012030	000207			

```

;*****
ROUTINE WAIT <TIME>
WAIT:
;* ROUTINE:WAIT
;* THIS ROUTINE IS USED TO DELAY EXECUTION OF THE
;* MAIN PROGRAM FOR A SPECIFIED AMOUNT OF TIME.
;* THIS IS ACCOMPLISHED BY INCREMENTING A
;* REGISTER UP TO A LIMIT. THE INNER LOOP IS SET
;* TO APPROXIMATE 1 MILLI SEC.
;*****
MOV R1,-(SP) ;;PUSH R1 ON STACK
MOV R2,-(SP) ;;PUSH R2 ON STACK
MOV R3,-(SP) ;;PUSH R3 ON STACK
LET R1 := TIME(R5)
MOV TIME(R5),R1
INCRU R2 FROM #1 TO R1 BY #1
MOV #1,R2
BR $232
$233: ADD #01,R2
$232: CMP R2,R1
BHI $234
INCR R3 FROM #0 TO #100 BY #1
CLR R3
BR $235
$236: INC R3
$235: CMP R3,#100
BGT $237
ENDINC
BR $236
$237: ENDINC
BR $233
$234: MOV (SP)+,R3 ;;POP STACK INTO R3
MOV (SP)+,R2 ;;POP STACK INTO R2
MOV (SP)+,R1 ;;POP STACK INTO R1
ENDRTN
$230:
$231: RTS PC
  
```

```

4348
4349      .SBTTL INTSRV INTERRUPT SERVICE ROUTINE
4350      ;;*****
4351 012032 INTSRV:
4352      ;* SERVICE ROUTINE: INTSRV
4353      ;* THIS GLOBAL ROUTINE DOES NOTHING BUT INCREMENT
4354      ;* 'INTFLAG' EACH TIME IT IS CALLED. IT ASSUMES
4355      ;* THAT THE MAIN CALLING ROUTINE WILL KNOW WHAT
4356      ;* TO LOOK FOR.
4357      ;;*****
4358
4359      ;ADD 1 TO 'INTERRUPT OCCURED' FLAG
4360 012032      LET INTFLAG := INTFLAG + #1
4361 012032 005267 000002      INC INTFLAG
4362 012036      ENDSRV      ;THAT'S ALL
4363 012036 000002      RTI
4364 012040 000000 INTFLAG: 0

```

```

4365
4366 012042          ROUTINE MYTYPE
4367 012042          MYTYPE:
4368                ;;*****
4369 012042 104401 012050          TYPE      .65$          ;;TYPE ASCIZ STRING
4370 012046 000405          BR      64$          ;;GET OVER THE ASCIZ
4371                ;;65$: .ASCIZ <CRLF>*TEST * *
4372 012062          64$:
4373 012062 016746 167112          MOV      $TESTN,-(SP)          ;;SAVE $TESTN FOR TYPEOUT
4374 012066 104402          TYPOC          ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
4375 012070 104401 012076          TYPE      .67$          ;;TYPE ASCIZ STRING
4376 012074 000405          BR      66$          ;;GET OVER THE ASCIZ
4377                ;;67$: .ASCIZ *.ERROR * *
4378 012110          66$:
4379 012110 116767 167000 167060          MOVB     $ITEMB,$FATAL          ; APT FATAL ERROR NUMBER
4380 012116 016746 167054          MOV      $FATAL,-(SP)          ;;SAVE $FATAL FOR TYPEOUT
4381 012122 104402          TYPOC          ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
4382 012124 104401 012132          TYPE      .69$          ;;TYPE ASCIZ STRING
4383 012130 000404          BR      68$          ;;GET OVER THE ASCIZ
4384                ;;69$: .ASCIZ *.PC = *
4385 012142          68$:
4386 012142 016746 166750          MOV      $ERRPC,-(SP)          ;;SAVE $ERRPC FOR TYPEOUT
4387 012146 104402          TYPOC          ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
4388 012150 104401 012156          TYPE      .71$          ;;TYPE ASCIZ STRING
4389 012154 000404          BR      70$          ;;GET OVER THE ASCIZ
4390                ;;71$: .ASCIZ *.CSR: *
4391 012166          70$:
4392 012166 016746 167062          MOV      DLADD,-(SP)          ;;SAVE DLADD FOR TYPEOUT
4393 012172 104402          TYPOC          ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
4394 012174 104401 012202          TYPE      .73$          ;;TYPE ASCIZ STRING
4395 012200 000405          BR      72$          ;;GET OVER THE ASCIZ
4396                ;;73$: .ASCIZ *.VECTOR: *
4397 012214          72$:
4398 012214 016746 167036          MOV      DLVEC,-(SP)          ;;SAVE DLVEC FOR TYPEOUT
4399 012220 104402          TYPOC          ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
4400                ENDRTN
4401 $240:
4402 012222          $241:
4403 012222 000207          RTS      PC
    
```



```

4404 012224 ROUTINE CYCLE
4405 012224 CYCLE:
4406 ;:.....
4407 ;* ROUTINE: CYCLE
4408 ;* THIS ROUTINE CAUSES ADRS TO POINT TO THE
4409 ;* ADDRESS OF DLV11-E UNDER TEST, ADRS .2 TO
4410 ;* POINT TO THE VECTOR OF THE DLV11-E UNDER TEST.
4411 ;* IT KEEPS TRACK OF THE CURRENT DEVICE AND BIT
4412 ;* MASKS.
4413 ;:.....
4414 012224 REPEAT
4415 012224 $244:
4416 012224 IF BITMASK EQ #0 THEN
4417 012224 005767 000122 TST BITMASK
4418 012230 001027 BNE $245
4419 012232 IF INITFLAG EQ #1 THEN
4420 012232 026727 000116 000001 CMP INITFLAG,#1
4421 012240 001003 BNE $246
4422 012242 LET INITFLAG := #0
4423 012242 005067 000106 CLR INITFLAG
4424 012246 ELSE
4425 012246 000403 BR $247
4426 012250 $246:
4427 012250 CALL $EOP ; AS A SUBROUTINE
4428 012250 004767 000110 JSR PC,$EOP
4429
4430 012254 SPECIALADDRESS: ; BECAUSE $EOP RETURNS AS A JUMP
4431 012254 LET RO := POP
4432 012254 012600 MOV (SP),RO
4433 012256 ENDIF
4434 012256 $247:
4435 012256 LET BITMASK := #1
4436 012256 012767 000001 000066 MOV #1,BITMASK
4437 012264 LET $DEVCT := #1
4438 012264 012767 000001 166712 MOV #1,$DEVCT
4439 012272 LET ADDRESS := $BASE
4440 012272 016767 166752 000056 MOV $BASE,ADDRESS
4441 012300 LET VECTOR := $VECT1
4442 012300 016767 166740 000052 MOV $VECT1,VECTOR
4443 012306 ELSE
4444 012306 000410 BR $250
4445 012310 $245:
4446 012310 LET R4 := #10
4447 012310 012704 000010 MOV #10,R4
4448 012314 LET BITMASK := BITMASK ROTATE 1
4449 012314 006167 000032 ROL BITMASK
4450 012320 LET ADDRESS := ADDRESS + R4
4451 012320 060467 000032 ADD R4,ADDRESS
4452 012324 LET VECTOR := VECTOR + R4
4453 012324 060467 000030 ADD R4,VECTOR
4454 012330 ENDIF
4455 012330 $250:
4456 012330 UNTIL BITMASK SETIN $DEVH
4457 012330 036767 000016 166714 BIT BITMASK,$DEVH
4458 012336 001732 BEQ $244
4459

```

```
4460 012340
4461 012340 012701 012356      MOV      #ADDRESS,ADRS      LET ADRS := #ADDRESS
4462 012344
4463 012344 005267 166634      INC      #DEVCT          LET #DEVCT := #DEVCT + #1
4464 012350
4465 012350 000404      BR      #242          RETURN
4466 012352 000000      BITMASK: 0
4467 012354 000001      INITFLAG: 1
4468 012356 000000      ADDRESS: 0
4469 012360 000000      VECTOR: 0
4470
4471 012362
4472 012362      #242:
4473 012362      #243:
4474 012362 000207      RTS      PC
4475
4476
```

ENDRTN

```

4477 .SBTTL END OF PASS ROUTINE
4478
4479
4480 ;*****
4481 ;*INCREMENT THE PASS NUMBER (#PASS)
4482 ;*INDICATE END-OF-PROGRAM AFTER 1 PASSES THRU THE PROGRAM
4483 ;*TYPE "END PASS @XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)
4484 ;*IF THERES A MONITOR GO TO IT
4485 ;*IF THERE ISN'T JUMP TO SPECIALADDRESS
4486 $EOP:
4487 012364 000004 SCOPE
4488 012366 005067 166510 CLR $TSTNM ;;ZERO THE TEST NUMBER
4489 012372 005067 166562 CLR $TIMES ;;ZERO THE NUMBER OF ITERATIONS
4490 012376 005267 166600 INC $PASS ;;INCREMENT THE PASS NUMBER
4491 012402 042767 100000 166572 BIC @100000,$PASS ;;DON'T ALLOW A NEG. NUMBER
4492 012410 005327 DEC (PC). ;;LOOP?
4493 012412 000001 $EOPCT: .WORD 1
4494 012414 003022 BGT $DOAGN ;;YES
4495 012416 012737 MOV (PC),B(PC). ;;RESTORE COUNTER
4496 012420 000001 $ENDCT: .WORD 1
4497 012422 012412 $EOPCT
4498 012424 104401 012471 TYPE , $ENDMG ;;TYPE "END PASS #"
4499 012430 016746 166546 MOV $PASS,-(SP) ;;SAVE $PASS FOR TYPEOUT
4500 012434 104405 TYPDS ;;GO TYPE--DECIMAL ASCII WITH SIGN
4501 012436 104401 012466 TYPE , $ENULL ;;TYPE A NULL CHARACTER
4502 012442 013700 000042 $GET42: MOV @42,RO ;;GET MONITOR ADDRESS
4503 012446 001405 BEQ $DOAGN ;;BRANCH IF NO MONITOR
4504 012450 000005 RESET ;;CLEAR THE WORLD
4505 012452 004710 $ENDAD: JSR PC,(RO) ;;GO TO MONITOR
4506 012454 000240 NOP ;;SAVE ROOM
4507 012456 000240 NOP ;;FOR
4508 012460 000240 NOP ;;ACT11
4509 012462 $DOAGN:
4510 012462 000137 JMP B(PC). ;;RETURN
4511 012464 012254 $RTNAD: .WORD SPECIALADDRESS
4512 012466 377 377 000 $ENULL: .BYTE -1,-1,0 ;;NULL CHARACTER STRING
4513 012471 015 042412 042116 $ENDMG: .ASCIZ <15><12>/END PASS #/
4514 012476 050040 051501 020123
4515 012504 000043
  
```

.SBTTL POWER DOWN AND UP ROUTINES

```

4516
4517
4518
4519
4520 012506 012737 012652 000024 ;*****
;POWER DOWN ROUTINE
;PWRDN: MOV @#ILLUP,@#PWRVEC ;;SET FOR FAST UP
4521 012514 012737 000340 000026 MOV @340,@#PWRVEC+2 ;;PRIO:7
4522 012522 010046 MOV R0,-(SP) ;;PUSH R0 ON STACK
4523 012524 010146 MOV R1,-(SP) ;;PUSH R1 ON STACK
4524 012526 010246 MOV R2,-(SP) ;;PUSH R2 ON STACK
4525 012530 010346 MOV R3,-(SP) ;;PUSH R3 ON STACK
4526 012532 010446 MOV R4,-(SP) ;;PUSH R4 ON STACK
4527 012534 010546 MOV R5,-(SP) ;;PUSH R5 ON STACK
4528 012536 017746 166376 MOV @SWR,-(SP) ;;PUSH @SWR ON STACK
4529 012542 010667 000110 MOV SP,@SAVR6 ;;SAVE SP
4530 012546 012737 012560 000024 MOV @#PWRUP,@#PWRVEC ;;SET UP VECTOR
4531 012554 000000 HALT
4532 012556 000776 BR -.2 ;;HANG UP
4533
4534 ;*****
4535 ;POWER UP ROUTINE
4536 012560 012737 012652 000024 ;PWRUP: MOV @#ILLUP,@#PWRVEC ;;SET FOR FAST DOWN
4537 012566 016706 000064 MOV @SAVR6,SP ;;GET SP
4538 012572 005067 000060 CLR @SAVR6 ;;WAIT LOOP FOR THE TTY
4539 012576 005267 000054 1@: INC @SAVR6 ;;WAIT FOR THE INC
4540 012602 001375 BNE 1@ ;;OF WORD
4541 012604 012677 166330 MOV (SP)+,@SWR ;;POP STACK INTO @SWR
4542 012610 012605 MOV (SP)+,R5 ;;POP STACK INTO R5
4543 012612 012604 MOV (SP)+,R4 ;;POP STACK INTO R4
4544 012614 012603 MOV (SP)+,R3 ;;POP STACK INTO R3
4545 012616 012602 MOV (SP)+,R2 ;;POP STACK INTO R2
4546 012620 012601 MOV (SP)+,R1 ;;POP STACK INTO R1
4547 012622 012600 MOV (SP)+,R0 ;;POP STACK INTO R0
4548 012624 012737 012506 000024 MOV @#PWRDN,@#PWRVEC ;;SET UP THE POWER DOWN VECTOR
4549 012632 012737 000340 000026 MOV @340,@#PWRVEC+2 ;;PRIO:7
4550 012640 104401 TYPE ;;REPORT THE POWER FAILURE
4551 012642 012660 ;PWRMG: .WORD @POWER ;;POWER FAIL MESSAGE POINTER
4552 012644 012716 MOV (PC)+,(SP) ;;RESTART AT START
4553 012646 001336 ;PWRAD: .WORD START ;;RESTART ADDRESS
4554 012650 000002 RTI
4555 012652 000000 ;ILLUP: HALT ;;THE POWER UP SEQUENCE WAS STARTED
4556 012654 000776 BR -.2 ;; BEFORE THE POWER DOWN WAS COMPLETE
4557 012656 000000 ;SAVR6: 0 ;;PUT THE SP HERE
4558 012660 005015 047520 042527 ;POWER: .ASCIZ <15><12>"POWER"
4559 012666 000122
4560 .EVEN
    
```

```

4561 .SBTTL TYPE ROUTINE
4562
4563 ;*****
4564 ;*ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
4565 ;*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
4566 ;*NOTE1: #NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
4567 ;*NOTE2: #FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
4568 ;*NOTE3: #FILLC CONTAINS THE CHARACTER TO FILL AFTER.
4569 ;*
4570 ;*CALL:
4571 ;*1) USING A TRAP INSTRUCTION
4572 ;* TYPE .MESADR ;:MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
4573 ;*OR
4574 ;* TYPE
4575 ;* MESADR
4576 ;*
4577
4578 012670 105767 166263 ;TYPE: TSTB #TPFLG ;:IS THERE A TERMINAL?
4579 012674 100002 BPL 1# ;:BR IF YES
4580 012676 000000 HALT ;:HALT HERE IF NO TERMINAL
4581 012700 000430 BR 3# ;:LEAVE
4582 012702 010046 1#: MOV RO,-(SP) ;:SAVE RO
4583 012704 017600 000002 MOV #2(SP),RO ;:GET ADDRESS OF ASCIZ STRING
4584 012710 122767 000001 166276 CMPB #APTENV,#ENV ;:RUNNING IN APT MODE
4585 012716 001011 BNE 62# ;:NO,GO CHECK FOR APT CONSOLE
4586 012720 132767 000100 166267 BITB #APTSPOOL,#ENVM ;:SPOOL MESSAGE TO APT
4587 012726 001405 BEQ 62# ;:NO,GO CHECK FOR CONSOLE
4588 012730 010067 000004 MOV RO,61# ;:SETUP MESSAGE ADDRESS FOR APT
4589 012734 004767 000774 JSR PC,#ATY3 ;:SPOOL MESSAGE TO APT
4590 012740 000000 61#: .WORD 0 ;:MESSAGE ADDRESS
4591 012742 132767 00J040 166245 62#: BITB #APTCSUP,#ENVM ;:APT CONSOLE SUPPRESSED
4592 012750 001003 BNE 60# ;:YES,SKIP TYPE OUT
4593 012752 112046 2#: MOVB (RO)+,-(SP) ;:PUSH CHARACTER TO BE TYPED ONTO STACK
4594 012754 001005 BNE 4# ;:BR IF IT ISN'T THE TERMINATOR
4595 012756 005726 TST (SP)+ ;:IF TERMINATOR POP IT OFF THE STACK
4596 012760 012600 60#: MOV (SP)+,RO ;:RESTORE RO
4597 012762 062716 000002 3#: ADD #2,(SP) ;:ADJUST RETURN PC
4598 012766 000002 RTI ;:RETURN
4599 012770 122716 000011 4#: CMPB #HT,(SP) ;:BRANCH IF <HT>
4600 012774 001430 BEQ 5# ;:
4601 012776 122716 000200 CMPB #CRLF,(SP) ;:BRANCH IF NOT <CRLF>
4602 013002 001006 BNE 5# ;:
4603 013004 005726 TST (SP)+ ;:POP <CR><LF> EQUIV
4604 013006 104401 TYPE ;:TYPE A CR AND LF
4605 013010 001171 #CRLF
4606 013012 105067 000130 CLRB #CHARCNT ;:CLEAR CHARACTER COUNT
4607 013016 000755 BR 2# ;:GET NEXT CHARACTER
4608 013020 004767 000056 5#: JSR PC,#TYPEC ;:GO TYPE THIS CHARACTER
4609 013024 126726 166126 6#: CMPB #FILLC,(SP)+ ;:IS IT TIME FOR FILLER CHARS.?
4610 013030 001350 BNE 2# ;:IF NO GO GET NEXT CHAR.
4611 013032 016746 166116 MOV #NULL,-(SP) ;:GET # OF FILLER CHARS. NEEDED
4612 ;:AND THE NULL CHAR.
4613 013036 105366 000001 7#: DECB 1(SP) ;:DOES A NULL NEED TO BE TYPED?
4614 013042 002770 BLT 6# ;:BR IF NO--GO POP THE NULL OFF OF STACK
4615 013044 004767 000032 JSR PC,#TYPEC ;:GO TYPE A NULL
4616 013050 105367 000072 DECB #CHARCNT ;:DO NOT COUNT AS A COUNT

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4617 013054 000770          BR      7#          ;;LOOP
4618
4619          ;HORIZONTAL TAB PROCESSOR
4620
4621 013056 112716 000040      8#:     MOVB   #' ,(SP)          ;;REPLACE TAB WITH SPACE
4622 013062 004767 000014      9#:     JSR    PC, #TYPEC          ;;TYPE A SPACE
4623 013066 132767 000007 000052  BITB   #7, #CHARCNT          ;;BRANCH IF NOT AT
4624 013074 001372          BNE    9#          ;;TAB STOP
4625 013076 005726          TST    (SP),          ;;POP SPACE OFF STACK
4626 013100 000724          BR     2#          ;;GET NEXT CHARACTER
4627 013102 105777 166042      #TYPEC: TSTB  #1TPS          ;;WAIT UNTIL PRINTER IS READY
4628 013106 100375          BPL    #TYPEC
4629 013110 116677 000002 166034  MOVB   2(SP), #1TPB          ;;LOAD CHAR TO BE TYPED INTO DATA REG.
4630 013116 122766 000015 000002  CMPB   #CR, 2(SP)          ;;IS CHARACTER A CARRIAGE RETURN?
4631 013124 001003          BNE    1#          ;;BRANCH IF NO
4632 013126 105067 000014          CLRB   #CHARCNT          ;;YES--CLEAR CHARACTER COUNT
4633 013132 000406          BR     1#          ;;EXIT
4634 013134 122766 000012 000002 1#:     CMPB   #LF, 2(SP)          ;;IS CHARACTER A LINE FEED?
4635 013142 001402          BEQ    #TYPEX          ;;BRANCH IF YES
4636 013144 105227          INCB   (PC),          ;;COUNT THE CHARACTER
4637 013146 000000      #CHARCNT: .WORD 0          ;;CHARACTER COUNT STORAGE
4638 013150 000207      #TYPEX: RTS    PC
4639

```

```

4640 .SBTTL TTY INPUT ROUTINE
4641
4642 ;:*****
4643 .ENABL LSB
4644
4645 ;:*****
4646 ;*SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
4647 ;*ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
4648 ;*SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP CALL
4649 ;*WHEN OPERATING IN TTY FLAG MODE.
4650 013152 022767 000176 165760 #CKSMR: CMP #SMREG,SMR ;;IS THE SOFT-SMR SELECTED;
4651 013160 001074 BNE 15# ;;BRANCH IF NO
4652 013162 105777 165756 TSTB #TKS ;;CHAR THERE?
4653 013166 100071 BPL 15# ;;IF NO, DON'T WAIT AROUND
4654 013170 117746 165752 MOVSB #TKB,-(SP) ;;SAVE THE CHAR
4655 013174 042716 177600 BIC #C177,(SP) ;;STRIP-OFF THE ASCII
4656 013200 022726 000007 CMP #7,(SP)+ ;;IS IT A CONTROL G?
4657 013204 001062 BNE 15# ;;NO, RETURN TO USER
4658 013206 126727 165722 000001 CMPSB #AUTOB,#1 ;;ARE WE RUNNING IN AUTO-MODE?
4659 013214 001456 BEQ 15# ;;BRANCH IF YES
4660
4661 013216 104401 013677 #GTSMR: TYPE ,#CNTLG ;;ECHO THE CONTROL-G (+G)
4662 013222 104401 013704 TYPE ,#MSMR ;;TYPE CURRENT CONTENTS
4663 013226 016746 164744 MOV SMREG,-(SP) ;;SAVE SMREG FOR TYPEOUT
4664 013232 104402 TYPOC ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
4665 013234 104401 013715 TYPE ,#MNEW ;;PROMPT FOR NEW SMR
4666 013240 005046 19#: CLR -(SP) ;;CLEAR COUNTER
4667 013242 005046 CLR -(SP) ;;THE NEW SMR
4668 013244 105777 165674 7#: TSTB #TKS ;;CHAR THERE?
4669 013250 100375 BPL 7# ;;IF NOT TRY AGAIN
4670
4671 013252 117746 165670 MOVSB #TKB,-(SP) ;;PICK UP CHAR
4672 013256 042716 177600 BIC #C177,(SP) ;;MAKE IT 7-BIT ASCII
4673
4674
4675
4676 013262 021627 000025 9#: CMP (SP),#25 ;;IS IT A CONTROL-U?
4677 013266 001005 BNE 10# ;;BRANCH IF NOT
4678 013270 104401 013672 TYPE ,#CNTLU ;;YES, ECHO CONTROL-U (+U)
4679 013274 062706 000006 20#: ADD #6,SP ;;IGNORE PREVIOUS INPUT
4680 013300 000757 BR 19# ;;LET'S TRY IT AGAIN
4681
4682
4683 013302 021627 000015 10#: CMP (SP),#15 ;;IS IT A <CR>?
4684 013306 001022 BNE 16# ;;BRANCH IF NO
4685 013310 005766 000004 TST 4(SP) ;;YES, IS IT THE FIRST CHAR?
4686 013314 001403 BEQ 11# ;;BRANCH IF YES
4687 013316 016677 000002 165614 MOV 2(SP),BSMR ;;SAVE NEW SMR
4688 013324 062706 000006 11#: ADD #6,SP ;;CLEAR UP STACK
4689 013330 104401 001171 14#: TYPE ,#CRLF ;;ECHO <CR> AND <LF>
4690 013334 126727 165575 000001 CMPSB #INTAG,#1 ;;RE-ENABLE TTY KBD INTERRUPTS?
4691 013342 001003 BNE 15# ;;BRANCH IF NOT
4692 013344 012777 000100 165572 MOV #100,#TKS ;;RE-ENABLE TTY KBD INTERRUPTS
4693 013352 000002 15#: RTI ;;RETURN
4694 013354 004767 177522 16#: JSR PC,#TYPEC ;;ECHO CHAR
4695 013360 021627 000060 CMP (SP),#60 ;;CHAR < 0?

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4696 013364 002420          BLT      18#          ;;BRANCH IF YES
4697 013366 021627 000067    CMP      (SP),#67      ;;CHAR > 7?
4698 013372 003015          BGT      18#          ;;BRANCH IF YES
4699 013374 042726 000060    BIC      #60,(SP)+    ;;STRIP-OFF ASCII
4700 013400 005766 000002    TST      2(SP)        ;;IS THIS THE FIRST CHAR
4701 013404 001403          BEQ      17#          ;;BRANCH IF YES
4702 013406 006316          ASL      (SP)         ;;NO, SHIFT PRESENT
4703 013410 006316          ASL      (SP)         ;; CHAR OVER TO MAKE
4704 013412 006316          ASL      (SP)         ;; ROOM FOR NEW ONE.
4705 013414 005266 000002    17#:    INC      2(SP)        ;;KEEP COUNT OF CHAR
4706 013420 056616 177776    BIS      -2(SP),(SP)  ;;SET IN NEW CHAR
4707 013424 000707          BR       7#           ;;GET THE NEXT ONE
4708 013426 104401 001170    18#:    TYPE     ,#QUES     ;;TYPE ?<CR><LF>
4709 013432 000720          BR       20#         ;;SIMULATE CONTROL-U
4710          .DSABL  LSB
4711
4712
4713          ;;*****
4714          ;*THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
4715          ;*CALL:
4716          ;*   RDCHR          ;;INPUT A SINGLE CHARACTER FROM THE TTY
4717          ;*   RETURN HERE   ;;CHARACTER IS ON THE STACK
4718          ;*                ;;WITH PARITY BIT STRIPPED OFF
4719          ;
4720
4721 013434 011646          $RDCHR: MOV      (SP),-(SP)  ;;PUSH DOWN THE PC
4722 013436 016666 000004 000002    MOV      4(SP),2(SP)  ;;SAVE THE PS
4723 013444 105777 165474    1#:    TSTB     #0TKS      ;;WAIT FOR
4724 013450 100375          BPL      1#           ;;A CHARACTER
4725 013452 117766 165470 000004    MOVB     #0TKB,4(SP)  ;;READ THE TTY
4726 013460 042766 177600 000004    BIC      #0C<177>,4(SP) ;;GET RID OF JUNK IF ANY
4727 013466 026627 000004 000023    CMP      4(SP),#23    ;;IS IT A CONTROL-S?
4728 013474 001013          BNE      3#           ;;BRANCH IF NO
4729 013476 105777 165442    2#:    TSTB     #0TKS      ;;WAIT FOR A CHARACTER
4730 013502 100375          BPL      2#           ;;LOOP UNTIL ITS THERE
4731 013504 117746 165436    MOVB     #0TKB,-(SP)  ;;GET CHARACTER
4732 013510 042716 177600    BIC      #0C177,(SP)  ;;MAKE IT 7-BIT ASCII
4733 013514 022627 000021    CMP      (SP),#21    ;;IS IT A CONTROL-Q?
4734 013520 001366          BNE      2#           ;;IF NOT DISCARD IT
4735 013522 000750          BR       1#           ;;YES, RESUME
4736 013524 026627 000004 000140    3#:    CMP      4(SP),#140  ;;IS IT UPPER CASE?
4737 013532 002407          BLT      4#           ;;BRANCH IF YES
4738 013534 026627 000004 000175    CMP      4(SP),#175  ;;IS IT A SPECIAL CHAR?
4739 013542 003003          BGT      4#           ;;BRANCH IF YES
4740 013544 042766 000040 000004    BIC      #40,4(SP)   ;;MAKE IT UPPER CASE
4741 013552 000002          4#:    RTI                ;;GO BACK TO USER
4742          ;;*****
4743          ;*THIS ROUTINE WILL INPUT A STRING FROM THE TTY
4744          ;*CALL:
4745          ;*   RDLIN          ;;INPUT A STRING FROM THE TTY
4746          ;*   RETURN HERE   ;;ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
4747          ;*                ;;TERMINATOR WILL BE A BYTE OF ALL 0'S
4748          ;
4749 013554 010346          $RDLIN: MOV      R3,-(SP)  ;;SAVE R3
4750 013556 012703 013662    1#:    MOV      #TTYIN,R3  ;;GET ADDRESS
4751 013562 022703 013672    2#:    CMP      #TTYIN+8.,R3 ;;BUFFER FULL?
    
```


4752	013566	101405				BLOS	4#		::BR IF YES
4753	013570	104410				RDCHR			::GO READ ONE CHARACTER FROM THE TTY
4754	013572	112613				MOVB	(SP)+,(R3)		::GET CHARACTER
4755	013574	122713	000177		10#:	CMPB	#177,(R3)		::IS IT A RUBOUT
4756	013600	001003				BNE	3#		::SKIP IF NOT
4757	013602	104401	001170		4#:	TYPE	,#QUES		::TYPE A '?'
4758	013606	000763				BR	1#		::CLEAR THE BUFFER AND LOOP
4759	013610	111367	000044		3#:	MOVB	(R3),9#		::ECHO THE CHARACTER
4760	013614	104401	013660			TYPE	.9#		
4761	013620	122723	000015			CMPB	#15,(R3)+		::CHECK FOR RETURN
4762	013624	001356				BNE	2#		::LOOP IF NOT RETURN
4763	013626	105063	177777			CLRB	-1(R3)		::CLEAR RETURN (THE 15)
4764	013632	104401	001172			TYPE	,#LF		::TYPE A LINE FEED
4765	013636	012603				MOV	(SP)+,R3		::RESTORE R3
4766	013640	011646				MOV	(SP),-(SP)		::ADJUST THE STACK AND PUT ADDRESS OF THE
4767	013642	016666	000004	000002		MOV	4(SP),2(SP)		::FIRST ASCII CHARACTER ON IT
4768	013650	012766	013662	000004		MOV	#TTYIN,4(SP)		
4769	013656	000002				RTI			::RETURN
4770	013660	000			9#:	.BYTE	0		::STORAGE FOR ASCII CHAR. TO TYPE
4771	013661	000				.BYTE	0		::TERMINATOR
4772	013662	000010			#TTYIN:	.BLKB	8.		::RESERVE 8 BYTES FOR TTY INPUT
4773	013672	052536	005015	000	#CNTLU:	.ASCIZ	/'U/'<15><12>		::CONTROL "U"
4774	013677	136	006507	000012	#CNTLG:	.ASCIZ	/'G/'<15><12>		::CONTROL "G"
4775	013704	005015	053523	020122	#MSWR:	.ASCIZ	<15><12>/SWR = /		
4776	013712	020075	000						
4777	013715	040	047040	053505	#MNEW:	.ASCIZ	/ NEW = /		
4778	013722	036440	000040						

.SBTTL APT COMMUNICATIONS ROUTINE

```

4779
4780
4781
4782 013726 112767 000001 000236 $ATY1: MOVB #1,$FFLG ;;TO REPORT FATAL ERROR
4783 013734 112767 000001 000226 $ATY3: MOVB #1,$MFLG ;;TO TYPE A MESSAGE
4784 013742 000403 BR $ATYC
4785 013744 112767 000001 000220 $ATY4: MOVB #1,$FFLG ;;TO ONLY REPORT FATAL ERROR
4786 013752 $ATYC:
4787 013752 010046 MOV R0,-(SP) ;;PUSH R0 ON STACK
4788 013754 010146 MOV R1,-(SP) ;;PUSH R1 ON STACK
4789 013756 105767 000206 1STB $MFLG ;;SHOULD TYPE A MESSAGE?
4790 013762 001450 BEQ 5# ;;IF NOT: BR
4791 013764 122767 000001 165222 CMPB @APTENV,$ENV ;;OPERATING UNDER APT?
4792 013772 001031 BNE 3# ;;IF NOT: BR
4793 013774 132767 000100 165213 BITB @APTSPOOL,$ENVM ;;SHOULD SPOOL MESSAGES?
4794 014002 001425 BEQ 3# ;;IF NOT: BR
4795 014004 017600 000004 MOV @4(SP),R0 ;;GET MESSAGE ADDR.
4796 014010 062766 000002 000004 ADD #2,4(SP) ;;BUMP RETURN ADDR.
4797 014016 005767 165152 1#: TST $MSGTYPE ;;SEE IF DONE W/ LAST XMISSION?
4798 014022 001375 BNE 1# ;;IF NOT: WAIT
4799 014024 010067 165160 MOV R0,$MSGAD ;;PUT ADDR IN MAILBOX
4800 014030 105720 2#: TSTB (R0)+ ;;FIND END OF MESSAGE
4801 014032 001376 BNE 2#
4802 014034 166700 165150 SUB $MSGAD,R0 ;;SUB START OF MESSAGE
4803 014040 006200 ASR R0 ;;GET MESSAGE LGTH IN WORDS
4804 014042 010067 165144 MOV R0,$MSGLGT ;;PUT LENGTH IN MAILBOX
4805 014046 012767 000004 165120 MOV #4,$MSGTYPE ;;TELL APT TO TAKE MSG.
4806 014054 000413 BR 5#
4807 014056 017667 000004 000016 3#: MOV @4(SP),4# ;;PUT MSG ADDR IN JSR LINKAGE
4808 014064 062766 000002 000004 ADD #2,4(SP) ;;BUMP RETURN ADDRESS
4809 014072 016746 163700 MOV 177776,-(SP) ;;PUSH 177776 ON STACK
4810 014076 004767 176566 JSR PC,$TYPE ;;CALL TYPE MACRO
4811 014102 000000 4#: .WORD 0
4812 014104 5#:
4813 014104 105767 000062 10#: TSTB $FFLG ;;SHOULD REPORT FATAL ERROR?
4814 014110 001416 BEQ 12# ;;IF NOT: BR
4815 014112 005767 165076 TST $ENV ;;RUNNING UNDER APT?
4816 014116 001413 BEQ 12# ;;IF NOT: BR
4817 014120 005767 165050 11#: TST $MSGTYPE ;;FINISHED LAST MESSAGE?
4818 014124 001375 BNE 11# ;;IF NOT: WAIT
4819 014126 017667 000004 165042 MOV @4(SP),$FATAL ;;GET ERROR #
4820 014134 062766 000002 000004 ADD #2,4(SP) ;;BUMP RETURN ADDR.
4821 014142 005267 165026 INC $MSGTYPE ;;TELL APT TO TAKE ERROR
4822 014146 105067 000020 12#: CLRB $FFLG ;;CLEAR FATAL FLAG
4823 014152 105067 000013 CLRB $LFLG ;;CLEAR LOG FLAG
4824 014156 105067 000006 CLRB $MFLG ;;CLEAR MESSAGE FLAG
4825 014162 012601 MOV (SP)+,R1 ;;POP STACK INTO R1
4826 014164 012600 MOV (SP)+,R0 ;;POP STACK INTO R0
    
```

4827	014166	000207	RTS	PC	::RETURN
4828	014170	000	#MFLG: .BYTE	0	::MESSG. FLAG
4829	014171	000	#LFLG: .BYTE	0	::LOG FLAG
4830	014172	000	#FFLG: .BYTE	0	::FATAL FLAG
4831		014174	.EVEN		
4832		000200	APTSIZE=200		
4833		000001	APTENV=001		
4834		000100	APTSPOOL=100		
4835		000040	APTCSUP=040		

4836 .SBTTL ERROR HANDLER ROUTINE

4837 ;*****
4838 ;*THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT.
4839 ;*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
4840 ;*AND GO TO MYTYPE ON ERROR
4841 ;*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
4842 ;*SW15=1 HALT ON ERROR
4843 ;*SW13=1 INHIBIT ERROR TYPEOUTS
4844 ;*SW10=1 BELL ON ERROR
4845 ;*SW09=1 LOOP ON ERROR
4846 ;*CALL
4847 ;*
4848 ;* ERROR N ;:ERROR=EMT AND N=ERROR ITEM NUMBER
4849

4850 \$ERROR:
4851 014174 104407 CKSWR ;:TEST FOR CHANGE IN SOFT-SWR
4852 014176 105267 164701 7\$: INCB \$ERFLG ;:SET THE ERROR FLAG
4853 014202 001775 BEQ 7\$;:DON'T LET THE FLAG GO TO ZERO
4854 014204 016777 164672 164730 MOV \$TSTNM,\$DISPLAY ;:DISPLAY TEST NUMBER AND ERROR FLAG
4855 014212 032777 002000 164720 BIT \$BIT10,\$BSW ;:BELL ON ERROR?
4856 014220 001402 BEQ 1\$;:NO - SKIP
4857 014222 104401 001164 TYPE . \$BELL ;:RING BELL
4858 014226 005267 164660 1\$: INC \$ERTTL ;:COUNT THE NUMBER OF ERRORS
4859 014232 011667 164660 MOV (SP),\$ERRPC ;:GET ADDRESS OF ERROR INSTRUCTION
4860 014236 162767 000002 164652 SUB \$2,\$ERRPC
4861 014244 117767 164646 164642 MOV \$ERRPC,\$ITEMB ;:STRIP AND SAVE THE ERROR ITEM CODE
4862 014252 032777 020000 164660 BIT \$BIT13,\$BSW ;:SKIP TYPEOUT IF SET
4863 014260 001004 BNE 20\$;:SKIP TYPEOUTS
4864 014262 004767 175554 JSR PC,MYTYPE ;:GO TO USER ERROR ROUTINE
4865 014266 104401 001171 TYPE . \$CRLF
4866 014272 20\$: CMPB \$APTENV,\$ENV ;:RUNNING IN APT MODE
4867 014272 122767 000001 164714 BNE 2\$;:NO,SKIP APT ERROR REPORT
4868 014300 001007 MOV \$ITEMB,21\$;:SET ITEM NUMBER AS ERROR NUMBER
4869 014302 116767 164606 000004 JSR PC,\$ATY4 ;:REPORT FATAL ERROR TO APT
4870 014310 004767 177430
4871 014314 000 21\$: .BYTE 0
4872 014315 000 .BYTE 0
4873 014316 000777 22\$: BR 22\$;:APT ERROR LOOP
4874 014320 005777 164614 2\$: TST \$BSW ;:HALT ON ERROR
4875 014324 100002 BPL 3\$;:SKIP IF CONTINUE
4876 014326 000000 HALT ;:HALT ON ERROR!
4877 014330 104407 CKSWR ;:TEST FOR CHANGE IN SOFT-SWR
4878 014332 032777 001000 164600 3\$: BIT \$BIT09,\$BSW ;:LOOP ON ERROR SWITCH SET?
4879 014340 001402 BEQ 4\$;:BR IF NO
4880 014342 016716 164542 MOV \$LPERR,(SP) ;:FUDGE RETURN FOR LOOPING
4881 014346 005767 164610 4\$: TST \$ESCAPE ;:CHECK FOR AN ESCAPE ADDRESS
4882 014352 001402 BEQ 5\$;:BR IF NONE
4883 014354 016716 164602 MOV \$ESCAPE,(SP) ;:FUDGE RETURN ADDRESS FOR ESCAPE
4884 014360 5\$:
4885 014360 022737 012452 000042 CMP \$ENDAD,\$42 ;:ACT-11 AUTO-ACCEPT?
4886 014366 001001 BNE 6\$;:BRANCH IF NO
4887 014370 000000 HALT ;:YES
4888 014372 6\$:
4889 014372 000002 RTI ;:RETURN

```

4890 .SBTTL SCOPE HANDLER ROUTINE
4891
4892 ;*****
4893 ;*THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
4894 ;*AND LOAD THE TEST NUMBER($TSTNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
4895 ;*AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
4896 ;*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
4897 ;*SW14=1 LOOP ON TEST
4898 ;*SW11=1 INHIBIT ITERATIONS
4899 ;*SM09=1 LOOP ON ERROR
4900 ;*SM08=1 LOOP ON TEST IN SWR<7:0>
4901 ;*CALL
4902 ;* SCOPE ;;SCOPE=JOT
4903
4904 $SCOPE:
4905 014374 104407 CKSWR ;;TEST FOR CHANGE IN SOFT-SWR
4906 014376 032777 040000 164534 1$: BIT #BIT14,$SWR ;;LOOP ON PRESENT TEST?
4907 014404 001114 BNE $OVER ;;YES IF SW14=1
4908 ;*****START OF CODE FOR THE XOR TESTER*****
4909 014406 000416 $XTSTR: BR 6$ ;;IF RUNNING ON THE "XOR" TESTER CHANGE
4910 ;;THIS INSTRUCTION TO A "NOP" (NOP=240)
4911 014410 013746 000004 MOV $ERRVEC,-(SP) ;;SAVE THE CONTENTS OF THE ERROR VECTOR
4912 014414 012737 014434 000004 MOV #5,$ERRVEC ;;SET FOR TIMEOUT
4913 014422 005737 177060 TST #177060 ;;TIME OUT ON XOR?
4914 014426 012637 000004 MOV (SP)+,$ERRVEC ;;RESTORE THE ERROR VECTOR
4915 014432 000463 BR $SVLAD ;;GO TO THE NEXT TEST
4916 014434 022626 5$: CMP (SP)+,(SP)+ ;;CLEAR THE STACK AFTER A TIME OUT
4917 014436 012637 000004 MOV (SP)+,$ERRVEC ;;RESTORE THE ERROR VECTOR
4918 014442 000423 BR 7$ ;;LOOP ON THE PRESENT TEST
4919 014444 6$: ;*****END OF CODE FOR THE XOR TESTER*****
4920 014444 032777 000400 164466 BIT #BIT08,$SWR ;;LOOP ON SPEC. TEST?
4921 014452 001404 BEQ 2$ ;;BR IF NO
4922 014454 127767 164460 164420 CMPB $SWR,$TSTNM ;;ON THE RIGHT TEST? SWR<7:0>
4923 014462 001465 BEQ $OVER ;;BR IF YES
4924 014464 105767 164413 2$: TSTB $ERFLG ;;HAS AN ERROR OCCURRED?
4925 014470 001421 BEQ 3$ ;;BR IF NO
4926 014472 126767 164417 164403 CMPB $ERMAX,$ERFLG ;;MAX. ERRORS FOR THIS TEST OCCURRED?
4927 014500 101015 BHI 3$ ;;BR IF NO
4928 014502 032777 001000 164430 BIT #BIT09,$SWR ;;LOOP ON ERROR?
4929 014510 001404 BEQ 4$ ;;BR IF NO
4930 014512 016767 164372 164366 7$: MOV $LPERR,$LPADR ;;SET LOOP ADDRESS TO LAST SCOPE
4931 014520 000446 BR $OVER
4932 014522 105067 164355 4$: CLRB $ERFLG ;;ZERO THE ERROR FLAG
4933 014526 005067 164426 CLR $TIMES ;;CLEAR THE NUMBER OF ITERATIONS TO MAKE
4934 014532 000415 BR 1$ ;;ESCAPE TO THE NEXT TEST
4935 014534 032777 004000 164376 3$: BIT #BIT11,$SWR ;;INHIBIT ITERATIONS?
4936 014542 001011 BNE 1$ ;;BR IF YES
4937 014544 005767 164432 TST $PASS ;;IF FIRST PASS OF PROGRAM
4938 014550 001406 BEQ 1$ ;; INHIBIT ITERATIONS
4939 014552 005267 164326 INC $ICNT ;;INCREMENT ITERATION COUNT
4940 014556 026767 164376 164320 CMP $TIMES,$ICNT ;;CHECK THE NUMBER OF ITERATIONS MADE
4941 014564 002024 BGE $OVER ;;BR IF MORE ITERATION REQUIRED
4942 014566 012767 000001 164310 1$: MOV #1,$ICNT ;;REINITIALIZE THE ITERATION COUNTER
4943 014574 016767 000052 164356 MOV $MXCNT,$TIMES ;;SET NUMBER OF ITERATIONS TO DO
4944 014602 105267 164274 $SVLAD: INCB $TSTNM ;;COUNT TEST NUMBERS
4945 014606 116767 164270 164364 MOVB $TSTNM,$TESTN ;;SET TEST NUMBER IN APT MAILBOX
  
```

4946	014614	011667	164266		MOV	(SP),#LPADR	::SAVE SCOPE LOOP ADDRESS
4947	014620	011667	164264		MOV	(SP),#LPERR	::SAVE ERROR LOOP ADDRESS
4948	014624	005067	164332		CLR	#ESCAPE	::CLEAR THE ESCAPE FROM ERROR ADDRESS
4949	0: 4630	112767	000001	164257	MOVB	#1,#ERMAX	::ONLY ALLOW ONE(1) ERROR ON NEXT TEST
4950	014636	016777	164240	164276	#OVER: MOV	#TSTM,#DISPLAY	::DISPLAY TEST NUMBER
4951	014644	016716	164236		MOV	#LPADR,(SP)	::FUDGE RETURN ADDRESS
4952	014650	000002			RTI		::FIXES PS
4953	014652	003720			#MXCNT: 2000.		::MAX. NUMBER OF ITERATIONS

.SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE

;;*****
; THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
; SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
; NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
; BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
; REPLACED WITH SPACES.

; CALL:
; * MOV NUM, -(SP) ; PUT THE BINARY NUMBER ON THE STACK
; * TYPDS ; GO TO THE ROUTINE

#TYPDS:
MOV R0, -(SP) ; PUSH R0 ON STACK
MOV R1, -(SP) ; PUSH R1 ON STACK
MOV R2, -(SP) ; PUSH R2 ON STACK
MOV R3, -(SP) ; PUSH R3 ON STACK
MOV R5, -(SP) ; PUSH R5 ON STACK
MOV @20200, -(SP) ; SET BLANK SWITCH AND SIGN
MOV 20(SP), R5 ; GET THE INPUT NUMBER
BPL 1# ; BR IF INPUT IS POS.
NEG R5 ; MAKE THE BINARY NUMBER POS.
MOVB #'-.1(SP) ; MAKE THE ASCII NUMBER NEG.
1# : CLR R0 ; ZERO THE CONSTANTS INDEX
MOV @DBLK, R3 ; SETUP THE OUTPUT POINTER
MOVB #' , (R3) ; SET THE FIRST CHARACTER TO A BLANK
2# : CLR R2 ; CLEAR THE BCD NUMBER
MOV @DTBL(R0), R1 ; GET THE CONSTANT
3# : SUB R1, R5 ; FORM THIS BCD DIGIT
BLT 4# ; BR IF DONE
INC R2 ; INCREASE THE BCD DIGIT BY 1
BR 3#
4# : ADD R1, R5 ; ADD BACK THE CONSTANT
TST R2 ; CHECK IF BCD DIGIT=0
BNE 5# ; FALL THROUGH IF 0
TSTB (SP) ; STILL DOING LEADING 0'S?
BMI 7# ; BR IF YES
5# : ASLB (SP) ; MSD?
BCC 6# ; BR IF NO
MOVB 1(SP), -(R3) ; YES--SET THE SIGN
6# : BIS #'0, R2 ; MAKE THE BCD DIGIT ASCII
7# : BIS #' , R2 ; MAKE IT A SPACE IF NOT ALREADY A DIGIT
MOVB R2, (R3) ; PUT THIS CHARACTER IN THE OUTPUT BUFFER
TST (R0) ; JUST INCREMENTING
CMP R0, #10 ; CHECK THE TABLE INDEX
BLT 2# ; GO DO THE NEXT DIGIT
BGT 8# ; GO TO EXIT
MOV R5, R2 ; GET THE LSD
BR 6# ; GO CHANGE TO ASCII
8# : TSTB (SP) ; WAS THE LSD THE FIRST NON-ZERO?
BPL 9# ; BR IF NO
MOVB -1(SP), -(R3) ; YES--SET THE SIGN FOR TYPING
9# : CLRB (R3) ; SET THE TERMINATOR
MOV (SP), R5 ; POP STACK INTO R5
MOV (SP), R3 ; POP STACK INTO R3
MOV (SP), R2 ; POP STACK INTO R2

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4966 014654
4967 014654 010046
4968 014656 010146
4969 014660 010246
4970 014662 010346
4971 014664 010546
4972 014666 012746 020200
4973 014672 016605 000020
4974 014676 100004
4975 014700 005405
4976 014702 112766 000055 000001
4977 014710 005000 1# :
4978 014712 012703 015070
4979 014716 112723 000040
4980 014722 005002 2# :
4981 014724 016001 015060
4982 014730 160105 3# :
4983 014732 002402
4984 014734 005202
4985 014736 000774
4986 014740 060105 4# :
4987 014742 005702
4988 014744 001002
4989 014746 105716
4990 014750 100407
4991 014752 106316 5# :
4992 014754 105003
4993 014756 116663 000001 177777
4994 014764 052702 000060 6# :
4995 014770 052702 000040 7# :
4996 014774 110223
4997 014776 005720
4998 015000 020027 000010
4999 015004 002746
5000 015006 003002
5001 015010 010502
5002 015012 000764
5003 015014 105726 8# :
5004 015016 100003
5005 015020 116663 177777 177776
5006 015026 105013 9# :
5007 015030 012605
5008 015032 012603
5009 015034 012602

5010	015036	012601			MOV	(SP),R1	;;POP STACK INTO R1
5011	015040	012600			MOV	(SP),R0	;;POP STACK INTO R0
5012	015042	104401	015070		TYPE	,#DBLK	;;NOW TYPE THE NUMBER
5013	015046	016666	000002	000004	MOV	2(SP),4(SP)	;;ADJUST THE STACK
5014	015054	012616			MOV	(SP), (SP)	
5015	013056	000002			RTI		;;RETURN TO USER
5016	015060	025420			#DTBL:	10000.	
5017	015062	001750				1000.	
5018	015064	000144				100.	
5019	015066	000012				10.	
5020	015070	000004			#DBLK:	.BLKW 4	

BINARY TO OCTAL (ASCII) AND TYPE

.SBTTL BINARY TO OCTAL (ASCII) AND TYPE

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5046 015100 017646 000000
5047 015104 116667 000001 000211
5048 015112 112667 000207
5049 015116 062716 000002
5050 015122 000406
5051 015124 112767 000001 000171
5052 015132 112767 000006 000165
5053 015140 112767 000005 000154
5054 015146 010346
5055 015150 010446
5056 015152 010546
5057 015154 116704 000145
5058 015160 005404
5059 015162 062704 000006
5060 015166 110467 000132
5061 015172 116704 000125
5062 015176 016605 000012
5063 015202 005003
5064 015204 006105
5065 015206 000404
5066 015210 006105
5067 015212 006105
5068 015214 006105
5069 015216 010503
5070 015220 006103
5071 015222 105367 000076
5072 015226 100016
5073 015230 042703 177770
5074 015234 001002
5075 015236 005704
5076 015240 001403

```

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;*****
;THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
;OCTAL (ASCII) NUMBER AND TYPE IT.
;#TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
;CALL:
;# MOV      NUM,-(SP)      ;;NUMBER TO BE TYPED
;# TYPOS    ;;CALL FOR TYPEOUT
;# .BYTE   N              ;;N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
;# .BYTE   M              ;;M=1 OR 0
;#                               ;;1=TYPE LEADING ZEROS
;#                               ;;0=SUPPRESS LEADING ZEROS
;#
;#TYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
;#TYPOS OR #TYPOC
;CALL:
;# MOV      NUM,-(SP)      ;;NUMBER TO BE TYPED
;# TYPON    ;;CALL FOR TYPEOUT
;#
;#TYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
;CALL:
;# MOV      NUM,-(SP)      ;;NUMBER TO BE TYPED
;# TYPOC    ;;CALL FOR TYPEOUT
;#
;#TYPOS: MOV      @ (SP),-(SP)  ;;PICKUP THE MODE
;#          MOVB   1(SP),#OFILL ;;LOAD ZERO FILL SWITCH
;#          MOVB   (SP)+,#OMODE+1 ;;NUMBER OF DIGITS TO TYPE
;#          ADD    @2,(SP)      ;;ADJUST RETURN ADDRESS
;#          BR     #TYPON
;#TYPOC: MOVB   @1,#OFILL      ;;SET THE ZERO FILL SWITCH
;#          MOVB   @6,#OMODE+1 ;;SET FOR SIX(6) DIGITS
;#TYPON: MOVB   @5,#OCNT      ;;SET THE ITERATION COUNT
;#          MOV    R3,-(SP)    ;;SAVE R3
;#          MOV    R4,-(SP)    ;;SAVE R4
;#          MOV    R5,-(SP)    ;;SAVE R5
;#          MOVB   @OMODE+1,R4 ;;GET THE NUMBER OF DIGITS TO TYPE
;#          NEG    R4
;#          ADD    @6,R4      ;;SUBTRACT IT FOR MAX. ALLOWED
;#          MOVB   R4,#OMODE   ;;SAVE IT FOR USE
;#          MOVB   @OFILL,R4  ;;GET THE ZERO FILL SWITCH
;#          MOV    12(SP),R5   ;;PICKUP THE INPUT NUMBER
;#          CLR    R3         ;;CLEAR THE OUTPUT WORD
;#          ROL   R5         ;;ROTATE MSB INTO "C"
;#          BR    3#        ;;GO DO MSB
;#          ROL   R5         ;;FORM THIS DIGIT
;#          ROL   R5
;#          ROL   R5
;#          MOV    R5,R3
;#          ROL   R3         ;;GET LSB OF THIS DIGIT
;#          DECB  @OMODE     ;;TYPE THIS DIGIT?
;#          BPL   7#        ;;BR IF NO
;#          BIC  @177770,R3  ;;GET RID OF JUNK
;#          BNE  4#        ;;TEST FOR 0
;#          TST  R4         ;;SUPPRESS THIS 0?
;#          BEQ  5#        ;;BR IF YES

```

5077	015242	005204		4:	INC	R4		:: DON'T SUPPRESS ANYMORE 0'S
5078	015244	052703	000060		BIS	#'0,R3		:: MAKE THIS DIGIT ASCII
5079	015250	052703	000040	5:	BIS	#' ,R3		:: MAKE ASCII IF NOT ALREADY
5080	015254	110367	000040		MOVB	R3,R4		:: SAVE FOR TYPING
5081	015260	104401	015320		TYPE	,R4		:: GO TYPE THIS DIGIT
5082	015264	105367	000032	7:	DECB	#OCNT		:: COUNT BY 1
5083	015270	003347			SGT	2:		:: BR IF MORE TO DO
5084	015272	002402			BLT	6:		:: BR IF DONE
5085	015274	005204			INC	R4		:: INSURE LAST DIGIT ISN'T A BLANK
5086	015276	000744			BR	2:		:: GO DO THE LAST DIGIT
5087	015300	012605		6:	MOV	(SP)+,R5		:: RESTORE R5
5088	015302	012604			MOV	(SP)+,R4		:: RESTORE R4
5089	015304	012603			MOV	(SP)+,R3		:: RESTORE R3
5090	015306	016666	000002 000004		MOV	2(SP),4(SP)		:: SET THE STACK FOR RETURNING
5091	015314	012616			MOV	(SP)+,(SP)		
5092	015316	000002			FTI			:: RETURN
5093	015320	000		8:	.BYTE	0		:: STORAGE FOR ASCII DIGIT
5094	015321	000			.BYTE	0		:: TERMINATOR FOR TYPE ROUTINE
5095	015322	000		#OCNT:	.BYTE	0		:: OCTAL DIGIT COUNTER
5096	015323	000		#OFILL:	.BYTE	0		:: ZERO FILL SWITCH
5097	015324	000000		#OMODE:	.WORD	0		:: NUMBER OF DIGITS TO TYPE

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 5106 015326 010046
 5107 015330 016600 000002
 5108 015334 005740
 5109 015336 111000
 5110 015340 006300
 5111 015342 016000 015362
 5112 015346 000200
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 5117 015350 011646
 5118 015352 016666 000004 000002
 5119 015360 000002
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 5128 015362 015350
 5129 015364 012670
 5130 015366 015124
 5131 015370 015100
 5132 015372 015140
 5133 015374 014654
 5134
 5135 015376 013222
 5136
 5137 015400 013152
 5138 015402 013434
 5139 015404 013554
 5140 000001

.SBTTL TRAP DECODER

```

;*****
;THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
;AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
;OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
;GO TO THAT ROUTINE.
  
```

```

$TRAP:  MOV    RO,-(SP)          ;;SAVE RO
        MOV    2(SP),RO        ;;GET TRAP ADDRESS
        TST   -(RO)           ;;BACKUP BY 2
        MOVB  (RO),RO         ;;GET RIGHT BYTE OF TRAP
        ASL   RO              ;;POSITION FOR INDEXING
        MOV   $TRPAD(RO),RO    ;;INDEX TO TABLE
        RTS   RO              ;;GO TO ROUTINE
  
```

;;THIS IS USE TO HANDLE THE "GETPRI" MACRO

```

$TRAP2: MOV   (SP),-(SP)      ;;MOVE THE PC DOWN
        MOV   4(SP),2(SP)    ;;MOVE THE PSW DOWN
        RTI                          ;;RESTORE THE PSW
  
```

.SBTTL TRAP TABLE

```

;THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
;BY THE "TRAP" INSTRUCTION.
  
```

	ROUTINE	

\$TRPAD:	.WORD \$TRAP2	
	\$TYPE ;;CALL=TYPE	TRAP+1(104401) TTY TYPEOUT ROUTINE
	\$TYPOC ;;CALL=TYPOC	TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS)
	\$TYPOS ;;CALL=TYPOS	TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS)
	\$TYPON ;;CALL=TYPON	TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL)
	\$TYPDS ;;CALL=TYPDS	TRAP+5(104405) TYPE DECIMAL NUMBER (WITH SIGN)
	\$GTSWR ;;CALL=GTSWR	TRAP+6(104406) GET SOFT-SWR SETTING
	\$CKSWR ;;CALL=CKSWR	TRAP+7(104407) TEST FOR CHANGE IN SOFT-SWR
	\$RDCHR ;;CALL=RDCHR	TRAP+10(104410) TTY TYPEIN CHARACTER ROUTINE
	\$RDLIN ;;CALL=RDLIN	TRAP+11(104411) TTY TYPEIN STRING ROUTINE

.END

EVENOD=	000040	9150												
FALSE =	000000	41720	4179											
FLAG	011644	4179*	4206*	4211	4234	42460								
FRERR =	020000	8520												
FRFD =	040000	9240	1507	1574	2050	2141	2230	2410	2510					
GNS =	***** U	932	1151	4128	4134	4140	4371	4377	4384	4390	4396	5129	5130	5131
		5132	5133	5135	5137	5138	5139							
GTSMR =	104406	1146	51350											
HOLD	011072	3968*	3969*	3972	39850									
HOLDSC	011645	4191*	4196*	4202	42470									
HOMLON=	000000	41510	4177											
HT =	000011	7090	4599	4640										
I	001272	10850	1205*	1226*	1229	1231	4280*	4283*	4285					
ILLMEM=	000004	8110	1200	1236										
INITFL	012354	1154*	4420	4423*	44670									
INTFLA	012040	1212*	1217	3346*	3386	3390	3412*	3428	3480*	3522	3526	3570*	3596	3600
		4361*	43640											
INTSRV	012032	1201	3358	3473	43510									
IOTVEC=	000020	8040	1097*	1098*										
LF =	000012	7100	4634	4640										
LOOP	001744	11550	4145											
MATNT =	000004	8840	1348	1361	1364	1378	1381	1395	1400	2789	2848	2914	3196	3483
		3544	3645	3729	3752	3911	4073							
MASK =	000000	42580	4272*	4288*	4290*	4295*								
MAX	006422	2942	2946	30210										
MYTYPE	012042	43670	4864											
NEW	007242	3219*	3221*	3237*	3239*	3248	3256	3258	3276	3278	33260			
NUMBER	011030	3925	39500	3975	4033									
NUMBR	011746	4274*	4275	4277*	4285	42980								
OLD	007246	3192*	3194*	3248	3256	3258	3276*	3278*	33270					
ORERR =	040000	8510	3066	3105	3133									
PARITY=	000020	9140												
PBAUDS=	004000	8740												
PBAUDO=	010000	8730												
PBAUD1=	020000	8720												
PBAUD2=	040000	8710												
PBAUD3=	100000	8700												
PBR =	000200	9170	3174											
PERR =	010000	8530												
PIRQ =	177772	7160												
PIRQVE=	000240	8100												
PRO =	000000	7330	3368	3417	3489	3576	3649	3755	3874					
PR1 =	000040	7340												
PR2 =	000100	7350												
PR3 =	000140	7360												
PR4 =	000200	7370												
PR5 =	000240	7380												
PR6 =	000300	7390												
PR7 =	00034C	7400	1202	1420	2421	2521	2624	3359	3474	3885	3891			
PS =	177776	7130	714											
PSW =	177776	7140												
PURVEC=	000024	8050	1103*	1104*	4520*	4521*	4530*	4536*	4548*	4549*				
RATES	007170	3210	3289	32940										
RBUF	001262	10810	1171*	1172*	2883	2953	3008	3062	3105	3133	3148	3207	3518	3660
		3698	3765	3802	4003	4077	4103							
RCSR	001260	10800	1169*	1518*	1521	1535*	1537	1550*	1553	1587	1600*	1603	1617*	1620

8F800 = 000400

10	1217	1260	1272	1288	1305	1324	1348	1364	1381	1400	1430	1446
1463	1482	1507	1521	1537	1553	1574	1587	1603	1620	1639	1663	1679
1696	1715	1739	1755	1772	1791	1815	1831	1848	1867	1901	1934	1966
1996	2011	2050	2075	2095	2115	2141	2164	2184	2204	2230	2254	2274
2294	2319	2342	2362	2382	2410	2443	2461	2470	2487	2510	2544	2568
2591	2614	2651	2673	2697	2749	2829	2886	2906	2928	2946	2967	2971
2974	2994	3011	3066	3084	3105	3133	3148	3174	3183	3224	3227	3248
3174	3183	3223	3224	3227	3248	3256	3258	3262	3271	3273	3386	3390
3596	3600	3713	3742	3813	3847	3933	3975	4012	4016	4033	4064	4095
4103	4188	4202	4234	4417	4420							

8F81F = 000110

10	1217	1223	1260	1266	1272	1278	1288	1294	1305	1311	1324	1330
1348	1354	1364	1370	1381	1387	1400	1406	1430	1436	1446	1452	1463
1469	1482	1488	1507	1513	1521	1527	1537	1543	1553	1559	1574	1580
1587	1593	1603	1609	1620	1626	1639	1645	1663	1669	1679	1685	1696
1702	1715	1721	1739	1745	1755	1761	1772	1778	1791	1797	1815	1821
1831	1837	1848	1854	1867	1873	1901	1912	1934	1945	1966	1979	1996
2003	2011	2026	2050	2059	2075	2083	2095	2103	2115	2123	2141	2148
2164	2172	2184	2192	2204	2212	2230	2238	2254	2262	2274	2282	2294
2302	2319	2326	2342	2350	2362	2370	2382	2390	2410	2418	2443	2449
2461	2467	2470	2476	2487	2493	2510	2518	2544	2550	2568	2574	2591
2597	2614	2621	2651	2657	2673	2679	2697	2703	2728	2733	2749	2755
2769	2774	2812	2817	2829	2835	2870	2875	2886	2892	2906	2912	2928
2933	2938	2946	2958	2971	2974	2985	2987	2994	3001	3011	3017	3066
3077	3084	3097	3105	3115	3133	3145	3148	3155	3174	3180	3183	3189
3227	3233	3241	3248	3252	3256	3258	3262	3271	3273	3386	3390	3396
3403	3405	3428	3434	3522	3526	3533	3539	3541	3560	3567	3596	3600
3607	3613	3615	3713	3722	3742	3749	3813	3822	3847	3853	3933	3939
3975	3981	4012	4016	4024	4029	4033	4040	4064	4070	4095	4100	4103
4109	4188	4193	4198	4202	4208	4234	4239	4417	4420	4425	4434	4444
4455												

8F81NC = 000210

10	3199	3281	3664	3724	3768	3825	4280	4292	4320	4328	4336	4339
----	------	------	------	------	------	------	------	------	------	------	------	------

8F81LO = 000200

10	4185	4211	4226									
----	------	------	------	--	--	--	--	--	--	--	--	--

8F81NH = 000160

10												
----	--	--	--	--	--	--	--	--	--	--	--	--

8F81ND = 000403

10	1217	1260	1272	1288	1305	1324	1348	1364	1381	1400	1430	1446
1463	1482	1507	1521	1537	1553	1574	1587	1603	1620	1639	1663	1679
1696	1715	1739	1755	1772	1791	1815	1831	1848	1867	1901	1934	1966
1996	2011	2050	2075	2095	2115	2141	2164	2184	2204	2230	2254	2274
2294	2319	2342	2362	2382	2410	2443	2461	2470	2487	2510	2544	2568
2591	2614	2651	2673	2697	2749	2829	2886	2906	2928	2946	2967	2971
2974	2994	3011	3066	3084	3105	3133	3148	3174	3183	3224	3227	3248
3256	3258	3386	3390	3428	3522	3526	3560	3596	3600	3713	3742	3813
3847	3933	3975	4012	4016	4033	4064	4188	4417	4420			

8F81OR = 000320

10	1219	1262	1274	1290	1307	1326	1350	1366	1383	1402	1432	1448
1465	1484	1509	1523	1539	1555	1576	1589	1605	1622	1641	1665	1681
1698	1717	1741	1757	1774	1793	1817	1833	1850	1869	1903	1936	1968
1998	2013	2052	2077	2097	2117	2143	2166	2186	2206	2232	2256	2276
2296	2321	2344	2364	2384	2412	2445	2463	2472	2489	2512	2546	2570
2593	2616	2653	2675	2699	2751	2831	2888	2908	2930	2948	2969	2973
2976	2996	3013	3068	3086	3107	3135	3150	3176	3185	3226	3229	3250
3258	3260	3388	3392	3430	3524	3528	3562	3598	3602	3715	3744	3815
3849	3935	3977	4014	4018	4035	4066	4105	4190	4204	4236	4419	4422
10	4151	4250	4258	4300	4307	4345	4368	4401	4406	4472		

8F81RTN = 000300

10												
----	--	--	--	--	--	--	--	--	--	--	--	--

8F81SEL = 000140

10												
----	--	--	--	--	--	--	--	--	--	--	--	--

8F81UNT = 000130

10	1207	1231	2926	2940	3923	3925	4415	4457				
----	------	------	------	------	------	------	------	------	--	--	--	--

8F81UMI = 000120

10	2966	2967	2989	3223	3224	3244	3258					
----	------	------	------	------	------	------	------	--	--	--	--	--

8F81YES = 000402

10	1217	1260	1272	1288	1305	1324	1348	1364	1381	1400	1430	1446
----	------	------	------	------	------	------	------	------	------	------	------	------

#LSTCN= 177777

1736*	1750*	1766*	1783*	1812*	1826*	1842*	1859*	1899*	1931*	1964*	2008*	2068*
2088*	2108*	2157*	2177*	2197*	2247*	2267*	2287*	2335*	2355*	2375*	2428*	2455*
2481*	2528*	2555*	2579*	2639*	2662*	2705*	2737*	2792*	2822*	2850*	3035*	3082*
3101*	3119*	3362*	3409*	3478*	3558*	4880	4930	4947*	4953			
10	1207	1208	1217	1219	1223	1231	1260	1262	1266	1272	1274	1278
1288	1290	1294	1305	1307	1311	1324	1326	1330	1348	1350	1354	1364
1366	1370	1381	1383	1387	1400	1402	1406	1430	1432	1436	1446	1448
1452	1463	1465	1469	1482	1484	1488	1507	1509	1513	1521	1523	1527
1537	1539	1543	1553	1555	1559	1574	1576	1580	1587	1589	1593	1603
1605	1609	1620	1622	1626	1639	1641	1645	1663	1665	1669	1679	1681
1685	1696	1698	1702	1715	1717	1721	1739	1741	1745	1755	1757	1761
1772	1774	1778	1791	1793	1797	1815	1817	1821	1831	1833	1837	1848
1850	1854	1867	1869	1873	1901	1903	1912	1934	1936	1945	1966	1968
1979	1996	1998	2003	2011	2013	2026	2050	2052	2059	2075	2077	2083
2095	2097	2103	2115	2117	2123	2141	2143	2148	2164	2166	2172	2184
2186	2192	2204	2206	2212	2230	2232	2238	2254	2256	2262	2274	2276
2282	2294	2296	2302	2319	2321	2326	2342	2344	2350	2362	2364	2370
2382	2384	2390	2410	2412	2418	2443	2445	2449	2461	2463	2467	2470
2472	2476	2487	2489	2493	2510	2512	2518	2544	2546	2550	2568	2570
2574	2591	2593	2597	2614	2616	2621	2651	2653	2657	2673	2675	2679
2697	2699	2703	2728	2729	2733	2749	2751	2755	2769	2770	2774	2812
2813	2817	2829	2831	2835	2870	2871	2875	2886	2888	2892	2906	2908
2912	2926	2927	2928	2930	2934	2935	2938	2940	2945	2946	2948	2958
2966	2967	2969	2971	2973	2974	2976	2985	2987	2989	2990	2994	2996
3001	3011	3013	3017	3066	3068	3077	3084	3086	3097	3105	3107	3115
3133	3135	3145	3148	3150	3155	3174	3176	3180	3183	3185	3189	3199
3201	3202	3203	3206	3223	3224	3226	3227	3229	3234	3235	3241	3244
3245	3248	3250	3253	3254	3256	3260	3263	3264	3271	3273	3281	3282
3386	3388	3390	3392	3397	3398	3403	3405	3428	3430	3434	3522	3524
3526	3528	3534	3535	3539	3541	3560	3562	3567	3596	3598	3600	3602
3608	3609	3613	3615	3664	3666	3667	3668	3671	3713	3715	3722	3724
3725	3742	3744	3749	3768	3770	3771	3772	3775	3813	3815	3822	3825
3826	3847	3849	3853	3923	3924	3925	3930	3933	3935	3939	3975	3977
3981	4012	4014	4016	4018	4024	4029	4033	4035	4040	4064	4066	4070
4095	4096	4100	4103	4105	4109	4151	4185	4186	4188	4190	4194	4195
4198	4202	4204	4208	4226	4227	4234	4236	4239	4250	4258	4280	4282
4283	4284	4287	4292	4293	4300	4307	4320	4322	4323	4324	4327	4328
4330	4331	4332	4335	4336	4337	4339	4340	4345	4368	4401	4406	4415
4416	4417	4419	4420	4422	4426	4427	4434	4445	4446	4455	4457	4472
10	1154	1155	1157	1158	1161	1162	1164	1165	1166	1167	1169	1170
1171	1172	1173	1174	1175	1176	1177	1178	1179	1180	1181	1182	1183
1184	1196	1197	1199	1200	1201	1202	1203	1204	1205	1206	1209	1210
1212	1213	1217	1218	1219	1226	1227	1228	1229	1230	1231	1232	1233
1236	1237	1238	1239	1240	1241	1260	1261	1262	1263	1264	1269	1270
1272	1273	1274	1283	1284	1285	1286	1288	1289	1290	1299	1300	1302
1303	1305	1306	1307	1316	1317	1319	1320	1324	1325	1326	1345	1346
1348	1349	1350	1359	1360	1361	1362	1364	1365	1366	1375	1376	1378
1379	1381	1382	1383	1392	1393	1395	1396	1400	1401	1402	1427	1428
1430	1431	1432	1441	1442	1443	1444	1446	1447	1448	1457	1458	1460
1461	1463	1464	1465	1474	1475	1477	1478	1482	1483	1484	1507	1508
1509	1510	1511	1516	1517	1518	1519	1521	1522	1523	1532	1533	1535
1536	1537	1538	1539	1548	1549	1550	1551	1553	1554	1555	1574	1575
1576	1577	1578	1584	1585	1587	1588	1589	1598	1599	1600	1601	1603
1604	1605	1614	1615	1617	1618	1620	1621	1622	1631	1632	1634	1635
1639	1640	1641	1660	1661	1663	1664	1665	1674	1675	1676	1677	1679
1680	1681	1690	1691	1693	1694	1696	1697	1698	1706	1707	1710	1711

#LSTIN= 000000

1715	1716	1717	1736	1737	1739	1740	1741	1750	1751	1752	1753	1755
1756	1757	1766	1767	1769	1770	1772	1773	1774	1783	1784	1786	1787
1791	1792	1793	1812	1813	1815	1816	1817	1826	1827	1828	1829	1831
1832	1833	1842	1843	1845	1846	1848	1849	1850	1859	1860	1862	1863
1867	1868	1869	1899	1900	1901	1902	1903	1931	1932	1934	1935	1936
1964	1965	1966	1967	1968	1996	1997	1998	2000	2001	2008	2009	2011
2012	2013	2050	2051	2052	2056	2057	2068	2069	2072	2073	2075	2076
2077	2088	2089	2092	2093	2095	2096	2097	2108	2109	2112	2113	2115
2116	2117	2141	2142	2143	2145	2146	2157	2158	2161	2162	2164	2165
2166	2177	2178	2181	2182	2184	2185	2186	2197	2198	2201	2202	2204
2205	2206	2230	2231	2232	2235	2236	2247	2248	2251	2252	2254	2255
2256	2267	2268	2271	2272	2274	2275	2276	2287	2288	2291	2292	2294
2295	2296	2319	2320	2321	2323	2324	2335	2336	2339	2340	2342	2343
2344	2355	2356	2359	2360	2362	2363	2364	2375	2376	2379	2380	2382
2383	2384	2410	2411	2412	2415	2416	2428	2429	2431	2432	2434	2435
2436	2437	2438	2440	2441	2443	2444	2445	2455	2456	2459	2460	2461
2462	2463	2470	2471	2472	2481	2482	2485	2486	2487	2488	2489	2510
2511	2512	2515	2516	2528	2529	2532	2533	2535	2536	2537	2538	2539
2541	2542	2544	2545	2546	2555	2556	2560	2561	2563	2564	2565	2566
2567	2568	2569	2570	2579	2580	2583	2584	2586	2587	2588	2589	2590
2591	2592	2593	2614	2615	2616	2618	2619	2633	2634	2635	2636	2639
2640	2643	2644	2646	2647	2648	2649	2650	2651	2652	2653	2662	2663
2665	2666	2668	2669	2670	2671	2672	2673	2674	2675	2697	2698	2699
2700	2701	2705	2706	2713	2714	2718	2719	2720	2721	2722	2723	2724
2725	2728	2729	2737	2738	2744	2745	2749	2750	2751	2761	2762	2763
2764	2765	2766	2767	2768	2769	2770	2789	2790	2792	2793	2796	2797
2802	2803	2804	2805	2806	2807	2808	2809	2812	2813	2822	2823	2829
2830	2831	2848	2849	2850	2851	2856	2857	2861	2862	2863	2864	2865
2866	2867	2868	2870	2871	2883	2884	2886	2887	2888	2906	2907	2908
2909	2910	2914	2915	2916	2917	2918	2919	2924	2925	2928	2929	2930
2931	2932	2933	2934	2936	2937	2940	2941	2942	2943	2944	2946	2947
2948	2953	2954	2955	2956	2967	2968	2969	2971	2972	2973	2974	2975
2976	2982	2983	2989	2990	2994	2995	2996	3008	3009	3011	3012	3013
3035	3036	3042	3043	3045	3046	3047	3048	3049	3052	3053	3055	3056
3057	3058	3059	3062	3063	3066	3067	3068	3074	3075	3082	3083	3084
3085	3086	3094	3095	3101	3102	3105	3106	3107	3119	3120	3124	3125
3127	3128	3129	3130	3131	3133	3134	3135	3142	3143	3148	3149	3150
3174	3175	3176	3177	3178	3183	3184	3185	3186	3187	3192	3193	3194
3195	3196	3197	3199	3200	3201	3202	3203	3204	3205	3206	3207	3208
3210	3211	3213	3214	3216	3217	3219	3220	3221	3222	3224	3225	3226
3227	3228	3229	3231	3232	3233	3234	3237	3238	3239	3240	3244	3245
3248	3249	3250	3252	3253	3256	3257	3258	3259	3260	3262	3263	3276
3277	3278	3279	3281	3282	3284	3285	3286	3287	3288	3289	3290	3346
3347	3350	3351	3353	3354	3356	3357	3358	3359	3360	3361	3362	3363
3365	3366	3375	3376	3379	3380	3381	3382	3383	3386	3387	3388	3390
3391	3392	3396	3397	3409	3410	3412	3413	3415	3416	3423	3424	3425
3426	3427	3428	3429	3430	3440	3441	3444	3445	3446	3447	3448	3449
3471	3472	3473	3474	3475	3476	3478	3479	3480	3481	3483	3484	3486
3487	3496	3497	3501	3502	3503	3504	3505	3506	3507	3508	3510	3511
3513	3514	3515	3516	3517	3518	3519	3522	3523	3524	3526	3527	3528
3533	3534	3544	3545	3547	3548	3558	3559	3560	3561	3562	3564	3565
3570	3571	3573	3574	3581	3582	3584	3585	3586	3587	3589	3590	3591
3592	3593	3596	3597	3598	3600	3601	3602	3607	3608	3617	3618	3619
3620	3623	3624	3627	3628	3629	3630	3631	3632	3645	3646	3655	3656
3657	3658	3660	3661	3664	3665	3666	3667	3668	3669	3670	3671	3676
3677	3678	3679	3680	3681	3682	3683	3686	3687	3689	3690	3691	3692

CROSS REFERENCE TABLE -- USER SYMBOLS

3693	3694	3695	3696	3698	3699	3706	3707	3708	3709	3710	3713	3714
3715	3719	3720	3724	3725	3729	3730	3742	3743	3744	3746	3747	3752
3753	3761	3762	3763	3764	3765	3766	3768	3769	3770	3771	3772	3773
3774	3775	3780	3781	3782	3783	3784	3785	3786	3787	3790	3791	3792
3793	3794	3795	3796	3797	3798	3799	3802	3803	3806	3807	3808	3809
3810	3813	3814	3815	3819	3820	3825	3826	3847	3848	3849	3850	3851
3856	3857	3858	3859	3880	3881	3883	3884	3885	3886	3889	3890	3891
3892	3895	3896	3898	3899	3901	3902	3905	3906	3911	3912	3915	3916
3918	3919	3925	3926	3927	3928	3929	3933	3934	3935	3942	3943	3944
3945	3965	3966	3968	3969	3970	3972	3973	3975	3976	3977	3979	3980
4000	4001	4003	4004	4005	4007	4008	4009	4012	4013	4014	4016	4017
4018	4020	4021	4022	4023	4027	4028	4033	4034	4035	4037	4038	4064
4065	4066	4067	4068	4073	4074	4077	4078	4082	4083	4085	4086	4087
4088	4089	4090	4091	4092	4093	4094	4095	4096	4103	4104	4105	4175
4176	4177	4178	4179	4180	4188	4189	4190	4191	4192	4193	4194	4196
4197	4202	4203	4204	4206	4207	4211	4212	4213	4214	4215	4219	4220
4221	4222	4223	4224	4225	4226	4227	4234	4235	4236	4237	4238	4241
4242	4243	4251	4252	4253	4254	4272	4273	4274	4275	4276	4277	4278
4280	4281	4282	4283	4284	4285	4286	4287	4288	4289	4290	4291	4292
4293	4295	4296	4297	4298	4302	4303	4318	4319	4320	4321	4322	4323
4324	4325	4326	4327	4328	4329	4330	4331	4332	4333	4334	4335	4336
4337	4339	4340	4347	4348	4361	4362	4403	4404	4417	4418	4419	4420
4421	4422	4423	4424	4425	4426	4428	4429	4432	4435	4436	4437	4438
4439	4440	4441	4442	4443	4444	4445	4447	4448	4449	4450	4451	4452
4453	4454	4457	4458	4459	4461	4462	4463	4464	4465	4466	4474	4475
10	1207	1208	1217	1219	1223	1231	1260	1262	1266	1272	1274	1278
1288	1290	1294	1305	1307	1311	1324	1326	1330	1348	1350	1354	1364
1366	1370	1381	1383	1387	1400	1402	1406	1430	1432	1436	1446	1448
1452	1463	1465	1469	1482	1484	1488	1507	1509	1513	1521	1523	1527
1537	1539	1543	1553	1555	1559	1574	1576	1580	1587	1589	1593	1603
1605	1609	1620	1622	1626	1639	1641	1645	1663	1665	1669	1679	1681
1685	1696	1698	1702	1715	1717	1721	1739	1741	1745	1755	1757	1761
1772	1774	1778	1791	1793	1797	1815	1817	1821	1831	1833	1837	1848
1850	1854	1867	1869	1873	1901	1903	1912	1934	1936	1945	1966	1968
1979	1996	1998	2003	2011	2013	2026	2050	2052	2059	2075	2077	2083
2095	2097	2103	2115	2117	2123	2141	2143	2148	2164	2166	2172	2184
2186	2192	2204	2206	2212	2230	2232	2238	2254	2256	2262	2274	2276
2282	2294	2296	2302	2319	2321	2326	2342	2344	2350	2362	2364	2370
2382	2384	2390	2410	2412	2418	2443	2445	2449	2461	2463	2467	2470
2472	2476	2487	2489	2493	2510	2512	2518	2544	2546	2550	2568	2570
2574	2591	2593	2597	2614	2616	2621	2651	2653	2657	2673	2675	2679
2697	2699	2703	2728	2729	2733	2749	2751	2755	2769	2770	2774	2812
2813	2817	2829	2831	2835	2870	2871	2875	2886	2888	2892	2906	2908
2912	2926	2927	2928	2930	2933	2934	2935	2938	2940	2946	2948	2958
2966	2967	2969	2971	2973	2974	2976	2985	2987	2989	2990	2994	2996
3001	3011	3013	3017	3066	3068	3077	3084	3086	3097	3105	3107	3115
3133	3135	3145	3148	3150	3155	3174	3176	3180	3183	3185	3189	3199
3201	3202	3203	3206	3223	3224	3226	3227	3229	3233	3234	3235	3241
3244	3245	3248	3250	3252	3253	3254	3256	3260	3262	3263	3264	3271
3273	3281	3282	3386	3388	3390	3392	3396	3397	3398	3403	3405	3428
3430	3434	3522	3524	3526	3528	3533	3534	3535	3539	3541	3560	3562
3567	3596	3598	3600	3602	3607	3608	3609	3613	3615	3664	3666	3667
3668	3671	3713	3715	3722	3724	3725	3742	3744	3749	3768	3770	3771
3772	3775	3813	3815	3822	3825	3826	3847	3849	3853	3923	3924	3925
3933	3935	3939	3975	3977	3981	4012	4014	4016	4018	4024	4029	4033
4035	4040	4064	4066	4070	4095	4096	4100	4103	4105	4109	4151	4185

9LSTST= 177777

#MXCNT 014652
#NESTL 177777

4943	4953	1217	1223	1231	1260	1266	1272	1278	1288	1294	1305	1311		
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1452	1463	1469	1482	1488	1507	1513	1521	1527	1537	1543	1553	1559	1679	1797
1574	1580	1587	1593	1603	1609	1620	1626	1639	1645	1663	1669	1679	1791	1966
1685	1696	1702	1715	1721	1739	1745	1755	1761	1772	1778	1791	1966	2123	2274
1815	1821	1831	1837	1848	1854	1867	1873	1901	1912	1934	1945	1966	2115	2262
1979	1996	2003	2011	2026	2050	2059	2075	2083	2095	2103	2115	2123	2150	2274
2141	2148	2164	2172	2184	2192	2204	2212	2230	2238	2254	2262	2274	2302	2418
2282	2294	2302	2319	2326	2342	2350	2362	2370	2382	2390	2410	2418	2500	2568
2443	2449	2461	2467	2470	2476	2487	2493	2510	2518	2544	2550	2568	2728	2733
2574	2591	2597	2614	2621	2651	2657	2673	2679	2697	2703	2728	2733	2906	2940
2749	2755	2769	2774	2812	2817	2829	2835	2870	2875	2886	2892	2906	2912	2926
2912	2926	2928	2933	2938	2940	2946	2958	2966	2971	2974	2985	2987	3097	3105
2989	2994	3001	3011	3017	3066	3077	3084	3097	3105	3115	3133	3145	3227	3233
3148	3155	3174	3180	3183	3189	3199	3223	3227	3233	3241	3244	3248	3390	3405
3252	3256	3262	3271	3273	3281	3386	3390	3396	3403	3405	3428	3434	3522	3533
3522	3526	3533	3539	3541	3560	3567	3596	3600	3607	3613	3615	3664	3724	3742
3713	3722	3724	3742	3749	3768	3813	3822	3825	3847	3853	3923	3925	4010	4016
3933	3939	3975	3981	4012	4016	4024	4029	4033	4040	4064	4070	4095	4100	4103
4100	4103	4109	4151	4185	4188	4193	4198	4202	4208	4211	4226	4234	4328	4336
4239	4250	4258	4280	4292	4300	4307	4320	4328	4336	4339	4345	4368	4401	4406
4401	4406	4415	4417	4420	4425	4434	4444	4455	4457	4472	4472	4472	4472	4472

#NSKO = 000300

1207	1231	1260	1266	1272	1278	1288	1294	1305	1311	1324	1330	1348
1354	1364	1370	1381	1387	1400	1406	1430	1436	1446	1452	1463	1469
1482	1488	1507	1513	1521	1527	1537	1543	1553	1559	1574	1580	1587
1593	1603	1609	1620	1626	1639	1645	1663	1669	1679	1685	1696	1702
1715	1721	1739	1745	1755	1761	1772	1778	1791	1797	1815	1821	1831
1837	1848	1854	1867	1873	1901	1912	1934	1945	1966	1979	1996	2003
2011	2026	2050	2059	2075	2083	2095	2103	2115	2123	2141	2148	2164
2172	2184	2192	2204	2212	2230	2238	2254	2262	2274	2282	2294	2302
2319	2326	2342	2350	2362	2370	2382	2390	2410	2418	2443	2449	2461
2467	2470	2476	2487	2493	2510	2518	2544	2550	2568	2574	2591	2597
2614	2621	2651	2657	2673	2679	2697	2703	2728	2733	2749	2755	2769
2774	2812	2817	2829	2835	2870	2875	2886	2892	2906	2912	2926	2940
2946	2958	2966	2989	2994	3001	3011	3017	3066	3077	3084	3097	3105
3115	3133	3145	3148	3155	3174	3180	3183	3189	3199	3281	3386	3405
3428	3434	3522	3541	3560	3567	3596	3615	3664	3724	3742	3749	3768
3825	3847	3853	3923	3925	3933	3939	3975	3981	4010	4029	4033	4040
4064	4070	4095	4100	4103	4109	4151	4250	4258	4300	4307	4345	4368
4401	4406	4472	4472	4472	4472	4472	4472	4472	4472	4472	4472	4472

#NSK1 = 000130

1217	1223	2928	2933	2938	2971	2987	3223	3244	3248	3252	3273	3390
3396	3403	3526	3533	3539	3600	3607	3613	3713	3722	3813	3822	4016
4024	4185	4211	4226	4234	4239	4280	4292	4320	4339	4415	4457	4457
2974	2985	3227	3233	3241	3256	3262	3271	4188	4193	4198	4202	4208
4328	4336	4417	4444	4455	4455	4455	4455	4455	4455	4455	4455	4455

#NSK2 = 000110

2974	2985	3227	3233	3241	3256	3262	3271	4188	4193	4198	4202	4208
4328	4336	4417	4444	4455	4455	4455	4455	4455	4455	4455	4455	4455

#NSK3 = 000110

4420	4425	4434	4434	4434	4434	4434	4434	4434	4434	4434	4434	4434
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#NULL 001:54

1003	4611	4640	4640	4640	4640	4640	4640	4640	4640	4640	4640	4640
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#MWTST 000001

1186	1188	1250	1252	1336	1413	1495	1497	1566	1568	1652	1728	1804
1887	1920	1953	1987	2040	2042	2131	2133	2220	2222	2310	2312	2398
2400	2500	2502	2606	2686	2688	2778	2780	2839	2895	2897	3027	3164
3166	3333	3335	3459	3461	3636	3638	3735	3836	3838	4055	4057	4121

#OCNT 015322

5053	5082	5095	5095	5095	5095	5095	5095	5095	5095	5095	5095	5095
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#OMODE 015324

5048	5052	5057	5060	5071	5097	5097	5097	5097	5097	5097	5097	5097
------	------	------	------	------	------	------	------	------	------	------	------	------

#OVER 014636

4907	4923	4931	4941	4950	4950	4950	4950	4950	4950	4950	4950	4950
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#PASS 001202

1022	1127	4490	4491	4499	4512	4937	4954	4954	4954	4954	4954	4954
------	------	------	------	------	------	------	------	------	------	------	------	------

\$PASTM	001006	967#												
\$POWER	012660	4551	4558#											
\$PWIRAD	012646	4553#												
\$PWIRDN	012506	1103	4520#	4548										
\$PWIRMG	012642	4551#												
\$PWIRUP	012560	4530	4536#											
\$QUES	001170	1010#	4640	4708	4757	4773	4890							
\$RDCHR	013434	4721#	5138											
\$RDDEC-	***** U	5140												
\$RDLIN	013554	4749#	5139											
\$RDOCT-	***** U	5140												
\$RDSZ	- 000010	4742#												
\$RTNAD	012464	4511#												
\$R2A	- ***** U	5140												
\$SAVLE-	177777	1#	2989#	2990#	3202#	3206#	3244#	3245#	3667#	3671#	3771#	3775#	4226#	4227#
		4283#	4287#	4323#	4327#	4331#	4335#							
\$SAVRE-	***** U	5140												
\$SAVR6	012656	4529#	4537	4538#	4539#	4557#								
\$SCOPE	014374	1097	4904#											
\$SETUP-	000137	1088#	1096	1097	1099	1101	1103	1105	1106	1107	1109	1136	1139	4488
		4645	4779	4851	4877	4885	4905							
\$SSKO	- 000236	2989#	2990	3202#	3206	3244#	3245	3667#	3671	3771#	3775	4226#	4227	4283#
		4287	4323#	4327	4331#	4335								
\$STUP	- 177777	1088#												
\$SVLAD	014602	4915	4944#											
\$SVPC	- 000204	941#	946											
\$SMR	- 167400	1#	689	693	694	695	696	697	698	699	1007	1008	1009	1106
		1107	1109	1110	1193	1256	1340	1417	1504	1571	1656	1732	1808	1891
		1924	1957	1991	2046	2137	2226	2315	2406	2506	2610	2692	2784	2843
		2901	3031	3171	3342	3466	3641	3739	3841	4061	4125	4483	4489	4504
		4510	4512	4554	4842	4843	4844	4845	4846	4855	4862	4874	4878	4890
		4896	4897	4898	4899	4900	4906	4918	4920	4921	4924	4925	4926	4933
		4934	4935	4947	4950	4953								
\$SMREG	001216	1030#	1130											
\$SMRPK-	000000	699	700	4900	4901	4922								
\$TAGLE-	177777	1#	1208#	1219#	1223#	1231#	1262#	1266#	1274#	1278#	1290#	1294#	1307#	1311#
		1326#	1330#	1350#	1354#	1366#	1370#	1383#	1387#	1402#	1406#	1432#	1436#	1448#
		1452#	1465#	1469#	1484#	1488#	1509#	1513#	1523#	1527#	1539#	1543#	1555#	1559#
		1576#	1580#	1589#	1593#	1605#	1609#	1622#	1626#	1641#	1645#	1665#	1669#	1681#
		1685#	1698#	1702#	1717#	1721#	1741#	1745#	1757#	1761#	1774#	1778#	1793#	1797#
		1817#	1821#	1833#	1837#	1850#	1854#	1869#	1873#	1903#	1912#	1936#	1945#	1968#
		1979#	1998#	2003#	2013#	2026#	2052#	2059#	2077#	2083#	2097#	2103#	2117#	2123#
		2143#	2148#	2166#	2172#	2186#	2192#	2206#	2212#	2232#	2238#	2256#	2262#	2276#
		2282#	2296#	2302#	2321#	2326#	2344#	2350#	2364#	2370#	2384#	2390#	2412#	2418#
		2445#	2449#	2463#	2467#	2472#	2476#	2489#	2493#	2512#	2518#	2546#	2550#	2570#
		2574#	2593#	2597#	2# 16#	2621#	2653#	2657#	2675#	2679#	2699#	2703#	2729#	2733#
		2751#	2755#	2770#	2774#	2813#	2817#	2831#	2835#	2871#	2875#	2888#	2892#	2908#
		2912#	2927#	2930#	2934#	2935#	2938#	2940#	2948#	2958#	2967#	2969#	2973#	2976#
		2985#	2987#	2989#	2996#	3001#	3013#	3017#	3068#	3077#	3086#	3097#	3107#	3115#
		3135#	3145#	3150#	3155#	3176#	3180#	3185#	3189#	3201#	3203#	3206#	3224#	3226#
		3229#	3234#	3235#	3241#	3244#	3250#	3253#	3254#	3260#	3263#	3264#	3271#	3273#
		3281#	3282#	3388#	3392#	3397#	3398#	3403#	3405#	3430#	3434#	3524#	3528#	3534#
		3535#	3539#	3541#	3562#	3567#	3598#	3602#	3608#	3609#	3613#	3615#	3666#	3668#
		3671#	3715#	3722#	3724#	3725#	3744#	3749#	3770#	3772#	3775#	3815#	3822#	3825#
		3826#	3849#	3853#	3924#	3925#	3935#	3939#	3977#	3981#	4014#	4018#	4024#	4029#
		4035#	4040#	4066#	4070#	4096#	4100#	4105#	4109#	4186#	4190#	4194#	4195#	4198#

STAGNU- 000251

42040	42080	4211	42260	42360	42390	42820	42840	42870	42920	42930	43220	43240
43270	43300	43320	43350	43360	43370	43390	43400	44160	44190	44220	44260	44270
44340	44450	44460	44550	44570								
10	1207	12080	1218	12190	1261	12620	1273	12740	1289	12900	1306	13070
1325	13260	1349	13500	1365	13660	1382	13830	1401	14020	1431	14320	1447
14480	1464	14650	1483	14840	1508	15090	1522	15230	1538	15390	1554	15550
1575	15760	1588	15890	1604	16050	1621	16220	1640	16410	1664	16650	1680
16810	1697	16980	1716	17170	1740	17410	1756	17570	1773	17740	1792	17930
1816	18170	1832	18330	1849	18500	1868	18690	1902	19030	1935	19360	1967
19680	1997	19980	2012	20130	2051	20520	2076	20770	2096	20970	2116	21170
2142	21430	2165	21660	2185	21860	2205	22060	2231	22320	2255	22560	2275
22760	2295	22960	2320	23210	2343	23440	2363	23640	2383	23840	2411	24120
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STEMP - 000300

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CLRVEC	10	1233	3441	3624											
COMMEN	10	8110													
DECR	10														
DECRU	10														
DEFAULT	10														
DEVREG	10														
DEVTYP	10														
DISPAT	10														
ELSE	10	2932	3232	3251	3261	3395	3532	3606	4192	4424	4443				
END	10														
ENDCLN	10														
ENDCOM	10	8110													
ENDDEC	10														
ENDDO	10	2988	3243												
ENDHRD	10														
ENDHW	10														
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ENDINI	10														
ENDLOO	10	4225													
ENDMOD	10														
ENDMSG	10														
ENDRTN	10	4249	4299	4344	4400	4471									
ENDSEL	10														
ENDSFT	10														
ENDSRV	10	3987	4047	4362											
ENDSUB	10	1224	1279	1295	1312	1331	1355	1371	1388	1407	1437	1453	1470	1489	1528
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	1838	1855	1875	1915	1948	1981	2029	2084	2104	2124	2173	2193	2213	2263	2283

	2303	2351	2371	2391	2451	2477	2494	2551	2575	2598	2658	2680	2734	2775	2819
	2836	2876	3078	3098	3116	3156	3406	3437	3548	3620					
ENDSW	10														
ENDTST	10	1244	1332	1408	1490	1561	1647	1723	1799	1876	1916	1949	1982	2030	2125
	2214	2304	2392	2495	2601	2681	2776	2837	2893	3023	3160	3328	3451	3634	3730
	3830	4050	4119												
EQUALS	10														
ERRDF	10	1220													
ERRHRD	10	1275	1291	1308	1327	1351	1367	1384	1403	1433	1449	1466	1485	1524	1540
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	3400	3431	3529	3536	3603	3610	3716	3816	3936	4097	4106				
ERROR	7050	1221	1276	1292	1309	1328	1352	1368	1385	1404	1434	1450	1467	1486	1525
	1541	1557	1591	1607	1624	1643	1667	1683	1700	1719	1743	1759	1776	1795	1819
	1835	1852	1871	1907	1940	1970	2016	2079	2099	2119	2168	2188	2208	2258	2278
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ESCAPE	10	8110													
EXIF	10														
EXIFB	10	4210													
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	3849	3946	4066	4112											
GETPRI	10	8110													
GETSWR	10	8110	11390												
GPHARD	10														
GPRMA	10														
GPRMD	10														
GPRML	10														
HEADER	10														
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	1771	1790	1814	1830	1847	1866	1900	1933	1965	1995	2010	2049	2074	2094	2114
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IFB	10	4102	4201	4233											
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IF.ERR	10	2727	2768	2811	2869	4094									
IF.NO.	10														
INCR	10	3198	3663	3767	4279	4327									
INCRU	10	4319													
INLINE	10														
LASTAD	10														
LEAVE	10														
LET	10	1153	1160	1163	1165	1168	1170	1173	1176	1179	1182	1195	1199	1200	1201
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	1299	1301	1316	1318	1345	1359	1360	1375	1377	1392	1394	1427	1441	1442	1457
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	1616	1631	1633	1660	1674	1675	1690	1692	1706	1709	1736	1750	1751	1766	1768
	1783	1785	1812	1826	1827	1842	1844	1859	1861	1899	1931	1964	2000	2008	2056

	2068	2071	2088	2091	2108	2111	2145	2157	2160	2177	2180	2197	2200	2235	2247
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	3644	3659	3685	3697	3705	3708	3719	3728	3746	3751	3764	3789	3801	3805	3808
	3819	3850	3879	3882	3884	3888	3890	3894	3897	3900	3904	3910	3914	3917	3941
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	4081	4084	4174	4176	4178	4190	4195	4205	4223	4271	4273	4287	4289	4294	4317
	4360	4422	4431	4435	4437	4439	4441	4446	4448	4450	4452	4460	4462		
LOCAL	10														
LOOP	10	4184													
MSG	11860	1188	12500	1252	14950	1497	15660	1568	20400	2042	21310	2133	22200	2222	23100
	2312	23980	2400	25000	2502	26860	2688	27780	2780	28950	2897	31640	3166	33330	3335
	34590	3461	36360	3638	38360	3838	40550	4057							
MULT	10	8110													
NEWTST	10	8110	1186	1250	1336	1413	1495	1566	1652	1728	1804	1887	1920	1953	1987
	2040	2131	2220	2310	2398	2500	2606	2686	2778	2839	2895	3027	3164	3333	3459
	3636	3735	3836	4055	4121										
NOLOCA	10														
POINTE	10														
POP	10	8110	1241	3449	3632	4341	4541	4542	4825	4826	5007				
PRINTB	10														
PUSH	10	8110	1234	3442	3625	4314	4522	4528	4786	4788	4809	4966			
REPEAT	10	1206	2925	3922	4414										
REPORT	10	8110													
RETURN	10	4236	4240	4296	4464										
ROUTIN	10	4149	4256	4305	4366	4404									
SAVR14	10														
SCOPE	7060	1192	1255	1339	1416	1503	1570	1655	1731	1807	1890	1923	1956	1990	2045
	2136	2225	2314	2405	2505	2609	2691	2783	2842	2900	3030	3170	3341	3465	3640
	3738	3840	4060	4124	4487										
SELECT	10														
SETPRI	10	8110	1420	2421	2521	2624	3368	3417	3489	3576	3649	3755	3874		
SETTRA	51210	5130	5131	5132	5133	5135	5137	5138	5139						
SETUP	10	8110	1088												
SETVEC	10	1198	3355	3470											
SKIP	10	8110	1264	1511	1578	2001	2057	2146	2236	2324	2416	2516	2600	2619	2701
	2910	2956	2983	3020	3075	3095	3113	3143	3158	3178	3187	3292	3565	3720	3747
	3820	3851	3947	4068	4113										
SLASH	10	8110													
SPACE	8110														
STARS	10	8110	827	829	847	849	867	869	889	891	910	925	939	950	952
	959	972	1013	1016	1186	1191	1245	1247	1250	1254	1335	1336	1338	1412	1413
	1415	1494	1495	1502	1565	1566	1569	1651	1652	1654	1727	1728	1730	1803	1804
	1806	1881	1884	1887	1889	1920	1922	1953	1955	1986	1987	1989	2032	2036	2040
	2044	2130	2131	2135	2219	2220	2224	2309	2310	2313	2397	2398	2404	2499	2500
	2504	2605	2606	2608	2685	2686	2690	2777	2778	2782	2838	2839	2841	2894	2895
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	3639	3734	3735	3737	3835	3836	3839	4054	4055	4059	4121	4123	4148	4169	4255
	4269	4304	4313	4350	4357	4368	4406	4413	4479	4518	4534	4563	4642	4645	4713

	4742	4781	4838	4892	4956	5023	5100								
STRUCT	10														
SWRSU	10	8110	11110												
TRMTRP	51210														
TYPBIN	10	8110													
TYPOEC	10	8110	4142	4499											
TYPNAM	10	8110	1132												
TYPNUM	10	8110													
TYPOCS	10	8110													
TYPOCT	10	8110	4130	4136	4373	4380	4386	4392	4398	4663					
TYPTXT	10	8110	4126	4132	4138	4369	4375	4382	4388	4394					
UNTIL	10	1230	2939	3924	4456										
UNTILB	10														
WAITMS	10	2433	2534	2562	2585	2645	2667	3044	3054	3126	3378	3422	3512	3588	4218
WHILE	10	2965	3222												
WHILEB	10														
%ADON	10	1207	1208	1217	1219	1260	1262	1272	1274	1288	1290	1305	1307	1324	1326
	1348	1350	1364	1366	1381	1383	1400	1402	1430	1432	1446	1448	1463	1465	1482
	1484	1507	1509	1521	1523	1537	1539	1553	1555	1574	1576	1587	1589	1603	1605
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	1757	1772	1774	1791	1793	1815	1817	1831	1833	1848	1850	1867	1869	1901	1903
	1934	1936	1966	1968	1996	1998	2011	2013	2050	2052	2075	2077	2095	2097	2115
	2117	2141	2143	2164	2166	2184	2186	2204	2206	2230	2232	2254	2256	2274	2276
	2294	2296	2319	2321	2342	2344	2362	2364	2382	2384	2410	2412	2443	2445	2461
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	3086	3105	3107	3133	3135	3148	3150	3174	3176	3183	3185	3199	3201	3202	3206
	3223	3224	3226	3227	3229	3235	3244	3248	3250	3254	3256	3260	3264	3386	3388
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	4226	4234	4236	4258	4260	4282	4283	4287	4307	4320	4322	4323	4327	4328	4330
	4331	4335	4368	4406	4415	4416	4417	4419	4420	4422	4427	4446			
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	1756	1773	1792	1816	1832	1849	1868	1902	1935	1967	1997	2012	2051	2076	2096
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	1772	1791	1815	1831	1848	1867	1901	1934	1966	1996	2011	2050	2075	2095	2115
	2141	2164	2184	2204	2230	2254	2274	2294	2319	2342	2362	2382	2410	2443	2461
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	2946	2967	2971	2974	2994	3011	3066	3084	3105	3133	3148	3174	3183	3224	3227
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\$CHK1	10	4012	4016	4033	4064	4103	4188	4202	4234	4417	4420				
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		1212	1236	1239	1240	1263	1269	1283	1299	1316	1345	1359	1375	1392	1427
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		1736	1750	1766	1783	1812	1826	1842	1859	1899	1931	1964	2000	2008	2056
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\$CKOP2	10	1171	1174	1177	1180	1226	1228	1237	1285	1302	1319	1361	1378	1395	1443
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		3617	3619	3628	3645	3706	3709	3729	3752	3806	3809	3911	3915	3918	3942
		3965	3968	3979	4000	4003	4007	4027	4037	4073	4082	4224	4274	4288	4290
		4449	4451	4453	4463										4361
\$CKR6	10	3285	4275												
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		2946	2967	2971	2974	2994	3011	3066	3084	3105	3133	3148	3174	3183	3224
		3248	3256	3258	3386	3390	3428	3522	3526	3560	3596	3600	3713	3742	3813
		3933	3975	4012	4016	4033	4064	4103	4188	4202	4234	4417	4420		3847
\$COMPA	10	1217	1260	1272	1288	1305	1324	1348	1364	1381	1400	1430	1446	1463	1482
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!OPNEG 1#
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!OPOR 1#

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\$OPSUB	10	4224													
\$OPSMB	10														
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	1761	1778	1797	1821	1837	1854	1873	1912	1945	1979	2003	2026	2059	2083	2103
	2123	2148	2172	2192	2212	2238	2262	2282	2302	2326	2350	2370	2390	2418	2449
	2467	2476	2493	2518	2550	2574	2597	2621	2657	2679	2703	2733	2755	2774	2817
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	4029	4040	4070	4100	4109	4194	4198	4208	4226	4227	4239	4250	4284	4287	4292
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\$UNTL2	10	2940	3925												
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##CMTM	9700														
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##ENDS	10														
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	2123	2148	2172	2192	2212	2238	2262	2282	2302	2326	2350	2370	2390	2418	2449
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. \$SUPR 10
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. \$TYPB 10
. \$TYPD 10 4954
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. \$TYPD 10 5021
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. 1170 10

. ABS. 015406 000

ERRORS DETECTED: 0

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RUN-TIME: 84 90 6 SECONDS

RUN-TIME RATIO: 287/181=1.5

CORE USED: 43K (85 PAGES)