

IDENTIFICATION

B 1

SEQ 0001

PRODUCT CODE: AC-8'94C-MC
PROJECT NAME: CVKACCO LSI-11 FIS INST
DATE CREATED: AUGUST 22, 1975
MAINTAINER: DIAGNOSTIC GROUP

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1.0 ABSTRACT

This program tests the LSI-11 floating instruction set <FADD, FSUB, FMUL, and FDIV> option with fixed number patterns, using each register at least once as the stack pointer. It also checks stack overflow and that the floating instructions can be interrupted (by the console teletype). [However, this test will not be executed when bit 5 of \$ENVM byte is high]. The program should be run for at least 2 passes with all switches low. The program is designed to run under APT, and ACT, systems. When running under APT, with bit 5 of \$ENVM low it will be required to have a SLU with TTY registers having addresses of 176560-66 and interrupt vectors of 70 for receiver and 74 for transmitter. Under such a condition it will also be required to change the run time of first pass from 5 seconds to the time given in Sec. 8.1, and the run time for the longest test from 3 seconds to 30 seconds.

2.0 REQUIREMENTS

LTC SWITCH MUST BE IN OFF POSITION TO RUN DIAGNOSTIC.

2.1 Equipment

LSI-11 standard computer with FIS option and 4K of memory.

2.2 Storage

Program Storage - The routines use memory 0 - 17500.

2.3 Preliminary Programs

None.

3.0 LOADING PROCEDURE

Use standard procedure for ABS Tapes.

4.0 STARTING PROCEDURE

4.1 Control Switch Settings

See 5.1.1 (all low for worst case testing).

4.2 Starting Address

After loading the program it should always be started at 200. If it is desired to save the pass counter then the program should be restarted at location RESTART (i.e. 222) otherwise the program can be restarted at 200.

4.3 Program And/or Operator Action

4.3.1 Stand Alone -

1. Load program into memory using ABS loader.
2. Set switches (see Sec 5.1.1) all low except bit 7 for worst case.
3. Type 200G.
4. The program will loop and 'END PASS' will be typed after completion of every pass. However type out will be suppressed if bit 5 of location \$ENVM is high.
5. A minimum of two passes should always be run.

4.3.2 Under Apt - Load the program, set the switches (see Sec. 5.1.1) and start. When under APT, with bit 5 of \$ENVM low it will be required to have a SLU with TTY registers having addresses of 176560-66 and interrupt vectors of 70 for receiver and 74 for transmitter. Under such a condition it will also be required to change the run time of first pass from 5 seconds to the time given in Sec. 8.1, and the run time for the longest test from 3 seconds to 30 seconds. The test times and pass times are suggested with bit 7 of \$SWREG, low, if it is desired to enable the iterations then the times should be multiplied by a factor of 256.

5.0 OPERATING PROCEDURE

5.1 Operational Switch Settings

All switches low except SW<11> is worst case testing. With bit 11 of the location \$SWREG (i.e. location 422), high each subtest will be looped upon until completion of 256 passes of that subtest. 'END PASS' will be typed upon completion of a pass of the entire program. Alternate pass will run with the T-bit set.

5.1.1 Switch Settings Are - A 16 bit location called \$SWREG (i.e. location 422) has been used to give the following options by inserting a 1 in their respective positions.

BIT #	OCTAL VALUE	FUNCTION
15	100000.....	Halt On Error
14	040000.....	Scope Loop
13	020000.....	Inhibit Printout
12	010000.....	Inhibit Trace Trapping
11	004000.....	Enable Iterations Of Subtest
10	002000.....	Bell On Error
09	001000.....	Loop On Error
08	0004XX.....	Loop On Test In Bits 7 Thru 0

An 8 bit byte \$ENVM (i.e. location 421) has been used to define the operating mode. All typeouts can be suppressed by making bit 5 of byte \$ENVM high, in other words by placing a 20000 in location 420.

5.2 Subroutine Abstracts

5.2.1 Scope - This subroutine call (via a TRAP instruction) is placed between each subtest in the instruction section. It records the starting address of each subtest as it is being entered in location 'LA \$'. If a scope loop is requested, the current subtest will be looped upon. SW<11> is a 1 inhibits iteration of subtests. The contents of 'LADS' may be used to determine the last subtest successfully completed.

5.2.2 HLT - This routine (called by an EMT instruction) prints out an error message (see 6.1). If SW<9> is a 1 and a HLT is executed, the subtest will be looped upon until 256 consecutive good passes are completed. To inhibit typeouts, make SW<13> a 1. To ring the bell on an error, make SW<10> a 1. A high bit 5 in location \$ENVM will inhibit any typeouts and ringing of bells.

5.2.3 T Bit Trap - If SW<12> is a 0, the T-Bit will be set on alternate passes. When the T-Bit is set, the processor traps after each instruction. The first instruction executed upon trapping is an 'RTT' which returns to the interrupted sequence of instructions. This sequence is continued until the end of the program is reached.

5.2.4 Trap Catcher - A '+2' - 'HALT' sequence is repeated from 0-776 to catch any unexpected traps. Thus any unexpected traps or interrupts will halt at the vector +2.

5.2.5 Floating Error Trap (To 244) - If a floating point error (overflow, underflow, or divide by zero) was expected, the vector will point to a unique ISR within the subtest where the error occurred which checks the data on the stack(s). If an error was not anticipated, an erroneous trap will be detected in traper.

5.2.6 NOP - A NOP is placed just before each FIS instruction. This allows the operator to patch in a halt for debugging purposes.

6.0 ERRCRS

6.1 Error Printout

The format is as follows:

```
ERRNM ADR PS SP ANS1 ANS2 ANS3 ANS4 ANS5 ANS6
```

Where:

ERRNM ; - Error Number
ADR - Address of Error HLT
PS - Processor Status
SP - Contents of Stack Pointer Register
ANS1-6 - Error Data Read from the Stack(s). From 0 to 6 of these may be typed depending on the number following the HLT; e.g., HLT+3 would type ANS1 thru ANS3, HLT (by itself) would stop after ERRNM, ADR, PS, and SP.

To find the failing test, loop at the listing above the address typed. In most cases the comment beside the HLT tells what was being checked and what was expected. All printouts will be suppressed when bit 5 of location \$ENVM is high. While running under APT the diagnostic will not support spooling of console outputs.

6.2 Error Recovery

Restart at 200 or 222 (see Sec. 4.2).

6.3 Error Counter

An error count is kept in location 'ERRORS'. It can only be cleared from the console or by reloading the program.

7.0 RESTRICTIONS

None.

8.0 MISCELLANEOUS

8.1 Execution Time

Due to the random characteristics of the interrupt tests, the execution time can be half a minute or more. However, normally 'END PASS' will be typed within 40 seconds with all switches down. Execution time will increase by a factor of 256 when iterations of each subtest are enabled.

8.2 Stack Pointer

Stack is initially set to 600.

8.3 Pass Count

A 16 bit location 'SPASS' (i.e. location 406) is used to keep pass count. It can be cleared by restarting the program at 200.

8.4 Test Number

A 16 bit location 'STESTN' (i.e. location 404) is used to keep track of the test number. Upper byte of this location gives the iteration number and the lower byte the test that was being executed.

00008.5 Power Fail

Each test can be power failed with no errors. To use, start the test as usual and power down then up at any time. The program should type 'POWER' and continue to run from where the power fail interrupted with no other error outputs.

00009.0 PROGRAM DESCRIPTION

This program tests all the FIS instructions of the LSI-11 (FADD, FSUB, FMUL, and FDIV). All registers are checked to see if they function properly as the stack pointer. The program has many subtests (the code between 2 scope statements) which are run once before continuing to the next subtest. SW<11> set to a 1 causes each subtest to be run 00256 times SW<9> set to a 1 enables loop on error. The location \$ICNT contains the iteration count and the location \$TESTN contains the test number. All the subtests should be run sequentially by starting at 00200 not by starting at the beginning of the subtest. To loop on a particular subtest, put the test number (see listing) in the right byte of the location \$SWREG and SW<8> set to a 1. This test will be looped upon until SW<8> is set to a 0 or the right byte is changed. If the test is non-existent, the program will be run as usual.

.ENDR

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01300	1169	44500	F SUB TEST SECTION
01400	1740	46000	TEST FLOATING SUB. INSTRUCTION WITH UNDERFLOW
01500	1813	46400	TEST FLOATING SUB. INSTRUCTION WITH OVERFLOW
01600	1886	46800	FMUL TEST SECTION
01700	2271	48100	TEST FLOATING MUL. INSTRUCTION WITH UNDERFLOW
01800	2344	48500	TEST FLOATING MUL. INSTRUCTION WITH OVERFLOW
01900	2417	48900	F DIV TEST SECTION
02000	2656	49800	TEST FLOATING DIV. INSTRUCTION WITH UNDERFLOW
02100	2729	50200	TEST FLOATING DIV INSTRUCTION WITH OVERFLOW
02200	2802	50600	TEST FLOATING DIV. INSTRUCTION FOR DIVIDE BY ZERO
02300	2953	51200	TEST OF ALL FIS AT ONCE
02400	3016	51700	ADDRESS ERROR TEST
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02600	3369		END OF PASS ROUTINE
02700	3408		SCOPE ROUTINE
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00100
00200
00300
00400

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K 1

SEQ 0010

31700

00100
00200
00300
00400
00500
00600
00700
00800
00900
01000
01100
01200
01300
01400
01500
01600
01700
01800
01900
02000
02100
02200
02300
02400
02500
02600
02700
02800
02900
03000
03100
03200
03300
03400
03500
03600
03700
03800
03900
04000
04100
04200
04300
04400
04500
04600
04700
04800
04900
05000
05100
05200
05300
05400
05500
05600
05700
05800
05900

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L 1

SEQ 0011

31900
32000

```
.ABS  
:TITLE CVKACC  
:*COPYRIGHT (C) AUGUST 1978  
:*DIGITAL EQUIPMENT CORP.  
:*MAYNARD, MASS. 01754  
:*  
:*PROGRAM BY DIAGNOSTIC ENGINEERING  
:*  
:*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC  
:*PACKAGE (MAINDEC-11-DZQAC-B), JULY 11, 1975.  
:*  
$TN-1  
$SWR-160000 ;:HALT ON ERROR, LOOP ON TEST, INHIBIT ERROR TYP0UT
```

000001
160000

32900
33600
34000
34000
35400
35500
36900
40200
40700
41200
42000
45100
49600
54900
55300

SWITCH

USE

```
-----  
: 8 LOOP ON TEST IN SW<7:0>  
: 9 LOOP ON ERROR  
: 10 1 - BELL ON ERROR  
: 11 INHIBIT ITERATIONS  
: 12 INHIBIT TRACE TRAP  
: 13 INHIBIT ERROR TYPEOUTS  
: 14 LOOP ON TEST  
: 15 HALT ON ERROR
```

55500 ;ERROR MESSAGE FORMAT:
55600 ; ERRNM ADR PSW SP ANS1 ANS2 ANS3 ANS4 ANS5 ANS6
55700

```
:WHERE ERRNM- ERROR NUMBER  
: ADR - ADDRESS OF 'HLT' INSTRUCTION + 2  
: PSW - PROCESSOR STATUS WORD  
: SP - STACK POINTER  
: ANS1 THRU ANS6 - DATA OFF THE STACK(S)  
: NOTE: ANS1 THRU ANS6 ARE NOT ALWAYS TYPED, DEPENDING ON THE  
: NUMBER ADDED TO THE 'HLT'. 'HLT' ALONE TYPES NONE,  
: 'HLT+1' TYPES ANS1, 'HLT+2' TYPES ANS1 AND ANS2, ETC.
```

104000
000000

56700
56800

HLT- EMT
RO- Z0

```

00400 58 000001 56900 R1 %1
00500 59 000002 57000 R2- %2
00600 60 000003 57100 R3 %3
00700 61 000004 57200 R4 %4
00800 62 000005 57300 R5 %5
00900 63 000005 57400 TTY= %5
01000 64 000006 57500 SP %6
01100 65 000007 57600 PC- %7
01200 66 000024 57700 PWRVEC 24
01300 67 104400 57800 SCOPE= TRAP
01400 68 100000 57900 SW15= 100000
01500 69 040000 58000 SW14- 40000
01600 70 020000 58100 SW13- 20000
01700 71 010000 58200 SW12= 10000
01800 72 004000 58300 SW11- 4000
01900 73 002000 58400 SW10 2000
02000 74 001000 58500 SW09 1000
02100 75 000400 58600 SW08= 400
02200 76 000004 58700 TYPE- IOT
02300 77 000001 58800 N= 1
02400 78 000001 58900 $F 1
02500 79
02600 80
02700 81 000000 59100
02800 82 60000 59200
02900 83
03000 84
03100 85
03200 86
03300 87 001000
03400 88 000046
03500 89 000046 015664
03600 90 000052 000052
03700 91 000052 000000
03800 92 001000
03900 93 60200
04000 94 60300

```

0 ;TRAP CATCHER FROM 0 - 776

.SBTTL ACT11 HOOKS
;HOOKS REQUIRED BY ACT11

-\$VPC- ;SAVE PC
-46
\$ENDAD ;:1)SET LOC.46 TO ADDRESS OF \$ENDAD IN .SEOP
52
:WORD 0 ;:2)SET LOC.52 TO ZERO
-\$VPC ;: RESTORE PC

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VECTOR AREA, STACKS, ANSWER AREA, AND SETUP ROUTINE

SEQ 0013

00300
00400 95
00500 96 000400
00600 97
00700 98
00800 99
00900 100
01000 101
01100 102
01200 103
01300 104 000400
01400 105 000400 000000
01500 106 000402 000000
01600 107 000404 000000
01700 108 000406 000000
01800 109 000410 000000
01900 110 000412 000000
02000 111 000414 000000
02100 112 000416 000000
02200 113 000420
02300 114 000420 000
02400 115 000421 000
02500 116 000422 000000
02600 117 000424 000000
02700 118 000426 000000
02800 119
02900 120
03000 121
03100 122
03200 123
03300 124
03400 125 000430
03500 126
03600 127
03700 128
03800 129
03900 130
04000 131
04100 132 000430
04200 133 000024 000024
04300 134 000024 000200
04400 135 000044 000044
04500 136 000044 000430
04600 137 000430
04700 138
04800 139
04900 140
05000 141
05100 142 000430
05200 143 000430 000000
05300 144 000432 000400
05400 145 000434 000003
05500 146 000436 000005
05600 147 000440 000000
05700 148 000442 000014
05800 149 000430
05900 150 000430

60800
60900

400
:*****

.SBTTL APT MAILBOX-ETABLE

.EVEN

\$MAIL: :: APT MAILBOX
\$MSGTY: .WORD AMSTY :: MESSAGE TYPE CODE
\$FATAL: .WORD AFATAL :: FATAL ERROR NUMBER
\$TESTN: .WORD ATESTN :: TEST NUMBER
\$PASS: .WORD APASS :: PASS COUNT
\$DEVCT: .WORD ADEVCT :: DEVICE COUNT
\$UNIT: .WORD AUNIT :: I/O UNIT NUMBER
\$MSGAD: .WORD AMSGAD :: MESSAGE ADDRESS
\$MSGLG: .WORD AMSLG :: MESSAGE LENGTH
\$ETABLE: :: APT ENVIRONMENT TABLE
\$ENV: .BYTE AENV :: ENVIRONMENT BYTE
\$ENVM: .BYTE AENVM :: ENVIRONMENT MODE BITS
\$SWREG: .WORD ASWREG :: APT SWITCH REGISTER
\$USWR: .WORD AUSWR :: USER SWITCHES
\$CPUOP: .WORD ACPUOP :: CPU TYPE, OPTIONS
BITS 15-11=CPU TYPE
11/04=01,11/05-02,11/20-03,11/40 04,11/45=05
11/70=06,PDQ=07,Q-10
BIT 10=REAL TIME CLOCK
BIT 9-FLOATING POINT PROCESSOR
BIT 8=MEMORY MANAGEMENT

\$ETEND:
.MEXIT

:*****

.SBTTL APT PARAMETER BLOCK

;;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
:*****
.\$X- :: SAVE CURRENT LOCATION
=24 :: SET POWER FAIL TO POINT TO START OF PROGRAM -
200 :: FOR APT START UP
=44 :: POINT TO APT INDIRECT ADDRESS PNTR.
\$APTHDR :: POINT TO APT HEADER BLOCK
-.\$X :: RESET LOCATION COUNTER
:*****
;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
;INTERFACE *SPEC.

\$APTHD:
\$HIBTS: .WORD 0 :: TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
\$MBADR: .WORD \$MAIL :: ADDRESS OF APT MAILBOX (BITS 0-15)
\$STMT: .WORD 3 :: RUN TIME OF LONGEST TEST
\$PASTM: .WORD 5 :: RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VER
\$UNITM: .WORD :: ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDI
\$ETEND-\$MAIL/2 ;; LENGTH MAILBOX-ETABLE (WORD)
\$APTHD

61200
61300

HLTADS:

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SEQ

```

00300
00400 194 ;*****
00500 195 38200
00600 196 38300 .SBTTL STARTING OF THE PROGRAM
00700 197 38400
00800 198 38500
00900 199 38600 . 200
01000 200 000200 000167 017104 38700 JMP NOOOP ;GO TO TYPE HEADING & INITIALIZATION
01100 201 000204 000167 000370 38800 RESTRT: JMP BEGIN ;START THE PROGRAM
01200 202 38900
01300 203 39000
01400 204 39100
01500 205 39200
01600 206 000600 39300 . 600
01700 207 39400
01800 208 000600 012706 000600 39500 BEGIN: MOV #BEGIN, SP ;INITIALIZE STACK POINTER
01900 209 000604 012737 000542 000014 39600 MOV #YESRT, @#14 ;SET TRACE TRAP VECTOR
02000 210 000612 012737 017170 000020 39700 MOV #STYPE, @#20 ;SET UP VECTOR 20
02100 211 000620 012737 017030 000024 39800 MOV #SPWRDN, @#24 ;SERVICE POWER DOWN ROUTINE FOR ANY FUTURE
02200 212 39900 ;POWER DOWN
02300 213 000626 012700 000030 40000 MOV #30, R0 ;SET R0 TO VECTOR 30
02400 214 000632 012720 016464 40100 MOV #HLTS, (0)+ ;SET EMT VECTOR
02500 215 000636 012720 000340 40200 MOV #340, (0)+
02600 216 000642 012720 015716 40300 MOV #SCOPE$, (0)+ ;SET TRAP VECTOR
02700 217 000646 012710 000340 40400 MOV #340, (0)
02800 218 000652 012737 000006 000004 40500 1$: MOV #6, @#4 ;RESTORE TIME-OUT VECTOR
02900 219 000660 132737 00000* 000420 40600 BITB #1, @#SENV ;ARE WE UNDER APT ?
03000 220 000666 001410 40700 BEQ 2$ ;IF NOT THEN GO TO 2$
03100 221 000670 012700 000554 40800 MOV #STPB+2, R0 ;OTHERWISE SET FOR THE OTHER SLU
03200 222 000674 012740 176566 40900 MOV #176566, -(R0)
03300 223 000700 012740 176564 41000 MOV #176564, -(R0)
03400 224 000704 012740 000074 41100 MOV #74, -(R0)
03500 225 000710 005067 177470 41200 2$: CLR $TESTN
03600 226 000714 005067 177550 41300 CLR LAD$ ;CLEAR LOOP ADDRESS
03700 227 41400
03800 228 41500

```

```

00300      229
00400      230
00500      231
00600      232
00700      233
00800      234
00900      235
01000      236
01100      237 000720 104400
01200      238 000722 004567 015260
01300      239 000726 000000 000000
01400      240 000732 000000 000000
01500      241 000736 000000
01600      242 000740 016456 000340
01700      243 000744 012700 000510
01800      244
01900      245 000750 000240
02000      246 000752 075000
02100      247
02200      248 000754 004767 015260
02300      249 000760 010067 177450
02400      250 000764 122767 000004 177440
02500      251 000772 001402
02600      252 000774 104000
02700      253 000776 000001
02800      254
02900      255 001000 022767 000514 177426
03000      256 001006 001402
03100      257 001010 104000
03200      258 001012 000002
03300      259
03400      260 001014 005767 177416
03500      261 001020 001402
03600      262 001022 104002
03700      263 001024 000003
03800      264
03900      265 001026 005767 177406
04000      266 001032 001402
04100      267 001034 104002
04200      268 001036 000004
04300      269
04400      270 001040 122767 000001 177336
04500      271 001046 001402
04600      272 001050 104000
04700      273 001052 000005
04800      274
04900

```

```

:*****
:TEST 1: FADD (LSI-11 FLOATING ADD INSTRUCTION)
:         000000,000000 + 000000,000000 = 000000,000000
:         PS = 004,          STACK POINTER = R0
:*****

TST1:  SCOPE
      JSR   R5,    PUSHR ;PUSH 4 WORDS ONTO R0 STACK, SET PRIORITY
      .WORD 000000,000000 ;SECOND OPERAND ON TOP
      .WORD 000000,000000 ;FIRST OPERAND ON BOTTOM
      .WORD 000          ;PROCESSOR PRIORITY LEVEL
      .WORD TRAPER,340   ;FIS TRAP VECTOR
      MOV   #STACK0,R0  ;SET UP STACK POINTER

      NOP
      FADD  R0          ;FLOATING ADD ON THE R0 STACK

      JSR   PC,    POPR  ;POP THE ANSWER
      MOV   R0,    $SP  ;SAVE 'STACK POINTER'
      CMPB #004,   $PSW ;CHECK PS (EXCEPT T BIT)
      BEQ  .+6        ;BRANCH IF OK
      HLT  1          ;PS NOT EQUAL TO 004
                        ;THE ERROR NUMBER IS 1

      CMP   #STACK4,$SP ;CHECK THE STACK POINTER (R0)
      BEQ  .+6        ;BRANCH IF OK
      HLT  2          ;STACK POINTER (R0) NOT EQUAL TO #STACK4
                        ;THE ERROR NUMBER IS 2

      TST  ANS1       ;CHECK FIRST HALF OF ANSWER
      BEQ  .+6        ;BRANCH IF OK
      HLT+2 ;ANS1 NOT EQUAL TO 000000
            3         ;THE ERROR NUMBER IS 3

      TST  ANS2       ;CHECK SECOND HALF OF ANSWER
      BEQ  .+6        ;BRANCH IF OK
      HLT+2 ;ANS2 NOT EQUAL TO 000000
            4         ;THE ERROR NUMBER IS 4

END1:  CMPB #1,      $TESTN ;CHECK THE TEST NUMBER
      BEQ  .+6        ;BRANCH IF OK
      HLT  5          ;WRONG TEST. PC MUST HAVE FOULED UP.
                        ;THE ERROR NUMBER IS 5

```



```

00300      321
00400      322
00500      323
00600      324
00700      325
00800      326
00900      327
01000      328
01100      329 001214 104400
01200      330 001216 004567 014764
01300      331 001222 040200 000000
01400      332 001226 040200 000000
01500      333 001232 000040
01600      334 001234 016456 000340
01700      335 001240 012701 000510
01800      336
01900      337 001244 000240
02000      338 001246 075001
02100      339 001250 004767 014764
02200      340 001254 010167 177154
02300      341 001260 105767 177146
02400      342 001264 001402
02500      343 001266 104000
02600      344 001270 000013
02700      345
02800      346 001272 022767 000514 177134
02900      347 001300 001402
03000      348 001302 104000
03100      349 001304 000014
03200      350
03300      351 001306 022767 040400 177122
03400      352 001314 001402
03500      353 001316 104002
03600      354 001320 000015
03700      355
03800      356 001322 005767 177112
03900      357 001326 001402
04000      358 001330 104002
04100      359 001332 000016
04200      360
04300      361 001334 122767 000003 177042
04400      362 001342 001402
04500      363 001344 104000
04600      364 001346 000017
04700      365
04800      366
04900

```

```

*****
:TEST 3:      FADD (LSJ-11 FLOATING ADD INSTRUCTION)
:              040200,000000 + 040200,000000 = 040400,000000
:              PS = 000,          STACK POINTER  R1
*****

TST3:  SCOPE
      JSR      R5      PUSH4      ;PUSH 4 WORDS ONTO R1 STACK, SET PRIORITY
      .WORD    040200,000000      ;SECOND OPERAND ON TOP
      .WORD    040200,000000      ;FIRST OPERAND ON BOTTOM
      .WORD    040          ;PROCESSOR PRIORITY LEVEL
      .WORD    TRAPER,340        ;FIS TRAP VECTOR
      MOV      #STACK0,R1        ;SET UP STACK POINTER

      NOP
      FADD     R1          ;FLOATING ADD ON THE R1 STACK

      JSR      PC,      POPR      ;POP THE ANSWER
      MOV      R1,      $SP      ;SAVE 'STACK POINTER'
      TSTB    $PSW          ;CHECK PS (EXCEPT T B&J)
      BEQ     .+6          ;BRANCH IF OK
      HLT     000          ;PS NOT EQUAL TO 000
      13          ;THE ERROR NUMBER IS 13

      CMP     #STACK4,$SP      ;CHECK THE STACK POINTER (R1)
      BEQ     .+6          ;BRANCH IF OK
      HLT     14          ;STACK POINTER (R1) NOT EQUAL TO #STACK4
      14          ;THE ERROR NUMBER IS 14

      CMP     #040400,ANS1     ;CHECK FIRST HALF OF ANSWER
      BEQ     .+6          ;BRANCH IF OK
      HLT     15          ;ANS1 NOT EQUAL TO 040400
      15          ;THE ERROR NUMBER IS 15

      TST     ANS2            ;CHECK SECOND HALF OF ANSWER
      BEQ     .+6          ;BRANCH IF OK
      HLT     16          ;ANS2 NOT EQUAL TO 000000
      16          ;THE ERROR NUMBER IS 16

END3:  CMPB    #3,          $TESTN ;CHECK THE TEST NUMBER
      BEQ     .+6          ;BRANCH IF OK
      HLT     17          ;WRONG TEST. PC MUST HAVE FOULED UP.
      17          ;THE ERROR NUMBER IS 17

```

```

00300 367
00400 368
00500 369
00600 370
00700 371
00800 372
00900 373
01000 374
01100 001350 104400
01200 375 001352 004567 014630
01300 376 001356 177777 177777
01400 377 001362 077777 177777
01500 378 001366 000100
01600 379 001370 016456 000340
01700 380 001374 012702 000510
01800 381
01900 382 001400 000240
02000 383 001402 075002
02100 384
02200 385 001404 004767 014630
02300 386 001410 010267 177020
02400 387 001414 122767 000004 177010
02500 388 001422 001402
02600 389 001424 104000
02700 390 001426 000020
02800 391
02900 392 001430 022767 000514 176776
03000 393 001436 001402
03100 394 001440 104000
03200 395 001442 000021
03300 396
03400 397 001444 005767 176766
03500 398 001450 001402
03600 399 001452 104002
03700 400 001454 000022
03800 401
03900 402 001456 005767 176756
04000 403 001462 001402
04100 404 001464 104002
04200 405 001466 000023
04300 406
04400 407 001470 122767 000004 176706
04500 408 001476 001402
04600 409 001500 104000
04700 410 001502 000024
04800 411
04900 412

```

```

:.....
:TEST 4: FADD (LSI-11 FLOATING ADD INSTRUCTION)
: 077777,177777 + 177777,177777 = 000000,000000
: PS = 004, STACK POINTER = R2
:.....
TST4: SCOPE
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R2 STACK, SET PRIORITY
.WORD 177777,177777 ;SECOND OPERAND ON TOP
.WORD 077777,177777 ;FIRST OPERAND ON BOTTOM
.WORD 100 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER,340 ;FIS TRAP VECTOR
MOV #STACK0,R2 ;SET UP STACK POINTER

NOP
FADD R2 ;FLOATING ADD ON THE R2 STACK

JSR PC, POPR ;POP THE ANSWER
MOV R2, SSP ;SAVE 'STACK POINTER'
CMPB #004, SPSW ;CHECK PS (EXCEPT T BIT)
BEQ .+6 ;BRANCH IF OK
HLT ;PS NOT EQUAL TO 004
20 ;THE ERROR NUMBER IS 20

CMP #STACK4,SSP ;CHECK THE STACK POINTER (R2)
BEQ .+6 ;BRANCH IF OK
HLT ;STACK POINTER (R2) NOT EQUAL TO #STACK4
21 ;THE ERROR NUMBER IS 21

TST ANS1 ;CHECK FIRST HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 000000
22 ;THE ERROR NUMBER IS 22

TST ANS2 ;CHECK SECOND HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 000000
23 ;THE ERROR NUMBER IS 23

END4: CMPB #4, $TESTN ;CHECK THE TEST NUMBER
BEQ .+6 ;BRANCH IF OK
HLT ;WRONG TEST! PC MUST HAVE FOULED UP.
24 ;THE ERROR NUMBER IS 24

```

00100
00200
00300
00400
00500
00600
00700
00800
00900
01000
01100
01200
01300
01400
01500
01600
01700
01800
01900
02000
02100
02200
02300
02400
02500
02600
02700
02800
02900
03000
03100
03200
03300
03400
03500
03600
03700
03800
03900
04000
04100
04200
04300
04400
04500
04600
04700
04800
04900

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:TEST 5: FADD (LSI-11 FLOATING ADD INSTRUCTION)
: 052525,052525 + 152525,052524 = 044600,000000
: PS 200, STACK POINTER - SP
:*****

413
414
415
416
417
418
419
420 001504 104400
421 001506 004567 014322
422 001512 152525 052524
423 001516 052525 052525
424 001522 000217
425 001524 016456 000340
426
427 001530 000240
428 001532 075006
429
430 001534 004767 014334
431 001540 022706 000600
432 001544 001405
433 001546 012706 000600
434 001552 104000
435 001554 000025
436 001556 000421
437
438 001560 122767 000200 176644
439 001566 001402
440 001570 104000
441 001572 000026
442
443 001574 022767 044600 176634
444 001602 001402
445 001604 104002
446 001606 000027
447
448 001610 005767 176624
449 001614 001402
450 001616 104002
451 001620 000030
452
453 001622 122767 000005 176554
454 001630 001402
455 001632 104000
456 001634 000031
457
458

SCOPE
TST5: JSR R5 PUSHS ; PUSH 4 WORDS ONTO STACK, SET PRIORI
.WORD 152525,052524 ; SECOND OPERAND ON TOP
.WORD 052525,052525 ; FIRST OPERAND ON BOTTOM
.WORD 217 ; PROCESSOR PRIORITY LEVEL
.WORD TRAPER,340 ; FIS TRAP VECTOR

NOP
FADD SP ; FLOATING ADD ON THE STACK

JSR PC, POPS ; POP THE ANSWER
CMP #BEGIN, SP ; CHECK THE STACK POINTER
BEQ TSA5 ; BRANCH IF OK
MOV #BEGIN, SP ; RESTORE STACK POINTER
HLT ; STACK POINTER FOULED UP
BR 25 ; THE ERROR NUMBER IS 25
BR 26 ; SKIP REST OF TEST

TSA5: CMPB #200, \$PSW ; CHECK PS (EXCEPT T BIT)
BEQ .+6 ; BRANCH IF OK
HLT ; PS NOT EQUAL TO 200
26 ; THE ERROR NUMBER IS 26

CMP #044600,ANS1 ; CHECK FIRST HALF OF ANSWER
BEQ .+6 ; BRANCH IF OK
HLT+2 ; ANS1 NOT EQUAL TO 044600
27 ; THE ERROR NUMBER IS 27

TST ANS2 ; CHECK SECOND HALF OF ANSWER
BEQ .+6 ; BRANCH IF OK
HLT+2 ; ANS2 NOT EQUAL TO 000000
30 ; THE ERROR NUMBER IS 30

END5: CMPB #5, \$TESTN ; CHECK THE TEST NUMBER
BEQ .+6 ; BRANCH IF OK
HLT ; WRONG TEST. PC MUST HAVE FOULED UP.
31 ; THE ERROR NUMBER IS 31

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SEQ 0021

00100 459
00200 460
00300 461
00400 462
00500 463
00600 464
00700 465
00800 466
00900 467
01000 468
01100 469
01200 470
01300 471
01400 472
01500 473
01600 474
01700 475
01800 476
01900 477
02000 478
02100 479
02200 480
02300 481
02400 482
02500 483
02600 484
02700 485
02800 486
02900 487
03000 488
03100 489
03200 490
03300 491
03400 492
03500 493
03600 494
03700 495
03800 496
03900 497
04000 498
04100 499
04200 500
04300 501
04400 502
04500 503
04600 504

001636 104400
001640 004567 014170
001644 025177 177777
001650 125200 000000
001654 000307
001656 016456 000340
001662 000240
001664 075006
001666 004767 014202
001672 022706 000600
001676 001405
001700 012706 000600
001704 104000
001706 000032
001710 000421
001712 122767 000210 176512
001720 001402
001722 104000
001724 000033
001726 022767 117200 176502
001734 001402
001736 104002
001740 000034
001742 005767 176472
001746 001402
001750 104002
001752 000035
001754 122767 000006 176422
001762 001402
001764 104000
001766 000036

```

.....
:TEST 6: FADD (LSI-11 FLOATING ADD INSTRUCTION)
: 125200,000000 + 025177,177777 = 117200,000000
: PS 210, STACK POINTER SP
:.....

```

```

SCOPE
TST6: JSR R5, PUSHES :PUSH 4 WORDS ONTO STACK, SET PRIORI
:WORD 025177,177777 :SECOND OPERAND ON TOP
:WORD 125200,000000 :FIRST OPERAND ON BOTTOM
:WORD 307 :PROCESSOR PRIORITY LEVEL
:WORD TRAPER,340 :FIS TRAP VECTOR

NOP
FADD SP :FLOATING ADD ON THE STACK

JSR PC, POPS :POP THE ANSWER
CMP #BEGIN, SP :CHECK THE STACK POINTER
BEQ TSA6 :BRANCH IF OK
MOV #BEGIN, SP :RESTORE STACK POINTER
HLT :STACK POINTER FOULED UP
32 :THE ERROR NUMBER IS 32
BR END6 :SKIP REST OF TEST

TSA6: CMPB #210, $PSW :CHECK PS (EXCEPT 1 BIT)
BEQ .+6 :BRANCH IF OK
HLT :PS NOT EQUAL TO 210
33 :THE ERROR NUMBER IS 33

CMP #117200,ANS1 :CHECK FIRST HALF OF ANSWER
BEQ .+6 :BRANCH IF OK
HLT+2 :ANS1 NOT EQUAL TO 117200
34 :THE ERROR NUMBER IS 34

TST ANS2 :CHECK SECOND HALF OF ANSWER
BEQ .+6 :BRANCH IF OK
HLT+2 :ANS2 NOT EQUAL TO 000000
35 :THE ERROR NUMBER IS 35

END6: CMPB #6, $TESTN :CHECK THE TEST NUMBER
BEQ .+6 :BRANCH IF OK
HLT :WRONG TEST. PC MUST HAVE FOULED UP.
36 :THE ERROR NUMBER IS 36

```

00100
00200
00300
00400
00500
00600
00700
00800
00900
01000
01100
01200
01300
01400
01500
01600
01700
01800
01900
02000
02100
02200
02300
02400
02500
02600
02700
02800
02900
03000
03100
03200
03300
03400
03500
03600
03700
03800
03900
04000
04100
04200
04300
04400
04500
04600
04700
04800
04900
05000

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505
506
507
508
509
510
511
512 001770 104400
513 001772 004567 014210
514 001776 100125 052525
515 002002 135753 024642
516 002006 000347
517 002010 016456 000340
518 002014 012705 000510
519
520 002020 000240
521 002022 075005
522
523 002024 004767 014210
524 002030 010567 176400
525 002034 122767 000210 176370
526 002042 001402
527 002044 104000
528 002046 000037
529
530 002050 022767 000514 176356
531 002056 001402
532 002060 104000
533 002062 000040
534
535 002064 022767 135753 176344
536 002072 001402
537 002074 104002
538 002076 000041
539
540 002100 022767 024642 176332
541 002106 001402
542 002110 104002
543 002112 000042
544
545 002114 122767 000007 176262
546 002122 001402
547 002124 104000
548 002126 000043
549
550

FADD TEST SECTION

```

.....
TEST 7:      FADD (LSI-11 FLOATING ADD INSTRUCTION)
              135753,024642 + 100125,052525 = 135753,024642
              PS = 210,      STACK POINTER  R5
.....

TEST:  SCOPE
        JSR      R5,      PUSH4      ; PUSH 4 WORDS ONTO R5 STACK, SET PRIORITY
        .WORD    100125,052525      ; SECOND OPERAND ON TOP
        .WORD    135753,024642      ; FIRST OPERAND ON BOTTOM
        .WORD    347                ; PROCESSOR PRIORITY LEVEL
        .WORD    TRAPER,340         ; FIS TRAP VECTOR
        MOV      #STACK0,R5        ; SET UP STACK POINTER

        NOP
        FADD     R5                ; FLOATING ADD ON THE R5 STACK

        JSR      PC,      POPR      ; POP THE ANSWER
        MOV      R5,      $SP      ; SAVE 'STACK POINTER'
        CMPB    #210,    $PSW      ; CHECK PS (EXCEPT T BIT)
        BEQ     .+6              ; BRANCH IF OK
        HLT     37                ; PS NOT EQUAL TO 210
        ; THE ERROR NUMBER IS 37

        CMP     #STACK4,$SP        ; CHECK THE STACK POINTER (R5)
        BEQ     .+6              ; BRANCH IF OK
        HLT     40                ; STACK POINTER (R5) NOT EQUAL TO #STACK4
        ; THE ERROR NUMBER IS 40

        CMP     #135753,ANS1      ; CHECK FIRST HALF OF ANSWER
        BEQ     .+6              ; BRANCH IF OK
        HLT+2  41                ; ANS1 NOT EQUAL TO 135753
        ; THE ERROR NUMBER IS 41

        CMP     #024642,ANS2      ; CHECK SECOND HALF OF ANSWER
        BEQ     .+6              ; BRANCH IF OK
        HLT+2  42                ; ANS2 NOT EQUAL TO 024642
        ; THE ERROR NUMBER IS 42

        END7:  CMPB    #7,      $TESTN ; CHECK THE TEST NUMBER
        BEQ     .+6              ; BRANCH IF OK
        HLT     43                ; WRONG TEST. PC MUST HAVE FOULED UP.
        ; THE ERROR NUMBER IS 43

```



```

00100
00200
00300
00400 597
00500 598
00600 599
00700 600
00800 601
00900 602
01000 603
01100 604 002270 104400
01200 605 002272 004567 013710
01300 606 002276 000200 000000
01400 607 002302 100400 000000
01500 608 002306 000140
01600 609 002310 016456 000340
01700 610 002314 012705 000510
01800 611
01900 612 002320 000240
02000 613 002322 075005
02100 614
02200 615 002324 004767 013710
02300 616 002330 010567 176100
02400 617 002334 122767 000010 175070
02500 618 002342 001402
02600 619 002344 104000
02700 620 002346 000051
02800 621
02900 622 002350 022767 000514 176056
03000 623 002356 001402
03100 624 002360 104000
03200 625 002362 000052
03300 626
03400 627 002364 022767 100200 176044
03500 628 002372 001402
03600 629 002374 104002
03700 630 002376 000053
03800 631
03900 632 002400 005767 176034
04000 633 002404 001402
04100 634 002406 104002
04200 635 002410 000054
04300 636
04400 637 002412 122767 000011 175764
04500 638 002420 001402
04600 639 002422 104000
04700 640 002424 000055
04800 641
04900 642

```

```

:*****
:TEST 11: FADD (LSI-11 FLOATING ADD INSTRUCTION)
: 100400,000000 + 000200,000000 = 100200,000000
: PS = 010, STACK POINTER R5
:*****
TST11: SCOPE
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R5 STACK, SET PRIORITY
.WORD 000200,000000 ;SECOND OPERAND ON TOP
.WORD 100400,000000 ;FIRST OPERAND ON BOTTOM
.WORD 140 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER,340 ;FIS TRAP VECTOR
MOV #STACK0,R5 ;SET UP STACK POINTER

NOP
FADD R5 ;FLOATING ADD ON THE R5 STACK

JSR PC, POPR ;POP THE ANSWER
MOV R5, $SP ;SAVE 'STACK POINTER'
CMPB #010, $PSW ;CHECK PS (EXCEPT T BIT)
BEQ .+6 ;BRANCH IF OK
HLT ;PS NOT EQUAL TO 010
51 ;THE ERROR NUMBER IS 51

CMP #STACK4,$SP ;CHECK THE STACK POINTER (R5)
BEQ .+6 ;BRANCH IF OK
HLT ;STACK POINTER (R5) NOT EQUAL TO #STACK4
52 ;THE ERROR NUMBER IS 52

CMP #100200,ANS1 ;CHECK FIRST HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 100200
53 ;THE ERROR NUMBER IS 53

TST ANS2 ;CHECK SECOND HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 000000
54 ;THE ERROR NUMBER IS 54

END11: CMPB #11, $TESTN ;CHECK THE TEST NUMBER
BEQ .+6 ;BRANCH IF OK
HLT ;WRONG TEST. PC MUST HAVE FOULED UP.
55 ;THE ERROR NUMBER IS 55

```



```

00100
00200
00300
00400 643
00500 644
00600 645
00700 646
00800 647
00900 648
01000 649
01100 650 002426 104400
01200 651 002430 004567 013552
01300 652 002434 100252 125252
01400 653 002440 000425 052525
01500 654 002444 000217
01600 655 002446 016456 000340
01700 656 002452 012704 000510
01800 657
01900 658 002456 000240
02000 659 002460 075004
02100 660
02200 661 002462 004767 013552
02300 662 002466 010467 175742
02400 663 002472 122767 000200 175732
02500 664 002500 001402
02600 665 002502 104000
02700 666 002504 000056
02800 667
02900 668 002506 022767 000514 175720
03000 669 002514 001402
03100 670 002516 104000
03200 671 002520 000057
03300 672
03400 673 002522 022767 000200 175706
03500 674 002530 001402
03600 675 002532 104002
03700 676 002534 000060
03800 677
03900 678 002536 005767 175676
04000 679 002542 001402
04100 680 002544 104002
04200 681 002546 000061
04300 682
04400 683 002550 122767 000012 175626
04500 684 002556 001402
04600 685 002560 104000
04700 686 002562 000062
04800 687
04900 688

```

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:*****
:TEST 12: FADD (LSI-11 FLOATING ADD INSTRUCTION)
: 000425,052525 + 100252,125252 = 000200,000000
: PS - 200, STACK POINTER - R4
:*****

```

```

TST12: SCOPE
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R4 STACK, SET PRIORITY
.WORD 100252,125252 ;SECOND OPERAND ON TOP
.WORD 000425,052525 ;FIRST OPERAND ON BOTTOM
.WORD 217 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER,340 ;FIS TRAP VECTOR
MOV #STACK0,R4 ;SET UP STACK POINTER

NOP
FADD R4 ;FLOATING ADD ON THE R4 STACK

JSR PC, POPR ;POP THE ANSWER
MOV R4, $SP ;SAVE 'STACK POINTER'
CMPB #200, $PSW ;CHECK PS (EXCEPT T BIT)
BEQ .+6 ;BRANCH IF OK
HLT ;PS NOT EQUAL TO 200
56 ;THE ERROR NUMBER IS 56

CMP #STACK4,$SP ;CHECK THE STACK POINTER (R4)
BEQ .+6 ;BRANCH IF OK
HLT ;STACK POINTER (R4) NOT EQUAL TO #STACK4
57 ;THE ERROR NUMBER IS 57

CMP #000200,ANS1 ;CHECK FIRST HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 000200
60 ;THE ERROR NUMBER IS 60

TST ANS2 ;CHECK SECOND HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 000000
61 ;THE ERROR NUMBER IS 61

END12: CMPB #12, $TESTN ;CHECK THE TEST NUMBER
BEQ .+6 ;BRANCH IF OK
HLT ;WRONG TEST! PC MUST HAVE FOULED UP.
62 ;THE ERROR NUMBER IS 62

```

:TEST 13: FADD (LSI-11 FLOATING ADD INSTRUCTION)
: 100425,052525 + 000252,125252 = 100200,000000
: PS = 210, STACK POINTER = SP
:*****

00400 689
00500 690
00600 691
00700 692
00800 693
00900 694
01000 695
01100 696 002064 104400
01200 697 002566 004567 013242
01300 698 002572 000252 125252
01400 699 002576 100425 052525
01500 700 002602 000307
01600 701 002604 016456 000340
01700 702
01800 703 002610 000240
01900 704 002612 075006
02000 705
02100 706 002614 004767 013254
02200 707 002620 022706 000600
02300 708 002624 001405
02400 709 002626 012706 000600
02500 710 002632 104000
02600 711 002634 000063
02700 712 002636 000421
02800 713
02900 714 002640 122767 000210 175564
03000 715 002646 001402
03100 716 002650 104000
03200 717 002652 000064
03300 718
03400 719 002654 022767 100200 175554
03500 720 002662 001402
03600 721 002664 104002
03700 722 002666 000065
03800 723
03900 724 002670 005767 175544
04000 725 002674 001402
04100 726 002676 104002
04200 727 002700 000066
04300 728
04400 729 002702 122767 000013 175474
04500 730 002710 001402
04600 731 002712 104000
04700 732 002714 000067
04800 733
04900 734

SCOPE
TST13: JSR R5 PUSHES :PUSH 4 WORDS ONTO STACK, SET PRIORI
.WORD 000252,125252 :SECOND OPERAND ON TOP
.WORD 100425,052525 :FIRST OPERAND ON BOTTOM
.WORD 307 :PROCESSOR PRIORITY LEVEL
.WORD TRAPER,340 :FIS TRAP VECTOR

NOP
FADD SP :FLOATING ADD ON THE STACK

JSR PC, POPS :POP THE ANSWER
CMP #BEGIN, SP :CHECK THE STACK POINTER
BEQ TSA13 :BRANCH IF OK
MOV #BEGIN, SP :RESTORE STACK POINTER
HLT :STACK POINTER FOULED UP
63 :THE ERROR NUMBER IS 63
BR END13 :SKIP REST OF TEST

TSA13: CMPB #210, \$PSW :CHECK PS (EXCEPT T BIT)
BEQ .+6 :BRANCH IF OK
HLT :PS NOT EQUAL TO 210
64 :THE ERROR NUMBER IS 64

CMP #100200,ANS1 :CHECK FIRST HALF OF ANSWER
BEQ .+6 :BRANCH IF OK
HLT+2 :ANS1 NOT EQUAL TO 100200
65 :THE ERROR NUMBER IS 65

TST ANS2 :CHECK SECOND HALF OF ANSWER
BEQ .+6 :BRANCH IF OK
HLT+2 :ANS2 NOT EQUAL TO 000000
66 :THE ERROR NUMBER IS 66

END13: CMPB #13, \$TESTN :CHECK THE TEST NUMBER
BEQ .+6 :BRANCH IF OK
HLT :WRONG TEST! PC MUST HAVE FOULED UP.
67 :THE ERROR NUMBER IS 67.

```

00300
00400 735
00500 736
00600 737
00700 738
00800 739
00900 740
01000 741
01100 742 002716 104400
01200 743 002720 004567 013110
01300 744 002724 077452 125252
01400 745 002730 077652 125252
01500 746 002734 000257
01600 747 002736 016456 000340
01700 748
01800 749 002742 000240
01900 750 002744 075006
02000 751
02100 752 002746 004767 013122
02200 753 002752 022706 000600
02300 754 002756 001405
02400 755 002760 012706 000600
02500 756 002764 104000
02600 757 002766 000070
02700 758 002770 000422
02800 759
02900 760 002772 122767 000200 175432
03000 761 003000 001402
03100 762 003002 104000
03200 763 003004 000071
03300 764
03400 765 003006 022767 077777 175422
03500 766 003014 001402
03600 767 003016 104002
03700 768 003020 000072
03800 769
03900 770 003022 022767 177777 175410
04000 771 003030 001402
04100 772 003032 104002
04200 773 003034 000073
04300 774
04400 775 003036 122767 000014 175340
04500 776 003044 001402
04600 777 003046 104000
04700 778 003050 000074
04800 779
04900 780

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:*****
:TEST 14: FADD (LSI-11 FLOATING ADD INSTRUCTION)
: 077652,125252 + 077452,125252 = 077777,177777
: PS 200, STACK POINTER = SP
:*****
SCOPE
TST14: JSR R5, PUSHS ;PUSH 4 WORDS ONTO STACK, SET PRIORI
.WORD 077452,125252 ;SECOND OPERAND ON TOP
.WORD 077652,125252 ;FIRST OPERAND ON BOTTOM
.WORD 257 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER,340 ;FIS TRAP VECTOR

NOP
FADD SP ;FLOATING ADD ON THE STACK

JSR PC, POPS ;POP THE ANSWER
CMP #BEGIN, SP ;CHECK THE STACK POINTER
BEQ TSA14 ;BRANCH IF OK
MOV #BEGIN, SP ;RESTORE STACK POINTER
HLT ;STACK POINTER FOULED UP
70 ;THE ERROR NUMBER IS 70
BR END14 ;SKIP REST OF TEST

TSA14: CMPB #200, SPSW ;CHECK PS (EXCEPT T BIT)
BEQ .+6 ;BRANCH IF OK
HLT ;PS NOT EQUAL TO 200
71 ;THE ERROR NUMBER IS 71

CMP #077777,ANS1 ;CHECK FIRST HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 077777
72 ;THE ERROR NUMBER IS 72

CMP #177777,ANS2 ;CHECK SECOND HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 177777
73 ;THE ERROR NUMBER IS 73

END14: CMPB #14, $TESTN ;CHECK THE TEST NUMBER
BEQ .+6 ;BRANCH IF OK
HLT ;WRONG TEST. PC MUST HAVE FOULED UP.
74 ;THE ERROR NUMBER IS 74

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00100
00200
00300
00400 781
00500 782
00600 783
00700 784
00800 785
00900 786
01000 787
01100 788 003052 104400
01200 789 003054 004567 013126
01300 790 003060 177652 125252
01400 791 003064 177452 125252
01500 792 003070 000357
01600 793 003072 016456 000340
01700 794 003076 012704 000510
01800 795
01900 796 003102 000240
02000 797 003104 075004
02100 798
02200 799 003106 004767 013126
02300 800 003112 010467 175316
02400 801 003116 122767 000210 175306
02500 802 003124 001402
02600 803 003126 104000
02700 804 003130 000075
02800 805
02900 806 003132 022767 000514 175274
03000 807 003140 001402
03100 808 003142 104000
03200 809 003144 000076
03300 810
03400 811 003146 022767 177777 175262
03500 812 003154 001402
03600 813 003156 104002
03700 814 003160 000077
03800 815
03900 816 003162 022767 177777 175250
04000 817 003170 001402
04100 818 003172 104002
04200 819 003174 000100
04300 820
04400 821 003176 122767 000015 175200
04500 822 003204 001402
04600 823 003206 104000
04700 824 003210 000101
04800 825
04900 826

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:*****
:TEST 15: FADD (LSI-11 FLOATING ADD INSTRUCTION)
: 177452,125252 + 177652,125252 = 177777,177777
: PS = 210, STACK POINTER R4
:*****
TST15: SCOPE
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R4 STACK, SET PRIORITY
.WORD 177652,125252 ;SECOND OPERAND ON TOP
.WORD 177452,125252 ;FIRST OPERAND ON BOTTOM
.WORD 357 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER,340 ;FIS TRAP VECTOR
MOV #STACK0,R4 ;SET UP STACK POINTER

NOP
FADD R4 ;FLOATING ADD ON THE R4 STACK

JSR PC, POPR ;POP THE ANSWER
MOV R4, $SP ;SAVE 'STACK POINTER'
CMPB #210, $PSW ;CHECK PS (EXCEPT T BIT)
BEQ .+6 ;BRANCH IF OK
HLT ;PS NOT EQUAL TO 210
75 ;THE ERROR NUMBER IS 75

CMP #STACK4,$SP ;CHECK THE STACK POINTER (R4)
BEQ .+6 ;BRANCH IF OK
HLT ;STACK POINTER (R4) NOT EQUAL TO #STACK4
76 ;THE ERROR NUMBER IS 76

CMP #177777,ANS1 ;CHECK FIRST HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 177777
77 ;THE ERROR NUMBER IS 77

CMP #177777,ANS2 ;CHECK SECOND HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 177777
100 ;THE ERROR NUMBER IS 100

END15: CMPB #15, $TESTN ;CHECK THE TEST NUMBER
BEQ .+6 ;BRANCH IF OK
HLT ;WRONG TEST. PC MUST HAVE FOULED UP.
101 ;THE ERROR NUMBER IS 101

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00300
00400 827
00500 828
00600 829
00700 830
00800 831
00900 832
01000 833
01100 834 003212 104400
01200 835 003214 004567 013140
01300 836 003220 003244
01400 837 003222 104000 104000
01500 838 003226 004000 105004
01600 839 003232 000144
01700 840 003234 016456 000340
01800 841
01900 842 003240 000240
02000 843 003242 075007
02100 844 003244 104000
02200 845 003246 104000
02300 846 003250 004000
02400 847 003252 105004
02500 848
02600 849 003254 004767 013130
02700 850 003260 105767 175146
02800 851 003264 001402
02900 852 003266 104000
03000 853 003270 000102
03100 854
03200 855 003272 022767 104000 175136
03300 856 003300 001402
03400 857 003302 104002
03500 858 003304 000103
03600 859
03700 860 003306 022767 104000 175124
03800 861 003314 001402
03900 862 003316 104002
04000 863 003320 000104
04100 864
04200 865 003322 022767 000401 175112
04300 866 003330 001402
04400 867 003332 104004
04500 868 003334 000105
04600 869
04700 870 003336 005767 175102
04800 871 003342 001402
04900 872 003344 104004
05000 873 003346 000106
05100 874
05200 875 003350 122767 000016 175026
05300 876 003356 001402
05400 877 003360 104000
05500 878 003362 000107
05600 879
05700 880
05800 881
05900 882

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:*****
:TEST 16: FADD (LSI-11 FLOATING ADD INSTRUCTION)
: 004000,105004 + 104000,104000 000401,000000
: PS = 000, STACK POINTER PC
:*****
TST16: SCOPE
JSR R5, PUSH7 ;PUSH 4 WORDS ONTO STACK, SET PRIORITY
.WORD STK16 ;TOP OF STACK
.WORD 104000,104000 ;SECOND OPERAND ON TOP
.WORD 004000,105004 ;FIRST OPERAND ON BOTTOM
.WORD 144 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER,340 ;FIS TRAP VECTOR

NOP
FADD PC ;FLOATING ADD ON FOLLOWING 4 WORDS
STK16: 104000 ;SHOULD CONTAIN 104000
104000 ;SHOULD CONTAIN 104000
004000 ;BEFORE FADD, 004000; AFTER, 000401
105004 ;BEFORE FADD, 105004; AFTER, 000000

JSR PC, POP7 ;POP THE ANSWER
TSTB $PSW ;CHECK PS (EXCEPT T BIT)
BEQ .+6 ;BRANCH IF OK
HLT ;PS NOT EQUAL TO 000
102 ;THE ERROR NUMBER IS 102

CMP #104000,ANS1 ;CHECK FIRST HALF OF INPUT DATA (STK16)
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 104000
103 ;THE ERROR NUMBER IS 103

CMP #104000,ANS2 ;CHECK SECOND HALF OF INPUT DATA (STK16+2)
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 104000
104 ;THE ERROR NUMBER IS 104

CMP #000401,ANS3 ;CHECK FIRST HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+4 ;ANS3 NOT EQUAL TO 000401
105 ;THE ERROR NUMBER IS 105

TST ANS4 ;CHECK SECOND HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+4 ;ANS4 NOT EQUAL TO 000000
106 ;THE ERROR NUMBER IS 106

END16: CMPB #16, $TESTN ;CHECK THE TEST NUMBER
BEQ .+6 ;BRANCH IF OK
HLT ;WRONG TEST! PC MUST HAVE FOULED UP.
107 ;THE ERROR NUMBER IS 107
:*****

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OC100 CVKACC MAR 71 30A(1052) 21-AUG-78 15:28 PAGE 22
 00200 CVKACC.P 16-AUG-78 08:41 TEST FLOATING ADD INSTRUCTION WITH UNDERFLOW

SEQ 0030

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00300      883      :TEST 17:      FADD (LSI-11 FLOATING ADD INSTRUCTION)
00400      884      :              100200,000000 + 000377,177777 > UNDERFLOW
00500      885      :              PS(ON STACK) 012,      STACK POINTER = R3
00600      886      :*****
00700      887      :
00800      888      003364 104400      TST17:  SCOPE
00900      889      003366 004567 012614      JSR      R5,      PUSHR      ;PUSH 4 WORDS ONTO R3 STACK, SET PRIORITY
01000      890      003372 000377 177777      .WORD    000377,177777      ;SECOND OPERAND ON TOP
01100      891      003376 100200 000000      .WORD    100200,000000      ;FIRST OPERAND ON BOTTOM
01200      892      003402 000157      .WORD    157                ;PROCESSOR PRIORITY LEVEL
01300      893      003404 003436 000000      .WORD    ISR17, 000         ;FIS TRAP VECTOR
01400      894      003410 012703 000510      MOV      #STACK0,R3        ;SET UP R3 AS STACK POINTER
01500      895
01600      896      003414 000240      NOP
01700      897      003416 075003      FADD     R3                ;FLOATING ADD ON THE R3 STACK
01800      898
01900      899      003420 004767 012614      RTA17:  JSR      PC,      POPR      ;POP THE 'ANSWER'
02000      900      003424 010367 175004      MOV      R3,      $SP      ;SAVE STACK POINTER (R3)
02100      901      003430 104002      HLT+2    110              ;FIS TRAP DIDN'T OCCURE!
02200      902      003432 000110      110              ;THE ERROR NUMBER IS 110
02300      903      003434 000462      BR
02400      904
02500      905      003436 004767 012626      ISR17:  JSR      PC,      POPER      ;POP ALL DATA OFF THE STACKS
02600      906      003442 010367 174766      MOV      R3,      $SP      ;SAVE STACK POINTER (R3)
02700      907      003446 105767 174760      TSTB    $PSW              ;CHECK PS AFTER FIS TRAP
02800      908      003452 001402      BEQ     .+6                ;BRANCH IF OK
02900      909      003454 104000      HLT     '11              ;PS AFTER FIS TRAP NOT EQUAL TO 000
03000      910      003456 000111      '11              ;THE ERROR NUMBER IS 111
03100      911
03200      912      003460 022767 000510 174746      CMP     #STACK0,$SP      ;CHECK THE STACK POINTER (R3)
03300      913      003466 001402      BEQ     .+6                ;BRANCH IF OK
03400      914      003470 104000      HLT     112              ;STACK POINTER (R3) NOT EQUAL TO #STACK0
03500      915      003472 000112      112              ;THE ERROR NUMBER IS 112
03600      916
03700      917      003474 022767 003420 174734      LMP     #RTA17,ANS1      ;CHECK FIS TRAP RETURN ADDRESS
03800      918      003502 001402      BEQ     .+6                ;BRANCH IF OK
03900      919      003504 104001      HLT+1   113              ;FIS TRAP AT WRONG ADDRESS
04000      920      003506 000113      113              ;THE ERROR NUMBER IS 113
04100      921
04200      922      003510 022767 000012 174722      CMP     #012,ANS2        ;CHECK PS BEFORE FIS TRAP
04300      923      003516 001402      BEQ     .+6                ;BRANCH IF OK
04400      924      003520 104002      HLT+2   114              ;PS AT FIS TRAP TIME NOT 012
04500      925      003522 000114      114              ;THE ERROR NUMBER IS 114
04600      926
04700      927      003524 022767 000377 174710      CMP     #000377,ANS3     ;CHECK DATA FROM THE STACK
04800      928      003532 001402      BEQ     .+6                ;BRANCH IF OK
04900      929      003534 104004      HLT+4   115              ;DATA ON STACK (000377) CHANGED
05000      930      003536 000115      115              ;THE ERROR NUMBER IS 115
05100      931
05200      932      003540 022767 177777 174676      CMP     #177777,ANS4     ;CHECK DATA FROM STACK
05300      933      003546 001402      BEQ     .+6                ;BRANCH IF OK
05400      934      003550 104004      HLT+4   116              ;DATA ON STACK (177777) CHANGED
05500      935      003552 000116      116              ;THE ERROR NUMBER IS 116
05600      936
05700      937      003554 022767 100200 174664      CMP     #100200,ANS5     ;CHECK DATA FROM STACK
05800      938      003562 001402      BEQ     .+6                ;BRANCH IF OK
05900

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00100 CVKACC MACY11 30A(1052) 21-AUG-78 15:28 PAGE 23
00200 CVKACC.P11 16-AUG-78 08:41 TEST FLOATING ADD INSTRUCTION WITH UNDERFLOW SEQ 0031
00300
00400 939 003564 104006 HLT+6 ;DATA ON STACK (100200) CHANGED
00500 940 003566 000117 117 ;THE ERROR NUMBER IS 117
00600 941
00700 942 003570 005767 174654 TST ANS6 ;CHECK DATA FROM STACK
00800 943 003574 001402 BEQ .+6 ;BRANCH IF OK
00900 944 003576 104006 HLT+6 ;DATA ON STACK (000000) CHANGED
01000 945 003600 000120 120 ;THE ERROR NUMBER IS 120
01100 946
01200 947 003602 122767 000017 174574 END17: CMPB #17, $TESTN ;CHECK THE TEST NUMBER
01300 948 003610 001402 BEQ .+6 ;BRANCH IF OK
01400 949 003612 104000 HLT ;WRONG TEST! PC MUST HAVE FOULED UP.
01500 950 003614 000121 121 ;THE ERROR NUMBER IS 121
01600 951
01700 952

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CVKACC MAR 11 30A(1052) 21-AUG-78 15:28 PAGE 24
CVKACC, P11 16-AUG-78 08:41 TEST FLOATING ADD INSTRUCTION WITH UNDERFLOW SEQ 0032
00100 953
00200 954
00300 955
00400 956
00500 957
00600 958
00700 959
00800 960
00900 961
01000 962
01100 963
01200 964
01300 965
01400 966
01500 967
01600 968
01700 969
01800 970
01900 971
02000 972
02100 973
02200 974
02300 975
02400 976
02500 977
02600 978
02700 979
02800 980
02900 981
03000 982
03100 983
03200 984
03300 985
03400 986
03500 987
03600 988
03700 989
03800 990
03900 991
04000 992
04100 993
04200 994
04300 995
04400 996
04500 997
04600 998
04700 999
04800 1000
04900 1001
05000 1002
05100 1003
05200 1004
05300 1005
05400 1006
05500 1007
05600 1008
05700 1009
05800 1010
05900 1011

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003616 104400
003620 004567 012210
003624 100377 177777
003630 000200 000000
003634 000257
003636 003664 000340
003642 000240
003644 075006
003646 004767 012222
003652 104002
003654 000122
003656 012706 000600
003662 000463
003664 004767 012236
003670 022706 000600
003674 001405
003676 012706 000600
003702 104000
003704 000123
003706 000451
003710 122767 000340 174514
003716 001402
003720 104000
003722 000124
003724 022767 003646 174504
003732 001402
003734 104001
003736 000125
003740 022767 000212 174472
003746 001402
003750 104002
003752 000126
003754 022767 100377 174460
003762 001402
003764 104004
003766 000127
003770 022767 177777 174448
003776 001402
004000 104004
004002 000130

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:*****
:TEST 20: FADD (LSI-11 FLOATING ADD INSTRUCTION)
: 000200,000000 + 100377,177777 ==> UNDERFLOW
: PS(ON STACK) - 212, STACK POINTER = SP
:*****
TST20:  SCOPE
        JSR   R5, PUSHS ;PUSH 4 WORDS ONTO STACK, SET PRIORITY
        .WORD 100377,177777 ;SECOND OPERAND ON TOP
        .WORD 000200,000000 ;FIRST OPERAND ON BOTTOM
        .WORD 257 ;PROCESSOR PRIORITY LEVEL
        .WORD ISR20, 340 ;FIS TRAP VECTOR

        NOP
        FADD  SP ;FLOATING ADD ON THE STACK

RTA20:  JSR   PC, POPS ;POP THE 'ANSWER'
        HLT+2 ;FIS TRAP DIDN'T OCCURE!
        122 ;THE ERROR NUMBER IS 122
        MOV  #BEGIN, SP ;RESTORE THE STACK POINTER
        BR   END20

ISR20:  JSR   PC, POPES ;POP ALL DATA OFF THE STACK
        CMP  #BEGIN, SP ;CHECK THE STACK POINTER
        BEQ  ISA20 ;BRANCH IF OK
        MOV  #BEGIN, SP ;RESTORE THE STACK POINTER
        HLT  123 ;STACK POINTER FOULED UP
        BR   123 ;THE ERROR NUMBER IS 123
        BR   END20 ;SKIP REST OF TEST

ISA20:  CMPB  #340, $PSW ;CHECK PS AFTER FIS TRAP
        BEQ  .+6 ;BRANCH IF OK
        HLT  124 ;PS AFTER FIS TRAP NOT EQUAL TO 340
        BR   124 ;THE ERROR NUMBER IS 124

        CMP  #RTA20, ANS1 ;CHECK FIS TRAP RETURN ADDRESS
        BEQ  .+6 ;BRANCH IF OK
        HLT+1 ;FIS TRAP AT WRONG ADDRESS
        125 ;THE ERROR NUMBER IS 125

        CMP  #212, ANS2 ;CHECK PS BEFORE FIS TRAP
        BEQ  .+6 ;BRANCH IF OK
        HLT+2 ;PS AT FIS TRAP TIME NOT 212
        126 ;THE ERROR NUMBER IS 126

        CMP  #100377, ANS3 ;CHECK DATA FROM THE STACK
        BEQ  .+6 ;BRANCH IF OK
        HLT+4 ;DATA ON STACK (100377) CHANGED
        127 ;THE ERROR NUMBER IS 127

        CMP  #177777, ANS4 ;CHECK DATA FROM STACK
        BEQ  .+6 ;BRANCH IF OK
        HLT+4 ;DATA ON STACK (177777) CHANGED
        130 ;THE ERROR NUMBER IS 130

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00100 (VKACC MAY11 30A(1052) 21-AUG-78 15:28 PAGE 26
00200 (VKACC.P11 16-AUG-78 08:41

TEST FLOATING ADD INSTRUCTION WITH OVERFLOW

SEQ 0034

00400 1025
00500 1026
00600 1027
00700 1028
00800 1029
00900 1030
01000 1031
01100 1032 004046 104400
01200 1033 004050 004567 012132
01300 1034 004054 177652 125252
01400 1035 004060 177452 125253
01500 1036 004064 000105
01600 1037 004066 004120 000252
01700 1038 004072 012701 000510
01800 1039
01900 1040 004076 000240
02000 1041 004100 075001
02100 1042
02200 1043 004102 004767 012132
02300 1044 004106 010167 174322
02400 1045 004112 104002
02500 1046 004114 000134
02600 1047 004116 000464
02700 1048
02800 1049 004120 004767 012144
02900 1050 004124 010167 174304
03000 1051 004130 122767 000252 174274
03100 1052 004136 001402
03200 1053 004140 104000
03300 1054 004142 000135
03400 1055
03500 1056 004144 022767 000510 174262
03600 1057 004152 001402
03700 1058 004154 104000
03800 1059 004156 000136
03900 1060
04000 1061 004160 022767 004102 174250
04100 1062 004166 001402
04200 1063 004170 104001
04300 1064 004172 000137
04400 1065
04500 1066 004174 022767 000002 174236
04600 1067 004202 001402
04700 1068 004204 104002
04800 1069 004206 000140
04900 1070
05000 1071 004210 022767 177652 174224
05100 1072 004216 001402
05200 1073 004220 104004
05300 1074 004222 000141
05400 1075
05500 1076 004224 022767 125252 174212
05600 1077 004232 001402
05700 1078 004234 104004
05800 1079 004236 000142

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:*****
:TEST 21:      FADD (LSI-11 FLOATING ADD INSTRUCTION)
:              177452,125253 + 177652,125252 --> OVERFLOW
:              PS(ON STACK) 002,      STACK POINTER R1
:*****
TST21:  SCOPE
        JSR      R5,      PUSHR      ;PUSH 4 WORDS ONTO R1 STACK, SET PRIORITY
        .WORD   177652,125252      ;SECOND OPERAND ON TOP
        .WORD   177452,125253      ;FIRST OPERAND ON BOTTOM
        .WORD   105                ;PROCESSOR PRIORITY LEVEL
        .WORD   ISR21, 252          ;FIS TRAP VECTOR
        MOV     #STACK0,R1         ;SET UP R1 AS STACK POINTER

        NOP
        FADD   R1                  ;FLOATING ADD ON THE R1 STACK

RTA21:  JSR      PC,      POPR       ;POP THE 'ANSWER'
        MOV     R1,      $SP        ;SAVE STACK POINTER (R1)
        HLT+2  134                ;FIS TRAP DIDN'T OCCURE!
        BR     END21              ;THE ERROR NUMBER IS 134

ISR21:  JSR      PC,      POPER      ;POP ALL DATA OFF THE STACKS
        MOV     R1,      $SP        ;SAVE STACK POINTER (R1)
        CMPB   #252, $PSW          ;CHECK PS AFTER FIS TRAP
        BEQ    +6                 ;BRANCH IF OK
        HLT    135                ;PS AFTER FIS TRAP NOT EQUAL TO 252
        ;THE ERROR NUMBER IS 135

        CMP     #STACK0,$SP        ;CHECK THE STACK POINTER (R1)
        BEQ    +6                 ;BRANCH IF OK
        HLT    136                ;STACK POINTER (R1) NOT EQUAL TO #STACK0
        ;THE ERROR NUMBER IS 136

        CMP     #RTA21, ANS1       ;CHECK FIS TRAP RETURN ADDRESS
        BEQ    +6                 ;BRANCH IF OK
        HLT+1  137                ;FIS TRAP AT WRONG ADDRESS
        ;THE ERROR NUMBER IS 137

        CMP     #002, ANS2         ;CHECK PS BEFORE FIS TRAP
        BEQ    +6                 ;BRANCH IF OK
        HLT+2  140                ;PS AT FIS TRAP TIME NOT 002
        ;THE ERROR NUMBER IS 140

        CMP     #177652,ANS3       ;CHECK DATA FROM THE STACK
        BEQ    +6                 ;BRANCH IF OK
        HLT+4  141                ;DATA ON STACK (177652) CHANGED
        ;THE ERROR NUMBER IS 141

        CMP     #125252,ANS4       ;CHECK DATA FROM STACK
        BEQ    +6                 ;BRANCH IF OK
        HLT+4  142                ;DATA ON STACK (125252) CHANGED
        ;THE ERROR NUMBER IS 142

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00100
00200
00300
00400
00500
00600
00700
00800
00900
01000
01100
01200
01300
01400
01500
01600
01700
01800
01900

(VKACC MACY11 30A(1052) 21-AUG-78 15:28 PAGE 27
(VKACC.P11 16-AUG-78 08:41
1081 004240 022767 177452 174200
1082 004246 001402
1083 004250 104006
1084 004252 000143
1085
1086 004254 022767 125253 174166
1087 004262 001402
1088 004264 104006
1089 004266 000144
1090
1091 004270 122767 000021 174106
1092 004276 001402
1093 004300 104000
1094 004302 000145
1095
1096

TEST FLOATING ADD INSTRUCTION WITH OVERFLOW

SEQ 0035

CMP #177452,ANS5 ;CHECK DATA FROM STACK
BEQ .+6 ;BRANCH IF OK
HLT+6 ;DATA ON STACK (177452) CHANGED
143 ;THE ERROR NUMBER IS 143

CMP #125253,ANS6 ;CHECK DATA FROM STACK
BEQ .+6 ;BRANCH IF OK
HLT+6 ;DATA ON STACK (125253) CHANGED
144 ;THE ERROR NUMBER IS 144

END21: CMPB #21, \$TESTN ;CHECK THE TEST NUMBER
BEQ .+6 ;BRANCH IF OK
HLT ;WRONG TEST! FC MUST HAVE FOULED UP.
145 ;THE ERROR NUMBER IS 145

00100
00200
00300
00400 1097
00500 1098
00600 1099
00700 1100
00800 1101
00900 1102
01000 1103
01100 1104 004304 104400
01200 1105 004306 004567 011522
01300 1106 004312 077452 125252
01400 1107 004316 077652 125253
01500 1108 004322 000003
01600 1109 004324 004352 000344
01700 1110
01800 1111 004330 000240
01900 1112 004332 075006
02000 1113
02100 1114 004334 004767 011534
02200 1115 004340 104002
02300 1116 004342 000146
02400 1117 004344 012706 000600
02500 1118 004350 000464
02600 1119
02700 1120 004352 004767 011550
02800 1121 004356 022706 000600
02900 1122 004362 001405
03000 1123 004364 012706 000600
03100 1124 004370 104000
03200 1125 004372 000147
03300 1126 004374 000452
03400 1127
03500 1128 004376 122767 000344 174026
03600 1129 004404 001402
03700 1130 004406 104000
03800 1131 004410 000150
03900 1132
04000 1133 004412 022767 004334 174026
04100 1134 004420 001402
04200 1135 004422 104001
04300 1136 004424 000151
04400 1137
04500 1138 004426 022767 000002 174004
04600 1139 004434 001402
04700 1140 004436 104002
04800 1141 004440 000152
04900 1142
05000 1143 004442 022767 077452 173772
05100 1144 004450 001402
05200 1145 004452 104004
05300 1146 004454 000153
05400 1147
05500 1148 004456 022767 125252 173760
05600 1149 004464 001402
05700 1150 004466 104004
05800 1151 004470 000154
05900 1152

```

:*****
:TEST 22:      FADD (LSI-1) FLOATING ADD INSTRUCTION)
:              077652,125253 + 077452,125252 ==> OVERFLOW
:              PS(ON STACK) = 002,      STACK POINTER  SP
:*****

```

```

TST22:  JSR      R5,      PUSH5      ;PUSH 4 WORDS ONTO STACK, SET PRIORITY
:         .WORD    077452,125252    ;SECOND OPERAND ON TOP
:         .WORD    077652,125253    ;FIRST OPERAND ON BOTTOM
:         .WORD    003              ;PROCESSOR PRIORITY LEVEL
:         .WORD    ISR22, 344       ;FIS TRAP VECTOR

```

```

NOP
FADD    SP              ;FLOATING ADD ON THE STACK

```

```

RTA22:  JSR      PC,      POPS       ;POP THE 'ANSWER'
:         HLT+2    146           ;FIS TRAP DIDN'T OCCURE!
:         MOV      #BEGIN, SP     ;THE ERROR NUMBER IS 146
:         BR       END22         ;RESTORE THE STACK POINTER

```

```

ISR22:  JSR      PC,      POPES      ;POP ALL DATA OFF THE STACK
:         CMP      #BEGIN, SP     ;CHECK THE STACK POINTER
:         BEQ     ISA22          ;BRANCH IF OK
:         MOV      #BEGIN, SP     ;RESTORE THE STACK POINTER
:         HLT     147            ;STACK POINTER FOULED UP
:         BR       END22         ;THE ERROR NUMBER IS 147
:         BR       END22         ;SKIP REST OF TEST

```

```

ISA22:  CMP     #344,  $PSW       ;CHECK PS AFTER FIS TRAP
:         BEQ     .+6            ;BRANCH IF OK
:         HLT     150            ;PS AFTER FIS TRAP NOT EQUAL TO 344
:         BR       .+50         ;THE ERROR NUMBER IS 150

```

```

CMP     #RTA22, ANS1         ;CHECK FIS TRAP RETURN ADDRESS
:         BEQ     .+6            ;BRANCH IF OK
:         HLT+1  151            ;FIS TRAP AT WRONG ADDRESS
:         BR       .+51         ;THE ERROR NUMBER IS 151

```

```

CMP     #002,  ANS2         ;CHECK PS BEFORE FIS TRAP
:         BEQ     .+6            ;BRANCH IF OK
:         HLT+2  152            ;PS AT FIS TRAP TIME NOT 002
:         BR       .+52         ;THE ERROR NUMBER IS 152

```

```

CMP     #077452, ANS3       ;CHECK DATA FROM THE STACK
:         BEQ     .+6            ;BRANCH IF OK
:         HLT+4  153            ;DATA ON STACK (077452) CHANGE!
:         BR       .+53         ;THE ERROR NUMBER IS 153

```

```

CMP     #125252, ANS4       ;CHECK DATA FROM STACK
:         BEQ     .+6            ;BRANCH IF OK
:         HLT+4  154            ;DATA ON STACK (125252) CHANGE!
:         BR       .+54         ;THE ERROR NUMBER IS 154

```

```

00100 CVKACC MACY 1 30A(1052) 21-AUG-78 15:28 PAGE 29
00200 CVKACC.P11 16-AUG-78 08:41 TEST FLOATING ADD INSTRUCTION WITH OVERFLOW
00300
00400 1153 004472 022767 077652 173746 CMP #077652,ANS5 ;CHECK DATA FROM STACK
00500 1154 004500 001402 BEQ .+6 ;BRANCH IF OK
00600 1155 004502 104006 HLT+6 ;DATA ON STACK (077652) CHANGED
00700 1156 004504 000155 155 ;THE ERROR NUMBER IS 155
00800 1157
00900 1158 004506 022767 125253 173734 CMP #125253,ANS6 ;CHECK DATA FROM STACK
01000 1159 004514 001402 BEQ .+6 ;BRANCH IF OK
01100 1160 004516 104006 HLT+6 ;DATA ON STACK (125253) CHANGED
01200 1161 004520 000156 156 ;THE ERROR NUMBER IS 156
01300 1162
01400 1163 004522 122767 000022 173654 END22: CMPB #22, $TESTN ;CHECK THE TEST NUMBER
01500 1164 004530 001402 BEQ .+6 ;BRANCH IF OK
01600 1165 004532 104000 HLT ;WRONG TEST! PC MUST HAVE FOULED UP.
01700 1166 004534 000157 157 ;THE ERROR NUMBER IS 157
01800 1167
01900 1168

```

```

00300
00400 1169
00500 1170
00600 1171
00700 1172
00800 1173
00900 1174
01000 1175
01100 1176 004536 104400
01200 1177 004540 004567 011442
01300 1178 004544 135352 051107
01400 1179 004550 177520 017552
01500 1180 004554 000040
01600 1181 004556 016456 000340
01700 1182 004562 012701 000510
01800 1183
01900 1184 004566 000240
02000 1185 004570 075011
02100 1186
02200 1187 004572 004767 011442
02300 1188 004576 010167 173632
02400 1189 004602 122767 000010 173622
02500 1190 004610 001402
02600 1191 004612 104000
02700 1192 004614 000160
02800 1193
02900 1194 004616 022767 000514 173610
03000 1195 004624 001402
03100 1196 004626 104000
03200 1197 004630 000161
03300 1198
03400 1199 004632 022767 177520 173576
03500 1200 004640 001402
03600 1201 004642 104002
03700 1202 004644 000162
03800 1203
03900 1204 004646 022767 017552 173564
04000 1205 004654 001402
04100 1206 004656 104002
04200 1207 004660 000163
04300 1208
04400 1209 004662 122767 000023 173514
04500 1210 004670 001402
04600 1211 004672 104000
04700 1212 004674 000164
04800 1213
04900 1214

```

```

*****
TEST 23: FSUB (LSI-11 FLOATING SUBTRACT INSTRUCTION)
177520,017552 - 135352,051107 = 177520,017552
PS = 010, STACK POINTER - R1
*****

TST23: SCOPE
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R1 STACK, SET PRIORITY
.WORD 135352,051107 ;SECOND OPERAND ON TOP
.WORD 177520,017552 ;FIRST OPERAND ON BOTTOM
.WORD 040 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER, 340 ;FIS TRAP VECTOR
MOV #STACK0,R1 ;SET UP STACK POINTER

NOP
FSUB R1 ;FLOATING SUBTRACT ON THE R1 STACK

JSR PC, POPR ;POP THE ANSWER
MOV R1, $SP ;SAVE 'STACK POINTER'
CMPB #010, $PSW ;CHECK PS (EXCEPT T BIT)
BEQ .+6 ;BRANCH IF OK
HLT 160 ;PS NOT EQUAL TO 010
;THE ERROR NUMBER IS 160

CMP #STACK4,$SP ;CHECK THE STACK POINTER (R1)
BEQ .+6 ;BRANCH IF OK
HLT 161 ;STACK POINTER (R1) NOT EQUAL TO #STACK4
;THE ERROR NUMBER IS 161

CMP #177520,ANS1 ;CHECK FIRST HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 162 ;ANS1 NOT EQUAL TO 177520
;THE ERROR NUMBER IS 162

CMP #017552,ANS2 ;CHECK SECOND HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 163 ;ANS2 NOT EQUAL TO 017552
;THE ERROR NUMBER IS 163

END23: (MPB #23, $TESTN ;CHECK THE TEST NUMBER
BEQ .+6 ;BRANCH IF OK
HLT 164 ;WRONG TEST! PC MUST HAVE FOULED UP.
;THE ERROR NUMBER IS 164

```

00300 1215
00400 1216
00500 1217
00600 1218
00700 1219
00800 1220
00900 1221
01000 1222 004676 104400
01100 1223 004700 004567 011302
01200 1224 004704 125252 125253
01300 1225 004710 125252 125252
01400 1226 004714 000047
01500 1227 004716 016456 000340
01600 1228 004722 012700 000510
01700 1229
01800 1230 004726 000240
01900 1231 004730 075010
02000 1232
02100 1233 004732 004767 011302
02200 1234 004736 010067 173472
02300 1235 004742 105767 173464
02400 1236 004746 001402
02500 1237 004750 104000
02600 1238 004752 000165
02700 1239
02800 1240 004754 022767 000514 173452
02900 1241 004762 001402
03000 1242 004764 104000
03100 1243 004766 000166
03200 1244
03300 1245 004770 022767 017400 173440
03400 1246 004776 001402
03500 1247 005000 104002
03600 1248 005002 000167
03700 1249
03800 1250 005004 005767 173430
03900 1251 005010 001402
04000 1252 005012 104002
04100 1253 005014 000170
04200 1254
04300 1255 005016 122767 000024 173360
04400 1256 005024 001402
04500 1257 005026 104000
04600 1258 005030 000171
04700 1259
04800 1260
04900

```

:*****
:TEST 24: FSUB (LSI-11 FLOATING SUBTRACT INSTRUCTION)
:          125252,125252 - 125252,125253 = 017400,000000
:          PS = 000,          STACK POINTER RO
:*****

TST24:  SCOPE
        JSR      R5,      PUSHR      ;PUSH 4 WORDS ONTO RO STACK, SET PRIORITY
        .WORD   125252,125253      ;SECOND OPERAND ON TOP
        .WORD   125252,125252      ;FIRST OPERAND ON BOTTOM
        .WORD   047                ;PROCESSOR PRIORITY LEVEL
        .WORD   TRAPER, 340        ;FIS TRAP VECTOR
        MOV     #STACK0,RO        ;SET UP STACK POINTER

        NOP
        FSUB   RO                ;FLOATING SUBTRACT ON THE RO STACK

        JSR    PC,      POPR      ;POP THE ANSWER
        MOV    RO,      $SSP     ;SAVE 'STACK POINTER'
        TSTB  $PSW         ;CHECK PS (EXCEPT T BIT)
        BEQ   .+6          ;BRANCH IF OK
        HLT   165          ;PS NOT EQUAL TO 000
                          ;THE ERROR NUMBER IS 165

        CMP   #STACK4,$SP      ;CHECK THE STACK POINTER (RO)
        BEQ  .+6              ;BRANCH IF OK
        HLT  166              ;STACK POINTER (RO) NOT EQUAL TO #STACK4
                          ;THE ERROR NUMBER IS 166

        CMP   #017400,ANS1     ;CHECK FIRST HALF OF ANSWER
        BEQ  .+6              ;BRANCH IF OK
        HLT+2 167             ;ANS1 NOT EQUAL TO 017400
                          ;THE ERROR NUMBER IS 167

        TST  ANS2              ;CHECK SECOND HALF OF ANSWER
        BEQ  .+6              ;BRANCH IF OK
        HLT+2 170             ;ANS2 NOT EQUAL TO 000000
                          ;THE ERROR NUMBER IS 170

END24:  CMPB  #24,      $TESTN   ;CHECK THE TEST NUMBER
        BEQ  .+6              ;BRANCH IF OK
        HLT  171              ;WRONG TEST, PC MUST HAVE FOULED UP.
                          ;THE ERROR NUMBER IS 171

```

```

00300
00400 1261
00500 1262
00600 1263
00700 1264
00800 1265
00900 1266
01000 1267
01100 1268 005032 104400
01200 1269 005034 004567 010774
01300 1270 005040 100177 177777
01400 1271 005044 002460 123456
01500 1272 005050 000015
01600 1273 005052 016456 000340
01700 1274
01800 1275 005056 000240
01900 1276 005060 075016
02000 1277
02100 1278 005062 004767 011006
02200 1279 005066 022706 000600
02300 1280 005072 001405
02400 1281 005074 012706 000600
02500 1282
02600 1283
02700 1284
02800 1285
02900 1286
03000 1287
03100 1288 005100 104000
03200 1289 005102 000172
03300 1290 005104 000422
03400 1291
03500 1292 005106 122767 000000 173316
03600 1293 005114 001402
03700 1294 005116 104000
03800 1295 005120 000173
03900 1296
04000 1297 005122 022767 002460 173306
04100 1298 005130 001402
04200 1299 005132 104002
04300 1300 005134 000174
04400 1301
04500 1302 005136 022767 123456 173274
04600 1303 005144 001402
04700 1304 005146 104002
04800 1305 005150 000175
04900 1306
05000 1307 005152 122767 000025 173224
05100 1308 005160 001402
05200 1309 005162 104000
05300 1310 005164 000176
05400 1311
05500 1312

```

```

:*****
:TEST 25: FSUB (LSI-11 FLOATING SUBTRACT INSTRUCTION)
: 002460,123456 - 100177,177777 - 002460,123456
: PS 000, STACK POINTER SP
:*****

```

```

TST25: SCOPE
JSR R5, PUSH5 :PUSH 4 WORDS ONTO STACK, SET PRIORITY
.WORD 100177,177777 :SECOND OPERAND ON TOP
.WORD 002460,123456 :FIRST OPERAND ON BOTTOM
.WORD 015 :PROCESSOR PRIORITY LEVEL
.WORD TRAPER, 340 :FIS TRAP VECTOR

NOP
FSUB SP :FLOATING SUBTRACT ON THE STACK

JSR PC, POPS :POP THE ANSWER
CMP #BEGIN, SP :CHECK THE STACK POINTER
BEQ TSA25 :BRANCH IF OK
MOV #BEGIN, SP :RESTORE STACK POINTER

HLT :STACK POINTER FOULED UP
172 :THE ERROR NUMBER IS 172
BR END25 :SKIP REST OF TEST

TSA25: CMPB #000, $PSW :CHECK PS (EXCEPT T BIT)
BEQ .+6 :BRANCH IF OK
HLT :PS NOT EQUAL TO 000
173 :THE ERROR NUMBER IS 173

CMP #002460,ANS1 :CHECK FIRST HALF OF ANSWER
BEQ .+6 :BRANCH IF OK
HLT+2 :ANS1 NOT EQUAL TO 002460
174 :THE ERROR NUMBER IS 174

CMP #123456,ANS2 :CHECK SECOND HALF OF ANSWER
BEQ .+6 :BRANCH IF OK
HLT+2 :ANS2 NOT EQUAL TO 123456
175 :THE ERROR NUMBER IS 175

END25: CMPB #25, $TESTN :CHECK THE TEST NUMBER
BEQ .+6 :BRANCH IF OK
HLT :WRONG TEST. PC MUST HAVE FOULED UP.
176 :THE ERROR NUMBER IS 176

```


00300
00400 1313
00500 1314
00600 1315
00700 1316
00800 1317
00900 1318
01000 1319
01100 1320 005166 104400
01200 1321 005170 004567 011012
01300 1322 005174 000252 125252
01400 1323 005200 000425 052525
01500 1324 005204 000217
01600 1325 005206 016456 000340
01700 1326 005212 012704 000510
01800 1327
01900 1328 005216 000240
02000 1329 005220 075014
02100 1330
02200 1331 005222 004767 011012
02300 1332 005226 010467 173202
02400 1333 005232 122767 000200 173172
02500 1334 005240 001402
02600 1335 005242 104000
02700 1336 005244 000177
02800 1337
02900 1338 005246 022767 000514 173160
03000 1339 005254 001402
03100 1340 005256 104000
03200 1341 005260 000200
03300 1342
03400 1343 005262 022767 000200 173146
03500 1344 005270 001402
03600 1345 005272 104002
03700 1346 005274 000201
03800 1347
03900 1348 005276 005767 173136
04000 1349 005302 001402
04100 1350 005304 104002
04200 1351 005306 000202
04300 1352
04400 1353 005310 122767 000026 173066
04500 1354 005316 001402
04600 1355 005320 104000
04700 1356 005322 000203
04800 1357
04900 1358

```

:*****
:TEST 26:      FSUB (LSI-11 FLOATING SUBTRACT INSTRUCTION)
:              000425,052525 - 000252,125252 = 000200,000000
:              PS - 200,      STACK POINTER = R4
:*****

TST26:  SCOPE
        JSR   R5,      PUSH 4 WORDS ONTO R4 STACK, SET PRIORITY
        .WORD 000252,125252 :SECOND OPERAND ON TOP
        .WORD 000425,052525 :FIRST OPERAND ON BOTTOM
        .WORD 217        :PROCESSOR PRIORITY LEVEL
        .WORD TRAPER, 340 :FIS TRAP VECTOR
        MOV   #STACK0,R4  :SET UP STACK POINTER

        NOP
        FSUB  R4          :FLOATING SUBTRACT ON THE R4 STACK

        JSR   PC,      POPR :POP THE ANSWER
        MOV   R4,      $SP  :SAVE 'STACK POINTER'
        CMPB #200,    $PSW  :CHECK PS (EXCEPT T BIT)
        BEQ   .+6       :BRANCH IF OK
        HLT   177      :PS NOT EQUAL TO 200
                    :THE ERROR NUMBER IS 177

        CMP   #STACK4,$SP  :CHECK THE STACK POINTER (R4)
        BEQ   .+6       :BRANCH IF OK
        HLT   200      :STACK POINTER (R4) NOT EQUAL TO #STACK4
                    :THE ERROR NUMBER IS 200

        CMP   #000200,ANS1 :CHECK FIRST HALF OF ANSWER
        BEQ   .+6       :BRANCH IF OK
        HLT+2 201      :ANS1 NOT EQUAL TO 000200
                    :THE ERROR NUMBER IS 201

        IST   ANS2       :CHECK SECOND HALF OF ANSWER
        BEQ   .+6       :BRANCH IF OK
        HLT+2 202      :ANS2 NOT EQUAL TO 000000
                    :THE ERROR NUMBER IS 202

END26:  CMPB #26,      $TESTN :CHECK THE TEST NUMBER
        BEQ   .+6       :BRANCH IF OK
        HLT   203      :WRONG TEST, PC MUST HAVE FOULED UP.
                    :THE ERROR NUMBER IS 203

```

```

00100
00200
00300
00400 1359
00500 1360
00600 1361
00700 1362
00800 1363
00900 1364
01000 1365
01100 1366 005324 104400
01200 1367 005326 004567 010502
01300 1368 005332 077652 125252
01400 1369 005336 177452 125252
01500 1370 005342 000357
01600 1371 005344 016456 000340
01700 1372
01800 1373 005350 000240
01900 1374 005352 075016
02000 1375
02100 1376 005354 004767 010514
02200 1377 005360 022706 000600
02300 1378 005364 001405
02400 1379 005366 012706 000600
02500 1380
02600 1381
02700 1382
02800 1383
02900 1384
03000 1385
03100 1386 005372 104000
03200 1387 005374 000204
03300 1388 005376 000422
03400 1389
03500 1390 005400 122767 000210 173024
03600 1391 005406 001402
03700 1392 005410 104000
03800 1393 005412 000205
03900 1394
04000 1395 005414 022767 177777 173014
04100 1396 005422 001402
04200 1397 005424 104002
04300 1398 005426 000206
04400 1399
04500 1400 005430 022767 177777 173002
04600 1401 005436 001402
04700 1402 005440 104002
04800 1403 005442 000207
04900 1404
05000 1405 005444 122767 000027 172732
05100 1406 005452 001402
05200 1407 005454 104000
05300 1408 005456 000210
05400 1409
05500 1410

```

```

:*****
:TEST 27: FSUB (LSI-11 FLOATING SUBTRACT INSTRUCTION)
: 177452,125252 - 077652,125252 = 177777,177777
: PS = 210, STACK POINTER SP
:*****
TST27: SCOPE
JSR R5, PUSHES ;PUSH 4 WORDS ONTO STACK, SET PRIORITY
.WORD 077652,125252 ;SECOND OPERAND ON TOP
.WORD 177452,125252 ;FIRST OPERAND ON BOTTOM
.WORD 357 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER, 340 ;FIS TRAP VECTOR

NOP
FSUB SP ;FLOATING SUBTRACT ON THE STACK

JSR PC, POPS ;POP THE ANSWER
CMP #BEGIN, SP ;CHECK THE STACK POINTER
BEQ TSA27 ;BRANCH IF OK
MOV #BEGIN, SP ;RESTORE STACK POINTER

HLT ;STACK POINTER FOULED UP
204 ;THE ERROR NUMBER IS 204
BR END27 ;SKIP REST OF TEST

TSA27: CMPB #210, $PSW ;CHECK PS (EXCEPT T BIT)
BEQ .+6 ;BRANCH IF OK
HLT ;PS NOT EQUAL TO 210
205 ;THE ERROR NUMBER IS 205

CMP #177777,ANS1 ;CHECK FIRST HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 177777
206 ;THE ERROR NUMBER IS 206

CMP #177777,ANS2 ;CHECK SECOND HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 177777
207 ;THE ERROR NUMBER IS 207

END27: CMPB #27, $TESTN ;CHECK THE TEST NUMBER
BEQ .+6 ;BRANCH IF OK
HLT ;WRONG TEST, PC MUST HAVE FOULED UP.
210 ;THE ERROR NUMBER IS 210

```


00100
00200
00300
00400
00500
00600
00700
00800
00900
01000
01100
01200
01300
01400
01500
01600
01700
01800
01900
02000
02100
02200
02300
02400
02500
02600
02700
02800
02900
03000
03100
03200
03300
03400
03500
03600
03700
03800
03900
04000
04100
04200
04300
04400
04500
04600
04700
04800
04900

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1457
1458
1459
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1464 005620 104400
1465 005622 004567 010360
1466 005626 135152 125252
1467 005632 143325 052525
1468 005636 000243
1469 005640 016456 000340
1470 005644 012700 000510
1471
1472 005650 000240
1473 005652 075010
1474
1475 005654 004767 010360
1476 005660 010067 172550
1477 005664 122767 000210 172540
1478 005672 001402
1479 005674 104000
1480 005676 000216
1481
1482 005700 022767 000514 172526
1483 005706 001402
1484 005710 104000
1485 005712 000217
1486
1487 005714 022767 143325 172514
1488 005722 001402
1489 005724 104002
1490 005726 000220
1491
1492 005730 022767 052525 172502
1493 005736 001402
1494 005740 104002
1495 005742 000221
1496
1497 005744 122767 000031 172432
1498 005752 001402
1499 005754 104000
1500 005756 000222
1501
1502

:TEST 31: FSUB (LSI-11 FLOATING SUBTRACT INSTRUCTION)
: 143325,052525 - 135152,125252 - 143325,052525
: PS 210, STACK POINTER - R0

TST31: SCOPE
JSR R5, PUSHR :PUSH 4 WORDS ONTO R0 STACK, SET PRIORITY
.WORD 135152,125252 :SECOND OPERAND ON TOP
.WORD 143325,052525 :FIRST OPERAND ON BOTTOM
.WORD 243 :PROCESSOR PRIORITY LEVEL
.WORD TRAPER, 340 :FIS TRAP VECTOR
MOV #STACK0,R0 :SET UP STACK POINTER

NOP
FSUB R0 :FLOATING SUBTRACT ON THE R0 STACK

JSR PC, POPR :POP THE ANSWER
MOV R0, \$SP :SAVE 'STACK POINTER'
CMPB #210, \$PSW :CHECK PS (EXCEPT T BIT)
BEQ .+6 :BRANCH IF OK
HLT :PS NOT EQUAL TO 210
216 :THE ERROR NUMBER IS 216

CMP #STACK4,\$SP :CHECK THE STACK POINTER (R0)
BEQ .+6 :BRANCH IF OK
HLT :STACK POINTER (R0) NOT EQUAL TO #STACK4
217 :THE ERROR NUMBER IS 217

CMP #143325,ANS1 :CHECK FIRST HALF OF ANSWER
BEQ .+6 :BRANCH IF OK
HLT+2 :ANS1 NOT EQUAL TO 143325
220 :THE ERROR NUMBER IS 220

CMP #052525,ANS2 :CHECK SECOND HALF OF ANSWER
BEQ .+6 :BRANCH IF OK
HLT+2 :ANS2 NOT EQUAL TO 052525
221 :THE ERROR NUMBER IS 221

END31: CMPB #31, \$TESTN :CHECK THE TEST NUMBER
BEQ .+6 :BRANCH IF OK
HLT :WRONG TEST! PC MUST HAVE FOULED UP.
222 :THE ERROR NUMBER IS 222

```

00300
00400 1503
00500 1504
00600 1505
00700 1506
00800 1507
00900 1508
01000 1509
01100 1510 005760 104400
01200 1511 005762 004567 010220
01300 1512 005766 143325 052525
01400 1513 005772 135152 125252
01500 1514 005776 000357
01600 1515 006000 016456 000340
01700 1516 006004 012705 000510
01800 1517
01900 1518 006010 000240
02000 1519 006012 075015
02100 1520
02200 1521 006014 004767 010220
02300 1522 006020 010567 172410
02400 1523 006024 122767 000200 172400
02500 1524 006032 001402
02600 1525 006034 104000
02700 1526 006036 000223
02800 1527
02900 1528 006040 022767 000514 172366
03000 1529 006046 001402
03100 1530 006050 104000
03200 1531 006052 000224
03300 1532
03400 1533 006054 022767 043325 172354
03500 1534 006062 001402
03600 1535 006064 104002
03700 1536 006066 000225
03800 1537
03900 1538 006070 022767 052525 172342
04000 1539 006076 001402
04100 1540 006100 104002
04200 1541 006102 000226
04300 1542
04400 1543 006104 122767 000032 172272
04500 1544 006112 001402
04600 1545 006114 104000
04700 1546 006116 000227
04800 1547
04900 1548

```

```

*****
TEST 32: FSUB (LSI-11 FLOATING SUBTRACT INSTRUCTION)
: 135152,125252 - 143325,052525 - 043325,052525
: PS = 200, STACK POINTER R5
*****

TST32: SCOPE
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R5 STACK, SET PRIORITY
.WORD 143325,052525 ;SECOND OPERAND ON TOP
.WORD 135152,125252 ;FIRST OPERAND ON BOTTOM
.WORD 357 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER, 340 ;FIS TRAP VECTOR
MOV #STACK0,R5 ;SET UP STACK POINTER

NOP
FSUB R5 ;FLOATING SUBTRACT ON THE R5 STACK

JSR PC, POPR ;POP THE ANSWER
MOV R5, $SP ;SAVE 'STACK POINTER'
CMPB #200, $PSW ;CHECK PS (EXCEPT T BIT)
BEQ .+6 ;BRANCH IF OK
HLT ;PS NOT EQUAL TO 200
223 ;THE ERROR NUMBER IS 223

CMP #STACK4,$SP ;CHECK THE STACK POINTER (R5)
BEQ .+6 ;BRANCH IF OK
HLT ;STACK POINTER (R5) NOT EQUAL TO #STACK4
224 ;THE ERROR NUMBER IS 224

CMP #043325,ANS1 ;CHECK FIRST HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 043325
225 ;THE ERROR NUMBER IS 225

CMP #052525,ANS2 ;CHECK SECOND HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 052525
226 ;THE ERROR NUMBER IS 226

END32: CMPB #32, $TESTN ;CHECK THE TEST NUMBER
BEQ .+6 ;BRANCH IF OK
HLT ;WRONG TEST. PC MUST HAVE FOULED UP.
227 ;THE ERROR NUMBER IS 227

```

00100
00200
00300
00400
00500
00600
00700
00800
00900
01000
01100
01200
01300
01400
01500
01600
01700
01800
01900
02000
02100
02200
02300
02400
02500
02600
02700
02800
02900
03000
03100
03200
03300
03400
03500
03600
03700
03800
03900
04000
04100
04200
04300
04400
04500
04600
04700
04800
04900

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1549
1550
1551
1552
1553
1554
1555
1556 006120 104400
1557 006122 004567 010060
1558 006126 035152 125252
1559 006132 043125 052525
1560 006136 000040
1561 006140 016456 000340
1562 006144 012702 000510
1563
1564 006150 000240
1565 006152 075012
1566
1567 006154 004767 010060
1568 006160 010267 172250
1569 006164 105767 172242
1570 006170 001402
1571 006172 104000
1572 006174 000230
1573
1574 006176 022767 000514 172230
1575 006204 001402
1576 006206 104000
1577 006210 000231
1578
1579 006212 022767 043125 172216
1580 006220 001402
1581 006222 104002
1582 006224 000232
1583
1584 006226 022767 052524 172204
1585 006234 001402
1586 006236 104002
1587 006240 000233
1588
1589 006242 122767 000033 172134
1590 006250 001402
1591 006252 104000
1592 006254 000234
1593
1594

H 4

:TEST 33: FSUB (LSI-11 FLOATING SUBTRACT INSTRUCTION)
: 043125,052525 - 035152,125252 = 043125,052524
: PS 000, STACK POINTER = R2
:*****

TST33: SCOPE
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R2 STACK, SET PRIORITY
.WORD 035152,125252 ;SECOND OPERAND ON TOP
.WORD 043125,052525 ;FIRST OPERAND ON BOTTOM
.WORD 040 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER, 340 ;FIS TRAP VECTOR
MOV #STACK0,R2 ;SET UP STACK POINTER

NOP
FSUB R2 ;FLOATING SUBTRACT ON THE R2 STACK

JSR PC, POPR ;POP THE ANSWER
MOV R2, \$SP ;SAVE 'STACK POINTER'
TSTB \$PSW ;CHECK PS (EXCEPT T BIT)
BEQ .+6 ;BRANCH IF OK
HLT ;PS NOT EQUAL TO 000
230 ;THE ERROR NUMBER IS 230

CMP #STACK4,\$SP ;CHECK THE STACK POINTER (R2)
BEQ .+6 ;BRANCH IF OK
HLT ;STACK POINTER (R2) NOT EQUAL TO #STACK4
231 ;THE ERROR NUMBER IS 231

CMP #043125,ANS1 ;CHECK FIRST HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 043125
232 ;THE ERROR NUMBER IS 232

CMP #052524,ANS2 ;CHECK SECOND HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 052524
233 ;THE ERROR NUMBER IS 233

END33: CMPB #33, \$TESTN ;CHECK THE TEST NUMBER
BEQ .+6 ;BRANCH IF OK
HLT ;WRONG TEST. PC MUST HAVE FOULED UP.
234 ;THE ERROR NUMBER IS 234

00100
00200
00300
00400
00500
00600
00700
00800
00900
01000
01100
01200
01300
01400
01500
01600
01700
01800
01900
02000
02100
02200
02300
02400
02500
02600
02700
02800
02900
03000
03100
03200
03300
03400
03500
03600
03700
03800
03900
04000
04100
04200
04300
04400
04500
04600
04700
04800
04900

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1595					
1596					
1597					
1598					
1599					
1600					
1601					
1602	006256	104400			
1603	006260	004567	007722		
1604	006264	000000	000000		
1605	006270	000000	000000		
1606	006274	000217			
1607	006276	016456	000340		
1608	006302	012700	000510		
1609					
1610	006306	000240			
1611	006310	075010			
1612					
1613	006312	004767	007722		
1614	006316	010067	172112		
1615	006322	122767	000204	172102	
1616	006330	001402			
1617	006332	104000			
1618	006334	000235			
1619					
1620	006336	022767	000514	172070	
1621	006344	001402			
1622	006346	104000			
1623	006350	000236			
1624					
1625	006352	005767	172060		
1626	006356	001402			
1627	006360	104002			
1628	006362	000237			
1629					
1630	006364	005767	172050		
1631	006370	001402			
1632	006372	104002			
1633	006374	000240			
1634					
1635	006376	122767	000034	172000	
1636	006404	001402			
1637	006406	104000			
1638	006410	000241			
1639					
1640					

:TEST 34: FSUB (LSI-11 FLOATING SUBTRACT INSTRUCTION)
: 000000,000000 - 000000,000000 = 000000,000000
: PS 204, STACK POINTER - R0
:*****

TST34: SCOPE
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R0 STACK, SET PRIORITY
.WORD 000000,000000 ;SECOND OPERAND ON TOP
.WORD 000000,000000 ;FIRST OPERAND ON BOTTOM
.WORD 217 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER, 340 ;FIS TRAP VECTOR
MOV #STACK0,R0 ;SET UP STACK POINTER

NOP
FSUB R0 ;FLOATING SUBTRACT ON THE R0 STACK

JSR PC, POPR ;POP THE ANSWER
MOV R0, \$SP ;SAVE 'STACK POINTER'
CMPB #204, \$PSW ;CHECK PS (EXCEPT T BIT)
BEQ .+6 ;BRANCH IF OK
HLT ;PS NOT EQUAL TO 204
235 ;THE ERROR NUMBER IS 235

CMP #STACK4,\$SP ;CHECK THE STACK POINTER (R0)
BEQ .+6 ;BRANCH IF OK
HLT ;STACK POINTER (R0) NOT EQUAL TO #STACK4
236 ;THE ERROR NUMBER IS 236

TST ANS1 ;CHECK FIRST HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 000000
237 ;THE ERROR NUMBER IS 237

TST ANS2 ;CHECK SECOND HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 000000
240 ;THE ERROR NUMBER IS 240

END34: CMPB #34, \$TESTN ;CHECK THE TEST NUMBER
BEQ .+6 ;BRANCH IF OK
HLT ;WRONG TEST. PC MUST HAVE FOULED UP.
241 ;THE ERROR NUMBER IS 241

```

00300 1641
00400 1642
00500 1643
00600 1644
00700 1645
00800 1646
00900 1647
01000 1648
01100 1649 006412 104400
01200 1650 006414 004567 007566
01300 1651 006420 177777 177777
01400 1652 006424 000000 000000
01500 1653 006430 000100
01600 1654 006432 016456 000340
01700 1655 006436 012702 000510
01800 1656
01900 1657 006442 000240
02000 1658 006444 075012
02100 1659
02200 1660 006446 004767 007566
02300 1661 006452 010267 171756
02400 1662 006456 105767 171750
02500 1663 006462 001402
02600 1664 006464 104000
02700 1665 006466 000242
02800 1666
02900 1667 006470 022767 000514 171736
03000 1668 006476 001402
03100 1669 006500 104000
03200 1670 006502 000243
03300 1671
03400 1672 006504 022767 077777 171724
03500 1673 006512 001402
03600 1674 006514 104002
03700 1675 006516 000244
03800 1676
03900 1677 006520 022767 177777 171712
04000 1678 006526 001402
04100 1679 006530 104002
04200 1680 006532 000245
04300 1681
04400 1682 006534 122767 000035 171642
04500 1683 006542 001402
04600 1684 006544 104000
04700 1685 006546 000246
04800 1686
04900 1687

```

```

:*****
:TEST 35: FSUB (LSI-11 FLOATING SUBTRACT INSTRUCTION)
:000000,000000 - 177777,177777 = 077777,177777
:PS = 000, STACK POINTER = R2
:*****

```

```

TST35: SCOPE
JSR R5, PUSH4 :PUSH 4 WORDS ONTO R2 STACK, SET PRIORITY
.WORD 177777,177777 :SECOND OPERAND ON TOP
.WORD 000000,000000 :FIRST OPERAND ON BOTTOM
.WORD 100 :PROCESSOR PRIORITY LEVEL
.WORD TRAPER, 340 :FIS TRAP VECTOR
MOV #STACK0,R2 :SET UP STACK POINTER

NOP
FSUB R2 :FLOATING SUBTRACT ON THE R2 STACK

JSR PC, POPR :POP THE ANSWER
MOV R2, $SP :SAVE 'STACK POINTER'
TSTB $PSW :CHECK PS (EXCEPT T BIT)
BEQ .+6 :BRANCH IF OK
HLT :PS NOT EQUAL TO 000
242 :THE ERROR NUMBER IS 242

CMP #STACK4,$SP :CHECK THE STACK POINTER (R2)
BEQ .+6 :BRANCH IF OK
HLT :STACK POINTER (R2) NOT EQUAL TO #STACK4
243 :THE ERROR NUMBER IS 243

CMP #077777,ANS1 :CHECK FIRST HALF OF ANSWER
BEQ .+6 :BRANCH IF OK
HLT+2 :ANS1 NOT EQUAL TO 077777
244 :THE ERROR NUMBER IS 244

CMP #177777,ANS2 :CHECK SECOND HALF OF ANSWER
BEQ .+6 :BRANCH IF OK
HLT+2 :ANS2 NOT EQUAL TO 177777
245 :THE ERROR NUMBER IS 245

END35: CMPB #35, $TESTN :CHECK THE TEST NUMBER
BEQ .+6 :BRANCH IF OK
HLT :WRONG TEST! PC MUST HAVE FOULED UP.
246 :THE ERROR NUMBER IS 246

```



```

00300
00400 1687
00500 1688
00600 1689
00700 1690
00800 1691
00900 1692
01000 1693
01100 1694 006550 104400
01200 1695 006552 004567 007256
01300 1696 006556 077777 177777
01400 1697 006562 000000 000000
01500 1698 006566 000217
01600 1699 006570 016456 000340
01700 1700
01800 1701 006574 000240
01900 1702 006576 075016
02000 1703
02100 1704 006600 004767 007270
02200 1705 006604 022706 000600
02300 1706 006610 001405
02400 1707 006612 012706 000600
02500 1708
02600 1709
02700 1710
02800 1711
02900 1712
03000 1713
03100 1714 006616 104000
03200 1715 006620 000247
03300 1716 006622 000422
03400 1717
03500 1718 006624 122767 000210 171600
03600 1719 006632 001402
03700 1720 006634 104000
03800 1721 006636 000250
03900 1722
04000 1723 006640 022767 177777 171570
04100 1724 006646 001402
04200 1725 006650 104002
04300 1726 006652 000251
04400 1727
04500 1728 006654 022767 177777 171556
04600 1729 006662 001402
04700 1730 006664 104002
04800 1731 006666 000252
04900 1732
05000 1733 006670 122767 000036 171506
05100 1734 006676 001402
05200 1735 006700 104000
05300 1736 006702 000253
05400 1737
05500 1738

```

```

:*****
:TEST 36: FSUB (LSI-11 FLOATING SUBTRACT INSTRUCTION)
: 000000,000000 - 077777,177777 = 177777,177777
: PS - 210, STACK POINTER - SP
:*****

```

```

TST36: SCOPE
JSR R5, PUSH5 ;PUSH 4 WORDS ONTO STACK, SET PRIORITY
.WORD 077777,177777 ;SECOND OPERAND ON TOP
.WORD 000000,000000 ;FIRST OPERAND ON BOTTOM
.WORD 217 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER, 340 ;FIS TRAP VECTOR

```

```

NOP
FSUB SP ;FLOATING SUBTRACT ON THE STACK

```

```

JSR PC, POPS ;POP THE ANSWER
CMP #BEGIN, SP ;CHECK THE STACK POINTER
BEQ TSA36 ;BRANCH IF OK
MOV #BEGIN, SP ;RESTORE STACK POINTER

```

```

HLT ;STACK POINTER FOULED UP
247 ;THE ERROR NUMBER IS 247
BR END36 ;SKIP REST OF TEST

```

```

TSA36: CMPB #210, $PSW ;CHECK PS (EXCEPT T BIT)
BEQ .+6 ;BRANCH IF OK
HLT ;PS NOT EQUAL TO 210
250 ;THE ERROR NUMBER IS 250

```

```

CMP #177777,ANS1 ;CHECK FIRST HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 177777
251 ;THE ERROR NUMBER IS 251

```

```

CMP #177777,ANS2 ;CHECK SECOND HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 177777
252 ;THE ERROR NUMBER IS 252

```

```

END36: CMPB #36, $TESTN ;CHECK THE TEST NUMBER
BEQ .+6 ;BRANCH IF OK
HLT ;WRONG TEST! PC MUST HAVE FOULED UP.
253 ;THE ERROR NUMBER IS 253

```

00300 1739
00400 1740
00500 1741
00600 1742
00700 1743
00800 1744
00900 1745
01000 1746 006704 104400
01100 1747 006706 004567 007122
01200 1748 006712 000252 125253
01300 1749 006716 000425 052525
01400 1750 006722 000257
01500 1751 006724 006752 000340
01600 1752
01700 1753 006730 000240
01800 1754 006732 075016
01900 1755
02000 1756 006734 004767 007134
02100 1757 006740 104002
02200 1758 006742 000254
02300 1759 006744 012706 000600
02400 1760 006750 000464
02500 1761
02600 1762 006752 004767 007150
02700 1763 006756 022706 000600
02800 1764 006762 001405
02900 1765 006764 012706 000600
03000 1766 006770 104000
03100 1767 006772 000255
03200 1768 006774 000452
03300 1769
03400 1770 006776 122767 000340 171426
03500 1771 007004 001402
03600 1772 007006 104000
03700 1773 007010 000256
03800 1774
03900 1775 007012 022767 006734 171416
04000 1776 007020 001402
04100 1777 007022 104001
04200 1778 007024 000257
04300 1779
04400 1780 007026 022767 000212 171404
04500 1781 007034 001402
04600 1782 007036 104002
04700 1783 007040 000260
04800 1784
04900 1785 007042 022767 000252 171372
05000 1786 007050 001402
05100 1787 007052 104004
05200 1788 007054 000261
05300 1789
05400 1790 007056 022767 125253 171360
05500 1791 007064 001402
05600 1792 007066 104004
05700 1793 007070 000262
05800 1794

```

:*****
:TEST 37:      FSUB (LSI-1) FLOATING SUBTRACT INSTRUCTION)
:              000425,052525 - 000252,125253 ==> UNDERFLOW
:              PS(ON STACK) = 212,      STACK POINTER = SP
:*****
TST37:  JSR      R5,      PUSH5      ;PUSH 4 WORDS ONTO STACK, SET PRIORITY
        .WORD    000252,125253      ;SECOND OPERAND ON TOP
        .WORD    000425,052525      ;FIRST OPERAND ON BOTTOM
        .WORD    257                ;PROCESSOR PRIORITY LEVEL
        .WORD    ISR37, 340         ;FIS TRAP VECTOR

NOP
FSUB    SP                        ;FLOATING SUBTRACT ON THE STACK

RTA37:  JSR      PC,      POPS       ;POP THE 'ANSWER'
        HLT+2                    ;FIS TRAP DIDN'T OCCURE!
        254                      ;THE ERROR NUMBER IS 254
        MOV      #BEGIN, SP        ;RESTORE THE STACK POINTER
        BR      END37

ISR37:  JSR      PC,      POPES      ;POP ALL DATA OFF THE STACK
        CMP      #BEGIN, SP        ;CHECK THE STACK POINTER
        BEQ      ISA37             ;BRANCH IF OK
        MOV      #BEGIN, SP        ;RESTORE THE STACK POINTER
        HLT      ;STACK POINTER FOULED UP
        255                      ;THE ERROR NUMBER IS 255
        BR      END37             ;SKIP REST OF TEST

ISA37:  CMP      #340, $PSW        ;CHECK PS AFTER FIS TRAP
        BEQ      .+6              ;BRANCH IF OK
        HLT      ;PS AFTER FIS TRAP NOT EQUAL TO 340
        256                      ;THE ERROR NUMBER IS 256

CMP      #RTA37, ANS1             ;CHECK FIS TRAP RETURN ADDRESS
BEQ      .+6                      ;BRANCH IF OK
HLT+1    ;FIS TRAP AT WRONG ADDRESS
257    ;THE ERROR NUMBER IS 257

CMP      #212, ANS2              ;CHECK PS BEFORE FIS TRAP
BEQ      .+6                      ;BRANCH IF OK
HLT+2    ;PS AT FIS TRAP TIME NOT 212
260    ;THE ERROR NUMBER IS 260

CMP      #000252,ANS3            ;CHECK DATA FROM THE STACK
BEQ      .+6                      ;BRANCH IF OK
HLT+4    ;DATA ON STACK (000252) CHANGED
261    ;THE ERROR NUMBER IS 261

CMP      #125253,ANS4           ;CHECK DATA FROM STACK
BEQ      .+6                      ;BRANCH IF OK
HLT+4    ;DATA ON STACK (125253) CHANGED
262    ;THE ERROR NUMBER IS 262

```



```

00100 1811
00200 1812
00300 1813
00400 1814
00500 1815
00600 1816
00700 1817
00800 1818
00900 1819
01000 1820
01100 1821
01200 1822
01300 1823
01400 1824
01500 1825
01600 1826
01700 1827
01800 1828
01900 1829
02000 1830
02100 1831
02200 1832
02300 1833
02400 1834
02500 1835
02600 1836
02700 1837
02800 1838
02900 1839
03000 1840
03100 1841
03200 1842
03300 1843
03400 1844
03500 1845
03600 1846
03700 1847
03800 1848
03900 1849
04000 1850
04100 1851
04200 1852
04300 1853
04400 1854
04500 1855
04600 1856
04700 1857
04800 1858
04900 1859
05000 1860
05100 1861
05200 1862
05300 1863
05400 1864
05500 1865
05600 1866
05700 1867
05800 1868

```

```

007136 104400
007140 004567 007042
007144 177452 125252
007150 077652 125253
007154 000015
007156 007210 000344
007162 012703 000510
007166 000240
007170 075013
007172 004767 007042
007176 010367 171232
007202 104002
007204 000266
007206 000464
007210 004767 007054
007214 010367 171214
007220 122767 000344 171204
007226 001402
007230 104000
007232 000267
007234 022767 000510 171172
007242 001402
007244 104000
007246 000270
007250 022767 007172 171160
007256 001402
007260 104001
007262 000271
007264 022767 000002 171146
007272 001402
007274 104002
007276 000272
007300 022767 177452 171134
007306 001402
007310 104004
007312 000273
007314 022767 125252 171122
007322 001402
007324 104004
007326 000274

```

```

:*****
:TEST 40: FSUB (LSI-11 FLOATING SUBTRACT INSTRUCTION)
: 077652,125253 - 177452,125252 ==> OVERFLOW
: PS(ON STACK) = 002, STACK POINTER = R3
:*****
TST40: SCOPE
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R3 STACK, SET PRIORITY
.WORD 177452,125252 ;SECOND OPERAND ON TOP
.WORD 077652,125253 ;FIRST OPERAND ON BOTTOM
.WORD 015 ;PROCESSOR PRIORITY LEVEL
.WORD ISR40, 344 ;FIS TRAP VECTOR
MOV #STACK0,R3 ;SET UP R3 AS STACK POINTER

NOP
FSUB R3 ;FLOATING SUBTRACT ON THE R3 STACK

RT40: JSR PC, POPR ;POP THE 'ANSWER'
MOV R3, $SP ;SAVE STACK POINTER (R3)
HLT+2 ;FIS TRAP DIDN'T OCCURE
266 ;THE ERROR NUMBER IS 266
BR END40

ISR40: JSR PC, POPER ;POP ALL DATA OFF THE STACKS
MOV R3, $SP ;SAVE STACK POINTER (R3)
CMPB #344, $PSW ;CHECK PS AFTER FIS TRAP
BEQ .+6 ;BRANCH IF OK
HLT ;PS AFTER FIS TRAP NOT EQUAL TO 344
267 ;THE ERROR NUMBER IS 267

CMP #STACK0,$SP ;CHECK THE STACK POINTER (R3)
BEQ .+6 ;BRANCH IF OK
HLT ;STACK POINTER (R3) NOT EQUAL TO #STACK0
270 ;THE ERROR NUMBER IS 270

CMP #RTA40, ANS1 ;CHECK FIS TRAP RETURN ADDRESS
BEQ .+6 ;BRANCH IF OK
HLT+1 ;FIS TRAP AT WRONG ADDRESS
271 ;THE ERROR NUMBER IS 271

CMP #002, ANS2 ;CHECK PS BEFORE FIS TRAP
BEQ .+6 ;BRANCH IF OK
HLT+2 ;PS AT FIS TRAP TIME NOT 002
272 ;THE ERROR NUMBER IS 272

CMP #177452,ANS3 ;CHECK DATA FROM THE STACK
BEQ .+6 ;BRANCH IF OK
HLT+4 ;DATA ON STACK (177452) CHANGED
273 ;THE ERROR NUMBER IS 273

CMP #125252,ANS4 ;CHECK DATA FROM STACK
BEQ .+6 ;BRANCH IF OK
HLT+4 ;DATA ON STACK (125252) CHANGED
274 ;THE ERROR NUMBER IS 274

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00100 (VKACC MARV11 30A(1052) 21-AUG-78 15:28 PAGE 45
00200 (VKACC.P 1 16-AUG-78 08:41 TEST FLOATING SUB. INSTRUCTION WITH OVERFLOW SEQ 0053
00300
00400 1867 007330 022767 077652 171110 CMP #077652,ANS5 ;CHECK DATA FROM STACK
00500 1868 007336 001402 BEQ .+6 ;BRANCH IF OK
00600 1869 007340 104006 HLT+6 ;DATA ON STACK (077652) CHANGED
00700 1870 007342 000275 275 ;THE ERROR NUMBER IS 275
00800 1871
00900 1872 007344 022767 125253 171076 CMP #125253,ANS6 ;CHECK DATA FROM STACK
01000 1873 007352 001402 BEQ .+6 ;BRANCH IF OK
01100 1874 007354 104006 HLT+6 ;DATA ON STACK (125253) CHANGED
01200 1875 007356 000276 276 ;THE ERROR NUMBER IS 276
01300 1876
01400 1877 007360 122767 000040 171016 END40. CMPB #40, $TESTN ;CHECK THE TEST NUMBER
01500 1878 007366 001402 BEQ .+6 ;BRANCH IF OK
01600 1879 007370 104000 HLT ;WRONG TEST PC MUST HAVE FOULED UP.
01700 1880 007372 000277 277 ;THE ERROR NUMBER IS 277
01800 1881
01900 1882

```

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00100
00200
00300
00400 1883
00500 1884
00600 1885
00700 1886
00800 1887
00900 1888
01000 1889
01100 1890 007374 104400
01200 1891 007376 004567 006604
01300 1892 007402 000000 000000
01400 1893 007406 000000 000000
01500 1894 007412 000111
01600 1895 007414 016456 000340
01700 1896 007420 012704 000510
01800 1897
01900 1898 007424 000240
02000 1899 007426 075024
02100 1900
02200 1901 007430 004767 006604
02300 1902 007434 010467 170774
02400 1903 007440 122767 000004 170764
02500 1904 007446 001402
02600 1905 007450 104000
02700 1906 007452 000300
02800 1907
02900 1908 007454 022767 000514 170752
03000 1909 007462 001402
03100 1910 007464 104000
03200 1911 007466 000301
03300 1912
03400 1913 007470 005767 170742
03500 1914 007474 001402
03600 1915 007476 104002
03700 1916 007500 000302
03800 1917
03900 1918 007502 005767 170732
04000 1919 007506 001402
04100 1920 007510 104002
04200 1921 007512 000303
04300 1922
04400 1923 007514 122767 000041 170662
04500 1924 007522 001402
04600 1925 007524 104000
04700 1926 007526 000304
04800 1927
04900 1928

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*****
:TEST 41: FMUL (LSI-11 FLOATING MULTIPLY INSTRUCTION)
: 000000,000000 * 000000,000000 = 000000,000000
: PS = 004, STACK POINTER = R4
*****

TST41: SCOPE
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R4 STACK, SET PRIORITY
:WORD 000000,000000 ;SECOND OPERAND ON TOP
:WORD 000000,000000 ;FIRST OPERAND ON BOTOM
:WORD 111 ;PROCESSOR PRIORITY LEVEL
:WORD TRAPER, 340 ;FIS TRAP VECTOR
MOV #STACK0,R4 ;SET UP STACK POINTER

NOP
FMUL R4 ;FLOATING MULTIPLY ON THE R4 STACK

JSR PC, POPR ;POP THE ANSWER
MOV R4, $SP ;SAVE 'STACK POINTER'
CMPB #004, $PSW ;CHECK PS (EXCEPT T BIT)
BEQ .+6 ;BRANCH IF OK
HLT ;PS NOT EQUAL TO 004
300 ;THE ERROR NUMBER IS 300

CMP #STACK4,$SP ;CHECK THE STACK POINTER (R4)
BEQ .+6 ;BRANCH IF OK
HLT ;STACK POINTER (R4) NOT EQUAL TO #STACK4
301 ;THE ERROR NUMBER IS 301

TST ANS1 ;CHECK FIRST HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 000000
302 ;THE ERROR NUMBER IS 302

TST ANS2 ;CHECK SECOND HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 000000
303 ;THE ERROR NUMBER IS 303

END41: CMPB #41, $TESTN ;CHECK THE TEST NUMBER
BEQ .+6 ;BRANCH IF OK
HLT ;WRONG TEST. PC MUST HAVE FOULED UP.
304 ;THE ERROR NUMBER IS 304

```

00100 CVKACC MACY11 30A(1052) 21-AUG-78 15:28 PAGE 47
00200 CVKACC P11 16-AUG-78 08:41 FMUL TEST SECTION

SEQ 0055

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00300 1929
00400 1930
00500 1931
00600 1932
00700 1933
00800 1934
00900 1935
01000 1936
01100 1937 007530 104400
01200 1938 007532 004567 006450
01300 1939 007536 052345 123456
01400 1940 007542 140200 000000
01500 1941 007546 000343
01600 1942 007550 016456 000340
01700 1943 007554 012702 000510
01800 1944
01900 1945 007560 000240
02000 1946 007562 075022
02100 1947
02200 1948 007564 004767 006450
02300 1949 007570 010267 170640
02400 1950 007574 122767 000210 170630
02500 1951 007602 001402
02600 1952 007604 104000
02700 1953 007606 000305
02800 1954
02900 1955 007610 022767 000514 170616
03000 1956 007616 001402
03100 1957 007620 104000
03200 1958 007622 000306
03300 1959
03400 1960 007624 022767 152345 170604
03500 1961 007632 001402
03600 1962 007634 104002
03700 1963 007636 000307
03800 1964
03900 1965 007640 022767 123456 170572
04000 1966 007646 001402
04100 1967 007650 104002
04200 1968 007652 000310
04300 1969
04400 1970 007654 122767 000042 170522
04500 1971 007662 001402
04600 1972 007664 104000
04700 1973 007666 000311
04800 1974
04900

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```

:*****
:TEST 42: FMUL (LS!-11 FLOATING MULTIPLY INSTRUCTION)
: 140200,000000 * 052345,123456 = 152345,123456
: PS 210, STACK POINTER R2
:*****
TST42: SCOPE
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R2 STACK, SET PRIORITY
.WORD 052345,123456 ;SECOND OPERAND ON TOP
.WORD 140200,000000 ;FIRST OPERAND ON BOTOM
.WORD 343 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER, 340 ;FIS TRAP VECTOR
MOV #STACK0,R2 ;SET UP STACK POINTER

NOP
FMUL R2 ;FLOATING MULTIPLY ON THE R2 STACK

JSR PC, POPR ;POP THE ANSWER
MOV R2, $SP ;SAVE 'STACK POINTER'
CMPB #210, $PSW ;CHECK PS (EXCEPT T BIT)
BEQ .+6 ;BRANCH IF OK
HLT ;PS NOT EQUAL TO 210
305 ;THE ERROR NUMBER IS 305

CMP #STACK4,$SP ;CHECK THE STACK POINTER (R2)
BEQ .+6 ;BRANCH IF OK
HLT ;STACK POINTER (R2) NOT EQUAL TO #STACK4
306 ;THE ERROR NUMBER IS 306

CMP #152345,ANS1 ;CHECK FIRST HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 152345
307 ;THE ERROR NUMBER IS 307

CMP #123456,ANS2 ;CHECK SECOND HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 123456
310 ;THE ERROR NUMBER IS 310

END42: CMPB #42, $TESTN ;CHECK THE TEST NUMBER
BEQ .+6 ;BRANCH IF OK
HLT ;WRONG TEST. PC MUST HAVE FOULED UP.
311 ;THE ERROR NUMBER IS 311

```

00300
00400 1975
00500 1976
00600 1977
00700 1978
00800 1979
00900 1980
01000 1981
01100 1982 007670 104400
01200 1983 007672 004567 006310
01300 1984 007676 135753 024642
01400 1985 007702 100125 052525
01500 1986 007706 000117
01600 1987 007710 016456 000340
01700 1988 007714 012705 000510
01800 1989
01900 1990 007720 000240
02000 1991 007722 075025
02100 1992
02200 1993 007724 004767 006310
02300 1994 007730 010567 170500
02400 1995 007734 122767 000004 170470
02500 1996 007742 001402
02600 1997 007744 104000
02700 1998 007746 000312
02800 1999
02900 2000 007750 022767 000514 170456
03000 2001 007756 001402
03100 2002 007760 104000
03200 2003 007762 000313
03300 2004
03400 2005 007764 005767 170446
03500 2006 007770 001402
03600 2007 007772 104002
03700 2008 007774 000314
03800 2009
03900 2010 007776 005767 170436
04000 2011 010002 001402
04100 2012 010004 104002
04200 2013 010006 000315
04300 2014
04400 2015 010010 122767 000043 170366
04500 2016 010016 001402
04600 2017 010020 104000
04700 2018 010022 000316
04800 2019
04900 2020

```

:*****
:TEST 43: FMUL (LSI-11 FLOATING MULTIPLY INSTRUCTION)
:          100125,052525 * 135753,024642 = 000000,000000
:          PS = 004,          STACK POINTER = R5
:*****
TST43:  SCOPE
        JSR      R5,      PUSHR      ;PUSH 4 WORDS ONTO R5 STACK, SET PRIORITY
        .WORD   135753,024642      ;SECOND OPERAND ON TOP
        .WORD   100125,052525      ;FIRST OPERAND ON BOTOM
        .WORD   117                ;PROCESSOR PRIORITY LEVEL
        .WORD   TRAPER, 340        ;FIS TRAP VECTOR
        MOV     #STACK0,R5        ;SET UP STACK POINTER

        NOP
        FMUL   R5                ;FLOATING MULTIPLY ON THE R5 STACK

        JSR    PC,      POPR      ;POP THE ANSWER
        MOV    R5,      $SP      ;SAVE 'STACK POINTER'
        CMPB  #004,    $PSW      ;CHECK PS (EXCEPT T BIT)
        BEQ   .+6            ;BRANCH IF OK
        HLT   312            ;PS NOT EQUAL TO 004
                          ;THE ERROR NUMBER IS 312

        CMP   #STACK4,$SP      ;CHECK THE STACK POINTER (R5)
        BEQ   .+6            ;BRANCH IF OK
        HLT   313            ;STACK POINTER (R5) NOT EQUAL TO #STACK4
                          ;THE ERROR NUMBER IS 313

        TST   ANS1            ;CHECK FIRST HALF OF ANSWER
        BEQ   .+6            ;BRANCH IF OK
        HLT+2 314            ;ANS1 NOT EQUAL TO 000000
                          ;THE ERROR NUMBER IS 314

        TST   ANS2            ;CHECK SECOND HALF OF ANSWER
        BEQ   .+6            ;BRANCH IF OK
        HLT+2 315            ;ANS2 NOT EQUAL TO 000000
                          ;THE ERROR NUMBER IS 315

END43:  CMPB  #43,      $TESTN    ;CHECK THE TEST NUMBER
        BEQ   .+6            ;BRANCH IF OK
        HLT   316            ;WRONG TEST. PC MUST HAVE FOULED UP.
                          ;THE ERROR NUMBER IS 316

```


00300
00400 2021
00500 2022
00600 2023
00700 2024
00800 2025
00900 2026
01000 2027
01100 2028 010024 104400
01200 2029 010026 004567 006154
01300 2030 010032 000052 125252
01400 2031 010036 161616 161616
01500 2032 010042 000217
01600 2033 010044 016456 000340
01700 2034 010050 012703 000510
01800 2035
01900 2036 010054 000240
02000 2037 010056 075023
02100 2038
02200 2039 010060 004767 006154
02300 2040 010064 010367 170344
02400 2041 010070 122767 000204 170334
02500 2042 010076 001402
02600 2043 010100 104000
02700 2044 010102 000317
02800 2045
02900 2046 010104 022767 000514 170322
03000 2047 010112 001402
03100 2048 010114 104000
03200 2049 010116 000320
03300 2050
03400 2051 010120 005767 170312
03500 2052 010124 001402
03600 2053 010126 104002
03700 2054 010130 000321
03800 2055
03900 2056 010132 005767 170302
04000 2057 010136 001402
04100 2058 010140 104002
04200 2059 010142 000322
04300 2060
04400 2061 010144 122767 000044 170232
04500 2062 010152 001402
04600 2063 010154 104000
04700 2064 010156 000323
04800 2065
04900 2066

```

:*****
:TEST 44:      FMUL (LSI-11 FLOATING MULTIPLY INSTRUCTION)
:      161616,161616 * 000052,125252 = 000000,000000
:      PS = 204,      STACK POINTER R3
:*****
TST44:  JSR      R5,      PUSHR      ;PUSH 4 WORDS ONTO R3 STACK, SET PRIORITY
        .WORD    000052,125252      ;SECOND OPERAND ON TOP
        .WORD    161616,161616      ;FIRST OPERAND ON BOTTOM
        .WORD    217                ;PROCESSOR PRIORITY LEVEL
        .WORD    TRAPER, 340        ;FIS TRAP VECTOR
        MOV      #STACK0,R3        ;SET UP STACK POINTER

        NOP
        FMUL    R3                ;FLOATING MULTIPLY ON THE R3 STACK

        JSR      PC,      POPR      ;POP THE ANSWER
        MOV      R3,      $SP      ;SAVE 'STACK POINTER'
        CMPB    #204,     $PSW     ;CHECK PS (EXCEPT T BIT)
        BEQ     .+6              ;BRANCH IF OK
        HLT     204              ;PS NOT EQUAL TO 204
        317                    ;THE ERROR NUMBER IS 317

        CMP     #STACK4,$SP      ;CHECK THE STACK POINTER (R3)
        BEQ     .+6              ;BRANCH IF OK
        HLT     320              ;STACK POINTER (R3) NOT EQUAL TO #STACK4
        320                    ;THE ERROR NUMBER IS 320

        TST     ANS1             ;CHECK FIRST HALF OF ANSWER
        BEQ     .+6              ;BRANCH IF OK
        HLT+2  321              ;ANS1 NOT EQUAL TO 000000
        321                    ;THE ERROR NUMBER IS 321

        TST     ANS2             ;CHECK SECOND HALF OF ANSWER
        BEQ     .+6              ;BRANCH IF OK
        HLT+2  322              ;ANS2 NOT EQUAL TO 000000
        322                    ;THE ERROR NUMBER IS 322

END44:  CMPB    #44,           $TESTN ;CHECK THE TEST NUMBER
        BEQ     .+6              ;BRANCH IF OK
        HLT     323              ;'WRONG TEST' PC MUST HAVE FOULED UP.
        323                    ;THE ERROR NUMBER IS 323

```

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00300
00400 2067
00500 2068
00600 2069
00700 2070
00800 2071
00900 2072
01000 2073
01100 2074 010160 104400
01200 2075 010162 004567 005646
C 300 2076 010166 041500 000000
01400 2077 010172 176452 125252
01500 2078 010176 000357
01600 2079 010200 016456 000340
01700 2080
01800 2081 010204 000240
01900 2082 010206 075026
02000 2083
02100 2084 010210 004767 005660
02200 2085 010214 022706 000600
02300 2086 010220 00405
02400 2087 010222 012706 000600
02500 2088 010226 104000
02600 2089 010230 000324
02700 2090 010232 000422
02800 2091
02900 2092 010234 122767 000210 170170
03000 2093 010242 001402
03100 2094 010244 104000
03200 2095 010246 000325
03300 2096
03400 2097 010250 022767 177777 170160
03500 2098 010256 001402
03600 2099 010260 104002
03700 2100 010262 000326
03800 2101
03900 2102 010264 022767 177777 170146
04000 2103 010272 001402
04100 2104 010274 104002
04200 2105 010276 000327
04300 2106
04400 2107 010300 122767 000045 170076
04500 2108 010306 001402
04600 2109 010310 104000
04700 2110 010312 000330
04800
04900

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```

*****
TEST 45: FMUL (LSI-11 FLOATING MULTIPLY INSTRUCTION)
          176452,125252 * 041500,000000 = 177777,177777
          PS = 210, STACK POINTER SP
*****

```

```

TST45: SCOPE
        JSR R5, PUSH5 :PUSH 4 WORDS ONTO STACK, SET PRIORITY
        .WORD 041500,000000 :SECOND OPERAND ON TOP
        .WORD 176452,125252 :FIRST OPERAND ON BOTTOM
        .WORD 357 :PROCESSOR PRIORITY LEVEL
        .WORD TRAPER, 340 :FIS TRAP VECTOR

NOP
FMUL SP :FLOATING MULTIPLY ON THE STACK

JSR PC, POPS :POP THE ANSWER
CMP #BEGIN, SP :CHECK THE STACK POINTER
BEQ TSA45 :BRANCH IF OK
MOV #BEGIN, SP :RESTORE STACK POINTER
HLT :STACK POINTER FOULED UP
324 :THE ERROR NUMBER IS 324
BR END45 :SKIP REST OF TEST

TSA45: CMPB #210, $PSW :CHECK PS (EXCEPT T BIT)
        BEQ .+6 :BRANCH IF OK
        HLT :PS NOT EQUAL TO 210
        325 :THE ERROR NUMBER IS 325

CMP #177777,ANS1 :CHECK FIRST HALF OF ANSWER
BEQ .+6 :BRANCH IF OK
HLT+2 :ANS1 NOT EQUAL TO 177777
326 :THE ERROR NUMBER IS 326

CMP #177777,ANS2 :CHECK SECOND HALF OF ANSWER
BEQ .+6 :BRANCH IF OK
HLT+2 :ANS2 NOT EQUAL TO 177777
327 :THE ERROR NUMBER IS 327

END45: CMPB #45, $TESTN :CHECK THE TEST NUMBER
        BEQ .+6 :BRANCH IF OK
        HLT :WRONG TEST. PC MUST HAVE FOULED UP.
        330 :THE ERROR NUMBER IS 330

```

```

00100
00200
00300
00400
00500
00600
00700
00800
00900
01000
01100
01200
01300
01400
01500
01600
01700
01800
01900
02000
02100
02200
02300
02400
02500
02600
02700
02800
02900
03000
03100
03200
03300
03400
03500
03600
03700
03800
03900
04000
04100
04200
04300
04400
04500
04600
04700
04800
04900

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2114
2115
2116
2117
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2119
2120
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2156

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:*****
:TEST 46: FMUL (LSI-11 FLOATING MULTIPLY INSTRUCTION)
: 124252,125252 * 114100,000001 = 000200,000000
: PS 200, STACK POINTER = R1
:*****

TST46: SCOPE
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R1 STACK, SET PRIORITY
.WORD 114100,000001 ;SECOND OPERAND ON TOP
.WORD 124252,125252 ;FIRST OPERAND ON BOTTOM
.WORD 200 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER, 340 ;FIS TRAP VECTOR
MOV #STACK0,R1 ;SET UP STACK POINTER

NOP
FMUL R1 ;FLOATING MULTIPLY ON THE R1 STACK

JSR PC, POPR ;POP THE ANSWER
MOV R1, $SP ;SAVE 'STACK POINTER'
CMPB #200, $PSW ;CHECK PS (EXCEPT T BIT)
BEQ .+6 ;BRANCH IF OK
HLT ;PS NOT EQUAL TO 200
331 ;THE ERROR NUMBER IS 331

CMP #STACK4,$SP ;CHECK THE STACK POINTER (R1)
BEQ .+6 ;BRANCH IF OK
HLT ;STACK POINTER (R1) NOT EQUAL TO #STACK4
332 ;THE ERROR NUMBER IS 332

CMP #000200,ANS1 ;CHECK FIRST HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 000200
333 ;THE ERROR NUMBER IS 333

TST ANS2 ;CHECK SECOND HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 000000
334 ;THE ERROR NUMBER IS 334

END46: CMPB #46, $TESTN ;CHECK THE TEST NUMBER
BEQ .+6 ;BRANCH IF OK
HLT ;WRONG TEST: PC MUST HAVE FOULED JP.
335 ;THE ERROR NUMBER IS 335

```

170044

000514 170032

000200 170020

005767 170010

000046 167740

```

00300 2159
00400 2160
00500 2161
00600 2162
00700 2163
00800 2164
00900 2165
01000 2166
01100 010452 104400
01200 2167 010454 004567 005700
01300 2168 010460 010504
01400 2169 010462 104000 104000
01500 2170 010466 104000 105004
01600 2171 010472 000252
01700 2172 010474 016456 000340
01800 2173
01900 2174 010500 000240
02000 2175 010502 075017
02100 2176 010504 104000
02200 2177 010506 104000
02300 2178 010510 104000
02400 2179 010512 105004
02500 2180
02600 2181 010514 004767 005670
02700 2182 010520 122767 000210 167704
02800 2183 010526 001402
02900 2184 010530 104000
03000 2185 010532 000336
03100 2186
03200 2187 010534 022767 104000 167674
03300 2188 010542 001402
03400 2189 010544 104002
03500 2190 010546 000337
03600 2191
03700 2192 010550 022767 104000 167662
03800 2193 010556 001402
03900 2194 010560 104002
04000 2195 010562 000340
04100 2196
04200 2197 010564 022767 100401 167650
04300 2198 010572 001402
04400 2199 010574 104004
04500 2200 010576 000341
04600 2201
04700 2202 010600 005767 167640
04800 2203 010604 001402
04900 2204 010606 104004
05000 2205 010610 000342
05100 2206
05200 2207 010612 122767 000047 167564
05300 2208 010620 001402
05400 2209 010622 104000
05500 2210 010624 000343
05600
05700

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```

:*****
:TEST 47: FSUB (LSI-11 FLOATING SUBTRACT INSTRUCTION)
: 104000,105004 - 104000,104000 = 100401,000000
: PS - 210, STACK POINTER - PC
:*****

```

```

TST47: JSR R5, PUSH7 ; PUSH 4 WORDS ONTO STACK, SET PRIORITY
        .WORD STK47 ; TOP OF STACK
        .WORD 104000,104000 ; SECOND OPERAND ON TOP
        .WORD 104000,105004 ; FIRST OPERAND ON BOTTOM
        .WORD 252 ; PROCESSOR PRIORITY LEVEL
        .WORD TRAPER,340 ; FIS TRAP VECTOR

NOP
STK47: FSUB PC ; FLOATING SUBTRACT ON FOLLOWING 4 WORDS
        104000 ; SHOULD CONTAIN 104000
        104000 ; SHOULD CONTAIN 104000
        104000 ; BEFORE FSUB, 104000; AFTER, 100401
        105004 ; BEFORE FSUB, 105004; AFTER, 000000

JSR PC, POP7 ; POP THE ANSWER
(MPB #210, $PSW ; CHECK PS (EXCEPT T BIT)
BEQ .+6 ; BRANCH IF OK
HLT ; PS NOT EQUAL TO 210
336 ; THE ERROR NUMBER IS 336

CMP #104000,ANS1 ; CHECK FIRST HALF OF INPUT DATA (STK47)
BEQ .+6 ; BRANCH IF OK
HLT+2 ; ANS1 NOT EQUAL TO 104000
337 ; THE ERROR NUMBER IS 337

CMP #104000,ANS2 ; CHECK SECOND HALF OF INPUT DATA (STK47+2)
BEQ .+6 ; BRANCH IF OK
HLT+2 ; ANS2 NOT EQUAL TO 104000
340 ; THE ERROR NUMBER IS 340

CMP #100401,ANS3 ; CHECK FIRST HALF OF ANSWER
BEQ .+6 ; BRANCH IF OK
HLT+4 ; ANS3 NOT EQUAL TO 100401
341 ; THE ERROR NUMBER IS 341

TST ANS4 ; CHECK SECOND HALF OF ANSWER
BEQ .+6 ; BRANCH IF OK
HLT+4 ; ANS4 NOT EQUAL TO 000000
342 ; THE ERROR NUMBER IS 342

END47: (MPB #47, $TESTN ; CHECK THE TEST NUMBER
        BEQ .+6 ; BRANCH IF OK
        HLT ; WRONG TEST! PC MUST HAVE FOULED UP.
        343 ; THE ERROR NUMBER IS 343

```

00300
 00400 2213
 00500 2214
 00600 2215
 00700 2216
 00800 2217
 00900 2218
 01000 2219
 01100 2220 010626 104400
 01200 2221 010630 004567 005524
 01300 2222 010634 010660
 01400 2223 010636 104000 104000
 01500 2224 010642 134600 073601
 01600 2225 010646 000246
 01700 2226 010650 016456 000340
 01800 2227
 01900 2228 010654 000240
 02000 2229 010656 075027
 02100 2230 010660 104000
 02200 2231 010662 104000
 02300 2232 010664 134600
 02400 2233 010666 073601
 02500 2234
 02600 2235 010670 004767 005514
 02700 2236 010674 122767 000200 167530
 02800 2237 010702 001402
 02900 2238 010704 104000
 03000 2239 010706 000344
 03100 2240
 03200 2241 010710 022767 104000 167520
 03300 2242 010716 001402
 03400 2243 010720 104002
 03500 2244 010722 000345
 03600 2245
 03700 2246 010724 022767 104000 167506
 03800 2247 010732 001402
 03900 2248 010734 104002
 04000 2249 010736 000346
 04100 2250
 04200 2251 010740 022767 000401 167474
 04300 2252 010746 001402
 04400 2253 010750 104004
 04500 2254 010752 000347
 04600 2255
 04700 2256 010754 005767 167464
 04800 2257 010760 001402
 04900 2258 010762 104004
 05000 2259 010764 000350
 05100 2260
 05200 2261 010766 122767 000050 167410
 05300 2262 010774 001402
 05400 2263 010776 104000
 05500 2264 011000 000351
 05600 2265
 05700 2266
 05800 2267
 05900 2268

```

:*****
:TEST 50:      FMUL (LSI-11 FLOATING MULTIPLY INSTRUCTION)
:      134600,073601 * 104000,104000 = 000401,000000
:      PS - 200,      STACK POINTER  PC
:*****

TST50:  SCOPE
        JSR      R5,      PUSH7      ;PUSH 4 WORDS ONTO STACK, SET PRIORITY
        .WORD    STK50          ;TOP OF STACK
        .WORD    104000,104000      ;SECOND OPERAND ON TOP
        .WORD    134600,073601     ;FIRST OPERAND ON BOTTOM
        .WORD    246              ;PROCESSOR PRIORITY LEVEL
        .WORD    TRAPER,340        ;FIS TRAP VECTOR

STK50:  NOP
        FMUL     PC              ;FLOATING MULTIPLY ON FOLLOWING 4 WORDS
        104000          ;SHOULD CONTAIN 104000
        104000          ;SHOULD CONTAIN 104000
        134600          ;BEFORE FMUL, 134600; AFTER, 000401
        073601          ;BEFORE FMUL, 073601; AFTER, 000000

        JSR      PC,      POP7      ;POP THE ANSWER
        CMPB    #200,    $PSW      ;CHECK PS (EXCEPT T BIT)
        BEQ     .+6           ;BRANCH IF OK
        HLT     .+6           ;PS NOT EQUAL TO 200
        344              ;THE ERROR NUMBER IS 344

        CMP     #104000,ANS1      ;CHECK FIRST HALF OF INPUT DATA (STK50)
        BEQ     .+6           ;BRANCH IF OK
        HLT+2  345          ;ANS1 NOT EQUAL TO 104000
        345              ;THE ERROR NUMBER IS 345

        CMP     #104000,ANS2      ;CHECK SECOND HALF OF INPUT DATA (STK50+2)
        BEQ     .+6           ;BRANCH IF OK
        HLT+2  346          ;ANS2 NOT EQUAL TO 104000
        346              ;THE ERROR NUMBER IS 346

        CMP     #000401,ANS3      ;CHECK FIRST HALF OF ANSWER
        BEQ     .+6           ;BRANCH IF OK
        HLT+4  347          ;ANS3 NOT EQUAL TO 000401
        347              ;THE ERROR NUMBER IS 347

        TST     ANS4            ;CHECK SECOND HALF OF ANSWER
        BEQ     .+6           ;BRANCH IF OK
        HLT+4  350          ;ANS4 NOT EQUAL TO 000000
        350              ;THE ERROR NUMBER IS 350

END50:  CMPB    #50,    $TESTN    ;CHECK THE TEST NUMBER
        BEQ     .+6           ;BRANCH IF OK
        HLT     .+6           ;WRONG TEST! PC MUST HAVE FOULED UP.
        351              ;THE ERROR NUMBER IS 351
    
```

:*****

```

00300
00400 2269
00500 2270
00600 2271
00700 2272
00800 2273
00900 2274 C11002 1,4400
01000 2275 011004 004567 005176
01100 2276 011010 114100 000000
01200 2277 011014 024252 125252
01300 2278 011020 000305
01400 2279 011022 011054 000057
01500 2280 011026 012700 000510
01600 2281
01700 2282 011032 000240
01800 2283 011034 075020
01900 2284
02000 2285 011036 004767 005176
02100 2286 011042 010067 167366
02200 2287 011046 104002
02300 2288 011050 000352
02400 2289 011052 000463
02500 2290
02600 2291 011054 004767 005210
02700 2292 011060 010067 167350
02800 2293 011064 122767 000057 167340
02900 2294 011072 001402
03000 2295 011074 104000
03100 2296 011076 000353
03200 2297
03300 2298 011100 022767 000510 167326
03400 2299 011106 001402
03500 2300 011110 104000
03600 2301 011112 000354
03700 2302
03800 2303 011114 022767 011036 167314
03900 2304 011122 001402
04000 2305 011124 104001
04100 2306 011126 000355
04200 2307
04300 2308 011130 022767 000212 167302
04400 2309 011136 001402
04500 2310 011140 104002
04600 2311 011142 000356
04700 2312
04800 2313 011144 022767 114100 167270
04900 2314 011152 001402
05000 2315 011154 104004
05100 2316 011156 000357
05200 2317
05300 2318 011160 005767 167260
05400 2319 011164 001402
05500 2320 011166 104004
05600 2321 011170 000360
05700 2322
05800 2323 011172 022767 024252 167240
05900 2324 011200 001402

```

```

:TEST 51: FMUL (LSI-11 FLOATING MULTIPLY INSTRUCTION)
: 024252,125252 * 114100,000000 ==> UNDERFLOW
: PS(ON STACK) = 212, STACK POINTER = R0
:*****
TST51: SCOPE
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R0 STACK, SET PRIORITY
.WORD 114100,000000 ;SECOND OPERAND ON TOP
.WORD 024252,125252 ;FIRST OPERAND ON BOTTOM
.WORD 305 ;PROCESSOR PRIORITY LEVEL
.WORD ISR51, 057 ;FIS TRAP VECTOR
MOV #STACK0,R0 ;SET UP R0 AS STACK POINTER

NOP
FMLI R0 ;FLOATING MULTIPLY ON THE R0 STACK

RTA51: JSR PC, POPR ;POP THE 'ANSWER'
MOV R0, $SP ;SAVE STACK POINTER (R0)
HLT+2 ;FIS TRAP DIDN'T OCCURE
352 ;THE ERROR NUMBER IS 352
BR END51

ISR51: JSR PC, POPER ;POP ALL DATA OFF THE STACKS
MOV R0, $SP ;SAVE STACK POINTER (R0)
CMPB #057, $PSW ;CHECK PS AFTER FIS TRAP
BEQ .+6 ;BRANCH IF OK
HLT ;PS AFTER FIS TRAP NOT EQUAL TO 057
353 ;THE ERROR NUMBER IS 353

CMP #STACK0,$SP ;CHECK THE STACK POINTER (R0)
BEQ .+6 ;BRANCH IF OK
HLT ;STACK POINTER (R0) NOT EQUAL TO #STACK0
354 ;THE ERROR NUMBER IS 354

CMP #RTA5, ANS1 ;CHECK FIS TRAP RETURN ADDRESS
BEQ .+6 ;BRANCH IF OK
HLT+1 ;FIS TRAP AT WRONG ADDRESS
355 ;THE ERROR NUMBER IS 355

CMP #212, ANS2 ;CHECK PS BEFORE FIS TRAP
BEQ .+6 ;BRANCH IF OK
HLT+2 ;PS AT FIS TRAP TIME NOT 212
356 ;THE ERROR NUMBER IS 356

CMP #114100,ANS3 ;CHECK DATA FROM THE STACK
BEQ .+6 ;BRANCH IF OK
HLT+4 ;DATA ON STACK (114100) CHANGED
357 ;THE ERROR NUMBER IS 357

TST ANS4 ;CHECK DATA FROM STACK
BEQ .+6 ;BRANCH IF OK
HLT+4 ;DATA ON STACK (000000) CHANGED
360 ;THE ERROR NUMBER IS 360

CMP #024252,ANS5 ;CHECK DATA FROM STACK
BEQ .+6 ;BRANCH IF OK

```

00100
00200
00300
00400
00500
00600
00700
00800
00900
01000
01100
01200
01300
01400
01500
01600
01700

CVKACC MACY11 30A(1052) 21-AUG-78 15:28 PAGE 55
CVKACC.P1 16-AUG-78 08:41
2325 011202 104006
2326 011204 000361
2327
2328 011206 022767 125252 167234
2329 011214 001402
2330 011216 104006
2331 011220 000362
2332
2333 011222 122767 0G0051 167154
2334 011230 001402
2335 011232 104000
2336 011234 000363
2337
2338

TEST FLOATING MUL. INSTRUCTION WITH UNDERFLOW

SEQ 0063

HLT+6
361
CMP
BEQ
HLT+6
362
END51: CMPB
BEQ
HLT
363

#125252,ANS6
+6
#51, \$TESTN
+6

;DATA ON STACK (024252) CHANGED
;THE ERROR NUMBER IS 361
;CHECK DATA FROM STACK
;BRANCH IF OK
;DATA ON STACK (125252) CHANGED
;THE ERROR NUMBER IS 362
;CHECK THE TEST NUMBER
;BRANCH IF OK
;WRONG TEST! PC MUST HAVE FOULED UP.
;THE ERROR NUMBER IS 363

00300
00400 2339
00500 2340
00600 2341
00700 2342
00800 2343
00900 2344
01000 2345
01100 2346 011236 104400
01200 2347 011240 004567 004570
01300 2348 011244 041500 000001
01400 2349 011250 076452 125252
01500 2350 011254 000105
01600 2351 011256 011304 000357
01700 2352
01800 2353 011262 000240
01900 2354 011264 075026
02000 2355
02100 2356 011266 004767 004602
02200 2357 011272 104002
02300 2358 011274 000364
02400 2359 011276 012706 000600
02500 2360 011302 000464
02600 2361
02700 2362 011304 004767 004616
02800 2363 011310 022706 000600
02900 2364 011314 001405
03000 2365 011316 012706 000600
03100 2366 011322 104000
03200 2367 011324 000365
03300 2368 011326 000452
03400 2369
03500 2370 011330 122767 000357 167074
03600 2371 011336 001402
03700 2372 011340 104000
03800 2373 011342 000366
03900 2374
04000 2375 011344 022767 011266 167064
04100 2376 011352 001402
04200 2377 011354 104001
04300 2378 011356 000367
04400 2379
04500 2380 011360 022767 000002 167052
04600 2381 011366 001402
04700 2382 011370 104002
04800 2383 011372 000370
04900 2384
05000 2385 011374 022767 041500 167040
05100 2386 011402 001402
05200 2387 011404 104004
05300 2388 011406 000371
05400 2389
05500 2390 011410 022767 000001 167026
05600 2391 011416 001402
05700 2392 011420 104004
05800 2393 011422 000372
05900 2394

```

:*****
:TEST 52:          FMUL (LSI-11 FLOATING MULTIPLY INSTRUCTION)
:          076452,125252 * 041500,000001 ==> OVERFLOW
:          PS(ON STACK) = 002,      STACK POINTER = SP
:*****

```

```

TST52:  JSR      R5,      PUSH5      ;PUSH 4 WORDS ONTO STACK, SET PRIORITY
:        .WORD    041500,000001      ;SECOND OPERAND ON TOP
:        .WORD    076452,125252      ;FIRST OPERAND ON BOTTOM
:        .WORD    105                 ;PROCESSOR PRIORITY LEVEL
:        .WORD    ISR52, 357         ;FIS TRAP VECTOR

```

```

NOP
FMUL    SP                ;FLOATING MULTIPLY ON THE STACK

```

```

RTA52:  JSR      PC,      POPS       ;POP THE 'ANSWER'
:        HLT+2    364          ;FIS TRAP DIDN'T OCCURE!
:        MOV      #BEGIN, SP      ;RESTORE THE STACK POINTER
:        BR

```

```

ISR52:  JSR      PC,      POPES      ;POP ALL DATA OFF THE STACK
:        CMP      #BEGIN, SP      ;CHECK THE STACK POINTER
:        BEQ     ISA52          ;BRANCH IF OK
:        MOV      #BEGIN, SP      ;RESTORE THE STACK POINTER
:        HLT     365           ;STACK POINTER FOULED UP
:        BR      365           ;THE ERROR NUMBER IS 365
:        BR      END52        ;SKIP REST OF TEST

```

```

ISA52:  CMPB     #357, $PSW        ;CHECK PS AFTER FIS TRAP
:        BEQ     .+6            ;BRANCH IF OK
:        HLT     366           ;PS AFTER FIS TRAP NOT EQUAL TO 357
:        BR      366           ;THE ERROR NUMBER IS 366

```

```

CMP     #RTA52, ANS1          ;CHECK FIS TRAP RETURN ADDRESS
BEQ     .+6                   ;BRANCH IF OK
HLT+1  367                   ;FIS TRAP AT WRONG ADDRESS
BR      367                   ;THE ERROR NUMBER IS 367

```

```

CMP     #002, ANSL           ;CHECK PS BEFORE FIS TRAP
BEQ     .+6                   ;BRANCH IF OK
HLT+2  370                   ;PS AT FIS TRAP TIME NOT 002
BR      370                   ;THE ERROR NUMBER IS 370

```

```

CMP     #041500,ANS3         ;CHECK DATA FROM THE STACK
BEQ     .+6                   ;BRANCH IF OK
HLT+4  371                   ;DATA ON STACK (041500) CHANGED
BR      371                   ;THE ERROR NUMBER IS 371

```

```

CMP     #000001,ANS4        ;CHECK DATA FROM STACK
BEQ     .+6                   ;BRANCH IF OK
HLT+4  372                   ;DATA ON STACK (000001) CHANGED
BR      372                   ;THE ERROR NUMBER IS 372

```


00300
00400 2457
00500 2458
00600 2459
00700 2460
00800 2461
00900 2462
01000 2463
01100 2464 011626 104400
01200 2465 011630 004567 004200
01300 2466 011634 027652 125253
01400 2467 011640 167452 125252
01500 2468 011644 000300
01600 2469 011646 016456 000340
01700 2470
01800 2471 011652 000240
01900 2472 011654 075036
02000 2473
02100 2474 011656 004767 004212
02200 2475 011662 022706 000600
02300 2476 011666 001405
02400 2477 011670 012706 000600
02500 2478 011674 104000
02600 2479 011676 000403
02700 2480 011700 000422
02800 2481
02900 2482 011702 122767 000210 166522
03000 2483 011710 001402
03100 2484 011712 104000
03200 2485 011714 000404
03300 2486
03400 2487 011716 022767 177777 166512
03500 2488 011724 001402
03600 2489 011726 104002
03700 2490 011730 000405
03800 2491
03900 2492 011732 022767 177777 166500
04000 2493 011740 001402
04100 2494 011742 104002
04200 2495 011744 000406
04300 2496
04400 2497 011746 122767 000054 166430
04500 2498 011754 001402
04600 2499 011756 104000
04700 2500 011760 000407
04800 2501
04900 2502

```

:*****
:TEST 54:      FDIV (LSI-11 FLOATING DIVIDE INSTRUCTION)
:      167452,125252 / 027652,125253 = 177777,177777
:      PS 210,      STACK POINTER = SP
:*****

```

```

TST54:  JSR      R5,      PUSH5      ;PUSH 4 WORDS ONTO STACK, SET PRIORITY
:      .WORD    027652,125253      ;SECOND OPERAND ON TOP
:      .WORD    167452,125252      ;FIRST OPERAND ON BOTTOM
:      .WORD    300                  ;PROCESSOR PRIORITY LEVEL
:      .WORD    TRAPER, 340         ;FIS TRAP VECTOR

```

```

NOP
FDIV      SP      ;FLOATING DIVIDE ON THE STACK

```

```

JSR      PC,      POPS      ;POP THE ANSWER
CMP      #BEGIN, SP      ;CHECK THE STACK POINTER
BEQ      TSA54      ;BRANCH IF OK
MOV      #BEGIN, SP      ;RESTORE STACK POINTER
HLT      ;STACK POINTER FOULED UP
403      ;THE ERROR NUMBER IS 403
BR      END54      ;SKIP REST OF TEST

```

```

TSA54:  CMPB     #210, $PSW      ;CHECK PS (EXCEPT T BIT)
BEQ      .+6         ;BRANCH IF OK
HLT      ;PS NOT EQUAL TO 210
404      ;THE ERROR NUMBER IS 404

```

```

CMP      #177777,ANS1      ;CHECK FIRST HALF OF ANSWER
BEQ      .+6         ;BRANCH IF OK
HLT+2    ;ANS1 NOT EQUAL TO 177777
405      ;THE ERROR NUMBER IS 405

```

```

CMP      #177777,ANS2      ;CHECK SECOND HALF OF ANSWER
BEQ      .+6         ;BRANCH IF OK
HLT+2    ;ANS2 NOT EQUAL TO 177777
406      ;THE ERROR NUMBER IS 406

```

```

END54:  CMPB     #54, $TESTN    ;CHECK THE TEST NUMBER
BEQ      .+6         ;BRANCH IF OK
HLT      ;WRONG TEST! PC MUST HAVE FOULED UP.
407      ;THE ERROR NUMBER IS 407

```

00300 2503
00400 2504
00500 2505
00600 2506
00700 2507
00800 2508
00900 2509
01000 2510 011762 104400
01100 2511 011764 004567 004216
01200 2512 011770 065252 125252
01300 2513 011774 125252 125252
01400 2514 012000 000217
01500 2515 012002 016456 000340
01600 2516 012006 012702 000510
01700 2517
01800 2518 012012 000240
01900 2519 012014 075032
02000 2520
02100 2521 012016 004767 004216
02200 2522 012022 010267 166406
02300 2523 012026 122767 000210 166376
02400 2524 012034 001402
02500 2525 012036 104000
02600 2526 012040 000410
02700 2527
02800 2528 012042 022767 000514 166364
02900 2529 012050 001402
03000 2530 012052 104000
03100 2531 012054 000411
03200 2532
03300 2533 012056 022767 100200 166352
03400 2534 012064 001402
03500 2535 012066 104002
03600 2536 012070 000412
03700 2537
03800 2538 012072 005767 166342
03900 2539 012076 001402
04000 2540 012100 104002
04100 2541 012102 000413
04200 2542
04300 2543 012104 122767 000055 166272
04400 2544 012112 001402
04500 2545 012114 104000
04600 2546 012116 000414
04700 2547
04800 2548
04900

```

:*****
:TEST 55: FDIV (LSI-11 FLOATING DIVIDE INSTRUCTION)
:      125252,125252 / 065252,125252 = 100200,000000
:      PS 210, STACK POINTER = R2
:*****

```

```

TST55: SCOPE
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R2 STACK, SET PRIORITY
.WORD 065252,125252 ;SECOND OPERAND ON TOP
.WORD 125252,125252 ;FIRST OPERAND ON BOTTOM
.WORD 217 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER, 340 ;FIS TRAP VECTOR
MOV #STACK0,R2 ;SHECK STACK POINTER

NOP
FDIV R2 ;FLOATING DIVIDE ON THE R2 STACK

JSR PC, POPR ;POP THE ANSWER
MOV R2, $SP ;SAVE 'STACK POINTER'
CMPB #210, $PSW ;CHECK PS (EXCEPT T BIT)
BEQ .+6 ;BRANCH IF OK
HLT ;PS NOT EQUAL TO 210
410 ;THE ERROR NUMBER IS 410

CMP #STACK4,$SP ;CHECK THE STACK POINTER (R2)
BEQ .+6 ;BRANCH IF OK
HLT ;STACK POINTER (R2) NOT EQUAL TO #STACK4
411 ;THE ERROR NUMBER IS 411

CMP #100200,ANS1 ;CHECK FIRST HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 100200
412 ;THE ERROR NUMBER IS 412

TST ANS2 ;CHECK SECOND HALF OF ANSWER
BEQ .+6 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 000000
413 ;THE ERROR NUMBER IS 413

END55: CMPB #55, $TESTN ;CHECK THE TEST NUMBER
BEQ .+6 ;BRANCH IF OK
HLT ;WRONG TEST. PC MUST HAVE FOULED UP.
414 ;THE ERROR NUMBER IS 414

```



```

00300
00400 2595
00500 2596
00600 2597
00700 2598
00800 2599
00900 2600
01000 2601
01100 2602 012254 104400
01200 2603 012256 004567 004076
01300 2604 012262 012306
01400 2605 012264 104000 104000
01500 2606 012270 102500 146000
01600 2607 012274 000357
01700 2608 012276 016456 000340
01800 2609
01900 2610 012302 000240
02000 2611 012304 075037
02100 2612 012306 104000
02200 2613 012310 104000
02300 2614 012312 102500
02400 2615 012314 146000
02500 2616
02600 2617 012316 004767 004066
02700 2618 012322 122767 000200 166102
02800 2619 012330 001402
02900 2620 012332 104000
03000 2621 012334 000422
03100 2622
03200 2623 012336 022767 104000 166072
03300 2624 012344 001402
03400 2625 012346 104002
03500 2626 012350 000423
03600 2627
03700 2628 012352 022767 104000 166060
03800 2629 012360 001402
03900 2630 012362 104002
04000 2631 012364 000424
04100 2632
04200 2633 012366 022767 036700 166046
04300 2634 012374 001402
04400 2635 012376 104004
04500 2636 012400 000425
04600 2637
04700 2638 012402 005767 166046
04800 2639 012406 001402
04900 2640 012410 104004
05000 2641 012412 000426
05100 2642
05200 2643 012414 122767 000357 165762
05300 2644 012422 001402
05400 2645 012424 104000
05500 2646 012426 000427
05600 2647
05700 2648
05800 2649
05900 2650

```

```

:*****
:TEST 57:      FDIV (LSI-11 FLOATING DIVIDE INSTRUCTION)
:      102500,146000 / 104000,104000 = 036700,000000
:      PS = 200,      STACK POINTER = PC
:*****
TST57:  SCOPE
        JSR      R5,      PUSH7      ;PUSH 4 WORDS ONTO STACK, SET PRIORITY
        .WORD    STK57
        .WORD    104000,104000      ;TOP OF STACK
        .WORD    102500,146000      ;SECOND OPERAND ON TOP
        .WORD    357                ;FIRST OPERAND ON BOTTOM
        .WORD    TRAPER,340         ;PROCESSOR PRIORITY LEVEL
        .WORD
STK57:  NOP
        FDIV    PC                  ;FLOATING DIVIDE ON FOLLOWING 4 WORDS
        104000      ;SHOULD CONTAIN 104000
        104000      ;SHOULD CONTAIN 104000
        102500      ;BEFORE FDIV, 102500; AFTER, 036700
        146000      ;BEFORE FDIV, 146000; AFTER, 000000
        JSR      PC,      POP7      ;POP THE ANSWER
        (MPB    #200,    $PSW      ;CHECK PS (EXCEPT T BIT)
        BEQ     .+6                ;BRANCH IF OK
        HLT     422                ;PS NOT EQUAL TO 200
        422                    ;THE ERROR NUMBER IS 422
        CMP     #104000,ANS1      ;CHECK FIRST HALF OF INPUT DATA (STK57)
        BEQ     .+6                ;BRANCH IF OK
        HLT+2  423                ;ANS1 NOT EQUAL TO 104000
        423                    ;THE ERROR NUMBER IS 423
        CMP     #104000,ANS2      ;CHECK SECOND HALF OF INPUT DATA (STK57+2)
        BEQ     .+6                ;BRANCH IF OK
        HLT+2  424                ;ANS2 NOT EQUAL TO 104000
        424                    ;THE ERROR NUMBER IS 424
        CMP     #036700,ANS3      ;CHECK FIRST HALF OF ANSWER
        BEQ     .+6                ;BRANCH IF OK
        HLT+4  425                ;ANS3 NOT EQUAL TO 036700
        425                    ;THE ERROR NUMBER IS 425
        TST     ANS4                ;CHECK SECOND HALF OF ANSWER
        BEQ     .+6                ;BRANCH IF OK
        HLT+4  426                ;ANS4 NOT EQUAL TO 000000
        426                    ;THE ERROR NUMBER IS 426
END57:  (MPB    #57,    $TESTN    ;CHECK THE TEST NUMBER
        BEQ     .+6                ;BRANCH IF OK
        HLT     427                ;WRONG TEST. PC MUST HAVE FOULED UP.
        427                    ;THE ERROR NUMBER IS 427
:*****

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00300
00400 2651
00500 2652
00600 2653
00700 2654
00800 2655
00900 2656 012430 104400
01000 2657 012432 004567 003550
01100 2658 012436 065252 125252
01200 2659 012442 025252 125251
01300 2660 012446 000015
01400 2661 012450 012502 000300
01500 2662 012454 012701 000510
01600 2663
01700 2664 012460 000240
01800 2665 012462 075031
01900 2666
02000 2667 012464 004767 003550
02100 2668 012470 010167 165740
02200 2669 012474 104002
02300 2670 012476 000430
02400 2671 012500 000464
02500 2672
02600 2673 012502 004767 003562
02700 2674 012506 010167 165722
02800 2675 012512 122767 000300 165712
02900 2676 012520 001402
03000 2677 012522 104000
03100 2678 012524 000431
03200 2679
03300 2680 012526 022767 000510 165700
03400 2681 012534 001402
03500 2682 012536 104000
03600 2683 012540 000432
03700 2684
03800 2685 012542 022767 012464 165666
03900 2686 012550 001402
04000 2687 012552 104001
04100 2688 012554 000433
04200 2689
04300 2690 012556 022767 000012 165654
04400 2691 012564 001402
04500 2692 012566 104002
04600 2693 012570 000434
04700 2694
04800 2695 012572 022767 065252 165642
04900 2696 012600 001402
05000 2697 012602 104004
05100 2698 012604 000435
05200 2699
05300 2700 012606 022767 125252 165630
05400 2701 012614 001402
05500 2702 012616 104004
05600 2703 012620 000436
05700 2704
05800 2705 012622 022767 025252 165610
05900 2706 012630 001402

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:TEST 60: FDIV (LSI-11 FLOATING DIVIDE INSTRUCTION)
: 025252,125251 / 065252,125252 ==> UNDERFLOW
: PS(ON STACK) = 012, STACK POINTER = R1
:*****
TST60: SCOPE
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R1 STACK, SET PRIORITY
.WORD 065252,125252 ;SECOND OPERAND ON TOP
.WORD 025252,125251 ;FIRST OPERAND ON BOTTOM
.WORD 015 ;PROCESSOR PRIORITY LEVEL
.WORD ISR60, 300 ;FIS TRAP VECTOR
MOV #STACK0,R1 ;SET UP R1 AS STACK POINTER

NOP
FDIV R1 ;FLOATING DIVIDE ON THE R1 STACK

RTA60: JSR PC, POPR ;POP THE 'ANSWER'
MOV R1, $SP ;SAVE STACK POINTER (R1)
HLT+2 ;FIS TRAP DIDN'T OCCURE!
430 ;THE ERROR NUMBER IS 430
BR END60

ISR60: JSR PC, POPER ;POP ALL DATA OFF THE STACKS
MOV R1, $SP ;SAVE STACK POINTER (R1)
CMPB #300, $PSW ;CHECK PS AFTER FIS TRAP
BEQ .+6 ;BRANCH IF OK
HLT ;PS AFTER FIS TRAP NOT EQUAL TO 300
431 ;THE ERROR NUMBER IS 431

CMP #STACK0,$SP ;CHECK THE STACK POINTER (R1)
BEQ .+6 ;BRANCH IF OK
HLT ;STACK POINTER (R1) NOT EQUAL TO #STACK0
432 ;THE ERROR NUMBER IS 432

CMP #RTA60, ANS1 ;CHECK FIS TRAP RETURN ADDRESS
BEQ .+6 ;BRANCH IF OK
HLT+1 ;FIS TRAP AT WRONG ADDRESS
433 ;THE ERROR NUMBER IS 433

CMP #012, ANS2 ;CHECK PS BEFORE FIS TRAP
BEQ .+6 ;BRANCH IF OK
HLT+2 ;PS AT FIS TRAP TIME NOT 012
434 ;THE ERROR NUMBER IS 434

CMP #065252,ANS3 ;CHECK DATA FROM THE STACK
BEQ .+6 ;BRANCH IF OK
HLT+4 ;DATA ON STACK (065252) CHANGED
435 ;THE ERROR NUMBER IS 435

CMP #125252,ANS4 ;CHECK DATA FROM STACK
BEQ .+6 ;BRANCH IF OK
HLT+4 ;DATA ON STACK (125252) CHANGED
436 ;THE ERROR NUMBER IS 436

CMP #025252,ANS5 ;CHECK DATA FROM STACK
BEQ .+6 ;BRANCH IF OK

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00100 CVKACC MACY11 30A(1052) 21-AUG-78 15:28 PAGE 64
00200 CVKACC.P11 16-AUG-78 08:41 TEST FLOATING DIV. INSTRUCTION WITH UNDERFLOW
00300
00400 2707 012632 104006, HLT+6 ;DATA ON STACK (025252) CHANGED
00500 2708 012634 000437 437 ;THE ERROR NUMBER IS 437
00600 2709
00700 2710 012636 022767 125251 165604 CMP #125251,ANS6 ;CHECK DATA FROM STACK
00800 2711 012644 001402 BEQ .+6 ;BRANCH IF OK
00900 2712 012646 104006 HLT+6 ;DATA ON STACK (125251) CHANGED
01000 2713 012650 000440 440 ;THE ERROR NUMBER IS 440
01100 2714
01200 2715 012652 122767 000060 165524 END60: CMPB #60, $TESTN ;CHECK THE TEST NUMBER
01300 2716 012660 001402 BEQ .+6 ;BRANCH IF OK
01400 2717 012662 104000 HLT ;WRONG TEST. PC MUST HAVE FOULED UP.
01500 2718 012664 000441 441 ;THE ERROR NUMBER IS 441
01600 2719
01700 2720

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01700
01800
01900

CWKACC MACY11 30A(1052) 21-AUG-78 15:28 PAGE 66
CWKACC.P11 16-AUG-78 08:41
2777 013060 022767 067452 165360
2778 013066 001402
2779 013070 104006
2780 013072 000451
2781
2782 013074 022767 125252 165346
2783 013102 001402
2784 013104 104006
2785 013106 000452
2786
2787 013110 122767 000061 165266
2788 013116 001402
2789 013120 104000
2790 013122 000453
2791
2792

TEST FLOATING DIV INSTRUCTION WITH OVERFLOW

SEQ 0014

CMP #067452,ANS5 :CHECK DATA FROM STACK
BEQ .+6 :BRANCH IF OK
HLT+6 :DATA ON STACK (067452) CHANGED
451 :THE ERROR NUMBER IS 451

CMP #125252,ANS6 :CHECK DATA FROM STACK
BEQ .+6 :BRANCH IF OK
HLT+6 :DATA ON STACK (125252) CHANGED
452 :THE ERROR NUMBER IS 452

END61: CMPB #61, \$TESTN :CHECK THE TEST NUMBER
BEQ .+6 :BRANCH IF OK
HLT :WRONG TEST. PC MUST HAVE FOULED UP.
453 :THE ERROR NUMBER IS 453

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CVKACC MAR '71 30A(1052) 21-AUG-78 15:28 PAGE 67
CVKACC.P11 16-AUG-78 08:41
2793
2794
2795
2796
2797
2798
2799
2800 013124 104400
2801 013126 004567 003054
2802 013132 100125 125252
2803 013136 052525 052525
2804 013142 000047
2805 013144 013176 000113
2806 013150 012705 000510
2807
2808 013154 000240
2809 013156 075035
2810
2811 013160 004767 003054
2812 013164 010567 165244
2813 013170 104002
2814 013172 000454
2815 013174 000464
2816
2817 013176 004767 003066
2818 013202 010567 165226
2819 013206 122767 000113 165216
2820 013214 001402
2821 013216 104000
2822 013220 000455
2823
2824 013222 022767 000510 165204
2825 013230 001402
2826 013232 104000
2827 013234 000456
2828
2829 013236 022767 013160 165172
2830 013244 001402
2831 013246 104001
2832 013250 000457
2833
2834 013252 022767 000013 165160
2835 013260 001402
2836 013262 104002
2837 013264 000460
2838
2839 013266 022767 100125 165146
2840 013274 001402
2841 013276 104004
2842 013300 000461
2843
2844 013302 022767 125252 165134
2845 013310 001402
2846 013312 104004
2847 013314 000462
2848

TEST FLOATING DIV. INSTRUCTION FOR DIVIDE BY ZERO

SEQ 0075

:TEST 62: FDIV (LSI-11 FLOATING DIVIDE INSTRUCTION)
: 052525,052525 / 100125,125252 ==> DIVIDE BY ZERO
: PS(ON STACK) - 013, STACK POINTER = R5
:*****

TST62: SCOPE
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R5 STACK, SET PRIORITY
.WORD 100125,125252 ;SECOND OPERAND ON TOP
.WORD 052525,052525 ;FIRST OPERAND ON BOTTOM
.WORD 047 ;PROCESSOR PRIORITY LEVEL
.WORD ISR62, 113 ;FIS TRAP VECTOR
MOV #STACK0,R5 ;SET UP R5 AS STACK POINTER

NOP
FDIV R5 ;FLOATING DIVIDE ON THE R5 STACK

RT462: JSR PC, POPR ;POP THE 'ANSWER'
MOV R5, \$SP ;SAVE STACK POINTER (R5)
HLT+2 ;FIS TRAP DIDN'T OCCURE!
454 ;THE ERROR NUMBER IS 454
BR END62

ISR62: JSR PC, POPER ;POP ALL DATA OFF THE STACKS
MOV R5, \$SP ;SAVE STACK POINTER (R5)
CMPB #113, \$PSW ;CHECK PS AFTER FIS TRAP
BEQ .+6 ;BRANCH IF OK
HLT ;PS AFTER FIS TRAP NOT EQUAL TO 113
455 ;THE ERROR NUMBER IS 455

CMP #STACK0,\$SP ;CHECK THE STACK POINTER (R5)
BEQ .+6 ;BRANCH IF OK
HLT ;STACK POINTER (R5) NOT EQUAL TO #STACK0
456 ;THE ERROR NUMBER IS 456

CMP #RTA62, ANS1 ;CHECK FIS TRAP RETURN ADDRESS
BEQ .+6 ;BRANCH IF OK
HLT+1 ;FIS TRAP AT WRONG ADDRESS
457 ;THE ERROR NUMBER IS 457

CMP #013, ANS2 ;CHECK PS BEFORE FIS TRAP
BEQ .+6 ;BRANCH IF OK
HLT+2 ;PS AT FIS TRAP TIME NOT 013
460 ;THE ERROR NUMBER IS 460

CMP #100125,ANS3 ;CHECK DATA FROM THE STACK
BEQ .+6 ;BRANCH IF OK
HLT+4 ;DATA ON STACK (100125) CHANGED
461 ;THE ERROR NUMBER IS 461

CMP #125252,ANS4 ;CHECK DATA FROM STACK
BEQ .+6 ;BRANCH IF OK
HLT+4 ;DATA ON STACK (125252) CHANGED
462 ;THE ERROR NUMBER IS 462

00100 (VKACC MACY11 30A(1052) 21-AUG-78 15:28 PAGE 69
00200 (VKACC.P11 16-AUG-78 08:41
00300

TEST FLOATING DIV. INSTRUCTION FOR DIVIDE BY ZERO

SEQ 0077

00400 2865
00500 2866
00600 2867
00700 2868
00800 2869
00900 2870
01000 2871
01100 2872 013362 104400
01200 2873 013364 004567 002444
01300 2874 013370 000006 123456
01400 2875 013374 100052 052525
01500 2876 013400 000357
01600 2877 013402 013430 000311
01700 2878
01800 2879 013406 000240
01900 2880 013410 075036
02000 2881
02100 2882 013412 004767 002456
02200 2883 013416 104002
02300 2884 013420 000466
02400 2885 013422 012706 000600
02500 2886 013426 000464
02600 2887
02700 2888 013430 004767 002472
02800 2889 013434 022706 000600
02900 2890 013440 001405
03000 2891 013442 012706 000600
03100 2892 013446 104000
03200 2893 013450 000467
03300 2894 013452 000452
03400 2895
03500 2896 013454 122767 000311 164750
03600 2897 013462 001402
03700 2898 013464 104000
03800 2899 013466 000470
03900 2900
04000 2901 013470 022767 013412 164740
04100 2902 013476 001402
04200 2903 013500 104001
04300 2904 013502 000471
04400 2905
04500 2906 013504 022767 000213 164726
04600 2907
04700 2908
04800 2909
04900 2910
05000 2911
05100 2912
05200 2913 013512 001402
05300 2914 013514 104002
05400 2915 013516 000472
05500 2916
05600 2917 013520 022767 000006 164714
05700 2918 013526 001402
05800 2919 013530 104004
05900 2920 013532 000473

:TEST 63: FDIV (LSI-11 FLOATING DIVIDE INSTRUCTION)
: 100052,052525 / 000006,123456 ==> DIVIDE BY ZERO
: PS(ON STACK) = 213, STACK POINTER = SP
:*****

TST63: SCOPE
JSR R5, PUSH5 ;PUSH 4 WORDS ONTO STACK, SET PRIORITY
.WORD 000006,123456 ;SECOND OPERAND ON TOP
.WORD 100052,052525 ;FIRST OPERAND ON BOTTOM
.WORD 357 ;PROCESSOR PRIORITY LEVEL
.WORD ISR63, 311 ;FIS TRAP VECTOR

NOP
FDIV SP ;FLOATING DIVIDE ON THE STACK

RTA63: JSR PC, POPS ;POP THE 'ANSWER'
HLT+2 ;FIS TRAP DIDN'T OCCURE!
466 ;THE ERROR NUMBER IS 466
MOV #REGIA, SP ;RESTORE THE STACK POINTER
BR FND63

ISR63: JSR PC, POPES ;POP ALL DATA OFF THE STACK
CMP #BEGIN, SP ;CHECK THE STACK POINTER
BEQ ISA63 ;BRANCH IF OK
MOV #BEGIN, SP ;RESTORE THE STACK POINTER
HLT ;STACK POINTER FOULED UP
467 ;THE ERROR NUMBER IS 467
BR END63 ;SKIP REST OF TEST

ISA63: CMPB #311, \$PSW ;CHECK PS AFTER FIS TRAP
BEQ .+6 ;BRANCH IF OK
HLT ;PS AFTER FIS TRAP NOT EQUAL TO 311
470 ;THE ERROR NUMBER IS 470

CMP #RTA63, ANS1 ;CHECK FIS TRAP RETURN ADDRESS
BEQ .+6 ;BRANCH IF OK
HLT+1 ;FIS TRAP AT WRONG ADDRESS
471 ;THE ERROR NUMBER IS 471

CMP #213, ANS2 ;CHECK PS BEFORE FIS TRAP

BEQ .+6 ;BRANCH IF OK
HLT+2 ;PS AT FIS TRAP TIME NOT 213
472 ;THE ERROR NUMBER IS 472

CMP #000006,ANS3 ;CHECK DATA FROM THE STACK
BEQ .+6 ;BRANCH IF OK
HLT+4 ;DATA ON STACK (000006) CHANGED
473 ;THE ERROR NUMBER IS 473

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00100 CVKACC MACY11 30A(1052) 21-AUG-78 15:28 PAGE 70
00200 CVKACC.P11 16-AUG-78 08:41 TEST FLOATING DIV. INSTRUCTION FOR DIVIDE BY ZERO
00300
00400 2921
00500 2922 013534 022767 123456 164702 CMP #123456,ANS4 ;CHECK DATA FROM STACK
00600 2923 013542 001402 BEQ .+6 ;BRANCH IF OK
00700 2924 013544 104004 HLT+4 ;DATA ON STACK (123456) CHANGED
00800 2925 013546 000474 474 ;THE ERROR NUMBER IS 474
00900 2926
01000 2927 013550 022767 100052 164670 CMP #100052,ANS5 ;CHECK DATA FROMONG TK
01100 2928 013556 001402 BEQ .+6 ;BRANCH IF OK
01200 2929 013560 104006 HLT+6 ;DATA ON STACK (100052) CHANGED
01300 2930 013562 000475 475 ;THE ERROR NUMBER IS 475
01400 2931
01500 2932 013564 022767 052525 164656 CMP #052525,ANS6 ;CHECK DATA FROM STACK
01600 2933 013572 001402 BEQ .+6 ;BRANCH IF OK
01700 2934 013574 104006 HLT+6 ;DATA ON STACK (052525) CHANGED
01800 2935 013576 000476 476 ;THE ERROR NUMBER IS 476
01900 2936
02000 2937 013600 122767 000063 64576 END63: CMPB #63, $TESTN ;CHECK THE TEST NUMBER
02100 2938 013606 001402 BEQ .+6 ;BRANCH IF OK
02200 2939 013610 104006 HLT ;WREST! PC MUST HAVE FOULED UP.
02300 2940 013612 000477 477 ;THE ERROR NUMBER IS 477
02400 2941
02500 2942

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00100 CVKACC MACY11 30A(1052) 21-AUG-78 15:28 PAGE 72
00200 CVKACC.P11 16-AUG-78 08:41 TEST OF ALL FIS AT ONCE

SEQ 0080

00300
00400 2999
00500 3000 014016 122767 000064 164360
00600 3001 014024 001402
00700 3002 014026 104000
00800 3003 014030 000504
00900 3004

END64: CMPB #64,
BEQ .+6
HLT
504

\$TESTN :CHECK THE TEST NUMBER
:BRANCH IF OK
:WRONG TEST! PC MUST HAVE FOULED UP.
:THE ERROR NUMBER IS 504


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00100
00200
00300
00400 3077
00500 3078
00600 3079
00700 3080
00800 3081
00900 3082
01000 3083 014310 104400
01100 3084 014312 012737 014402 000004
01200 3085 014320 012737 000340 000006
01300 3086 014326 004567 001654
01400 3087 014332 065432 123456
01500 3088 014336 037654 032107
01600 3089 014342 000202
01700 3090 014344 016456 000340
01800 3091 014350
01900 3092 014350 106427
02000 3093 014354 012705 160000
02100 3094
02200 3095 014360 000240
02300 3096 014362 075025
02400 3097
02500 3098 014364
02600 3099 014364 106767
02700 3100 014370 010567 164040
02800 3101 014374 104000
02900 3102 014376 000517
03000 3103 014400 000434
03100 3104
03200 3105 014402 004767 001662
03300 3106 014406 010567 164022
03400 3107 014412 122767 000340 164012
03500 3108 014420 001402
03600 3109 014422 104000
03700 3110 014424 000520
03800 3111
03900 3112 014426 022767 160000 164000
04000 3113 014434 001402
04100 3114 014436 104000
04200 3115 014440 000521
04300 3116
04400 3117 014442 022767 014364 163766
04500 3118 014450 001402
04600 3119 014452 104001
04700 3120 014454 000522
04800 3121
04900 3122 014456 022767 000210 163754
05000 3123 014464 001402
05100 3124 014466 104002
05200 3125 014470 000523
05300 3126
05400 3127 014472 122767 000066 163704
05500 3128 014500 001402
05600 3129 014502 104000
05700 3130 014504 000524

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:*****
:TEST 66: TEST THAT STACK POINTER ADDRESS ERROR CAUSES ABORT
: INSTRUCTION - FMUL, STACK POINTER = R5
:*****

TST66: SCOPE
MOV #ISR66, @#4 ;SET UP ADDRESS TRAP VECTOR
MOV #340, @#6
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R5 STACK, SET PRIORITY
.WORD 065432,123456 ;SECOND OPERAND ON TOP
.WORD 037654,032107 ;FIRST OPERAND ON BOTTOM
.WORD 202 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER, 340 ;FIS TRAP VECTOR
MTPS #202 ;SET PROCESSOR STATUS
.WORD 106400...C
MOV #160000,R5 ;SET UP R5 AS STACK POINTER

NOP
FMUL R5 ;FLOATING MULTIPLY ON THE R5 STACK

RTA66: MFPS $PSW ;SAVE THE PSW
.WORD 106700...C
MOV R5, $SP ;SAVE STACK POINTER (R5)
HLT 517 ;FIS TRAP DIDN'T OCCURE
BR END66 ;THE ERROR NUMBER IS 517

ISR66: JSR PC, POPER ;POP ALL DATA OFF THE STACKS
MOV R5, $SP ;SAVE STACK POINTER (R5)
CMPB #340, $PSW ;CHECK PS AFTER ADR. ERR. TRAP
BEQ +6 ;BRANCH IF OK
HLT ;PS AFTER TRAP NOT EQUAL TO 340
520 ;THE ERROR NUMBER IS 520

CMP #160000,$SP ;CHECK THE STACK POINTER (R5)
BEQ +6 ;BRANCH IF OK
HLT 521 ;STACK POINTER (R5) NOT EQUAL TO #160000
521 ;THE ERROR NUMBER IS 521

CMP #RTA66, ANS1 ;CHECK FIS TRAP RETURN ADDRESS
BEQ +6 ;BRANCH IF OK
HLT+1 ;FIS TRAP AT WRONG ADDRESS
522 ;THE ERROR NUMBER IS 522

CMP #210, ANS2 ;CHECK PS BEFORE FIS TRAP
BEQ +6 ;BRANCH IF OK
HLT+2 ;PS AT FIS TRAP TIME NOT 210
523 ;THE ERROR NUMBER IS 523

END66: CMPB #66, $TESTN ;CHECK THE TEST NUMBER
BEQ +6 ;BRANCH IF OK
HLT ;WRONG TEST! PC MUST HAVE FOULED UP.
524 ;THE ERROR NUMBER IS 524

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00100 3131 014506 012737 000006 000004 52200      MOV    #6,    @#4    ;RESTORE TIME-OUT VECTOR
00200 3132 014514 005037 000006 52300      CLR    @#6
00300 3133 014520 012767 000003 164010 52700      MOV    #3,    TIMES ;REDUCE NUMBER OF ITERATIONS
00400 3134
00500 3135
00600 3136
00700 3137
00800 3138
00900 3139
01000 3140
01100 3141 014526 104400
01200 3142 014530 132737 000040 000421
01300 3143 014536 001177
01400 3144 014540 013704 000546
01500 3145 014544 012724 014646
01600 3146 014550 012714 000340
01700 3147 014554 032737 004000 000422
01800 3148 014562 001005
01900 3149 014564 000004 000473
02000 3150
02100 3151 014570 012767 014576 163672
02200 3152 014576 004567 001404
02300 3153 014602 000177 134543
02400 3154 014606 035700 143235
02500 3155 014612 000143
02600 3156 014614 016456 000340
02700 3157 014620 012701 000510
02800 3158 014624 012767 000060 163702
02900 3159 014632 112777 000100 163710
03000 3160
03100 3161 014640 075001
03200 3162 014642 024141
03300 3163 014644 000775
03400 3164
03500 3165 014646 105077 163676
03600 3166 014652 022716 014640
03700 3167 014656 001424
03800 3168 014660 022766 014640 000004
03900 3169 014666 001423
04000 3170 014670 005337 000534
04100 3171 014674 001517
04200 3172 014676 112777 000015 163646
04300 3173 014704 105777 163640
04400 3174 014710 100375
04500 3175 014712 112777 000015 163632
04600 3176 014720 012777 000100 163622
04700 3177 014726 000002
04800 3178
04900 3179 014730 004767 001334
05000 3180 014734 000403
05100 3181
05200 3182 014736 022626
05300 3183 014740 004767 001330
05400 3184 014744 005746
05500 3185 014746 012746 014640
05600 3186 014752 022706 000574

```

:*****
:TEST 67: TEST THAT FIS ABORTS PROPERLY WHEN INTERRUPTED
: 035700,143235 + 000177,134543 = 035700,143235
: PS - .PS, STACK POINTER = R1
:*****

```

TST67: SCOPE
BITB #40,@#SENVM
BNE END67+2 ;EXIT THIS TEST IF BIT 5 OF SENVM IS HIGH
MOV @#TTYOUT,R4
MOV #ISR67,(R4)+ ;SET UP TELEPRINTER INTERRUPT VECTOR
MOV #340,(R4)
BIT #SW11,@#SSWREG ;TEST FOR ITERATIONS
BNE 1$ ;BRANCH TO AVOID HANG UP
TYPE, RETURN+1 ;RETURN+1 CAN BE REPLACED WITH THE ADDRESS 0
;TO TYPE CARRIAGE RETURN, LINE FEED

1$: MOV #.6, LAD$ ;RESET LOOP ADDRESS
JSR R5, PUSHR ;PUSH 4 WORDS ONTO R1 STACK, SET PRIORITY
.WORD 000177,134543 ;SECOND OPERAND ON TOP
.WORD 035700,143235 ;FIRST OPERAND ON BOTTOM
.WORD 143 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER, 340 ;FIS TRAP VECTOR
MOV #STACK0,R1 ;SET UP STACK POINTER
MOV #60,TEMP ;INITIALIZE COUNTER FOR FIS INTERRUPTS
MOVB #100,@#STPS ;SET TTY INTERRUPT ENABLE

RTA67: FADD R1 ;FLOATING ADD ON THE STACK
CMP -(R1), -(R1) ;RESET THE STACK POINTER FOR NEXT PASS
BR RTA67 ;REPEAT UNTIL INTERRUPTED

ISR67: CLRB @#STPS ;CLEAR THE INTERRUPT ENABLE
CMP #RTA67,(SP) ;CHECK IF INTERRUPT AT FIS INSTR.
BEQ 3$ ;BRANCH IF IT DID
CMP #RTA67,4(SP) ;CHECK FOR INTERRUPT WITH T-BIT SET
BEQ 4$ ;BRANCH IF IT DID
DEC @#TEMP ;DID INTERRUPTS OCCUR TEMP # OF TIMES
BEQ END67 ;IF YES THEN SKIP TO END OF THIS TEST

1$: MOVB #15,@#TPB ;CONTINUE TO TYPE 'CR'
2$: TSTB @#TPS ;LOOP HERE UNTILL DONE BIT COMES ON
BPL 2$
MOVB #15,@#TPB ;TYPE ANOTHER 'CR'
MOV #100,@#STPS ;SET TTY INTERRUPT ENABLE

3$: JSR PC, POPER ;SAVE ALL THE STUFF ON THE STACK
BR 5$

4$: CMP (SP)+,(SP)+ ;RESET THE STACK TO IGNORE THE TRACE TRAP
JSR PC, POPER1 ;POP ALL THE STUFF OFF THE STACK
5$: TST -(SP) ;SAVE PSW FOR FUTURE RTI
MCL #RTA67,-(SP) ;PLACE THE RTI ADDRESS BACK IN SP
CMP #BEGIN-4,SP ;CHECK THE STACK POINTER

```

00100	3187	014756	001407			BEQ	6\$:BRANCH IF OK
00200	3188	014760	010667	163450		MOV	SP,	\$SP		:SAVE FOR TYPING
00300	3189	014764	012706	000574		MOV	#BEGIN-4,SP			:RESTORE THE STACK POINTER
00400	3190	014770	104000			HLT				:STACK POINTER FOULED UP
00500	3191	014772	000525			525				:THE ERROR NUMBER IS 525
00600	3192	014774	000457			BR	END67			:SKIP REST OF TEST
00700	3193									
00800	3194	014776	010167	163432		MOV	R1,	\$SP		:SAVE STACK POINTER
00900	3195	015002	122767	000344	163422	CMPB	#344,	\$PSW		:CHECK PS AFTER INTERUPT
01000	3196	015010	001402			BEQ	+.6			:BRANCH IF OK
01100	3197	015012	104000			HLT				:PS AFTER INTERUPT NOT EQUAL TO LVLA
01200	3198	015014	000526			526				:THE ERROR NUMBER IS 526
01300	3199									
01400	3200	015016	022767	000510	163410	CMP	#STACK0,\$SP			:CHECK THE STACK POINTER (R1)
01500	3201	015024	001402			BEQ	+.6			:BRANCH IF OK
01600	3202	015026	104000			HLT				:STACK POINTER (R1) NOT EQUAL TO #STACK0
01700	3203	015030	000527			527				:THE ERROR NUMBER IS 527
01800	3204									
01900	3205	015032	022767	014640	163376	CMP	#RTA67,ANS1			:CHECK FIS TRAP RETURN ADDRESS
02000	3206	015040	001402			BEQ	+.6			:BRANCH IF OK
02100	3207	015042	104001			HLT +1				:FIS TRAP AT WRONG ADDRESS
02200	3208	015044	000530			530				:THE ERROR NUMBER IS 530
02300	3209									
02400	3210									
02500	3211	015046	022767	000177	163368	CMP	#000177,ANS3			:CHECK DATA FROM THE STACK
02600	3212	015054	001402			BEQ	+.6			:BRANCH IF OK
02700	3213	015056	104004			HLT+4				:DATA ON STACK (000177) CHANGED
02800	3214	015060	000531			531				:THE ERROR NUMBER IS 531
02900	3215									
03000	3216	015062	022767	134543	163354	CMP	#134543,ANS4			:CHECK DATA FROM STACK
03100	3217	015070	001402			BEQ	+.6			:BRANCH IF OK
03200	3218	015072	104004			HLT+4				:DATA ON STACK (134543) CHANGED
03300	3219	015074	000532			532				:THE ERROR NUMBER IS 532
03400	3220									
03500	3221	015076	022767	035700	163342	CMP	#035700,ANS5			:CHECK DATA FROM STACK
03600	3222	015104	001402			BEQ	+.6			:BRANCH IF OK
03700	3223	015106	104006			HLT+6				:DATA ON STACK (035700) CHANGED
03800	3224	015110	000533			533				:THE ERROR NUMBER IS 533
03900	3225									
04000	3226	015112	022767	143235	163330	CMP	#143235,ANS6			:CHECK DATA FROM STACK
04100	3227	015120	001402			BEQ	+.6			:BRANCH IF OK
04200	3228	015122	104006			HLT+6				:DATA ON STACK (143235) CHANGED
04300	3229	015124	000534			534				:THE ERROR NUMBER IS 534
04400	3230									
04500	3231	015126	005367	163402		DEC	TEMP			:STAY IN THE LOOP FOR 30 TIMES
04600	3232	015132	001261			BNE	1\$			
04700	3233									
04800	3234	015134	022626			CMP	(SP)+,	(SP)+		:RESTORE STACK POINTER TO 500
04900	3235	015136	122767	000067	163240	CMPB	#67,	\$TESTN		:CHECK THE TEST NUMBER
05000	3236	015144	001402			BEQ	+.6			:BRANCH IF OK
05100	3237	015146	104000			HLT				:WRONG TEST. PC MUST HAVE FOULED UP.
05200	3238	015150	000535			535				:THE ERROR NUMBER IS 535
05300	3239	015152				MTPS	#340			
05400	3240	015152	106427			.WORD	*03400...			
05500	3241									
05600										
05700										
05800										

END67:

```

00100
00200
00300
00400 3242
00500 3243
00600 3244
00700 3245
00800 3246
00900 3247
01000 3248
01100 3249 015156 104400
01200 3250 015160 132737 0G0040 000421
01300 3251 015166 001176
01400 3252 015170 013704 000546
01500 3253 015174 012724 015276
01600 3254 015200 012714 000340
01700 3255 015204 032737 004000 000422
01800 3256 015212 001005
01900 3257 015214 000004 000473
02000 3258
02100 3259 015220 012767 015226 163242
02200 3260 015226 004567 000754
02300 3261 015232 040200 000000
02400 3262 015236 107070 070707
02500 3263 015242 000100
02600 3264 015244 016456 000340
02700 3265 015250 012700 000510
02800 3266 015254 012767 000060 163252
02900 3267 015262 112777 000100 163260
03000 3268
03100 3269 015270 075020
03200 3270 015272 024040
03300 3271 015274 000775
03400 3272
03500 3273 015276 105077 163246
03600 3274 015302 022716 015270
03700 3275 015306 001424
03800 3276 015310 022766 015270 000004
03900 3277 015316 001423
04000 3278 015320 005337 000534
04100 3279 015324 001516
04200 3280 015326 112777 000015 163216
04300 3281 015334 105777 163210
04400 3282 015340 100375
04500 3283 015342 112777 000015 163202
04600 3284 015350 012777 000100 163172
04700 3285 015356 000002
04800 3286
04900 3287 015360 004767 000704
05000 3288 015364 000403
05100 3289
05200 3290 015366 022626
05300 3291 015370 004767 000700
05400 3292 015374 005746
05500 3293 015376 012746 015270
05600 3294 015402 022706 000574
05700 3295 015406 001407
05800 3296 015410 010667 163020
05900 3297 015414 012706 000574

```

```

:*****
:TEST 70: TEST THAT FIS ABORTS PROPERLY WHEN INTERRUPTED
: 107070,070707 * 040200,000000 107070,070707
: PS - .PS, STACK POINTER R0
:*****
TST70: SCOPE
BITB #40,@$ENVM
BNE END70+2 ;EXIT THIS TEST IF BIT 5 OF $ENVM IS HIGH
MOV @TTYOUT,R4
MOV #ISR70,(R4)+ ;SET UP TELEPRINTER INTERRUPT VECTOR
MOV #340,(R4)
BIT #SW11,@$$SWREG ;TEST FOR ITERATIONS
BNE 1$ ;BRANCH TO AVOID HANG UP
TYPE, RETURN+1 ;RETURN+1 CAN BE REPLACED WITH THE ADDRESS 0
;TO TYPE CARRIAGE RETURN, LINE FEED
1$: MOV #,+6,LAD$ ;RESET LOOP ADDRESS
JSR R5,PUSHR ;PUSH 4 WORDS ONTO R0 STACK, SET PRIORITY
.WORD 040200,000000 ;SECOND OPERAND ON TOP
.WORD 107070,070707 ;FIRST OPERAND ON BOTTOM
.WORD 100 ;PROCESSOR PRIORITY LEVEL
.WORD TRAPER,340 ;FIS TRAP VECTOR
MOV #STACK0,R0 ;SET UP STACK POINTER
MOV #60,TEMP ;INITIALIZE COUNTER FOR FIS INTERRUPTS
MOVB #100,@$TPS ;SET TTY INTERRUPT ENABLE

RTA70: FMUL R0 ;FLOATING MULTIPLY ON THE STACK
CMP -(R0),-(R0) ;RESET THE STACK POINTER FOR NEXT PASS
BR RTA70 ;REPEAT UNTIL INTERRUPTED

ISR70: CLR B @$TPS ;CLEAR THE INTERRUPT ENABLE
CMP #RTA70,(SP) ;CHECK IF INTERRUPT AT FIS INSTR.
BEQ 3$ ;BRANCH IF IT DID
CMP #RTA70,4(SP) ;CHECK FOR INTERRUPT WITH T-BIT SET
BEQ 4$ ;BRANCH IF IT DID
DEC @TEMP ;DID INTERRUPTS OCCUR TEMP # OF TIMES
BEQ END70 ;IF YES THEN SKIP TO END OF THIS TEST
1$: MOVB #15,@$TPB ;CONTINUE TO TYPE 'CR'
2$: TSTB @$TPS ;LOOP HERE UNTILL DONE BIT COMES ON
BPL 2$
MOVB #15,@$TPB ;TYPE ANOTHER 'CR'
MVB #100,@$TPS ;SET TTY INTERRUPT ENABLE

3$: JSR PC,POPER ;SAVE ALL THE STUFF ON THE STACK
BR 5$

4$: CMP (SP)+,(SP)+ ;RESET THE STACK TO IGNORE THE TRACE TRAP
JSR PC,POPER1 ;POP ALL THE STUFF OFF THE STACK
5$: TST -(SP) ;SAVE PSW FOR FUTURE RTI
MOV #RTA70,-(SP) ;PLACE THE RTI ADDRESS BACK IN SP
CMP #BEGIN-4,SP ;CHECK THE STACK POINTER
BEQ 6$ ;BRANCH IF OK
MOV SP,$SP ;SAVE FOR TYPING
MOVB #BEGIN-4,SP ;RESTORE THE STACK POINTER

```

```

00300
00400      3298 015420 104000      HLT                ;STACK POINTER FOULED UP
00500      3299 015422 000536      536                ;THE ERROR NUMBER IS 536
00600      3300 015424 000456      BR      END70      ;SKIP REST OF TEST
00700      3301
00800      3302 015426 010067 163002      68:  MOV      R0,    $SP      ;SAVE STACK POINTER
00900      3303 015432 122767 000344 162772      CMPB     #344,   $PSW      ;CHECK PS AFTER INTERUPT
01000      3304 015440 001402      BEQ     .+6         ;BRANCH IF OK
01100      3305 015442 104000      HLT                ;PS AFTER INTERUPT NOT EQUAL TO LVL A
01200      3306 015444 000537      537                ;THE ERROR NUMBER IS 537
01300      3307
01400      3308 015446 022767 000510 162760      CMP      #STACK0,$SP    ;CHECK THE STACK POINTER (R0)
01500      3309 015454 001402      BEQ     .+6         ;BRANCH IF OK
01600      3310 015456 104000      HLT                ;STACK POINTER (R0) NOT EQUAL TO #STACK0
01700      3311 015460 000540      540                ;THE ERROR NUMBER IS 540
01800      3312
01900      3313 015462 022767 015270 162746      CMP      #RTA70,ANS1    ;CHECK FIS TRAP RETURN ADDRESS
02000      3314 015470 001402      BEQ     .+6         ;BRANCH IF OK
02100      3315 015472 104001      HLT+1   541         ;FIS TRAP AT WRONG ADDRESS
02200      3316 015474 000541      541                ;THE ERROR NUMBER IS 541
02300      3317
02400      3318
02500      3319 015476 022767 040200 162736      CMP      #040200,ANS3   ;CHECK DATA FROM THE STACK
02600      3320 015504 001402      BEQ     .+6         ;BRANCH IF OK
02700      3321 015506 104004      HLT+4   542         ;DATA ON STACK (040200) CHANGED
02800      3322 015510 000542      542                ;THE ERROR NUMBER IS 542
02900      3323
03000      3324 015512 005767 162726      TST     ANS4         ;CHECK DATA FROM STACK
03100      3325 015516 001402      BEQ     .+6         ;BRANCH IF OK
03200      3326 015520 104004      HLT+4   543         ;DATA ON STACK (000000) CHANGED
03300      3327 015522 000543      543                ;THE ERROR NUMBER IS 543
03400      3328
03500      3329 015524 022767 107070 162714      CMP      #107070,ANS5   ;CHECK DATA FROM STACK
03600      3330 015532 001402      BEQ     .+6         ;BRANCH IF OK
03700      3331 015534 104006      HLT+6   544         ;DATA ON STACK (107070) CHANGED
03800      3332 015536 000544      544                ;THE ERROR NUMBER IS 544
03900      3333
04000      3334 015540 022767 070707 162702      CMP      #070707,ANS6   ;CHECK DATA FROM STACK
04100      3335 015546 001402      BEQ     .+6         ;BRANCH IF OK
04200      3336 015550 104006      HLT+6   545         ;DATA ON STACK (070707) CHANGED
04300      3337 015552 000545      545                ;THE ERROR NUMBER IS 545
04400      3338
04500      3339 015554 005367 162754      DEC     TEMP         ;STAY IN THE LOOP FOR 30 TIMES
04600      3340 015560 001262      BNE     1$          1$
04700      3341
04800      3342 015562 022626      END70: CMP      (SP)+, (SP)+    ;RESTORE STACK POINTER TO 500
04900      3343 015564 122767 000070 162612      CMPB     #70,    $TESTN   ;CHECK THE TEST NUMBER
05000      3344 015572 001402      BEQ     .+6         ;BRANCH IF OK
05100      3345 015574 104000      HLT                ;WRONG TEST PC MUST HAVE FOULED UP.
05200      3346 015576 000546      546                ;THE ERROR NUMBER IS 546
05300      3347 015600
05400      3348 015600 106427      MTPC    #340        ;
05500      3349 015600 106400      .WORD   106400

```

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INTERUPT ABORT TEST SECTION

SEQ 0088

00300
00400 3350 015604 012767 000377 162724 53000
00500 3351 015612 010477 162730 53100
00600 3352 015616 005014 53200
00700 3353 53300

MOV #377,
MOV R4,
(LR (R4)

TIMES ;SET NUMBER OF ITERATIONS TO 377
@TTYOUT ;RESTORE TTY INTERUPT VECTOR


```

00300
00400 3354
00500 3355
00600 3356
00700 3357
00800 3358
00900 3359
01000 3360
01100 3361
01200 3362
01300 3363
01400 3364
01500 3365 015620
01600 3366 015620 104400
01700 3367 015622 005267 162560
01800 3368 015626 042767 100000 162557
01900 3369 015634 005327
02000 3370 015636 000001
02100 3371 015640 003015
02200 3372 015642 012737
02300 3373 015644 000001
02400 3374 015646 015636
02500 3375 015650 000004 015700
02600 3376 015654
02700 3377
02800 3378 015654 013700 000042
02900 3379 015660 001405
03000 3380 015662 000005
03100 3381 015664 004710
03200 3382 015666 000240
03300 3383 015670 000240
03400 3384 015672 000240
03500 3385 015674
03600 3386 015674 000137 000600
03700 3387 015700 005015 047105 020104
03800 3388 015706 040520 051523
03900 3389 015712 377 377 000
04000 3390 015716
04100 3391 53600
04200 3392 015644 000001 54100

```

```

;*****
.SBTTL END OF PASS ROUTINE
;*INCREMENT THE PASS NUMBER ($PASS)
;*TYPE 'END PASS'
;*IF THERES A MONITOR GO TO IT
;*IF THERE ISN'T JUMP TO BEGIN
;*IF IT IS DESIRED TO HAVE A BELL INDICATE THE 'END OF PASS' LOCATION
;*SENDMG CAN BE CHANGED TO 7.

$EOP:
SCOPE
INC $PASS ;;INCREMENT THE PASS NUMBER
BIC #100000,$PASS ;;DON'T ALLOW A NEG. NUMBER
DEC (PC)+ ;;LOOP?
$EOPCT: .WORD 1
BGT $DOAGN ;;YES
MOV (PC)+,@(PC)+ ;;RESTORE COUNTER
$ENDCT: .WORD 1
$EOPCT
TYPE ,SENDMG ;;TYPE 'END PASS'

$GET42:
MOV @#42,R0 ;;GET MONITOR ADDRESS
BEQ $DOAGN ;;BRANCH IF NO MONITOR
RESET ;;CLEAR THE WORLD
$ENDAD: JSR PC,(R0) ;;GO TO MONITOR
NOP ;;SAVE ROOM
NOP ;;FOR
NOP ;;ACT11

$DOAGN: JMP @#BEGIN ;;RETURN
$ENDMG: .ASCII <15><12>/END PASS/

$ENULL: .BYTE -1,-1,0 ;;NULL CHARACTER STRING
.EVEN

$ENDCT: 1

```

```

00100 3393
00200 3394
00300 3395
00400 3396
00500 3397 015716 032737 000400 000422
00600 3398 015724 001404
00700 3399 015726 123767 000422 162450
00800 3400 015734 001431
00900 3401 015736 032737 040000 000422
01000 3402 015744 001023
01100 3403 015746 032737 004000 000422
01200 3404 015754 001412
01300 3405 015756 105767 162521
01400 3406 015762 001404
01500 3407 015764 126767 162546 162511
01600 3408 015772 001010
01700 3409 015774 112767 000001 62501
01800 3410 016002 105267 162376
01900 3411 016006 011667 162456
02000 3412 016012 000002
02100 3413
02200 3414 016014 105267 162463
02300 3415 016020 005767 162444
02400 3416 016024 001766
02500 3417 016026 016716 162436
02600 3418 016032 000002
02700 3419
02800
02900
03000

```

```

;*****
.SBTTL SCOPE ROUTINE
SCOPE$: BIT #SW08,@$$SWREG ;KILL LDUB OR LOOP ON SPEC. TEST
        BEQ 1$
        CMPB @$$SWREG,$TESTN ;ON RIGHT TEST? *SW7-0*
        BEQ OVER$
1$: BIT #SW14,@$$SWREG ;LOOP ON TEST
        BNE KITS
        BIT #SW11,@$$SWREG ;KILL ITERATIONS
        BEQ SVLAD$
        TSTB $ICNT
        BEQ 2$ ;BRANCH IF FIRST
        CMPB TIMES,$ICNT ;DONE?
        BNE KITS ;BRANCH IF NOT
2$: MOVB #1,$ICNT ;FIRST ITERATION
SVLAD$: INCB $TESTN ;COUNT TEST NUMBERS
        MOV (6),LAD$ ;SAVE LOOP ADDRESS
        RTI ;RETURN

KITS$: INCB $ICNT
OVER$: TST LAD$ ;FIRST ONE?
        BEQ SVLAD$
        MOV LAD$,(6) ;FUDGE RETURN ADDRESS
        RTI ;FIXES PS

```

```

00400 3420 55000
00500 3421 55100
00600 3422 55200
00700 3423 016034 005726 55300
00800 3424 016036 062705 000010 55400
00900 3425 016042 014546 55500
01000 3426 016044 014546 55600
01100 3427 016046 014546 55700
01200 3428 016050 014546 55800
01300 3429 016052 062705 000010 55900
01400 3430 016056 56300
01500 3431 016056 106425
01600 3432 016060 005205 56700
01700 3433 016062 012577 162376 56800
01800 3434 016066 012577 162374 56900
01900 3435 016072 000115 57000
02000 3436 57100
02100 3437 57200
02200 3438 57300
02300 3439 57400
02400 3440 57500
02500 3441 016074 57900
02600 3442 016074 106767
02700 3443 016100 042767 000020 162324 58300
02800 3444 016106 012604 58400
02900 3445 016110 012667 162322 58500
03000 3446 016114 012667 162320 58600
03100 3447 016120 010667 162310 58700
03200 3448 016124 000114 58800
03300 3449 58900
03400 3450 59000
03500 3451 59100
03600 3452 59200
03700 3453 59300
03800 3454 59400
03900 3455 59500
04000 3456 016126 59900
04100 3457 016126 106767
04200 3458 016132 012604 60300
04300 3459 016134 012667 162276 60400
04400 3460 016140 011667 162274 60500
04500 3461 016144 042767 000020 162266 60600
04600 3462 016152 012746 016160 60700
04700 3463 016156 000002 60800
04800 3464 016160 012667 162256 60900
04900 3465 016164 012667 162254 61000
05000 3466 016170 012667 162252 61100
05100 3467 016174 012667 162250 61200
05200 3468 016200 010667 162230 61300
05300 3469 016204 000114 61400
05400 3470 61500
05500 3471 61600
05600 3472 61700
05700 3473 016206 012704 000510 61800
05800 3474 016212 012524 61900
05900 3475 016214 012524 62000
    
```

```

;SUBROUTINE TO PUSH 4 WORDS ONTO THE STACK
PUSHS: TST (SP)+ ;POP STACK BY 1
        ADD #10, R5 ;POINT TO END OF DATA
        MOV -(R5), -(SP) ;PUSH DATA ONTO THE STACK
        MOV -(R5), -(SP) ;PUSH DATA ONTO THE STACK
        MOV -(R5), -(SP) ;PUSH DATA ONTO THE STACK
        MOV -(R5), -(SP) ;PUSH DATA ONTO THE STACK
        ADD #10, R5 ;POINT TO END OF DATA
        MTPS (R5)+ ;SET THE PROCESSOR STATUS
        .WORD 106400...C
        INC R5
        MOV (R5)+, @FISVEC ;SET UP FIS ERROR TRAP VECTOR
        MOV (R5)+, @FISLVL ;TRAP STATUS
        JMP (R5) ;RETURN
    
```

```

;SUBROUTINE TO POP 2 WORDS OFF THE STACK
;ALSO SAVES THE PROCESSOR STATUS WORD (EXCEPT T BIT)
POPS: MFPS $PSW ;SAVE PROCESSOR STATUS WORD
        .WORD 106700...C
        BIC #20, $PSW ;CLEAR T-BIT
        MOV (SP)+, R4 ;SAVE RTS ADDRESS
        MOV (SP)+, ANS1 ;SAVE THE ANSWER
        MOV (SP)+, ANS2
        MOV SP, $SP ;SAVE THE STACK POINTER
        JMP (R4) ;RETURN
    
```

```

;SUBROUTINE TO POP 6 WORDS OFF THE STACK.
;THE FIRST TWO WERE PUT ON BY THE ERROR TRAP,
;THE LAST FOUR WERE THE ORIGINAL INPUT DATA.
;ALSO SAVES THE PS AND STACK POINTER.
POPE: MFPS $PSW ;SAVE PROCESSOR STATUS WORD
        .WORD 106700...C
        MOV (SP)+, R4 ;SAVE RTS ADDRESS
        MOV (SP)+, ANS1 ;SAVE RTI ADDRESS
        MOV (SP)+, ANS2 ;SAVE RTI STATUS
        BIC #20, ANS2 ;CLEAR THE T-BIT
        MOV #1$, -(SP)
        RTI ;RESTORE THE PROCESSOR STATUS
        1$: MOV (SP)+, ANS3 ;SAVE DATA
        MOV (SP)+, ANS4
        MOV (SP)+, ANS5
        MOV (SP)+, ANS6
        MOV SP, $SP ;SAVE SP
        JMP (R4) ;RTS
    
```

```

;SUBROUTINE TO PUSH 4 WORDS ONTO THE STACK
PUSHR: MOV #STACK0,R4 ;SET R4 TO STACK
        MOV (R5)+, (R4)+ ;PUT DATA ON STACK
        MOV (R5)+, (R4)+
    
```

00100 CVKACC MACY11 30A(1052) 21-AUG-78 15:28 PAGE 84
 00200 CVKACC.P11 16-AUG-78 08:41 PUSH AND POP SUBROUTINES

SEQ 0092

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00400 3476 016216 012524 62100 MOV (R5)+, (R4)+ ;
00500 3477 016220 012524 62200 MOV (R5)+, (R4)+ ;
00600 3478 016222 62F90 MTPS (R5)+ ;SET THE PROCESSOR STATUS
00700 3479 016222 106425 .WORD 106400!..C
00800 3480 016224 005205 63000 INC R5
00900 3481 016226 012577 162232 63100 MOV (R5)+, @FISVEC ;SET UP FIS ERROR TRAP VECTOR
01000 3482 016232 012577 162230 63200 MOV (R5)+, @FISLVL ;TRAP STATUS
01100 3483 016236 000205 63300 RTS R5 ;RETURN
01200 3484 63400
01300 3485 63500
01400 3486 63600 ;SUBROUTINE TO POP 2 WORDS OFF THE STACK
01500 3487 63700 ;ALSO SAVES THE PROCESSOR STATUS WORD (EXCEPT T BIT)
01600 3488 63800
01700 3489 016240 64200 POPR: MFPS $PSW ;SAVE PROCESSOR STATUS WORD
01800 3490 016240 106767 .WORD 106700!..C
01900 3491 016244 042767 000020 162160 64600 BIC #20, $PSW ;CLEAR T-BIT
02000 3492 016252 016767 162236 62156 64700 MOV STACK4, ANS1 ;SAVE THE ANSWER
02100 3493 016260 016767 162232 162152 64800 MOV STACK6, ANS2 ;
02200 3494 016266 000207 64900 RTS PC
02300 3495 65000
02400 3496 65100
02500 3497 65200 ;SUBROUTINE TO POP 6 WORDS OFF THE STACKS.
02600 3498 65300 ;THE TWO OFF THE R6 STACK WERE PUT ON BY THE ERROR TRAP,
02700 3499 65400 ;THE FOUR OFF THE SOFTWARE STACK WERE THE ORIGINAL INPUT DATA.
02800 3500 65500 ;ALSO SAVES THE PS AND STACK POINTER AFTER THE FIS TRAP.
02900 3501 65600
03000 3502 016270 66000 POPER: MFPS $PSW ;SAVE PROCESSOR STATUS WORD
03100 3503 016270 106767 .WORD 106700!..C
03200 3504 016274 012667 000056 66400 POPER1: MOV (SP)+, SAVRTS ;SAVE RTS ADDRESS
03300 3505 016300 012667 162132 66500 MOV (SP)+, ANS1 ;SAVE RTI ADDRESS
03400 3506 016304 011667 162130 66600 MOV (SP), ANS2 ;SAVE RTI STATUS
03500 3507 016310 042767 000020 162122 66700 BIC #20, ANS2 ;CLEAR THE T-BIT
03600 3508 016316 012746 016324 66800 MOV #1$, -(SP)
03700 3509 016322 000002 66900 RTI ;RESTORE PROCESSOR STATUS
03800 3510 016324 016767 162160 162110 67000 1$: MOV STACK0, ANS3 ;SAVE DATA
03900 3511 016332 016767 162154 162104 67100 MOV STACK2, ANS4 ;
04000 3512 016340 016767 162150 162100 67200 MOV STACK4, ANS5 ;
04100 3513 016346 016767 162144 162074 67300 MOV STACK6, ANS6 ;
04200 3514 016354 000137 67400 JMP @PC+ ;SIMULATED RTS
04300 3515 016356 000000 67500 SAVRTS: 0
04400 3516 67600
04500 3517 67700 ;SUBROUTINE TO PUSH 4 WORDS ONTO THE PC STACK
04600 3518 67800
04700 3519 016360 012504 67900 PUSH7: MOV (R5)+, R4 ;SET R4 TO STACK
04800 3520 016362 012524 68000 MOV (R5)+, (R4)+ ;PUT DATA ON STACK
04900 3521 016364 012524 68100 MOV (R5)+, (R4)+ ;
05000 3522 016366 012524 68200 MOV (R5)+, (R4)+ ;
05100 3523 016370 012524 68300 MOV (R5)+, (R4)+ ;
05200 3524 016372 68700 MTPS (R5)+ ;SET THE PROCESSOR STATUS
05300 3525 016372 106425 .WORD 106400!..C
05400 3526 016374 005205 69100 INC R5
05500 3527 016376 012577 162062 69200 MOV (R5)+, @FISVEC ;SET UP FIS ERROR TRAP VECTOR
05600 3528 016402 012577 162060 69300 MOV (R5)+, @FISLVL ;TRAP STATUS
05700 3529 016406 000205 69400 RTS R5 ;RETURN
05800 3530 69500
05900 3531 69600 ;SUBROUTINE TO POP 4 WORDS OFF THE PC "STACK"

```

00100 CVKACC MACY11 30A(1052) 21-AUG-78 15:28 PAGE 85
 00200 CVKACC.P1 16-AUG-78 08:41 PUSH AND POP SUBROUTINES

SEQ 0093

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00300
00400 3532 69700 ;ALSO SAVES THE PROCESSOR STATUS WORD (EXCEPT T BIT)
00500 3533 69800
00600 3534 016410 70200 POP7: MFPS $PSW ;SAVE PROCESSOR STATUS WORD
00700 3535 016410 106767 .WORD 106700...C
00800 3536 016414 042767 000020 162010 70600 BIC #20, $PSW ;CLEAR T-BIT
00900 3537 016422 011600 70700 MOV (SP), R0 ;GET RETURN ADDRESS
01000 3538 016424 162700 000014 70800 SUB #14, R0 ;POINT TO TOP OF 'PC STACK'
01100 3539 016430 012067 162002 70900 MOV (R0)+, ANS1 ;SAVE 1ST HALF INPUT DATA
01200 3540 016434 012067 162000 71000 MOV (R0)+, ANS2 ;SAVE 2ND HALF INPUT DATA
01300 3541 016440 010067 161770 71100 MOV R0, $SP ;SAVE ASSUMED END PC 'STACK POINTER'
01400 3542 016444 012067 161772 71200 MOV (R0)+, ANS3 ;SAVE 1ST HALF OF ANSWER
01500 3543 016450 012067 161770 71300 MOV (R0)+, ANS4 ;SAVE 2ND HALF OF ANSWER
01600 3544 016454 000207 71400 . RTS PC
01700 3545 71500
01800 3546 71600 ;ERRONIOUS TRAP SERVICE ROUTINE
01900 3547 71700
02000 3548 016456 104000 71800 TRAPER: HLT ;FIS SHOULDN'T HAVE TRAPED
02100 3549 016460 000547 547 ;THE ERROR NUMBER IS 547
02200 3550 016462 000002 72000 RTI
02300 3551 72100

```

```

00300
00400 3552
00500 3553
00600 3554 016464 032737 002000 000422
00700 3555 016472 001402
00800 3556 016474 000004 000504
00900 3557 016500 005267 161756
01000 3558 016504 032737 020000 000422
01100 3559 016512 001023
01200 3560 016514 000004 000472
01300 3561 016520 013637 000402
01400 3562 016524 014667 161700
01500 3563 016530 162767 000002 161672
01600 3564 016536 017605 000000
01700 3565 016542 004767 000124
01800 3566 016546 062716 000002
01900 3567 016552 000004 000500
02000 3568 016556 004767 000046
02100 3569 016562 105767 161632
02200 3570 016566 001403
02300 3571 016570 005237 000400
02400 3572 016574 000777
02500 3573 016576 005737 000422
02600 3574 016602 100001
02700 3575 016604 000000
02800 3576 016606 032737 001000 000422
02900 3577 016614 001001
03000 3578 016616 000002
03100 3579 016620 105067 161657
03200 3580 016624 000167 177164
03300 3581
03400 3582

```

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*****
:SBTTL HLT ROUTINE (ERROR TYPEOUT)
HLTS: BIT #SW10,@#SWREG :SHOULD IT RING THE BELL ON ERROR?
      BEQ 1$ :NO - SKIP
      TYPE ,SBELL :RING BELL
1$: INC ERRORS :COUNT THE NUMBER OF ERRORS
     BIT #SW13,@#SWREG :SKIP TYPEOUT IF SET
     BNE 2$ :SKIP TYPEOUTS
     TYPE ,RETURN
     MOV @ (6)+,@#SFATAL :PLACE THE ERROR NUMBER IN LOCATION SFATAL
     MOV -(6),HLTADS :PUT ADDRESS OF INSTRUCTION ON STACK
     SUB #2,HLTADS
     MOV @ (6),TTY :TYPE @ (6) IN OCTAL
     JSR %7,PRINTR :TYPE LEADING ZERO'S
     ADD #2,(6) :ADJUST THE RETURN ADDRESS
     TYPE ,SPACE+3
     JSR PC,ERRORS :GO TO USER ERROR ROUTINE
2$: TSTB $ENV :ARE WE RUNNING UNDER APT?
     BEQ 4$ :IF NOT THEN GO TO 4$
     INC @#MSGTY :OTHERWISE INFORM APT
     BR . :AND LOOP
4$: TST @#SWREG :HALT ON ERROR
     BPL .+4 :SKIP IF CONTINUE
     HALT :HALT ON ERROR!
     BIT #SW09,@#SWREG :CHECK FOR INHIBIT LOOP ON ERROR
     BNE .+4 :SKIP IF LOOP ON ERROR
     RTI
     CLRB $ICNT
     JMP <ITS :LOOP ON TEST UNTIL NO ERRORS

```

72300

CVKACC MACY11 30A(1052) 21-AUG-78 15:28 PAGE 87
CVKACC.P11 16-AUG-78 08:41 HLT ROUTINE (ERROR TYPEOUT)

SEQ 0095

00100						
00200						
00300						
00400	3583					72500
00500	3584					
00600	3585					72700
00700	3586					72800
00800	3587					72900
00900	3588	016630	117767	161574	161702	73000
01000	3589	016636	062767	000002	161674	73100
01100	3590	016644	012703	000430		73200
01200	3591	016650				73300
01300	3592	016650	012305			
01400	3593	016652	004767	000014		
01500	3594	016656	000004	000501		73400
01600	3595	016662	105367	161652		73500
01700	3596	016666	100370			73600
01800	3597	016670	000207			73700
01900	3598					73800

.SBTTL USER ERROR ROUTINE

```

ERRORS:  MOV  @HLTAD$,TYPCNT  :TYPE COUNT IS LOW BYTE OF HLT
          ADD  #2, TYPCNT      :TYPE COUNT - X+2
          MOV  @HLTAD$,R3      :TOP OF DATA TO BE TYPED

ERR1$:   MOV  (R3)+,TTY        :TYPE (R3)+ IN OCTAL
          JSR  %7,PRINTR       :TYPE LEADING ZERO'S
          TYPE, SPACE+4        :SPACE
          DECB TYPCNT          :CHECK FOR DONE
          BPL  ERR1$           :BRANCH IF NOT DONE
          RTS  PC

```

00100 CVKACC MACY11 30A(1052) 21-AUG-78 15:28 PAGE 88
 00200 CVKACC P11 16-AUG-78 08:41 OCTAL WORD & ADDRESS TYPER

SEQ 009x

00300											
00400	3599	016672	112767	000001	161644	PRINTR:	MOVB	#1, .PR		:SET ZERO FILL SWITCH	
00500	3600	016700	000402				BR	+.6		:SKIP	
00600	3601	016702	005067	161636		PRINTS:	CLR	.PR		:SUPRESS LEADING ZERO'S	
00700	3602	016706	112767	177772	161631		MOVB	#-6, .PR+1		:SET COUNT	
00800	3603	016714	010446				MOV	R4, -(6)		:SAVE R4	
00900	3604	016716	012704	017020			MOV	#.PRBUF, R4		:SET POINTER TO FIRST ASCII CHAR.	
01000	3605	016722	105014				CLRB	(4)		:CLEAR FIRST BYTE	
01100	3606	016724	000405				BR	.PRF		:ROTATE FIRST BIT	
01200	3607	016726	105014			.PRL:	CLRB	(4)		:CLEAR BYTE OF CHARACTER	
01300	3608	016730	006105				ROL	TTY		:ROTATE BIT INTO C	
01400	3609	016732	106114				ROLB	(4)		:PACK IT	
01500	3610	016734	006105				ROL	TTY		:ROTATE BIT INTO C	
01600	3611	016736	106114				ROLB	(4)		:PACK IT	
01700	3612	016740	006105			.PRF:	ROL	TTY		:ROTATE BIT INTO C	
01800	3613	016742	106114				ROLB	(4)		:PACK IT	
01900	3614	016744	105714				TSTB	(4)		:IS IT ZERO?	
02000	3615	016746	001402				BEQ	+.6		:SKIP INC	
02100	3616	016750	105267	161570			INCB	.PR		:SET FILL SWITCH	
02200	3617	016754	105767	161564			TSTB	.PR		:CHECK FILL SWITCH	
02300	3618	016760	001402				BEQ	+.6		:SKIP BITSET	
02400	3619	016762	152724	000060			BISB	#'0, (4)+		:MAKE INTO ASCII CHAR	
02500	3620	016766	105267	161553			INCB	.PR+		:INC COUNT	
02600	3621	016772	001355				BNE	.PRL		:REPEAT	
02700	3622	016774	022704	017020			CMP	#.PRBUF, R4		:EMPTY BUFFER?	
02800	3623	017000	001002				BNE	+.6		:SKIP IF NOT	
02900	3624	017002	112724	000060			MOVB	#'0, (4)+		:LOAD 1 ZERO	
03000	3625	017006	105014				CLRB	(4)		:NULL TERMINATOR	
03100	3626	017010	000004	017020			TYPE	..PRBUF		:TYPE IT	
03200	3627	017014	012604				MOV	(6)+, R4		:RESTORE R4	
03300	3628	017016	000207				RTS	PC		:RETURN	
03400	3629										
03500	3630	017020	000004			.PRBUF:	.BLKW	4		:OUTPUT BUFFER	

00100
00200
00300
00400
00500
00600
00700
00800
00900
01000
01100
01200
01300
01400
01500
01600
01700
01800
01900
02000
02100
02200
02300
02400
02500
02600
02700
02800
02900
03000
03100
03200
03300
03400
03500
03600
03700
03800
03900
04000
04100
04200
04300

CVKACC MACY11 30A(1052) 21-AUG-78 15:28 PAGE 89
CVKACC.P11 16-AUG-78 08:41
3631
3632
3633
3634
3635
017030 012737 017152 000024
017036 012737 000340 000026
017044 010046
017046 010146
017050 010246
017052 010346
017054 010446
017056 010546
017060 010667 000072
017064 012737 017076 000024
017072 000000
017074 000776
3648
3649
017076 016706 000054
017102 005067 000050
017106 005267 000044
017112 001375
017114 012605
017116 012604
017120 012603
017122 012602
017124 012601
017126 012600
017130 012737 017030 000024
017136 012737 000340 000026
017144 000004
017146 017160
017150 000002
017152 000000
017154 000776
017156 000000
017160 005015 047520 142527
017166 000122
367

OCTAL WORD & ADDRESS TYPE

SEQ 0097

.SBTTL POWER DOWN AND UP ROUTINES

.POWER DOWN ROUTINE

\$PWRDN: MOV #SILLUP,@#PWRVEC ;;SET FOR FAST UP
MOV #340,@#PWRVEC+2 ;;PRIO:7
MOV R0,-(SP) ;;PUSH R0 ON STACK
MOV R1,-(SP) ;;PUSH R1 ON STACK
MOV R2,-(SP) ;;PUSH R2 ON STACK
MOV R3,-(SP) ;;PUSH R3 ON STACK
MOV R4,-(SP) ;;PUSH R4 ON STACK
MOV R5,-(SP) ;;PUSH R5 ON STACK
MOV SP,\$SAVR6 ;;SAVE SP
MOV #SPWRUP,@#PWRVEC ;;SET UP VECTOR
HALT
BR .-2 ;;HANG UP

.POWER UP ROUTINE

\$PWRUP: MOV \$SAVR6,SP ;;GET SP
CLR \$SAVR6 ;;WAIT LOOP FOR THE TTY
1\$: INC \$SAVR6 ;;WAIT FOR THE INC
BNE 1\$;;OF WORD
MOV (SP)+,R5 ;;POP STACK INTO R5
MOV (SP)+,R4 ;;POP STACK INTO R4
MOV (SP)+,R3 ;;POP STACK INTO R3
MOV (SP)+,R2 ;;POP STACK INTO R2
MOV (SP)+,R1 ;;POP STACK INTO R1
MOV (SP)+,R0 ;;POP STACK INTO R0
MOV #SPWRDN,@#PWRVEC ;;SET UP THE POWER DOWN VECTOR
MOV #340,@#PWRVEC+2 ;;PRIO:7
TYPE \$POWER ;;REPORT THE POWER FAILURE
\$PWRMG: .WORD \$POWER ;;POWER FAIL MESSAGE POINTER
RTI
\$SILLUP: HALT ;;THE POWER UP SEQUENCE WAS STARTED
BR .-2 ;;BEFORE THE POWER DOWN WAS COMPLETE
\$SAVR6: 0 ;;PUT THE SP HERE
\$POWER: .ASCIZ <15><12>'POWER'

.EVEN

```

00300
00400 3671 74700
00500 3672 74800 ;* TYPE OUT ROUTINE
00600 3673 74900 ;* -----
00700 3674 75000 ;*
00800 3675 75100 ;*
00900 3676 75200 ;* THIS ROUTINE IS USED TO TYPE ASCIZ MESSAGES
01000 3677 75300 ;*
01100 3678 75400
01200 3679 017170 132737 000040 000421 75500 $TYPE: BITB #40,@$SENVM ;HAS THE CONSOLE OUTPUTS BEEN SUPPRESSED?
01300 3680 017176 001007 75600 BNE 3$ ;IF SO THEN RETURN FROM THE SUBROUTINE VIA 3
01400 3681 017200 010046 75700 MOV RO,-(SP) ;OTHERWISE SAVE RO
01500 3682 017202 017600 000002 75800 MOV @2(SP),RO ;GET THE ADDRESS OF THE ASSCIZ STRING
01600 3683 017206 112046 75900 2$: MOVB (RO)+,-(SP) ;PUSH THE CHARACTER TO BE TYPED ONTO STACK
01700 3684 017210 001005 76000 BNE 4$ ;BRANCH IF IT IS NOT THE TERMINATOR
01800 3685 017212 005726 76100 TST (SP)+
01900 3686 017214 012600 76200 MOV (SP)+,RO ;OTHERWISE RESTORE THE STACK AND RO
02000 3687 017216 062716 000002 76300 3$: ADD #2,(SP) ;ADJUST THE RETURN PC
02100 3688 017222 000002 76400 RTI ;AND RETURN
02200 3689 76500
02300 3690 017224 105777 161320 76600 4$: TSTB @$TPS ;IS THE PRINTER AVAILABLE?
02400 3691 017230 100375 76700 BP_ 4$ ;IF NOT THEN LOOP HERE
02500 3692 017232 112677 161314 76800 MOVB (SP)+,@$TPB ;OUT PUT THE CHARACTER
02600 3693 017236 000763 76900 BR 2$ ;AND GO BACK
02700 3694 017240 005015 053104 040513 77000 $TITLE: .ASCIZ <15><12>/DVKACC - LSI-11 FIS INSTRUCTION TEST/
02800 3695 017246 041503 026440 046040
02900 3696 017254 044523 030455 020061
03000 3697 017262 044506 020123 044440
03100 3698 017270 051516 051124 041525
03200 3699 017276 044524 047117 052040
03300 3700 017304 051505 000124
03400 3701 77100 .EVEN
03500 3702 017310 012706 000600 77200 NOOOP: MOV #BEGIN,SP ;THAT WAS THE HEADING FOR THE DIAGNOSTIC
03600 3703 017314 132737 000001 000420 77300 BITB #1,@$SENV ;INITIALIZE STACK POINTER TO 600
03700 3704 017322 001011 77400 BNE 22$ ;ARE WE UNDER APT
03800 3705 017324 132737 000040 000421 77450 BITB #40,@$SENV ;IF SO THEN DO NOT TYPE HEADING
03900 3706 017332 001005 77460 BNE 22$ ;HAVE THE CONSOLE OUTPUTS BEEN SUPPRESSED
04000 3707 017334 012737 017170 000020 77500 MOV #1,$TYPE,@#20 ;IF SO THEN DO NOT PRINT HEADING
04100 3708 017342 000004 017240 77600 TYPE ., $TITLE ;SET UP VECTOR 20 TO PRINT HEADING
04200 3709 017346 012767 000001 161162 77700 22$: MOV #1,TIMES ;TYPE HEADING 'VKACC -LSI-11 ...'
04300 3710 017354 012700 000410 77800 MOV #1,$DEVCT,RO ;# OF ITERATIONS IN THE FIRST PASS
04400 3711 017360 005040 77900 2$: CLR -(RO) ;PREPARE TO INITIALIZE THE PROGRAM
04500 3712 017362 022700 000400 78000 CMP #1,$MAIL,RO
04600 3713 017366 001374 78100 BNE 2$
04700 3714 017370 000167 160610 78200 JMP RE$TRT ;START THE PROGRAM
04800 3715 000001 78300 .END

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CVKACC	MACY11	30A(1052)	21-AUG-78	15:28	PAGE 97			
CVKACC.P11	16-AUG-78	08:41	CROSS REFERENCE TABLE -- USER SYMBOLS					
00100								
00200								
00300								
00400	TST11	002272				605#		
00500	TST12	002430				651#		
00600	TST13	002566				697#		
00700	TST14	002720				743#		
00800	TST15	003054				789#		
00900	TST16	003214				835#		
01000	TST17	003366				889#		
01100	TST2	001056				283#		
01200	TST20	003620				961#		
01300	TST21	004050				1033#		
01400	TST22	004306				1105#		
01500	TST23	004540				1177#		
01600	TST24	004700				1223#		
01700	TST25	005034				1269#		
01800	TST26	005170				1321#		
01900	TST27	005326				1367#		
02000	TST3	001216				329#		
02100	TST30	005462				1419#		
02200	TST31	005622				1465#		
02300	TST32	005762				1511#		
02400	TST33	006122				1557#		
02500	TST34	006260				1603#		
02600	TST35	006414				1649#		
02700	TST36	006552				1695#		
02800	TST37	006706				1747#		
02900	TST4	001352				375#		
03000	TST40	007140				1819#		
03100	TST41	007376				1891#		
03200	TST42	007532				1937#		
03300	TST43	007672				1983#		
03400	TST44	010026				2029#		
03500	TST45	010162				2075#		
03600	TST46	010316				2121#		
03700	TST47	010454				2167#		
03800	TST5	001506				421#		
03900	TST50	010630				2221#		
04000	TST51	011004				2275#		
04100	TST52	011240				2347#		
04200	TST53	011472				2419#		
04300	TST54	011630				2465#		
04400	TST55	011764				2511#		
04500	TST56	012122				2557#		
04600	TST57	012256				2603#		
04700	TST6	001640				467#		
04800	TST60	012432				2657#		
04900	TST61	012670				2729#		
05000	TST62	013126				2801#		
05100	TST63	013364				2873#		
05200	TST64	013616				2953#		
05300	TST65	014034				3012#		
05400	TST66	014312				3084#		
05500	TST67	014530				3142#		
05600	TST7	001772				513#		
05700	TST70	015160				3250#		
05800	TTYOUT	000546				191#	3744	3252
05900	TYPCNT	000540				188#	3588*	3589*
								3351*
								3595*

	TYPE =	000004	76#	3149	3257	3375	3556	3560	3567	3594	3626	3662	3708		
00400	YESRT	000542	189#	209											
00600	\$APTHD	000430	136	142#	149										
00700	\$BELL	000504	176#	3556											
00800	\$CPUPP	000426	118#												
00900	\$DEVCT	000410	109#	3710											
01000	\$DOAGN	015674	3371	3379	3385#										
01100	\$ENDAD	015664	89	3381#											
01200	\$ENDCT	015644	3373#	3392											
01300	\$ENDMG	015700	3375	3387#											
01400	\$ENULL	015712	3389#												
01500	\$ENV	000420	114#	219	3569	3703									
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03300			936#	903#	910	911#	915	916#	920	921#	925	926#	930	931#	93
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03700			1132#	1079	1084	1085#	1089	1090#	1094	1095#	1116	1117#	1125	1126#	113
03800			166	1136	1137#	1141	1142#	1146	1147#	1151	1152#	1156	1157#	1161	116
03900			1239#	1167#	1192	1193#	1197	1198#	1202	1203#	1207	1208#	1212	1213#	123
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04800			1783	1727#	1731	1732#	1736	1737#	1758	1759#	1767	1768#	1773	1774#	177
04900			1833#	1783	1784#	1788	1789#	1797	1794#	1798	1799#	1803	1804#	1808	183
05000			1870	1833#	1840	1841#	1845	1847#	1850	1851#	1855	1856#	1860	1861#	186
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01300			2940	2941#	2982	2983#	2987	2988#	2993	2994#	2998	2999#	3003	3004#	302
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03700			1477	1523	1569	1615	1661	1718	1770	1837	1903	1949	1995	2041	209
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04700			576	616*	622	662*	668	800*	806	900*	906*	912	1044*	1050*	105
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04900			1574	1614*	1620	1660*	1666	1830*	1836*	1842	1902*	1908	1948*	1954	199
05000			2000	2040*	2046	2132*	2138	2286*	2292*	2298	2430*	2436	2522*	2528	256
05100			2574	2668*	2674*	2680	2740*	2746*	2752	2812*	2818*	2824	2978*	2984	302
05200			3033*	3039	3100*	3106*	3112	3188*	3194*	3200	3296*	3302*	3308	3447*	346
05300			3541*												
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CVK100 (CVKAC MARY11 30A(1052) 21-AUG-78 15:28 PAGE 104
00200 (CVKACC.P11 16-AUG-78 08:41 CROSS REFERENCE TABLE -- MACRO NAMES

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03100 . ABS. 017374 000

03200
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03400
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03700 RUN-TIME: 17 23 1 SECONDS
03800 RUN-TIME RATIO: 575/42 13.4
03900 CORE USED: 41K (81 PAGES)

04000
04100