

RK11/05F/J

RK11 BASIC LOGIC TEST 2
MD-11-DZRKK-E

EP-DZRKK-E-DL-A
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The main body of the document consists of a 10x10 grid of 100 small, square diagrams. Each diagram is extremely faint and illegible, appearing as a series of light-colored lines and shapes against a dark background. The diagrams are arranged in a regular grid pattern, with approximately 10 diagrams per row and 10 per column. The overall appearance is that of a technical test sheet or a collection of small-scale circuit diagrams.

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IDENTIFICATION

PRODUCT CODE:	MAINDEC-11-DZRKK-E-D
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QUICK LOOK-UP OPERATING INSTRUCTIONS
FOR A QUICK REFERENCE, LOOK UP THE FOLLOWING SECTIONS:

- 1.0 ABSTRACT
- 2.0 REQUIREMENTS
- 4.1 LOADING AND OPERATOR ACTION
- 7.0 SWITCH OPTIONS

FOR A MORE COMPLETE EXPLANATION REFER TO THE TABLE OF CONTENTS BELOW AND THE FOLLOWING DOCUMENT.

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1.0 ABSTRACT

THE RK11 LOGIC TESTS CONSIST OF A SERIES OF TESTS AIMED AT CHECKING THE BASIC LOGIC OF THE RK11 CONTROLLER. THIS PROGRAM IS THE SECOND PART OF THE TWO-PART RK11 LOGIC TESTS. IT SHOULD BE NOTED THAT LOGIC TEST I AND LOGIC TEST II TOGETHER CONSTITUTE A COMPLETE PROGRAM AND BOTH OF THEM SHOULD BE RUN.

WHEN USED IN CONJUNCTION WITH A DRIVE IT IS CAPABLE OF DETECTING FAULTS IN THE DRIVE ALSO.

USED CORRECTLY THIS PROGRAM CAN BE AN EFFECTIVE ANALYTIC AND DIAGNOSTIC TOOL.

2.0 REQUIREMENTS

2.1 EQUIPMENT

- A. PDP11 WITH CONSOLE TELETYPE.
- B. 8K OF MEMORY
- C. RK11 OR RKV11 CONTROLLER
- D. 1-8 RK05 OR RK05F DRIVES OR THE RK05 SIMULATOR (DRIVE TYPES MAY BE MIXED)

2.2 PRELIMINARY PROGRAMS

RK11 BASIC LOGIC TEST I (MD-11-DZRKJ)

2.3 EXECUTION TIME

ERROR FREE FIRST PASS ON PDP11/20 WITH CORE MEMORY TAKES APPROXIMATELY TWO MINUTES. CONSIDERABLY LESS FOR FASTER MACHINES OR MEMORIES.

3.0 STARTING ADDRESS

200 FOR ANY MODE OF OPERATION. NORMAL START UP WITH ALL SWITCHES DOWN.

4.0 PROGRAM CONTROL MODES & OPERATOR ACTION

PAPER TAPE LOADING
RKDP DUMP MODE
RKDP CHAIN MODE
ACT11

- 4.1 PAPER TAPE LOADING
- 4.1.1 LOAD PROGRAM INTO MEMORY USING STANDARD PROCEDURE FOR .ABS TAPES.
- 4.1.2 MAKE SURE THAT THE DRIVES TO BE CHECKED ARE LOADED WITH DISKS AND ARE IN 'RUN'. 'WRT ENABLE' THEM. CHECK THAT 'WRT PROT' LIGHT ON THESE DRIVES IS OFF. PUT DRIVES THAT ARE NOT TO BE TESTED ON 'LOAD'.
- 4.1.3 LOAD ADDRESS 200
- 4.1.4 SET SWITCHES IF DESIRED (SEE SEC 7.0) IF TESTING ON SIMULATOR PUT SW<10> UP.
PRESS START.
- 4.1.5 THE PROGRAM IDENTIFIES ITSELF (NAME, MAINDEC NO), THEN THE FOLLOWING QUESTION IS ASKED:

DRIVES TO BE TESTED?

THE USER SHOULD TYPE IN THE DRIVE NUMBERS THAT ARE IN 'RUN' AND TO BE TESTED. CARRIAGE RETURN SHOULD TERMINATE THE STRING. IF AN RK-05F IS TO BE TESTED, TYPE THE SUFFIX 'F' WITH THE FIRST DRIVE OF THE PAIR. FOR EXAMPLE, IF DRIVES 2 AND 3 ARE ON AN RK-05F, TYPE ONLY 2F.

EXMP: DRIVES TO BE TESTED? 0,1,2<CR>

THE DRIVES DO NOT HAVE TO BE IN LOGICAL ORDER.

EXMP: DRIVES TO BE TESTED? 2,4<CR>

IF ANY ONE DRIVE IS TO BE TESTED, TYPE IN THAT NUMBER. IT DOES NOT HAVE TO BE DRIVE 0.

THUS A NORMAL SEQUENCE WITH DRIVES 0,1 WOULD BE:

RK11 BASIC LOGIC TEST 2
MAINDEC-11-DZRKK-E
DRIVES TO BE TESTED? 0,1<CR>

- 4.1.6 THERE IS A "RUBOUT" FEATURE WHICH ALLOWS RUBBING OUT ANY NUMBER OF CHARACTERS THAT WERE TYPED IN WRONG. THE RUBBED OUT CHARACTERS ARE ECHOED BACK WITHIN SLASHES.

"IU" DELETES THE ENTIRE LINE

- 4.1.7 IF REPLY TO ANY OF THE ABOVE QUESTION IS IN A WRONG FORMAT (EX: 012<CR>:0,8<CR>: 0,A<CR>: M<CR> ETC) IT IS AUTOMATICALLY REJECTED, A "??" IS PRINTED OUT;

THE CORRECT ANSWER CAN NOW BE RETYPED AGAIN.

- 4.1.8 THE DRIVE NUMBER BEING TESTED OUT IS PRINTED:

DRIVE N :N=0,1...7
IF THE DRIVE IS AN RK-05F, AN F IS APPENDED

AT THE END OF A PASS THE FOLLOWING TYPE-OUT OCCURS

END PASS # X

WHERE X= PASS NUMBER (1,2,3---), CONTROL IS PASSED TO THE BEGINNING OF THE PROGRAM AND RE-EXECUTION BEGINS. NO QUESTIONS ARE TO BE ANSWERED AGAIN.

- 4.1.9 ERROR FREE PASSES OF THE PROGRAM APPEAR AS SHOWN BELOW.

```

RK11 BASIC LOGIC TEST 2
MAINDEC-11-DZRKK-E
DRIVES TO BE TESTED?
0,1<CR>
DRIVE 0
DRIVE 1
END PASS # 1
0
DRIVE 1
END PASS # 2
...
...

```

- 4.2 RKDP DUMP MODE

- 4.2.1 THE PROGRAM IS LOADED INTO THE MEMORY BY THE RKDP MONITOR

- 4.2.2 START AS NORMALLY USING SA 200

- 4.2.3 THE PROGRAM IDENTIFIES ITSELF (NAME,MAINDEC NO.). ON FINDING OUT THAT THE LOADING WAS BY RKDP (DUMP MODE), THE FOLLOWING MESSAGE APPEARS:

'TO TEST DRIVE 'N' HALT PROGRAM, REMOVE RKDP PACK AND REPLACE IT WITH A WORK PACK, CLEAR LOCATION 40, AND RESTART PROGRAM'

IF DRIVE 'N' IS TO BE TESTED, THE RKDP PACK ON THAT

DRIVE SHOULD BE REPLACED BY ANOTHER PACK, THE DRIVE SHOULD BE PUT ON 'WRT ENABL' (BECAUSE RKDP WRITE PROTECTS THE DRIVE).

IF DRIVE 'N' IS NOT TO BE CHECKED, THEN THE MESSAGE SHOULD BE IGNORED.

AFTER THIS, THE SEQUENCE OF QUESTIONING IS AS EXPLAINED IN SEC 4.1.5.

4.3 RKDP CHAIN MODE

THE PROGRAM IS CHAIN-LOADED FROM THE RKDP PACK ON DRIVE 'N'. AFTER THE PROGRAM IDENTIFIES ITSELF THE FOLLOWING PRINTOUT OCCURS.

'DRIVE 'N' NOT TESTED'

THERE IS NO OPERATOR INTERVENTION REQUIRED. THE PROGRAM FINDS OUT THE NUMBER OF DRIVES PRESENT.

4.4 ACT11 MODE

THE PROGRAM IS LOADED BY THE ACT11 MONITOR. ON STARTING, IDENTIFIES ITSELF, ASCERTAINS THE NUMBER OF DRIVES AND PROCEEDS WITH THE EXECUTION OF THE TESTS AS BEFORE.

5.0 DRIVE SELECTION

IF ANY PARTICULAR DRIVE IS TO BE SELECTED FOR TESTING, PUT THAT DRIVE ON 'RUN', 'WRITE ENABLE'; PUT REST OF THE DRIVES ON 'LOAD', 'WRITE LOCK' AND IN REPLY TO THE QUESTIONS 'TO BE TESTED?' TYPE IN THE DRIVE NUMBER FOLLOWED BY CR. SEE SEC 4.1.5.

6.0 DRIVE-LESS TEST

USE RK11 BASIC LOGIC TEST I, WHICH IS ACTUALLY THE FIRST PART OF THE TWO-PART RK11 BASIC LOGIC TESTS. SEE SEC 1.0, 2.2.

7.0 SWITCH OPTIONS

IF THE PROGRAM IS BEING RUN ON A SWITCHLESS PROCESSOR (I.E. AN 11/34) THE PROGRAM WILL DETERMINE THAT THE HARDWARE SWITCH REGISTER IS NOT PRESENT AND WILL USE A 'SOFTWARE' SWITCH REGISTER. THE 'SOFTWARE' SWITCH REGISTER IS LOCATED AT LOCATION 176 (8). THE SETTINGS OF THE 'SOFTWARE' SWITCHES ARE CONTROLLED THROUGH A KEYBOARD ROUTINE WHICH IS CALLED BY TYPING A 'CONTROL G'. THE PROGRAM WILL RECOGNIZE THE 'CONTROL G' WHENEVER THE PROGRAM ENTERS

THE SCOPE ROUTINE OR BEGINS A NEW TEST. THE 'SOFTWARE' SWITCH VALUES ARE ENTERED AS AN OCTAL NUMBER IN RESPONSE TO THE PROMPT FROM THE SWITCH ENTRY ROUTINE:

'SWR = NNNNNN NEW ='

EACH TIME SWITCH SETTING ARE ENTERED, THE ENTIRE SWITCH REGISTER IMAGE MUST BE ENTERED. LEADING ZEROS ARE NOT REQUIRED. 'RUBOUT' AND 'CONTROL U' FUNCTIONS MAY BE USED TO CORRECT TYPING ERRORS DURING SWITCH ENTRY.

ON PROCESSORS WITH HARDWARE SWITCH REGISTERS, THE 'SOFTWARE' SWITCH REGISTER MAY BE USED. IF THE PROGRAM FINDS ALL 16 SWITCHES IN THE 'UP' POSITION, ALL SWITCH REGISTER REFERENCES WILL BE TO THE 'SOFTWARE' REGISTER AND THE PROCEDURES DESCRIBED ABOVE MUST BE FOLLOWED.

SW<15>=1	HALT ON ERROR
SW<14>=1	LOOP ON TEST
SW<13>=1	INHIBIT ERROR PRINTOUTS
SW<12>=1	CYCLE ON ERROR TO THE PREVIOUS 'SCOPE' STATEMENT
SW<11>=1	INHIBIT ITERATIONS
SW<10>=1	TESTING ON SIMULATOR
SW<09>=1	LOOP ON SPECIFIC ERROR
SW<08>=1	LOOP ON TEST AS PER SW<07:00>
SW<06>=1	DROP THE DRIVE AFTER MAXIMUM ALLOWABLE NUMBER OF ERRORS OCCUR

7.1 SW<15>

THE PROGRAM HALTS ON ENCOUNTERING AN ERROR, AFTER TYPING OUT THE ERROR MESSAGE AND PERTINENT INFORMATION. PRESSING "CONTINUE" RESTORES NORMAL OPERATION OF THE PROGRAM.

7.2 SW<14>

THE PROGRAM LOOPS ON THE SUBTEST THAT IS BEING EXECUTED WHEN THE SWITCH IS PUT ON. THIS SWITCH IS USED NORMALLY ALONG SW 15. SEE SEC 8.0.

7.3 SW <13>

THIS SWITCH INHIBITS ALL ERROR MESSAGES. NORMALLY USED WHEN LOOPING ON TEST (SW 14) OR LOOPING ON ERROR (SW 9).

7.4 SW <12>

THIS SWITCH ALLOWS THE PROGRAM TO CYCLE FROM THE POINT OF ERROR TO THE PREVIOUS SCOPE STATEMENT. NOTE THAT IN DOING SO ANY INITIALIZATION BEING DONE AT THE BEGINNING OF THE SUBTEST WILL BE DONE AGAIN AND AGAIN. SEE SEC 8.0 FOR DIFFERENT SCOPE LOOPS

AVAILABLE.

7.5 SW <11>

EACH SUBTEST WILL BE EXECUTED ONLY ONCE. NORMALLY AFTER THE FIRST PASS, EACH SUBTEST IS ITERATED A NUMBER OF TIMES (USUALLY 50, 5 IN SOME CASES). SETTING THIS SWITCH INHIBITS ITERATIONS, SO THAT QUICK PASSES CAN BE MADE.

7.6 SW <10>

THIS SWITCH WHEN SET INDICATES THAT TESTING IS BEING DONE ON A SIMULATOR. THE SWITCH SHOULD BE PUT UP BEFORE STARTING THE PROGRAM. NOTE THAT RK11C IS NOT COMPATIBLE WITH THE SIMULATOR.

7.7 SW <09>

THIS SWITCH PROVIDES THE TIGHTEST POSSIBLE SCOPE LOOP. NOTE THAT SW12 THE INITIALIZATION OF PARAMETERS AT THE BEGINNING OF THE SUBTEST MAY NOT BE DONE IN THIS CASE. THIS SWITCH IS HELPFUL WHEN A PARTICULAR PART OF A SUBTEST IS BEING REPEATED USING DIFFERENT PARAMETERS AND YOU WANT TO SCOPE ON THE PARAMETER IN ERROR. (EXAMPLE: RKDA IS BEING WRITTEN AND READ BACK WITH COUNT PATTERNS FROM 1 TO 177777. PATTERN 561 IS GIVING ERROR, YOU MIGHT NOT WANT TO GO THROUGH THE 560 PATTERNS BEFORE HITTING ERROR ON THE 561TH PATTERN. IN THIS CASE SW 9 WILL GIVE YOU A SCOPE LOOP ON THE 561TH PATTERN ONLY

7.8 SW <08>

THIS SWITCH IS USED TO SELECT A PARTICULAR TEST (AS PER SW<00-07>) FOR EXECUTION AND SUBSEQUENT LOOPING. THUS IF TEST 15 IS TO BE SELECTED THE SWITCH SETTING WOULD BE 000415. IT SHOULD BE NOTED THAT BEFORE SELECTING TEST 15, ALL THE PREVIOUS TESTS (1-14) WILL BE EXECUTED.

7.9 SW<06>

THIS SWITCH ALLOWS THE PROGRAM TO DROP A DRIVE FROM THE SELECTION LIST AND TESTING AFTER MAXIMUM ALLOWABLE ERROR COUNT (TOTAL NUMBER OF ERRORS) ON THAT DRIVE IS EXCEEDED. THE MAXIMUM ALLOWABLE ERROR COUNT IS 5, AFTER 5 ERRORS HAVE OCCURED DRIVE IS DROPPED AND A MESSAGE (DRIVE # XXX DROPPED) IS PRINTED.

8.0 SCOPE LOOPS

THERE ARE THREE KINDS OF SCOPE LOOPS AVAILABLE

1. SW14: LOOPING IS DONE FOR THE ENTIRE SUB-TEST
2. SW12: LOOPING IS DONE FROM THE POINT OF ERROR BACK TO THE PREVIOUS 'SCOPE' STATEMENT.
3. SW09: PROVIDE THE TIGHTEST POSSIBLE SCOPE LOOP SEE SEC. 7.7

EXAMPLE:

TST1: SCOPE
:

INITIALIZATION

```

:
ERROR 1
:
ERROR 2
:
ERROR 3
:
ERROR 4
:
:
TST2: SCOPE

```

THE SEQUENCE OF LOOPING FOR DIFFERENT CASES IS EXPLAINED BELOW. NOTE THAT 'TST1' AND 'TST2' ARE TAGS WHICH DEFINE THE BOUNDARY OF A TEST, (IN THIS CASE TEST 1). TEST 1 STARTS AT 'TST1' AND ENDS JUST BEFORE 'TST2'.

IN THE ILLUSTRATION BELOW --> INDICATES THE POINT FROM WHERE RETURN IS MADE AND LOOPING IS DONE.

1. ERROR 2 OCCURS, SW 14 SET.

TST1..ERROR 2..TST2-->TST1..ERROR 2..TST2-->TST1...

2. ERROR 2 OCCURS, SW 12 SET.

TST1...ERROR 2-->TST1...ERROR2-->TST1...

3. ERROR 2,3; SW 14 SET.

TST1..ERROR 2..ERROR 3..TST2-->TST1..ERROR 2..ERROR 3..TST2-->TST1...

4. ERROR 2,3; SW 12 SET.

TST1...ERROR 2-->TST1...ERROR 2-->TST1....

L01

NOTE THAT LOOPING IS DONE FROM THE VERY FIRST ERROR ENCOUNTERED. THE MORE BASIC AND EARLIER IT OCCURS AND IS DETECTED AND SHOULD BE FIXED.

IN THE ABOVE EXAMPLE NO PART OF THE SUB-TEST IS BEING REPEASING DIFFERENT PARAMETERS, HENCE IT SO HAPPENS THAT SW 9 AND 12 GIVE THE SAME KIND OF LOOPS. THE EXAMPLE BELOW WILL DEMONSTRATE THE DIFFERENCE BETWEEN SW 9 AND 12.

TST1: SCOPE
 :

INITIALIZATION

```

: ERROR 1
: MOV #1$, $LPERR ; '$LPERR' CONTAINS
: ; THE ADDRESS TO LOOP
: ; BACK ON ERROR- SW 9
1$: :
: ER I N REPETITIONS
: ;
TST2: SCOPE
: ;

```

1. SW 12 SET, ERROR 2 OCCURS DURING K.TH REPETITIONS

TST1..1,2...K.ERROR 2-->TST1..1,2...K.ERROR 2-->TST1..

2. SW 9 SET, ERROR 2 OCCURS DURING K.TH REPETITION

1\$..K..ERROR 2-->1\$..K..ERROR 2-->1\$...

9.0 PROGRAM STRUCTURE

THERE ARE THREE DISTINCT PARTS OF THE PROGRAM.

SET-UP PHASE
 DRIVE-DEPENDENT CONTROLLER TESTS

9.1 SET-UP PHASE

SETTING UP OF INITIAL POINTERS, VECTORS, TABLES IS DONE IN THIS PART. IN THIS SECTION THE DECISION IS MADE ABOUT THE PROGRAM MODE-PAPER TAPE, RKDP DUMP, CHAIN OR ACT11. IF IN A NON-INTERVENTION MODE (CHAIN, ACT11) NUMBER OF DRIVES AND THE TYPE OF CONTROLLER IS FOUND OUT. FLAGS ARE SET TO INDICATE

WHICH DRIVES ARE TO BE TESTED, ETC.

9.2 DRIVE DEPENDENT CONTROLLER TESTS

THIS SECTION FORMS A MAJOR PART OF THE PROGRAM WHEREIN MOST OF THE CONTROLLER IS CHECKED.

JUST BEFORE ENTERING THIS SECTION THE PROGRAM FINDS OUT WHICH DRIVE IS TO BE CHECKED. IF IN RKDP CHAIN MODE, DRIVE 'N' IF PRESENT, IS SKIPPED AND THE NEXT AVAILABLE DRIVE IS SELECTED.

THE DRIVE NUMBER BEING TESTED IS PRINTED OUT:

DRIVE N ;N=0,1,2...7

THE TESTING IS DONE IN A LOGICAL HIERCHY, SIMPLER THINGS FIRST, THEN MORE COMPLEX AND SO ON.

IN ONE OF THE TESTS THE ENTIRE DISK PACK IS FORMATTED, CHECKS ARE MADE FOR ERROR CONDITIONS. THE FIRST WORD OF EVERY SECTOR IS WRITTEN AS A PSUEDO-HEADER, REFLECTING THE ABSOLUTE ADDRESS OF THAT SECTOR (DRIVE #, CYLINDER #, SURFACE #, SECTOR #). EXAMPLE: THE PSUEDO-HEADER FOR SECTOR 5, SURFACE 0, CYLINDER 20, DRIVE 0 WOULD BE 001005.

IN THE NEXT TEST THE HEADERS FROM THE ENTIRE PACK ARE READ AND CHECKED FOR CORRECTNESS. IN A SUBSEQUENT TEST ALL THE PSUEDO-HEADERS ARE READ AND VERIFIED.

ALL THE FUNCTIONS ARE CHECKED OUT. 'SEEK' IS CHECKED IN THE THREE DIFFERENT VELOCITY MODES (HIGH, MEDIUM, LOW). VARIOUS ERRORS LIKE 'NXD', 'NXC', ETC. ARE SIMULATED AND CHECKED.

HARDWARE POGIC IS CHECKED USING ALL THE DRIVES THAT HAVE BEEN INDICATED.

AT THE END OF THIS SECTION, A CHECK IS MADE IF ALL INDICATED DRIVES HAVE BEEN TESTED. IF NOT, CONTROL IS TRANSFERRED TO THE BEGINNING OF THIS SECTION.

THUS ONE PASS OF THE PROGRAM INVOLVES DOING

1. SUBTEST #1 ONCE
2. DRIVE-DEPENDENT TESTS FOR ALL THE SELECTED DRIVES.

10.0 ERROR REPORTING

THE ERROR TABLE STARTING AT \$ERRTB CONTAINS INFORMATION PERTAINING TO EVERY ERROR THAT CAN OCCUR. EACH ITEM IN THE TABLE CONSISTS OF FOUR

ENTRIES.

- A. EM - THIS IS A POINTER TO THE ERROR MESSAGE TO BE TYPED OUT WHEN THE ERROR OCCURS.
- B. DH - THIS IS A POINTER TO THE DATA HEADER TO BE TYPED OUT.
- C. DT - THIS IS A POINTER TO THE DATA WHICH IS TO BE TYPED TYPED OUT UNDER THE HEADERS.
- D. D - THIS IS A TERMINATOR SIGNIFYING THE END OF THE ITEM.

THE ERROR CALL IS AN EMT INSTRUCTION WITH ITS LOWER BYTE ENCODED TO INDICATE THE ERROR NUMBER. THUS "OR 1" WOULD BE (EMT+1) IE 104001.

EVERY ERROR CORRESPONDS TO AN ITEM IN THE ERROR TABLE. THUS "ERROR 14" WOULD CORRESPOND TO ITEM 14. AS FAR AS POSSIBLE, THE ERROR MESSAGES HAVE BEEN KEPT SHORT, BUT CLARITY IS NOT SACRIFICED FOR BREVITY. INSPITE OF THIS, IF THE USER FINDS A NEED, HE CAN LOOK UP THE ENTIRE ERROR MESSAGE IN THE ERROR ITEMS TABLE FOUND IN THE BEGINNING OF THE LISTINGS. THUS FOR "ERROR 14", "ITEM 14" IN THE ITEM TABLE CAN BE LOOKED UP. WHEN THE ERROR INSTRUCTION IS EXECUTED A TRAP OCCURS TO THE ERROR HA LOCATED AT \$ERROR WHICH PROCESSES THE ERROR CALL. SEE SEC 12.3

11.0 ERROR INTERPRETATION

WHENEVER AN ERROR MESSAGE IS PRINTED OUT, ALL REGISTERS AND OTHER DATA PERTAINING TO THE ERROR ARE ALSO GIVEN. RKDS, RKER...RKBA INDICATE THE CONTENTS OF THE CORRESPONDING REGISTERS AT THE TIME OF ERROR.

EVERY ERROR MESSAGE CONTAINS A PC. THIS PC INDICATES THE POSITION IN PROGRAM WHERE THE ERROR CALL IS LOCATED. THE ERROR MESSAGE, BECAUSE OF PRACTICAL CONSIDERATIONS IS MADE SHORT AND MEANINGFUL. THE USER IS ADVTO LOOK UP THE PC IN THE PROGRAM LISTING, WHERE HE WILL FIND MORE INFORMATION ABOUT THE ERROR. IN MANY INSTANCES, A SINGLE FAULT WILL GIVE RISE TO MORE THAN ONE ERROR REPORT. A LITTLE DELIBERATION AND CAREFUL EXAMINATION OF THE DATA GIVEN WILL BE CERTAINLY VERY HELPFUL IN PINPOINTING THE FAULT. A BRIEF EXPLANATION OF WHAT IS BEING CHECKED IN THE SUBTEST IS GIVEN AT THE BEGINNING OF EVERY SUBTEST. ALL THE NUMBERS GIVEN WITH ERROR MESSAGES ARE IN OCTAL.

12.0 HANDLERS AND COMMON ROUTINES

THE COMPOSED ROUTINES USED IN THE PROGRAM ARE CALLED IN TWO WAYS.

- A. AS A SUBROUTINE THROUGH 'JSR' CALL
- B. THROUGH A 'TRAP' HANDLER

12.1 TRAP HANDLER

MANY COMMONLY USED ROUTINES IN THE PROGRAM ARE CALLED USING THE TRAP INSTRUCTION AND THE 'TRAP' HANDLER. THE LOWER BYTE OF THE TRAP INSTRUCTION IS ENCODED DIFFERENTLY FOR DIFFERENT ROUTINES. THE TRAP HANDLER IS LOCATED AT '\$TRAP'. WHEN A CALL FOR A ROUTINE IS EXECUTED, A TRAP OCCURS TO THE HANDLER 'ARAP'. THE HANDLER PICKS UP THE LOWER BYTE OF THE "CALL INSTRUCTION" AND USES IT TO FORM THE STARTING ADDRESS OF THE ROUTINE TO GO TO FOR SERVICE.

12.2 SCOPE HANDLER

THE 'IOT' TRAP IS USED BY THE 'SCOPE' STATEMENT. WHEN 'SCOPE' IS EXECUTED, AN IOT TRAP OCCURS TO MEMORY LOCATION '\$SCOPE'. THE SCOPE HANDLER STARTS AT '\$SCOPE'. DEPENDING ON THE SWITCH SETTINGS THE HANDLER DECIDES TO LOOP ON TEXT, INHIBIT ITERATIONS ETC. THERE ARE CERTAIN POINTERS AND FLAGS WHICH ARE ADJUSTED. THUS, IT IS NOT ADVISABLE START THE PROGRAM AT ANY GIVEN LOCATION SINCE THE VARIOUS POINTERS AND FLAGS MAY NOT BE CORRECTLY ADJUSTED.

12.3 ERROR HANDLER

AN EMT TRAP INSTRUCTION IS USED BY THE ERROR CALL. THE LOWER BYTE IS ENCODED TO GIVE DIFFERENT ERROR CALLS. (EX: ERROR 1 = 104000+1; ERROR 16 = 104000+16). WHEN THE ERROR STATEMENT IS EXECUTED, A TRAP OCCURS TO MEMORY LOCATION '\$ERROR'. THE ERROR HANDLER IS LOCATED AT '\$ERROR'. THE HANDLER FORMS THE POINTER TO ERROR TABLE, WHICH IS USED IF AN ERROR MESSAGE IS TO BE TYPED DEPENDING ON THE SWITCH SETTINGS, A DECISION ABOUT HALTING ON ERROR, INHIBITING TYPEOUT, LOOPING ON ERROR ETC. IS MADE. IF AN ERROR MESSAGE IS TO BE TYPED OUT AN EXIT IS MADE TO THE ERROR MESSAGE TYPEOUT ROUTINE LOCATED AT '\$ERRTYP'.

12.4 CONTROL RESET ROUTINE

THE CALL FOR THIS ROUTINE IS "CNT.RESET" AND IS AN ENCODED 'TRAP' INSTRUCTION. WHEN "CNT.RESET" IS EXECUTED THE CONTROL RESET ROUTINE STARTING AT

"CN.RST" IS ENTERED. A CONTROL RESET IS ISSUED THE PROGRAM WAITS TILL THE CONTROL READY SETS, ON WHICH THE ROUTINE IS EXITED. IF CONTROL READY DOES NOT SET WITHIN A CERTAIN TIME AN ERROR IS REPORTED. THE PC TYPED OUT IS THE LOCATION WHERE THE "CNT.RESET" CALL IS LOCATED. THE WAITING TIME IS 2.8 MS FOR 11/20 AND 560 US FOR 11/45 WITH BIPOLAR MEMORY.

12.5 CONTROL READY ROUTINE

THIS ROUTINE IS CALLED BY "CNT.RDY" (AN ENCODED 'TRAP' INSTRUCTION) AND IS LOCATED AT "CN.RDY". THE ROUTINE WAITS FOR THE CONTROL READY TO SET AND WHEN IT DOES, EXITS IF CONTROL READY DOES NOT SET WITHIN A SPECIFIED TIME AN ERROR MESSAGE IS GIVEN

CNTRL RDY DIDN'T SET
PC = XXXXXX RKCS = YYYYYY

THE PC IS THE LOCATION AT WHICH THE "CNT.RDY" CALL IS LOCATED. THE WAITING TIME IS 949 MS FOR 11/20 AND 189 MS FOR 11/45 WITH BIPOLAR MEMORY.

12.6 DRIVE RESET ROUTINE

THE DRIVE - RESET ROUTINE IS LOCATED AT "DRESET" AND IS CALLED BY A "JSR". IT ISSUES A DRIVE RESET AND WAITS FOR THE R/W/S RDY TO SET, ON WHICH THE ROUTINE IS EXITED. THE WAITING TIME IS 4959 MS FOR 11/20 AND 991 MS FOR 11/45 WITH BIPOLAR MEMORY.

12.7 TIME DELAY ROUTINE

THIS ROUTINE PROVIDES A VARIABLE TIME DELAY. THE CALL IS DELAY ,N WHERE N=1 TO 177777 (OCTAL) TIME DELAY PROVIDED= 7.5 TIMES(X) N MICRO SECS FOR 11/20, 1.5N US FOR 11/45 (N CONVERTED TO DECIMAL BEFORE COMPUTING DELAY) IF THE USER WANTS TO CHANGE THE DELAY AT ANY POINT IT CAN BE DONE BY SIMPLY CHANGING VARIABLE 'N'.

12.8 WAIT FOR INTERRUPT ROUTINE

THIS ROUTINE PROVIDES A VARIABLE TIME LIMIT DURING WHICH RK11 INTERRUPT MAY OCCUR. THE IS
WAT.INT ,N N=1 TO 177777 (OCTAL)
WAITING TIME=7.5 TIMES(X) N US FOR 11/20, 1.5N US

FOR 11/45 UPON ENTERING THE ROUTINE CPU PRIORITY IS DROPPED SO THAT RK11 CAN INTERRUPT.

12.9 OTHER ROUTINES

THERE ARE OTHER COMMONLY USED ROUTINES AS LISTED BELOW.

\$TYPE:
TYPE ROUTINE FOR TYPING OUT ASCII STRINGS.
LOCATED AT "\$TYPE"
CALLED BY "TYPE"

\$TYPOC:
ROUTINE FOR TYPING OUT OCTAL NUMBERS.
LOCATED AT "\$TYPOC"
CALLED BY "TYPOC"

\$TYPDS:
ROUTINE FOR TYPING OUT DECIMAL NUMBERS.
LOCATED AT "\$TYPDS"
CALLED BY "TYPDS"

\$RDLIN:
ROUTINE FOR INPUTTING ASCII STRINGS FROM TTY.
LOCATED AT "\$RDLIN"
CALLED BY "RDLIN"

\$ERRTYP:
ROUTINE FOR TYPING OUT ERROR MESSAGES.
LOCATED AT \$ERRTYP
CALLED BY "JSR \$ERRTYP"

\$PWRDN:
ROUTINE FOR HANDLING POWER FAILURE.
LOCATED AT \$PWRDN
CALLED WHEN THERE IS A POWER FAILURE.

\$PWRUP:
ROUTINE FOR HANDLING POWER UP AFTER A POWER FAIL.
LOCATED AT \$PWRUP
CALLED WHEN POWER RETURNS AFTER HAVING GONE DOWN.

13.0 UNEXPECTED TIMEOUTS AND RK11 INTERRUPTS

WHEN AN UNEXPECTED TIMEOUT OCCURS, THE PC AT WHICH TIME OUT OCCURED IS TYPED OUT AND THE PROGRAM HALTS. IF IT IS INTACT, IT CAN BE RESTARTED BY PRESSING CONTINUE.

IF AN UNEXPECTED RK11 INTERRUPT OCCURS THE PROGRAM TYPES OUT THE PC AT WHICH THE INTERRUPT CAME IN AND THEN HALTS. PRESSING CONTINUE WOULD RESTART THE PROGRAM FROM BEGINING. SW 9- LOOPING CAITY IS PROVIDED AS A TROUBLE SHOOTING AID.

14.0 QUICK VERIFYING MODE

THE FIRST PASS OF THE PROGRAM IS A QUICK VERIFYING MODE. ALL THE TESTS ARE DONE ONLY ONCE, ON SUBSEQUENT PASSES THE TESTS ARE ITERATED (NORMALLY 50 TIMES, 5 IN SOME CASES). THUS THE FIRST PASS TAKES A SHORTER TIME TO COMPLETE, WHEREAS SUBSEQUENT PASSES TAKE MORE TIME.

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.TITLE MD-11-DZRKK-E, RK11 BASIC LOGIC TEST 2
;*COPYRIGHT (C) 1974,1977
;*DIGITAL EQUIPMENT CORP.
;*MAYNARD, MASS. 01754
;*
;*PROGRAM BY JIM KAPADIA
;*
;*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
;*PACKAGE (MAINDEC-11-DZQAC-C3), JAN 19, 1977.
;*
;*PROGRAM REVISED BY TOM SAWYER, MARCH, 1976
;*REVISED BY CHUCK HESS, AUGUST, 1976
.SBTTL OPERATIONAL SWITCH SETTINGS
;*
;*      SWITCH      USE
;*      -----      -----
;*      15          HALT ON ERROR
;*      14          LOOP ON TEST
;*      13          INHIBIT ERROR TYPEOUTS
;*      12          CYCLE ON ERROR TO PREVIOUS 'SCOPE' STATEMENT
;*      11          INHIBIT ITERATIONS
;*      10          TESTING ON SIMULATOR
;*      9           LOOP ON ERROR
;*      8           LOOP ON TEST IN SWR<7:0>
;*      6           DROP THE DRIVE IF MORE THAN 5 ERRORS

```

```

;*****
;YOU ARE ADVISED TO READ THE DOCUMENT BEFORE USING THIS PROGRAM.
;ON GETTING AN ERROR REFER TO THE LISTINGS AT THE PC POINTED

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001100

000011
000012
000015
000200
177776

177774
177772
177570
177570

000000
000001
000002
000003
000004
000005
000006
000007
000006
000007

000000
000040
000100
000140
000200
000240
000300
000340

100000
040000
020000
010000
004000
002000
001000
000400
000200
000100

:OUT IN THE ERROR MESSAGE, ADJACENT ERROR MESSAGES IF FOLLOWED
:CAREFULLY COULD LEAD TO AN EASY PINPOINTING OF THE FAULT

:SBTTL BASIC DEFINITIONS

:*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***

STACK= 1100
.EQUIV EMT,ERROR ;:BASIC DEFINITION OF ERROR CALL
.EQUIV IOT,SCOPE ;:BASIC DEFINITION OF SCOPE CALL

:*MISCELLANEOUS DEFINITIONS

HT= 11 ;:CODE FOR HORIZONTAL TAB
LF= 12 ;:CODE FOR LINE FEED
CR= 15 ;:CODE FOR CARRIAGE RETURN
CRLF= 200 ;:CODE FOR CARRIAGE RETURN-LINE FEED
PS= 177776 ;:PROCESSOR STATUS WORD
.EQUIV PS,PSW
STKLMT= 177774 ;:STACK LIMIT REGISTER
PIRQ= 177772 ;:PROGRAM INTERRUPT REQUEST REGISTER
DSWR= 177570 ;:HARDWARE SWITCH REGISTER
DDISP= 177570 ;:HARDWARE DISPLAY REGISTER

:*GENERAL PURPOSE REGISTER DEFINITIONS

R0= %0 ;:GENERAL REGISTER
R1= %1 ;:GENERAL REGISTER
R2= %2 ;:GENERAL REGISTER
R3= %3 ;:GENERAL REGISTER
R4= %4 ;:GENERAL REGISTER
R5= %5 ;:GENERAL REGISTER
R6= %6 ;:GENERAL REGISTER
R7= %7 ;:GENERAL REGISTER
SP= %6 ;:STACK POINTER
PC= %7 ;:PROGRAM COUNTER

:*PRIORITY LEVEL DEFINITIONS

PR0= 0 ;:PRIORITY LEVEL 0
PR1= 40 ;:PRIORITY LEVEL 1
PR2= 100 ;:PRIORITY LEVEL 2
PR3= 140 ;:PRIORITY LEVEL 3
PR4= 200 ;:PRIORITY LEVEL 4
PR5= 240 ;:PRIORITY LEVEL 5
PR6= 300 ;:PRIORITY LEVEL 6
PR7= 340 ;:PRIORITY LEVEL 7

:*"SWITCH REGISTER" SWITCH DEFINITIONS

SW15= 100000
SW14= 40000
SW13= 20000
SW12= 10000
SW11= 4000
SW10= 2000
SW09= 1000
SW08= 400
SW07= 200
SW06= 100

```

950      000040      SW05= 40
951      000020      SW04= 20
952      000010      SW03= 10
953      000004      SW02= 4
954      000002      SW01= 2
955      000001      SW00= 1
956      .EQUIV SW09,SW9
957      .EQUIV SW08,SW8
958      .EQUIV SW07,SW7
959      .EQUIV SW06,SW6
960      .EQUIV SW05,SW5
961      .EQUIV SW04,SW4
962      .EQUIV SW03,SW3
963      .EQUIV SW02,SW2
964      .EQUIV SW01,SW1
965      .EQUIV SW00,SW0
966
967      .: *DATA BIT DEFINITIONS (BIT00 TO BIT15)
968      100000      BIT15= 100000
969      040000      BIT14= 40000
970      020000      BIT13= 20000
971      010000      BIT12= 10000
972      004000      BIT11= 4000
973      002000      BIT10= 2000
974      001000      BIT09= 1000
975      000400      BIT08= 400
976      000200      BIT07= 200
977      000100      BIT06= 100
978      000040      BIT05= 40
979      000020      BIT04= 20
980      000010      BIT03= 10
981      000004      BIT02= 4
982      000002      BIT01= 2
983      000001      BIT00= 1
984      .EQUIV BIT09,BIT9
985      .EQUIV BIT08,BIT8
986      .EQUIV BIT07,BIT7
987      .EQUIV BIT06,BIT6
988      .EQUIV BIT05,BIT5
989      .EQUIV BIT04,BIT4
990      .EQUIV BIT03,BIT3
991      .EQUIV BIT02,BIT2
992      .EQUIV BIT01,BIT1
993      .EQUIV BIT00,BIT0
994
995      .: *BASIC "CPU" TRAP VECTOR ADDRESSES
996      000004      ERRVEC= 4      ;; TIME OUT AND OTHER ERRORS
997      000010      RESVEC= 10     ;; RESERVED AND ILLEGAL INSTRUCTIONS
998      000014      TBITVEC=14    ;; "T" BIT
999      000014      TRTVEC= 14     ;; TRACE TRAP
1000     000014      BPTVEC= 14     ;; BREAKPOINT TRAP (BPT)
1001     000020      IOTVEC= 20     ;; INPUT/OUTPUT TRAP (IOT) **SCOPE**
1002     000024      PWRVEC= 24     ;; POWER FAIL
1003     000030      EMTVEC= 30     ;; EMULATOR TRAP (EMT) **ERROR**
1004     000034      TRAPVEC=34    ;; "TRAP" TRAP
1005     000060      TKVEC= 60      ;; TTY KEYBOARD VECTOR

```

```

1006      000064
1007      000240
1008
1009
1010      000000
1011
1012
1013
1014      000174
1015 000174 000000
1016 000176 000000
1017
1018 000200 000137 002636
1019
1020
1021
1022
1023      000204
1024      000046
1025 000046 020734
1026      000052
1027 000052 000000
1028      000204

```

```

TPVEC= 64          ;; TTY PRINTER VECTOR
PIRQVEC=240       ;; PROGRAM INTERRUPT REQUEST VECTOR
.SBTTL TRAP CATCHER

      =0
; *ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
; *SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
; *LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS
      =174
DISPREG: .WORD 0   ;; SOFTWARE DISPLAY REGISTER
SWREG:   .WORD 0   ;; SOFTWARE SWITCH REGISTER
.SBTTL STARTING ADDRESS(ES)
      JMP @#START ;; JUMP TO STARTING ADDRESS OF PROGRAM
.SBTTL ACT11 HOOKS

; *****
; HOOKS REQUIRED BY ACT11
      $SVPC=.      ; SAVE PC
      =46
$ENDAD          ;; 1)SET LOC.46 TO ADDRESS OF $ENDAD IN .$EOP
      =52
      .WORD 0      ;; 2)SET LOC.52 TO ZERO
      .= $SVPC     ;; RESTORE PC

```

.SBTTL COMMON TAGS

1029
1030
1031
1032
1033
1034
1035 001100 001100
1036 001100 000000
1037 001100 000000
1038 001102 000
1039 001103 000
1040 001104 000000
1041 001106 000000
1042 001110 000000
1043 001112 000000
1044 001114 000
1045 001115 001
1046 001116 000000
1047 001120 000000
1048 001122 000000
1049 001124 000000
1050 001126 000000
1051 001130 000000
1052 001132 000000
1053 001134 000
1054 001135 000
1055 001136 000000
1056 001140 177570
1057 001142 177570
1058 001144 177560
1059 001146 177562
1060 001150 177564
1061 001152 177566
1062 001154 000
1063 001155 002
1064 001156 012
1065 001157 000
1066 001160 000000
1067
1068 001162 000000
1069 001164 000000
1070 001166 000000
1071 001170 000000
1072 001172 000000
1073 001174 000000
1074 001176 000000
1075 001200 000000
1076 001202 000000
1077 001204 000000
1078 001206 000000
1079 001210 000000
1080 001212 077
1081 001213 015
1082 001214 000012
1083
1084 001216 005015 051104 053111

*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
*USED IN THE PROGRAM.

.=1100

SCMTAG: .WORD 0
SPASS: .WORD 0
\$STNM: .BYTE 0
SERFLG: .BYTE 0
\$ICNT: .WORD 0
\$LPADR: .WORD 0
\$LPERR: .WORD 0
\$ERTTL: .WORD 0
\$ITEMB: .BYTE 0
\$ERMAX: .BYTE 1
\$ERRPC: .WORD 0
\$GDADR: .WORD 0
\$BDADR: .WORD 0
\$GDDAT: .WORD 0
\$BDDAT: .WORD 0
\$AUTOB: .BYTE 0
\$INTAG: .BYTE 0
\$SWR: .WORD DSWR
DISPLAY: .WORD DDISP
\$TKS: 177560
\$TKB: 177562
\$TPS: 177564
\$TPB: 177566
\$NULL: .BYTE 0
\$FILLS: .BYTE 2
\$FILLC: .BYTE 12
\$STPFLG: .BYTE 0
\$REGAD: .WORD 0
\$REG0: .WORD 0
\$REG1: .WORD 0
\$REG2: .WORD 0
\$REG3: .WORD 0
\$REG4: .WORD 0
\$REG5: .WORD 0
\$REG6: .WORD 0
\$REG7: .WORD 0
\$REG10: .WORD 0
\$REG11: .WORD 0
\$TIMES: 0
\$ESCAPE: 0
\$QUES: .ASCII /?/
\$CRLF: .ASCII <15>
\$LF: .ASCIZ <12>
MSG1: .ASCIZ <15><12>/DRIVE PRESENT/

START OF COMMON TAGS
CONTAINS PASS COUNT
CONTAINS THE TEST NUMBER
CONTAINS ERROR FLAG
CONTAINS SUBTEST ITERATION COUNT
CONTAINS SCOPE LOOP ADDRESS
CONTAINS SCOPE RETURN FOR ERRORS
CONTAINS TOTAL ERRORS DETECTED
CONTAINS ITEM CONTROL BYTE
CONTAINS MAX. ERRORS PER TEST
CONTAINS PC OF LAST ERROR INSTRUCTION
CONTAINS ADDRESS OF 'GOOD' DATA
CONTAINS ADDRESS OF 'BAD' DATA
CONTAINS 'GOOD' DATA
CONTAINS 'BAD' DATA
RESERVED--NOT TO BE USED
AUTOMATIC MODE INDICATOR
INTERRUPT MODE INDICATOR
ADDRESS OF SWITCH REGISTER
ADDRESS OF DISPLAY REGISTER
TTY KBD STATUS
TTY KBD BUFFER
TTY PRINTER STATUS REG. ADDRESS
TTY PRINTER BUFFER REG. ADDRESS
CONTAINS NULL CHARACTER FOR FILLS
CONTAINS # OF FILLER CHARACTERS REQUIRED
INSERT FILL CHARS. AFTER A "LINE FEED"
"TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
CONTAINS THE ADDRESS FROM WHICH (\$REG0) WAS OBTAINED
CONTAINS ((\$REGAD)+0)
CONTAINS ((\$REGAD)+2)
CONTAINS ((\$REGAD)+4)
CONTAINS ((\$REGAD)+6)
CONTAINS ((\$REGAD)+10)
CONTAINS ((\$REGAD)+12)
CONTAINS ((\$REGAD)+14)
CONTAINS ((\$REGAD)+16)
CONTAINS ((\$REGAD)+20)
CONTAINS ((\$REGAD)+22)
MAX. NUMBER OF ITERATIONS
ESCAPE ON ERROR ADDRESS
QUESTION MARK
CARRIAGE RETURN
LINE FEED

```

1085 001224 020105 051120 051505
1086 001232 052116 000
1087 001236 001236
1088 001236 005015 047516 042516 MSG2: .EVEN
1089 001244 000 .ASCIZ <15><12>/NONE/
1090
1091 001245 015 041412 052116 MSG3: .ASCIZ <15><12>/CNT RDY DIDN'T SET/
1092 001252 051040 054504 042040
1093 001260 042111 023516 020124
1094 001266 042523 000124
1095
1096 001272 005015 051104 053111 MSG4: .ASCIZ <15><12>/DRIVE /
1097 001300 020105 000
1098
1099 001303 015 040412 046114 MSG5: .ASCII <15><12>/ALL DRVS/
1100 001310 042040 053122 123
1101
1102 001315 040 051104 050117 MSG6: .ASCIZ / DROPD/<15><12>
1103 001322 006504 000012
1104 .EVEN
1105

```

```

;RK11 REGISTERS
;IF FOR ANY REASON THE REGISTER ADDRESSES ARE DIFFERENT FROM THESE
;(GIVEN BELOW), THE CONTENTS OF THE APPROPRIATE POINTERS SHOULD BE
;MODIFIED SO THAT THE CORRECT ADDRESS IS USED.
;

```

```

1111 .EVEN
1112 001326 177400 RKDS: 177400
1113 001330 177402 RKER: 177402
1114 001332 177404 RKCS: 177404
1115 001334 177406 RKWC: 177406
1116 001336 177410 RKBA: 177410
1117 001340 177412 RKDA: 177412
1118 001342 177416 RKDB: 177416
1119
1120

```

```

;TAGS AND GENERAL DATA AREA
;
;

```

```

1121
1122
1123
1124
1125 001344 000000 SIMUL: 0 ;FLAG TO BE SET TO 1 WHEN ON SIMULATOR
1126 001346 000000 FTITLE: 0 ;FLAG FOR PRINTING PROGRAM TITLE
1127 001350 000000 DRIVAD: 0 ;CONTAINS ADDRESS OF THE DRIVE UNDER TEST
1128 001352 000000 DRVDON: 0 ;CONTAINS THE NUMBER OF DRIVES CHECKED.
1129 ;IT IS INCREMENTED EACH TIME THE TESTS FOR
1130 ;A DRIVE IS COMPLETED.
1131 001354 000000 DRVPTR: 0 ;CONTAINS THE POINTER TO THE DRIVE FLAG (DRIVED
1132 ;-DRIVE?) OF THE DRIVE TO BE CHECKED NEXT.
1133 001356 000000 INDX1: 0 ;GENERAL INDEX FOR KEEPING COUNT
1134 001360 000000 INDX2: 0 ;GENERAL INDEX
1135 001362 000000 COUNT: 0 ;GENERAL COUNT REGISTER
1136 001364 000000 COUNT1: 0 ;COUNT REGISTER USED FOR 'DRESET' SUBROUTINE
1137 001366 000000 TIMER: 0 ;TIMER REGISTER
1138 001370 000000 EFLG1: 0 ;SET, TO INDICATE A PARTICULAR
1139 ;ERROR CONDITION
1140

```

1141 001372 000100
1142 001374 001000
1143 001376 014500
1144 001400 000200
1145
1146
1147
1148
1149 001402 000220
1150
1151
1152 001404 000000
1153 001406 000000
1154
1155
1156 001410 000000
1157
1158 001412 000000
1159
1160
1161
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1163
1164
1165
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1167
1168
1169
1170 001414 000000
1171 001416 000000
1172 001420 000000
1173 001422 000000
1174 001424 000000
1175 001426 000000
1176 001430 000000
1177 001432 000000
1178
1179 001434 000000
1180 001436 000000
1181 001440 000000

SEEK0: 100
SEEK1: 1000
SEEK2: 14500
RKPRI: 200

RKVEC: 220

FFLAG: 0
ODDEVN: 0

DDPCH: 0
DRIVS: 0

;CONTAINS ADDRESS OF CYLINDER 2
;CONTAINS ADDRESS OF CYLINDER 20
;CONTAINS ADDRESS OF CYLINDER 312
;CONTAINS THE CPU LEVEL AT WHICH
;RK11 NORMALLY INTERRUPTS. THIS WORD
;SHOULD BE CHANGED IF RK11 IS DESINGATED
;0 BR LEVEL OTHER THAN 5. E.G. IF IT IS CHANGED
;TO 6, THIS WORD SHOULD BE CHANGED TO 240.
;CONTAINS THE NORMAL VECTOR ADDRESS TO WHICH
;RK11 INTERRUPTS. IF THIS IS NOT 50, CHANGE
;THIS WORD TO CONTAIN MODIFIED VECTOR ADDRESS.

;USED TO DETERMINE WHICH OF RK-OSF DRIVES ACTIVE
;0 IF EVEN DRIVE
;-1 IF ODD DRIVE
;IF PROGRAM LOADED FROM RK05, CONTAINS
;ADDRESS OF DRIVE WITH RKDP PACK
;CONTAINS THE NUMBER OF DRIVES PRESENT

;THE FLAGS BELOW (BIT 0) ARE SET TO 1 TO INDICATE THAT A PARTICULAR DRIVE
;IS PRESENT AND IS TO BE TESTED. BIT 12, IF SET, INDICATES THAT THE DRIVE
;WAS DROPPED AFTER MAXIMUM ALLOWABLE NUMBER OF ERRORS OCCURED ON THAT
;DRIVE (SW 6 SET).
;IF MORE THAN 5 ERRORS OCCUR IN THE HARDWARE POLLING TEST (LAST)
;THEN ALL DRIVES ARE DROPPED. BUT BIT 12 IS NOT SET.

DRIVO: 0 ;FLAG SET TO 1 WHEN DRIVE 0 PRESENT
DRIV1: 0 ;FOR DRIVE 1
DRIV2: 0 ;FOR DRIVE 2
DRIV3: 0 ;FOR DRIVE 3
DRIV4: 0 ;FOR DRIVE 4
DRIV5: 0 ;FOR DRIVE 5
DRIV6: 0 ;FOR DRIVE 6
DRIV7: 0 ;FOR DRIVE 7

T56FLG: 0
PHYDRV: 0
SIZYET: 0

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1190
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1200
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1202
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1234
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1236
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001442

.SBTTL ERROR POINTER TABLE

;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
 ;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
 ;*LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
 ;*NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
 ;*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

```

;*      EM      ;;POINTS TO THE ERROR MESSAGE
;*      DH      ;;POINTS TO THE DATA HEADER
;*      DT      ;;POINTS TO THE DATA
;*      DF      ;;POINTS TO THE DATA FORMAT
    
```

\$ERRTB:

;;THE ERROR ITEMS TABLE CONSISTS OF ALL THE POSSIBLE ERROR MESSAGES
 ;USED IN THIS PROGRAM. AN ERROR CALL IN THE PROGRAM CORRESPONDS TO
 ;THE ITEM NUMBER IN THE ERROR TABLE. THUS 'ERROR 1' IN THE
 ;PROGRAM CORRESPONDS TO 'ITEM 1' IN THE ERROR TABLE.
 ;'EM###' IS THE POINTER TO THE ERROR MESSAGE WHICH WILL BE TYPED
 ;OUT IN CASE THAT ERROR WERE TO OCCUR. THUS FOR 'ERROR 1' THE ERROR
 ;MESSAGE TYPE OUT WILL BE 'TIME OUT ON RK11 REG'.
 ;'DH###' IS THE POINTER TO THE HEADER BLOCK WHICH WILL BE TYPED OUT
 ;IMMEDIATELY AFTER THE ERROR MESSAGE.
 ;'DT###' SERVES AS A POINTER TO THE MEMORY LOCATIONS WHERE
 ;THE INFORMATION RELEVANT TO THE ERROR TYPE OUTS (LIKE PC, CONTENTS
 ;OF RKCS ETC.) WILL BE PICKED UP FROM.
 ;THE LAST ROW CONTAINING 'D' SERVES AS A TERMINATOR.
 ;EXAMPLE:
 ;IF ON RUNNING THIS PROGRAM A TIMEOUT WERE TO OCCUR ON ADDRESSING RKDS
 ;(177400), BECAUSE OF SOME FAULT, THE FOLOWING TYPEOUT WOULD
 ;OCCUR ON THE TELETYPE.

```

                TIME OUT ON RK11 REG
                PC      REG
                #####  177400
    
```

;;NOTE THAT ##### WOULD BE THE ACTUAL PC WHERE 'ERROR 1' IS LOCATED.

;;THE ERROR HANDLER IS LOCATED AT '\$ERROR'. THE ERROR CALL IS AN 'EMT'
 ;INSTRUCTION WITH ITS LOWER BYTE ENCODED TO PROVIDE INDEXING TO THE
 ;ITEMS IN THE ERROR TABLE.
 ;THUS 'ERROR 1' IS 104001
 ;'ERROR 103' IS 104126 ETC.

;ERROR ITEMS TABLE

1294			; ITEM	11	
1295					
1296	001542	025545		EM34	; 'SOK' DID NOT SET
1297	001544	032207		DH34	; PC RKDS
1298	001546	031720		DT1	; SERRPC \$REGO
1299	001550	000000		0	
1300					
1301			; ITEM	12	
1302					
1303	001552	025564		EM35	; 'SEC COUNTR' DIDN'T COUNT TO 0
1304	001554	032225		DH35	; PC SEC-CNTR
1305	001556	031720		DT1	; SERRPC \$REGO
1306	001560	000000		0	
1307					
1308			; ITEM	13	
1309					
1310	001562	025617		EM36	; 'SEC COUNTR' DIDN'T INCREMENT
1311	001564	032245		DH36	; PC PRSNT-COUNT NXT-COUNT
1312	001566	031726		DT2	; SERRPC \$REGO \$REG1
1313	001570	000000		0	
1314					
1315			; ITEM	14	
1316					
1317	001572	025647		EM37	; 'SECTOR COUNTER' INCREMENTED WRONG
1318	001574	032043		DH4	; PC EXPCTD RECVD
1319	001576	031726		DT2	; SERRPC \$REGO \$REG1
1320	001600	000000		0	
1321					
1322			; ITEM	15	
1323					
1324	001602	025703		EM40	; DIDN'T GET SC=SA FOR THIS SECTOR
1325	001604	032275		DH40	; PC SECTOR RKDS
1326	001606	031726		DT2	; SERRPC \$REGO \$REG1
1327	001610	000000		0	
1328					
1329			; ITEM	16	
1330					
1331	001612	025743		EM41	; ERROR-'R/W/S RDY' SHOULD BE SET.
1332	001614	032207		DH34	; PC RKDS
1333	001616	031720		DT1	; SERRPC \$REGO
1334	001620	000000		0	
1335					
1336			; ITEM	17	
1337					
1338	001622	025411		EM13	; RKBA ERROR
1339	001624	032043		DH4	; PC EXPCT RECVD
1340	001626	031726		DT2	; SERRPC \$REGO \$REG1
1341	001630	000000		0	
1342					
1343			; ITEM	20	
1344					
1345	001632	026000		EM43	; UNEXPECTED RK11 INTERRUPT
1346	001634	032144		DH21	; PC
1347	001636	031752		DT21	; SERRPC
1348	001640	000000		0	
1349					

1350			; ITEM	21	
1351					
1352	001642	026032		EM44	;'CNTRL RDY' DIDN'T SET AFTER SEEK OR DRIVE RESET
1353	001644	032323		DH44	;'PC RKCS RKER RKDS RKDA
1354	001646	031736		DT20	;'SERRPC \$REG0 \$REG1 \$REG2 \$REG3.
1355	001650	000000		0	
1356					
1357			; ITEM	22	
1358					
1359	001652	026106		EM45	;'ERR' OR 'HE' SET ON SEEK OR DRIVE RESET
1360	001654	032323		DH44	;'PC RKCS RKER RKDS RKDA
1361	001656	031736		DT20	;'SERRPC \$REG0 \$REG1 \$REG2 \$REG3
1362	001660	000000		0	
1363					
1364			; ITEM	23	
1365					
1366	001662	026154		EM46	;'RKER BIT, ON SEEK OR DRIVE RESET
1367	001664	032151		DH30	;'PC RKCS RKER RKDS
1368	001666	031756		DT26	;'SERRPC \$REG0 \$REG1 \$REG2
1369	001670	000000		0	
1370					
1371			; ITEM	24	
1372					
1373	001672	026212		EM47	;'RKCS CHANGED AFTER FUNCTION WAS DONE
1374	001674	032043		DH4	;'PC EXPCT RECVD
1375	001676	031726		DT2	;'SERRPC \$REG0 \$REG1
1376	001700	000000		0	
1377					
1378			; ITEM	25	
1379					
1380	001702	026254		EM50	;'R/W/S RDY' DID NOT CLEAR
1381	001704	032151		DH30	;'PC RKCS RKER RKDS
1382	001706	031756		DT26	;'SERRPC \$REG0 \$REG1 \$REG2
1383	001710	000000		0	
1384					
1385			; ITEM	26	
1386					
1387	001712	026303		EM51	;'R/W/S RDY' DIDN'T SET AFTER SEEK OR DRIVE RESET
1388	001714	032323		DH44	;'PC RKCS RKER RKDS RKDA
1389	001716	031736		DT20	;'SERRPC \$REG0 \$REG1 \$REG2 \$REG3
1390	001720	000000		0	
1391					
1392			; ITEM	27	
1393					
1394	001722	026356		EM52	;'RKDA CHANGED AFTER SEEK
1395	001724	032043		DH4	;'PC EXPCTD REGVD
1396	001726	031726		DT2	;'SERRPC \$REG0 \$REG1
1397	001730	000000		0	
1398					
1399			; ITEM	30	
1400					
1401	001732	026403		EM53	;'CNTRL RDY' DIDN'T CLEAR AS GO WAS SET
1402	001734	032151		DH30	;'PC RKCS RKER RKDS
1403	001736	031756		DT26	;'SERRPC \$REG0 \$REG1 \$REG2
1404	001740	000000		0	
1405					

1406			; ITEM	31	
1407				EMS4	: 'CNTRL RDY' DIDN'T SET ON DOING WRITE/FMT STARTING
1408	001742	026446			: FROM <DSK-ADRES>
1409				DHS4	: PC RKCS RKER RKDS RKDA
1410	001744	032370			: DRV# CYL <DSK-ADRES> SUR SECTR
1411				DT54	: \$ERRPC \$REG0 \$REG1 \$REG2 \$REG3
1412	001746	031770			: \$REG4 \$REG5 \$REG6 \$REG7
1413				0	
1414	001750	000000			
1415					
1416			; ITEM	32	
1417				EMS5	: 'HE' OR 'ERR' ON WRITE/FMT STARTING FROM
1418	001752	026540			: <DSK-ADRES>
1419				DHS4	: PC RKCS RKER RKDS RKDA
1420	001754	032370			: DRV# CYL <DSK-ADRES> SUR SECTR
1421				DT54	: \$ERRPC \$REG0 \$REG1 \$REG2 \$REG3
1422	001756	031770			: \$REG4 \$REG5 \$REG6 \$REG7
1423				0	
1424	001760	000000			
1425					
1426			; ITEM	33	
1427				EMS6	: RKDA INCREMENTED WRONG ON WRITE OR WRITE FORMAT
1428	001762	026617		DHS6	: PC EXPCT: DRV# CYL SUR SECTR
1429	001764	032477			: RECVD: DRV# CYL SUR SECTR
1430				DT54	: \$ERRPC \$REG0 \$REG1 \$REG2 \$REG3
1431	001766	031770			: \$REG4 \$REG5 \$REG6 \$REG7
1432				0	
1433	001770	000000			
1434					
1435			; ITEM	34	
1436				EMS7	: RKWC DIDN'T OVERFLOW ON WRITE OR WRITE FORMAT
1437	001772	026656		DHS	: PC RECVD
1438	001774	032071		DT1	: \$ERRPC \$REG0
1439	001776	031720		0	
1440	002000	000000			
1441					
1442			; ITEM	35	
1443				EMS0	: RKBA INCREMENTED WRONG ON WRITE OR WRITE FORMAT
1444	002002	026714		DH4	: PC EXPCT RECVD
1445	002004	032043		DT2	: \$ERRPC \$REG0 \$REG1
1446	002006	031726		0	
1447	002010	000000			
1448					
1449			; ITEM	36	
1450				EMS1	: RKER SET, ON WRITE/READ/FORMAT
1451	002012	026753		DH30	: PC RKCS RKER RKDS
1452	002014	032151		DT26	: \$ERRPC \$REG0 \$REG1 \$REG2
1453	002016	031756		0	
1454	002020	000000			
1455					
1456			; ITEM	37	
1457				EMS2	: RKDB ERROR
1458	002022	027010		DH4	: PC EXPCT RECVD
1459	002024	032043		DT2	: \$ERRPC \$REG0 \$REG1
1460	002026	031726		0	
1461	002030	000000			

1518	002120	000000	0	
1519				
1520			; ITEM	47
1521				
1522	002122	027416	EM72	;WRONG DRIVE ID IN RKDS AFTER SEEK
1523	002124	032043	DH4	;PC EXPCT RECVD
1524	002126	031726	DT2	;SERRPC \$REGO \$REG1
1525	002130	000000	0	
1526				
1527			; ITEM	50
1528				
1529	002132	027460	EM73	;HARDWARE POLL, DRIVE ID BITS(13-15) SHOULD BE CLEAR
1530	002134	032207	DH34	;PC RKDS
1531	002136	031726	DT2	;SERRPC \$REGO
1532	002140	000000	0	
1533				
1534			; ITEM	51
1535				
1536	002142	027532	EM74	;HARDWARE POLL, INTERRUPTING DRIVE # NOT PRESENT
1537	002144	032726	DH74	;PC DRIVE #
1538	002146	031720	DT1	;SERRPC \$REGO
1539	002150	000000	0	
1540				
1541			; ITEM	52
1542				
1543	002152	027602	EM75	; 'DRIVE #' DID NOT INTERRUPT DURING HARDWARE POLL
1544	002154	032726	DH74	;PC DRIVE #
1545	002156	031720	DT1	;SERRPC \$REGO
1546	002160	000000	0	
1547				
1548			; ITEM	53
1549				
1550	002162	027652	EM76	;SCP DID NOT SET AFTER WAS DONE
1551	002164	033102	DH117	;PC RKCS
1552	002166	031720	DT1	;SERRPC \$REGO
1553	002170	000000	0	
1554				
1555			; ITEM	54
1556				
1557	002172	027715	EM77	;RKDA CHANGED AFTER 'DRIVE RESET'
1558	002174	032043	DH4	;PC EXPCT RECVD
1559	002176	031726	DT2	;SERRPC \$REGO \$REG1
1560	002200	000000	0	
1561				
1562			; ITEM	55
1563				
1564	002202	027752	EM100	;DATA ERROR AT WORD#
1565	002204	032747	DH100	;PC WORD# EXPCT RECVD
1566	002206	031756	DT26	;SERRPC \$REGO \$REG1 \$REG2
1567	002210	000000	0	
1568				
1569			; ITEM	56
1570				
1571	002212	027775	EM101	;CNTAL RDY DID NOT SET AFTER READ CHECK
1572	002214	032323	DH44	;PC RKCS RKER RKDS RKDA
1573	002216	031736	DT20	;SERRPC \$REGO \$REG1 \$REG2 \$REG3

1574	002220	000000	0	
1575				
1576			; ITEM	57
1577				
1578	002222	030037	EM102	; 'ERR' OF 'HE' SET ON READ CHECK
1579	002224	032151	DH30	; PC RKCS RKER RKDS
1580	002226	031756	DT26	; \$ERRPC \$REG0 \$REG1 \$REG2
1581	002230	000000	0	
1582				
1583			; ITEM	60
1584				
1585	002232	030063	EM103	; 'CSE' ON READ CHECK
1586	002234	033004	DH103	; PC RKER
1587	002236	031720	DT1	; \$ERRPC \$REG0
1588	002240	000000	0	
1589				
1590			; ITEM	61
1591				
1592	002242	030101	EM104	; RKWC DID NOT OVERFLOW ON READ CHECK OR WRITE CHECK
1593	002244	033020	DH104	; PC RECVD RKCS
1594	002246	031726	DT2	; \$ERRPC \$REG0 \$REG1
1595	002250	000000	0	
1596				
1597			; ITEM	62
1598				
1599	002252	030152	EM105	; RKDA INCREMENTED WRONG ON READ CHECK
1600	002254	032043	DH4	; PC EXPCT RECVD
1601	002256	031726	DT2	; \$ERRPC \$REG0 \$REG1
1602	002260	000000	0	
1603				
1604			; ITEM	63
1605				
1606	002262	030210	EM106	; RKBA CHANGED AFTER READ CHECK
1607	002264	032043	DH4	; PC EXPCT RECVD
1608	002266	031726	DT2	; \$ERRPC \$REG0 \$REG1
1609	002270	000000	0	
1610				
1611			; ITEM	64
1612				
1613	002272	030241	EM107	; MEMORY WORD CHANGED AFTER READ CHECK
1614	002274	033044	DH107	; PC LOC EXPCT RECVD
1615	002276	031756	DT26	; \$ERRPC \$REG0 \$REG1 \$REG2
1616	002300	000000	0	
1617				
1618			; ITEM	65
1619				
1620	002302	030302	EM110	; CNTRL RDY DID NOT SET AFTER WRITE CHECK
1621	002304	032323	DH44	; PC RKCS RKER RKDS RKDA
1622	002306	031736	DT20	; \$ERRPC \$REG0 \$REG1 \$REG2 \$REG3
1623	002310	000000	0	
1624				
1625			; ITEM	66
1626				
1627	002312	030345	EM111	; HE OR ERR BIT SET AFTER DOING WRITE CHECK
1628	002314	032151	DH30	; PC RKCS RKER RKDS
1629	002316	031756	DT26	; \$ERRPC \$REG0 \$REG1 \$REG2

1630	002320	000000	0					
1631								
1632				; ITEM	67			
1633								
1634	002322	030372	EM112			; WRITE CHECK ERROR		
1635	002324	032151	DH30			; PC RKCS RKER RKDS		
1636	002326	031756	DT26			; \$ERRPC \$REGO \$REG1 \$REG2		
1637	002330	000000	0					
1638								
1639				; ITEM	70			
1640								
1641	002332	030413	EM113			; RKDA INCREMENTED WRONG ON WRITE CHECK		
1642	002334	032043	DH4			; PC EXPCT RECVD		
1643	002336	031726	DT2			; \$ERRPC \$REGO \$REG1		
1644	002340	000000	0					
1645								
1646				; ITEM	71			
1647								
1648	002342	030452	EM114			; RKBA INCREMENTED WRONG ON WRITE CHECK		
1649	002344	032043	DH4			; PC EXPCT RECVD		
1650	002346	031726	DT2			; \$ERRPC \$REGO \$REG1		
1651	002350	000000	0					
1652								
1653				; ITEM	72			
1654								
1655	002352	030511	EM115			; RKBA INCREMENTED WITH IBA SET		
1656	002354	032043	DH4			; PC EXPCT RECVD		
1657	002356	031726	DT2			; \$ERRPC \$REGO \$REG1		
1658	002360	000000	0					
1659								
1660				; ITEM	73			
1661								
1662	002362	030545	EM116			; WRONG MEMORY LOCATION CHANGED WITH IBA SET		
1663	002364	032747	DH100			; PC WORD# EXPCT RECVD		
1664	002366	031756	DT26			; \$ERRPC \$REGO \$REG1 \$REG2		
1665	002370	000000	0					
1666								
1667				; ITEM	74			
1668								
1669	002372	030620	EM117			; RK11 DID NOT INTERRUPT WHEN IDE WAS SET		
1670	002374	033102	DH117			; PC RKCS		
1671	002376	031720	DT1			; \$ERRPC \$REGO		
1672	002400	000000	0					
1673								
1674				; ITEM	75			
1675								
1676	002402	030665	EM120			; RK11 DID NOT INTERRUPT AFTER SEEK WAS INITIATED		
1677	002404	033102	DH117			; PC RKCS		
1678	002406	031720	DT1			; \$ERRPC \$REGO		
1679	002410	000000	0					
1680								
1681				; ITEM	76			
1682								
1683	002412	030740	EM121			; SCP SET BEFORE SEEK COMPLETED		
1684	002414	033102	DH117			; PC RKCS		
1685	002416	031720	DT1			; \$ERRPC \$REGO		

1686	002420	000000	0	
1687				
1688			: ITEM	77
1689				
1690	002422	030776	EM122	:RK11 DID NOT INTERRUPT AFTER SEEK COMPLETED
1691	002424	032151	DH30	:PC RKCS RKER RKDS
1692	002426	031756	DT26	:SERRPC \$REG0 \$REG1 \$REG2
1693	002430	000000	0	
1694				
1695			: ITEM	100
1696				
1697	002432	031045	EM123	:CNTRL RESET DID NOT CLEAR 'SCP' BIT
1698	002434	033102	DH117	:PC RKCS
1699	002436	031720	DT1	:SERRPC \$REG0
1700	002440	000000	0	
1701				
1702			: ITEM	101
1703				
1704	002442	031104	EM124	:RK11 DID NOT INTERRUPT AFTER READ WAS DONE
1705	002444	033102	DH117	:PC RKCS
1706	002446	031720	DT1	:SERRPC \$REG0
1707	002450	000000	0	
1708				
1709			: ITEM	102
1710				
1711	002452	031146	EM125	:CNTRL RESET DID NOT CLEAR REGISTER
1712	002454	032014	DH2	:PC REGADD RECVD
1713	002456	031726	DT2	:SERRPC \$REG0 \$REG1
1714	002460	000000	0	
1715				
1716			: ITEM	103
1717				
1718	002462	031205	EM126	:RK11 DID NOT INTERRUPT AT CPU LEVEL
1719	002464	033116	DH126	:PC LEVEL RKCS
1720	002466	031726	DT2	:SERRPC \$REG0 \$REG1
1721	002470	000000	0	
1722				
1723			: ITEM	104
1724				
1725	002472	031246	EM127	:RK11 INTERRUPTED AT WRONG CPU LEVEL
1726	002474	033116	DH126	:PC LEVEL RKCS
1727	002476	031726	DT2	:SERRPC \$REG0 \$REG1
1728	002500	000000	0	
1729				
1730			: ITEM	105
1731				
1732	002502	031310	EM130	: 'ERR BIT' DID NOT SET IN RKER
1733	002504	033144	DH130	:PC RKCS RKER ERR BIT
1734	002506	031756	DT26	:SERRPC \$REG0 \$REG1 \$REG2
1735	002510	000000	0	
1736				
1737				
1738			: ITEM	106
1739				
1740	002512	031345	EM131	:HE OR ERR DID NOT SET
1741	002514	033203	DH131	:PC RKCS RKER

1798	002614	032726	DH74	;PC	DRIVE #	
1799	002616	031720	DT1	;\$ERRPC	\$REGO	
1800	002620	000000	0			
1801						
1802			;ITEM	117		
1803						
1804	002622	025364	EM11	;RKWC ERROR		
1805	002624	032043	DH4	;PC	EXPCT	RECVD
1806	002626	031726	DT2	;\$ERRPC	\$REGO	\$REG1
1807	002630	000000	0			
1808			;ITEM	120		
1809	002632	031656	EM142			
1810	002634	000000	0			
1811						
1812						
1813						

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1814 002636 000005 START: RESET ;CLEAR THE BUS
1815 ;;GIVE DRIVES TIME TO LOAD HEADS IN CASE OF AN APT START.
1816 002640 023737 000042 000046 CMP #42,#46 ;ARE WE IN ACT11 AUTOMATIC MODE?
1817 002646 001016 BNE STARTA ;NO, SKIP DELAY
1818 002650 005077 176464 CLR #RKDA ;SELECT UNIT 0
1819 002654 012700 000250 MOV #250,R0 ;WAIT FOR..
1820 002660 032777 000200 176440 20$: BIT #200,#RKDS ;DRIVE READY..
1821 002666 001006 BNE STARTA ;IN CASE..
1822 002670 005001 CLR R1 ;OF APT..
1823 002672 005301 DEC R1 ;START, BUT..
1824 002674 001376 BNE #-2 ;DON'T WAIT..
1825 002676 005300 DEC R0 ;FOREVER.
1826 002700 001367 BNE 20$ ;RKDS BIT 7 (DRIVE READY) NEVER SET
1827 002702 000000 HALT
1828 002704
1829
1830 STARTA: INITIALIZE THE COMMON TAGS
; ;CLEAR THE COMMON TAGS ($CMTAG) AREA
1831 002704 012706 001100 MOV #CMTAG,R6 ;;FIRST LOCATION TO BE CLEARED
1832 002710 005026 CLR (R6)+ ;;CLEAR MEMORY LOCATION
1833 002712 022706 001140 CMP #SWR,R6 ;;DONE?
1834 002716 001374 BNE #-6 ;;LOOP BACK IF NO
1835 002720 012706 001100 MOV #STACK,SP ;;SETUP THE STACK POINTER
1836 ;;INITIALIZE A FEW VECTORS
1837 002724 012737 022134 000020 MOV #SCOPE,#IOTVEC ;;IOT VECTOR FOR SCOPE ROUTINE
1838 002732 012737 000340 000022 MOV #340,#IOTVEC+2 ;;LEVEL 7
1839 002740 012737 022406 000030 MOV #ERROR,#EMTVEC ;;EMT VECTOR FOR ERROR ROUTINE
1840 002746 012737 000340 000032 MOV #340,#EMTVEC+2 ;;LEVEL 7
1841 002754 012737 024672 000034 MOV #STRAP,#TRAPVEC ;;TRAP VECTOR FOR TRAP CALLS
1842 002762 012737 000340 000036 MOV #340,#TRAPVEC+2 ;;LEVEL 7
1843 002770 012737 024772 000024 MOV #SPWRDN,#PWRVEC ;;POWER FAILURE VECTOR
1844 002776 012737 000340 000026 MOV #340,#PWRVEC+2 ;;LEVEL 7
1845 003004 005037 001206 CLR $TIMES ;;INITIALIZE NUMBER OF ITERATIONS
1846 003010 005037 001210 CLR $ESCAPE ;;CLEAR THE ESCAPE ON ERROR ADDRESS
1847 003014 112737 000001 001115 MOVB #1,$ERMAX ;;ALLOW ONE ERROR PER TEST
1848 003022 012737 003022 001106 MOV #,$SLPADR ;;INITIALIZE THE LOOP ADDRESS FOR SCOPE
1849 003030 012737 003030 001110 MOV #,$SLPERR ;;SETUP THE ERROR LOOP ADDRESS
1850 ;;SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
1851 ;;EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER.
1852 003036 013746 000004 MOV #ERRVEC,-(SP) ;;SAVE ERROR VECTOR
1853 003042 012737 003076 000004 MOV #64$,#ERRVEC ;;SET UP ERROR VECTOR
1854 003050 012737 177570 001140 MOV #DSWR,SWR ;;SETUP FOR A HARDWARE SWICH REGISTER
1855 003056 012737 177570 001142 MOV #DDISP,DISPLAY ;;AND A HARDWARE DISPLAY REGISTER
1856 003064 022777 177777 176046 CMP #-1,#SWR ;;TRY TO REFERENCE HARDWARE SWR
1857 003072 001012 BNE 66$ ;;BRANCH IF NO TIMEOUT TRAP OCCURRED
1858 ;;AND THE HARDWARE SWR IS NOT = -1
1859 003074 000403 BR 65$ ;;BRANCH IF NO TIMEOUT
1860 003076 012716 003104 64$: MOV #65$,(SP) ;;SET UP FOR TRAP RETURN
1861 003102 000002 RTI
1862 003104 012737 000176 001140 65$: MOV #SWREG,SWR ;;POINT TO SOFTWARE SWR
1863 003112 012737 000174 001142 MOV #DISPREG,DISPLAY
1864 003120 012637 000004 66$: MOV (SP)+,#ERRVEC ;;RESTORE ERROR VECTOR
1865
1866 003124 023737 000042 000046 CMP #42,#46 ;ARE WE IN ACT11 AUTOMATIC MODE?
1867 003132 001416 BEQ 69$ ;YES, SKIP TITLE
1868 .SBTTL TYPE PROGRAM NAME
1869 ;;TYPE THE NAME OF THE PROGRAM IF FIRST PASS

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1870 003134 005227 177777      INC      #-1      ;; FIRST TIME?
1871 003140 001043      BNE      67$      ;; BRANCH IF NO
1872 003142 104401 003200      TYPE     68$      ;; TYPE ASCIZ STRING
1873      .SBTTL GET VALUE FOR SOFTWARE SWITCH REGISTER
1874 003146 005737 000042      TST     2#42     ;; ARE WE RUNNING UNDER XXDP/ACT?
1875 003152 001006      BNE      69$      ;; BRANCH IF YES
1876 003154 023727 001140 000176      CMP     SWR,#SWREG ;; SOFTWARE SWITCH REG SELECTED?
1877 003162 001005      BNE      70$      ;; BRANCH IF NO
1878 003164 104406      GTSWR
1879 003166 000403      BR      70$      ;; GET SOFT-SWR SETTINGS
1880 003170 112737 000001 001134 69$: MOV     #1,$AUTOB ;; SET AUTO-MODE INDICATOR
1881 003176      70$:
1882 003176 000424      BR      67$      ;; GET OVER THE ASCIZ
1883      ;; 68$: .ASCIZ <CRLF>/RK11 LOGIC TEST 2/<15><12>/MAINDEC-11-DZRKK-E/<CRLF>
1884      67$:
1885 003250      MOV     #DDPCH,RO
1886 003254 012701 001410      MOV     #-13,R1
1887 003260 005020      CLR     (R0)+    1$:
1888 003262 005201      INC     R1
1889 003264 001375      BNE     1$
1890 003266 005227 177777      INC     #-1      ;; FIRST START ?
1891 003272 001020      BNE     START1   ;; BR IF NOT
1892 003274 013746 000004      MOV     ERRVEC,-(SP) ;; SAVE ERROR VECTOR ADDRESS
1893 003300 012737 003314 000004      MOV     #2$,ERRVEC ;; NEW VECTOR ADDRESS
1894 003306 005737 177776      TST     PS      ;; SEE IF PROGRAM CAN REFERENCE THE
1895      ;; PROCESSOR STATUS WORD
1896 003312 000406      BR      3$      ;; BR IF REFERENCE DIDN'T CAUSE TRAP
1897 003314 012737 000140 001400 2$: MOV     #140,RKPRI ;; SETUP INTERRUPTING PRIORITY TO VALUE
1898      ;; WHICH WILL ALLOW INTERRUPT ON AN LSI-11
1899 003322 012716 003330      MOV     #3$, (SP) ;; SETUP RETURN ADDRESS
1900 003326 000002      RTI
1901 003330 012637 000004      3$: MOV     (SP)+,ERRVEC ;; RESTORE THE ERROR VECTOR
1902
1903      ;; FIND OUT IF ACT11, 'XXDP' CHAIN OR DUMP MODE
1904
1905 003334 012700 001410      START1: MOV     #DDPCH,RO
1906 003340 012701 177766      MOV     #-12,R1   ;; CLEAR OUT DRIVE TABLE AREA
1907 003344 005020      1$: CLR     (R0)+
1908 003346 005201      INC     R1
1909 003350 001375      BNE     1$
1910 003352 122737 000002 000041      CMPB   #2,41     ;; LOADED FROM AN RK05 ?
1911 003360 001166      BNE     ST2      ;; BR IF NOT
1912 003362 013737 000040 001410      MOV     40,DDPCH ;; GET DEVICE INDICATOR AND DRIVE ADDRESS OF
1913      ;; LOADING RK05
1914 003370 122737 000010 001410      CMPB   #10,DDPCH ;; VALID DRIVE NUMBER IN BYTE 40 ?
1915 003376 101002      BHI     2$      ;; BR IF YES
1916 003400 105037 001410      CLRB   DDPCH    ;; MUST BE DRIVE ZERO WHICH LOADED
1917      ;; THIS PROGRAM
1918 003404 005737 000042      2$: TST     42     ;; CHAIN MODE OR ACT11 AUTO ACCEPT ?
1919 003410 001432      BEQ     4$
1920 003412 005737 001410      TST     DDPCH   ;; BR IF NEITHER
1921 003416 001002      BNE     3$      ;; RUNNING FROM AN RK05 ?
1922 003420 000137 004262      JMP     ST3     ;; BR IF YES
1923 003424      3$:
1924 003424 104401 003432      TYPE     65$    ;; TYPE ASCIZ STRING
1925 003430 000413      BR      64$    ;; GET OVER THE ASCIZ

```

```

1926
1927 003460
1928 003460 005046
1929 003462 113716 001410
1930 003466 104403
1931 003470 001
1932 003471 000
1933 003472 000137 004262
1934 003476 005227 177777
1935 003502 001115
1936 003504 104401 003512
1937 003510 000411
1938
1939 003534
1940 003534 005046
1941 003536 113716 001410
1942 003542 104403
1943 003544 001
1944 003545 000
1945 003546 104401 003554
1946 003552 000431
1947
1948 003636
1949 003636 104401 003644
1950 003642 000435
1951
1952 003736
1953
1954
1955
1956
1957
1958
1959 003736 012700 001412
1960 003742 012701 177767
1961 003746 005020
1962 003750 005201
1963 003752 001375
1964 003754 104401 003762
1965 003760 000415
1966
1967 004014
1968 004014 104411
1969 004016 012600
1970 004020 012701 177770
1971 004024 112002
1972 004026 042702 177400
1973 004032 012703 001414
1974 004036 012704 177770
1975 004042 012705 000060
1976 004046 020502
1977
1978 004050 001414
1979 004052 005205
1980 004054 005723
1981 004056 005204
    
```

```

;;65$: .ASCIZ <15><12>/NOT TESTING DRIVE /
64$: CLR -(SP) ;CLEAR WORD ON STACK
      MOV DDPCH,(SP) ;GET DRIVE ADDRESS
      TYPOS ;TYPE THE ADDRESS
      .BYTE 1 ;ONLY 1 CHARACTER
      .BYTE 0 ;SUPPRESS LEADING ZEROS
      JMP ST3 ;GET NUMBER OF DRIVES
4$: INC #-1 ;FIRST TIME THROUGH HERE ?
     BNE ST2 ;BR IF NOT
     TYPE 67$ ;TYPE ASCIZ STRING
     BR 66$ ;GET OVER THE ASCIZ
;;67$: .ASCIZ <15><12>/TO TEST DRIVE /
66$: CLR -(SP) ;CLEAR WORD ON THE STACK
      MOV DDPCH,(SP) ;GET DRIVE ADDRESS
      TYPOS ;TYPE THE DRIVE ADDRESS
      .BYTE 1 ;ONLY 1 CHARACTER
      .BYTE 0 ;SUPPRESS LEADING ZEROS
      TYPE 69$ ;TYPE ASCIZ STRING
      BR 68$ ;GET OVER THE ASCIZ
;;69$: .ASCIZ / HALT PROGRAM, REMOVE RKDP PACK AND REPLACE IT/<15><12>
68$: TYPE 71$ ;TYPE ASCIZ STRING
      BR 70$ ;GET OVER THE ASCIZ
;;71$: .ASCIZ /WITH A WORK PACK, CLEAR LOCATION 40, AND RESTART PROGRAM/
70$:

;FIND OUT FROM USER WHICH DRIVES (LOGICAL ADDRESSES) ARE TO BE
;TESTED (DRIVES TO BE TESTED ?). IN REPLY THE USER SHOULD TYPE IN THE
;LOGICAL ADDRESSES SEPERATED BY COMMAS. THUS IF 2 DRIVES 0,1 ARE PRESENT:
; 'DRIVS TO B TSTD?'
; '0,1<CR>' A CAR. RET. SHOULD BE TYPED TO TERMINATE THE LIST.
ST2: MOV #DRIVS,R0
      MOV #-11,R1
13$: CLR (R0)+
      INC R1
      BNE 13$
      TYPE 65$ ;TYPE ASCIZ STRING
      BR 64$ ;GET OVER THE ASCIZ
;;65$: .ASCIZ <15><12>/DRIVES TO BE TESTED ?/<15><12>
64$: RDLIN
      MOV (SP)+,R0 ;GET STARTING ADRES OF ASCII STRING
      MOV #-10,R1 ;SET UP COUNT
1$: MOV (R0)+,R2 ;GET ASCII CHARACTER
     BIC #177400,R2 ;MASK UNWANTED BITS
      MOV #DRIVO,R3
      MOV #-10,R4
      MOV #60,R5
2$: CMP R5,R2 ;WAS THE TYPED IN CHARACTER
     ;A NUMBER BETWEEN 0-7?
     BEQ 3$ ;YES, BRANCH
     INC R5 ;NO, INCREMENT
     TST (R3)+ ;INCREMENT POINTER TO DRV FLAG
     INC R4 ;CHARACTER THAT WAS INPUT
    
```



```

2038                                     :CHECK NUMBER OF DRIVES
2039 004262 012737 177777 001440 ST3:  MOV    # -1, SIZYET      ;CHECK FOR RKDSF LATER
2040 004270 012737 004442 000004      MOV    #55, @#4        ;SET UP ADRES FOR TIME-OUT VECTOR
2041 004276 005777 175024              TST    @RKDS          ;REFERENCE RKDS
2042 004302 005777 175032              TST    @RKDA          ;REFERENCE RKDA
2043 004306 012737 004534 000004      MOV    #BADTMO, @#4
2044 004314 104401                      TYPE
2045 004316 001216                      MSG1
2046 004320 012700 177770              MOV    # -10, R0      ;INITIALIZE COUNT FOR THE 8 DRIVES
2047 004324 005037 001412              CLR    DRVS           ;INITIALIZE # OF DRIVES PRESENT TO 0
2048 004330 005001                      CLR    R1             ;INITIALIZE ADDRESS TO DRIVE 0
2049 004332 005004                      CLR    R4
2050 004334 012702 001414              MOV    #DRIVO, R2
2051 004340 010177 174774 1$:        MOV    R1, @RKDA      ;ADDRESS THE DRIVE
2052 004344 020177 174770              CMP    R1, @RKDA     ;CHECK, WAS IT ADDRESSED?
2053 004350 001405                      BEQ    3$             ;YES
2054 004352 012703 004356              MOV    #2$, R3
2055 004356 004737 021022 2$:        JSR    PC, TYERM     ;WHILE CHECKING NUMBER OF DRIVE
2056                                     ;UNDER NON-MANUAL MODE :-
2057                                     ;RKDA HAD TO BE ADRESED BUT
2058                                     ;IT WAS FOUND THAT THE DRIVE NO
2059                                     ;THAT WAS WRITTEN COULD NOT BE READ BACK
2060                                     ;CORRECTLY.
2061
2062 004362 00J413                      BR     4$
2063 004364 032777 000200 174734 3$:   BIT    #200, @RKDS   ;CHECK IF 'DRY' BIT IS SET, IF SET DRIVE IS
2064                                     ;PRESENT
2065                                     ;
2065 004372 001407                      BEQ    4$
2066 004374 104401                      TYPE
2067 004376 001213                      $CRLF
2068 004400 005237 001412              INC    DRVS           ;IF PRESENT, INCREMENT # OF DRIVES
2069 004404 005212                      INC    (R2)           ;SET UP FLAG INDICATING THIS DRIVE PRESENT
2070 004406 010446                      MOV    R4, -(SP)
2071 004410 104402                      TYPOC
2072 004412 005722 4$:                TST    (R2)+          ;SHIFT POINTER TO NXT DRIVE INDICATOR
2073 004414 062701 020000              ADD    #20000, R1    ;SET UP ADDRESS FOR THE NEXT DRIVE
2074 004420 005204                      INC    R4             ;HAVE U CHECKED FOR ALL 8 DRIVES
2075 004422 005200                      INC    R0
2076 004424 001345                      BNE    1$
2077 004426 005737 001412              TST    DRVS
2078 004432 001011                      BNE    ST4
2079 004434 104401                      TYPE
2080 004436 001236                      MSG2
2081 004440 000406                      BR     ST4
2082                                     ;GO CHECK THE DRIVE INDEPENDENT
2083 004442 011603 5$:                MOV    (SP), R3      ;CONTROLLER LOGIC
2084 004444 022626                      CMP    (SP)+, (SP)+ ;GET PC WHERE TIMEOUT OCCURED
2085 004446 062703 177776              ADD    #-2, R3      ;RESTORE STACK
2086 004452 004737 021022              JSR    PC, TYERM     ;GO TYPE ERROR MESSAGE
2087                                     ;WHILE CHECKING FOR THE NUMBER OF
2088                                     ;DRIVES IN NON-MANUAL MODE:-
2089                                     ;RKDS AND RKDA HAD TO BE REFERENCED, TIMEOUT
2090                                     ;OCCURED ON REFERENCING.PC IN THE ERROR
2091                                     ;MESSAGE INDICATES WHERE THE TIMEOUT OCCURED:
2092
2093
    
```

```

2094
2095
2096 004456 005037 001434      ST4:  CLR      T56FLG
2097 004462 005737 001412      TST      DRIVS
2098 004466 001004              BNE      1$
2099 004470 004737 021736      JSR      PC,WATIME
2100 004474 000137 020646      JMP      $EOP
2101 004500 012737 001414 001354 1$:  MOV      #DRIVO,DRVPTR
2102 004506 005037 001352      CLR      DRVDON      ;INITIALIZE THE NO. OF DRIVES
2103                          ;THAT HAVE BEEN CHECKED
2104 004512 005037 001350      CLR      DRIVAD      ;INITIALIZE DRIVE ADDRESS TO
2105                          ;THE FIRST DRIVE
2106 004516 012737 004534 000004  MOV      #BADTMO,2#4  ;SET TIME OUT VECTOR FOR UNEXPECTED
2107                          ;TIME OUTS
2108 004524 012777 004600 174650  MOV      #BADINT,2#KVEC ;SET UP RK11 INTERRUPT VECTOR FOR
2109                          ;UNEXPECTED INTERRUPTS FROM RK11
2110 004532 000465              BR       TST1        ;GO TO TEST 1
2111
2112
2113
2114
2115                          ;THIS ROUTINE HANDLES UNEXPECTED TIME OUTS
2116
2117 004534 011600      BADTMO: MOV      (SP),RO ;SAVE PC WHERE TIME OUT OCCURED
2118 004536 005740      TST      -(RO)
2119 004540 022626      CMP      (SP)+,(SP)+ ;RESTORE STACK POINTER
2120 004542 104401 004550      TYPE    65$        ;:TYPE ASCIZ STRING
2121 004546 000407      BR       64$        ;:GET OVER THE ASCIZ
2122      ;:65$: .ASCIZ <15><12>/TIMOUT,PC=/
2123      64$:
2124 004566              MOV      RO,-(SP)   ;SET UP FOR TYPING OUT PC
2125 004570 104402              TYPOC    ;GO TYPE OUT OCTAL PC
2126 004572 000000      HALT
2127 004574 000137 002636      JMP      2#START
2128
2129
2130
2131                          ;THIS ROUTINE HANDLES UNEXPECTED INTERRUPTS FROM RK11
2132                          ;SW 9 AND 10 FOR LOOPING ON ERROR
2133                          ;AND LOOPING ON TEST IN WHICH TIMEOUT
2134                          ;OCCURRED, ARE PROVIDED.
2135
2136 004600 011600      BADINT: MOV      (SP),RO ;SAVE PC WHERE INTERRUPT OCCURED
2137 004602 005740      TST      -(RO)
2138 004604 032777 020000 174326  BIT      #20000,2#SWR ;INHIBIT ERROR TYPEOUT?
2139 004612 001014              BNE      1$        ;YES, DON'T TYPE OUT
2140 004614 104401              TYPE
2141 004616 001213              $CRLF
2142 004620 104401              TYPE
2143 004622 026000              EM43              ;TYPE 'UNEXPEXTED RK11 INTERRUPT'
2144                          ;TYPE ' AT PC='
2145 004624 104401 004632      TYPE    65$        ;:TYPE ASCIZ STRING
2146 004630 000403      BR       64$        ;:GET OVER THE ASCIZ
2147      ;:65$: .ASCIZ /,PC=/
2148      64$:
2149 004640              MOV      RO,-(SP)   ;SET UP FOR TYPING OUT PC

```

```

2150 004642 104402          TYP0C          ;GO TYPE OCTAL PC WHERE BAD
2151                          ;INTERUPT OCCURED
2152 004644 032777 001000 174266 1$: BIT #1000, QSWR ;LOOP ON ERROR?
2153 004652 001403          BEQ 2$          ;NO, BRANCH
2154 004654 022626          CMP (SP)+, (SP)+ ;YES, REPOSITION STACK
2155 004656 000177 174224          JMP Q$LPADR      ;GO TO THE STARTING ADDRESS OF
2156                          ;THE TEST THAT GAVE UNEXPECTED INTERRUPT
2157 004662 032777 040000 174250 2$: BIT #40000, QSWR ;LOOP ON TEST?
2158 004670 001401          BEQ 3$          ;NO, BRANCH
2159 004672 000002          RTI              ;YES, LOOP. GO BACK WHER U INTERRUPTED FROM.
2160 004674 000000          3$: HALT          ;UNEXPECTED INTERRUPT OCCURED AS
2161                          ;INDICATED IN THE TYPE OUT.U CAN LOOP
2162                          ;ON ERROR, TEST, OR INHIBIT TYPEOUT BY
2163                          ;SETTING APPROPRIATE SWITCHES.
2164 004676 000137 002636          JMP Q$START      ;GO BACK TO THE START OF THE
2165                          ;PROGRAM. THUS PRESSING CONTINUE
2166                          ;AFTER THE ABOVE HALT WILL
2167                          ;RESTART THE PROGRAM

```

```

;RESTART AFTER POWER FAIL
;THE PROGRAM WOULD RESTART HERE IF POWER CAME BACK AFTER A FALIURE.

```

```

2174 004702 004737 021736 PFSTRT: JSR PC, WATIME ;KILL TIME

```

```

;*****
;*TEST 1 CHECK THAT THE DRIVES THAT ARE NOT SPECIFIED ARE NOT FOUND TO BE PRESENT
; *THIS TEST CHECKS THAT THE DRIVES THAT ARE NOT SPECIFIED
; *(IN RESPONSE TO "DRIVS TO BE TSTD?") ARE NOT FOUND TO BE PRESENT.
; *EVERY DRIVE FROM 0 TO 7 IS ADDRESSED. IF A PARTICULAR DRIVE
; *GIVES 'DRY' (IN RKDS), IT IS CHECKED THAT THIS DRIVE
; *WAS SPECIFIED BY THE USER, IF IT WAS NOT AN ERROR IS
; *REPORTED, GIVING THE DRIVE NUMBER. IT IS LIKELY THAT THE USER
; *MAY HAVE FORGOTTEN TO PUT THE DRIVE (THAT IS NOT SPECIFIED) ON
; *'LOAD'. IF THIS IS THE CASE THEN PUT THIS DRIVE ON 'LOAD'.
; *IF THIS IS NOT THE CASE, THERE IS A GENUINE ERROR. (TWO DIFFERENT
; *DRIVE ADDRESSES MAY BE RESULTING IN THE SELECTION OF THE SAME
; *PHYSICAL DRIVE.)
;*****

```

```

2191 004706 000004 †ST1: SCOPE
2192 004710 012700 001414          MOV #DRIVO, R0 ;INITIALIZE POINTER
2193 004714 005001          CLR R1         ;INITIALIZE DRIVE ADRES 0
2194 004716 005002          CLR R2         ;INITIALIZE DRIVE # 0
2195 004720 005737 001410 1$: TST DDPCH     ;LOADED FROM AN RK05 ?
2196 004724 001403          BEQ 2$         ;B IF NOT
2197 004726 120237 001410          CMPB R2, DDPCH ;LOADED FROM THIS DRIVE ?
2198 004732 001435          BEQ 4$         ;BR IF YES
2199 004734 010177 174400 2$: MOV R1, Q$RKDA ;ADRES THE DRIVE
2200 004740 105777 174362          TSTB Q$RKDS   ;DRIVE READY?
2201 004744 100005          BPL 3$         ;NO, THIS DRIVE NOT PRESENT
2202                          ;YES, THIS DRIVE SELECTED
2203                          ;WAS THIS DRIVE SPECIFIED BY
2204
2205 004746 005710          TST Q$RD

```

CHECK THAT THE DRIVES THAT ARE NOT SPECIFIED ARE NOT FOUND TO BE PRESENT

```

2206
2207 004750 001026
2208
2209
2210
2211 004752 010237 001162
2212 004756 104116
2213
2214
2215
2216
2217
2218
2219 004760 005710
2220 004762 001421
2221
2222 004764 004737 020770
2223 004770 104010
2224
2225
2226
2227
2228
2229 004772 005010
2230
2231 004774 010003
2232 004776 162703 001414
2233 005002 042703 000003
2234 005006 062703 001414
2235 005012 042723 100000
2236 005016 042713 100000
2237 005022 005337 001412
2238 005026 005202
2239 005030 005720
2240 005032 062701 020000
2241 005036 001330
2242
2243
2244
2245
2246
2247
2248
2249
2250 005040
2251
2252
2253
2254
2255
2256
2257
2258
2259 005040 000004
2260 005042 012737 000001 001206
2261 005050 012737 000002 001102

```

```

BNE 4$
MOV R2,$REGO
ERROR 116
3$: TST 2R0
BEQ 4$
JSR PC,GT4RG
ERROR 10
CLR 2R0
MOV R0,R3
SUB #DRIVO,R3
BIC #3,R3
ADD #DRIVO,R3
BIC #100000,(R3)+
BIC #100000,(R3)
4$: DEC DRIVS
INC R2
TST (R0)+
ADD #20000,R1
BNE 1$

```

```

;THE USER?
;YES, OK
;NO, THIS DRIVE # WAS NOT SPECIFIED
;BY THE USER, BUT STILL IS GIVING
;'DRY' WHEN ADRESED. REPORT EROR.
;GET DRIVE #
;THIS DRIVE # WAS NOT SPECIFIED BY
;THE USER, BUT WHEN ADRESED GAVE
;'DRY'. CHECK THAT THIS DRIVE # IF
;PHYSICALLY PRESENT IS ON 'LOAD'. IF
;THIS IS NOT THE CASE, THEN ONE DRIVE
;MAY BE GETTING SELECTED BY TWO DIFFERENT
;LOGICAL ADDRESSES.
;CHECK THAT THIS DRIVE WAS NOT INDICATED
;IF IT WAS, & IT IS NOT FOUND TO BE
;PRESENT (DRY CLEAR), REPORT ERROR.
;GET RKCS, ER, DS, DA
;DRIVE # (AS IN RKDA) WAS INDICATED BY
;THE USER, BUT WAS NOT FOUND TO BE PRESENT.
;CHECK THAT THE ROTARY DRIVE SELECTION
;SWITCH ON THE MODULE IS SET TO THE RIGHT
;DRIVE #.
;THIS DRIVE IS NOT FOUND TO BE PRESENT
;HENCE DROP IT FROM THE SELECTION TABLE.
;DRIVE ADDR
;MINUS OFFSET FOR TABLE
;EVEN DRIVE OF PAIR
;POINT TO EVEN OF PAIR IF RK05 F
;NOT SPECIFIED AS F MODEL
;SAME
;DECREMENT DRIVE COUNT
;INCRMNT DRIVE #
;INCRMNT POINTER
;INCRMNT ADRES TO NXT DRIVE
;LUP BAK IF NOT DONE

```

```

;THIS PART OF THE PROGRAM IS GOING TO BE REPEATED FOR
;EACH DRIVE PRESENT
;
;'DRIVAD' CONTAINS IN BITS 15,14,13 THE ADDRESS OF THE
;DRIVE BEING CURRENTLY CHECKED.
;

```

NUDRV:

```

;*****
; *TEST 2 FIND OUT NEXT DRIVE TO BE CHECKED
; THIS CODE FINDS OUT THE NEXT DRIVE THAT IS PRESENT AND THEN SETS UP
; THE ADDRESS IN DRIVAD (BITS 13,14,15). THUS THROUGHOUT THE FOLLOWING TESTS
; THE DRIVE TESTED IS THE DRIVE WHOOSE ADDRESS IS IN 'DRIVAD'.
;*****
↑ST2: SCOPE
MOV #1,$TIMES ;:DO 1 ITERATION
MOV #2,$STNM ;:RESET POINTER TO THIS TEST

```



```

2318 ;THIS IS A VERY BASIC ERR& IF IT
2319 ;OCCURS GO BACK TO TEST 10
2320 005242 013700 001326 MOV RKDS,RO
2321 005246 013777 001350 174064 MOV DRIVAD,DRKDA ;ADDRESS THE DRIVE UNDER TEST
2322 005254 005710 TST DR0 ;CHECK IF ANY BIT OF RKDS IS SET?
2323 005256 001003 BNE 1$ ;IF SET, BRANCH
2324 005260 011037 001162 MOV DR0,$REGO ;GET RKDS
2325 005264 104004 ERROR 4 ;RKDS ERROR! RKDS IF ADDRESSED
2326 ;CORRECTLY SHOULD BE NON-ZERO
2327 005266 012777 000015 174036 1$: MOV #15,DRKCS ;ISSUE A DRV RESET, IF DRIVE
2328 ;POWER IS LO, DPL WILL SET
2329 005274 005001 CLR R1
2330 005276 032710 010000 2$: BIT #10000,DR0 ;IS 'DPL' BIT SET?
2331 005302 001003 BNE 3$ ;DPL IS SET, BRANCH
2332 005304 005201 INC R1 ;WAIT FOR SOME TIME TO
2333 005306 001373 BNE 2$ ;SEE IF DPL WOULD SET
2334 005310 000403 BR 4$-2 ;OK, DPL NOT SET
2335 005312 004737 020776 3$: JSR PC,GT3RG ;GO, GET RKCS, ER, DS
2336 005316 104005 ERROR 5 ;DPL BIT OF RKDS IS SET, CHECK DRIVE POWER
2337
2338
2339 005320 005001 CLR R1
2340 005322 032710 000100 4$: BIT #100,DR0 ;DID R/W/S RDY BIT SET?
2341 005326 001010 BNE TST4 ;YES, EXIT
2342 005330 104417 000011 DELAY 11 ;TIME DELAY
2343 005334 005201 INC R1 ;WAIT FOR R/W/S RDY
2344 005336 001371 BNE 4$
2345 005340 017737 173762 001162 MOV DRKDS,$REGO ;GET RKDS
2346 005346 104016 ERROR 16 ;R/W/S RDY DID NOT SET AFTER
2347 ;DRIVE RESET. DRIVE RESET WAS DONE
2348 ;TO CHECK 'DPL' BIT. THIS TEST
2349 ;IS NOT FOR CHECKING DRIVE RESET.
2350 ;U MIGHT WANT TO USE THE TEST PROVIDED
2351 ;FOR CHECKING DRIVE RESET.
2352
2353 ;*****
2354 ;*TEST 4 CHECK THAT 'DRIVE UNSAFE' IS CLEAR, 'HDEN' IS SET, 'WPS' IS CLEAR
2355 ;*****
2356 005350 000004 TST4: SCOPE
2357 005352 104413 CNT.RESET ;GO, DO CONTROL RESET
2358 ;THIS IS A CALL FOR THE 'CNTRL-
2359 ;RESET' ROUTINE. A CONTROL RESET IS
2360 ;ISSUED AND AFTER A CERTAIN TIME
2361 ;IF THE 'CNTRL RDY' DOES NOT SET
2362 ;AN ERROR IS REPORTED. NOTE THAT
2363 ;THE PC IN ERROR MESSAGE IS THE
2364 ;PC WHERE 'CNT.RESET' IS LOCATED.
2365 ;THIS IS A VERY BASIC ERR & IF IT
2366 ;OCCURS GO BACK TO TEST 10
2367 005354 013777 001350 173756 MOV DRIVAD,DRKDA ;SET DRIVE ADDRESS
2368 005362 017700 173740 MOV DRKDS,RL ;GET RKDS
2369 005366 032700 002000 BIT #2000,RO ;IS 'DRU' BIT OF RKDS SET?
2370 005372 001403 BEQ 1$ ;NO
2371 005374 004737 020776 JSR PC,GT3RG ;GO, GET RKCS, ER, DS
2372 005400 104006 ERROR 6 ;'DRU' BIT OF RKDS IS SET, CHECK
2373 ;DRIV BY PUTTING RUN/LOAD SW TO LOAD

```

```

2374                                     ; THEN BACK TO RUN
2375 005402 032700 004000 1$: BIT #4000,RO ; IS 'HDEN' BIT SET?
2376 005406 001004 BNE 2$ ; YES, BRANCH
2377 005410 017737 173712 001162 MOV #RKDS,$REGO ; GET RKDS
2378 005416 104007 ERROR 7 ; ERROR, 'RKDS' BIT IS NOT SET
2379
2380 005420 032777 000040 173700 2$: BIT #40,ARKDS ; IS 'WPS' CLEAR?
2381 005426 001403 BEQ TST5 ; YES, EXIT
2382 005430 004737 020770 JSR PC,GT4RG ; GET RKCS, ER, DS, DA
2383 005434 104114 ERROR 114 ; 'WPS'-WRITE PROTECT STATUS- BIT OF
2384 ; OF RKDS SHOULD BE CLEAR, IF THIS DRIVE
2385 ; IS WRITE ENABLED. CHECK & SEE IF THIS
2386 ; DRIVE IS WRITE ENABLED, IF IT IS NOT,
2387 ; WRITE ENABLE IT.
2388
2389
2390
2391
2392
2393
2394
2395
2396
2397
2398
2399
2400
2401
2402
2403
2404 005442 013777 001350 173670 MOV DRIVAD,ARKDA ; ADDR THE DRIVE
2405 005450 105777 173652 TSTB ARKDS ; IS 'DRY' SET?
2406 005454 100403 BMI TST6 ; YES, OK
2407 005456 004737 020770 JSR PC,GT4RG ; GO, GET RKCS, ER, DS, DA
2408 005462 104010 ERROR 10 ; 'DRY' NOT SET
2409
2410
2411
2412
2413
2414
2415
2416
2417
2418
2419
2420
2421
2422
2423
2424
2425
2426
2427
2428
2429

```

```

*****
;*TEST 5 CHECK THAT 'DRIVE READY' IS SET IN RKDS
*****

```

```

TST5: SCOPE ; GO, DO CONTROL RESET
CNT.RESET ; THIS IS A CALL FOR THE 'CNTRL-
; RESET' ROUTINE. A CONTROL RESET IS
; ISSUED AND AFTER A CERTAIN TIME
; IF THE 'CNTRL RDY' DOES NOT SET
; AN ERROR IS REPORTED. NOTE THAT
; THE PC IN ERROR MESSAGE IS THE
; PC WHERE 'CNT.RESET' IS LOCATED.
; THIS IS A VERY BASIC ERR & IF IT
; OCCURS GO BACK TO TEST 10
; ADDR THE DRIVE
; IS 'DRY' SET?
; YES, OK
; GO, GET RKCS, ER, DS, DA
; 'DRY' NOT SET

```

```

*****
;*TEST 6 CHECK THAT 'SOK' BIT CAN SET
;* THIS TEST CHECKS THAT WITHIN A CERTAIN TIME
;* 'SOK' BIT CAN SET, IF IT DOES NOT AN ERROR IS REPORTED
*****

```

```

TST6: SCOPE ; ADDR THE DRIVE
MOV DRIVAD,ARKDA ; INITIALIZE COUNT FOR TIMING WAIT LOOP
CLR R1 ; IS SOK SET?
1$: BIT #400,ARKDS ; EXIT
BNE TST7 ; NO, WAIT
INC R1 ; WAITED LONG?
BNE 1$ ; GET RKDS
MOV ARKDS,$REGO ; WAITED LONG BUT 'SEC OK' BIT DID NOT
ERROR 11 ; SET

```

```

*****
;*TEST 7 CHECK THAT 'SECTOR COUNTER' CAN COUNT FROM 0-13
*****

```

```

2430
2431
2432
2433
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2445 005522 000004
2446 005524 104413
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2456 005526 013777 001350 173604
2457 005534 013700 001326
2458 005540 005037 001356
2459 005544 005005
2460
2461 005546 012704 177764
2462 005552 012703 000001
2463
2464
2465 005556 005037 001360 1$:
2466
2467 005562 005237 001356
2468 005566 001440
2469 005570 005237 001360 2$:
2470 005574 001441
2471
2472 005576 011001
2473 005600 032701 000400
2474 005604 001771
2475 005606 021001
2476 005610 001362
2477 005612 042701 177760
2478 005616 001357
2479
2480 005620 005204 3$:
2481 005622 001447
2482 005624 005205 4$:
2483 005626 001431
2484 005630 011002
2485 005632 032702 000400

```

```

;* THIS TEST CHECKS THAT THE SECTOR COUNTER CAN COUNT FROM
;* 0-13
;* 1) FIRST, FOR INITIALIZING PURPOSES THERE IS A TIMED LOOP
;* DURING WHICH SECTOR COUNTER SHOULD COUNT DOWN TO 0. IF THIS
;* IS NOT DONE AN ERROR IS REPORTED
;* 2) AFTER A COUNT OF 0 IS REACHED, THE PROGRAM WAITS
;* FOR A CERTAIN TIME, DURING WHICH THE SEC COUNTER
;* IS SAMPLED. IF THE COUNTER DOES NOT CHANGE WITHIN THIS
;* TIME PERIOD AN ERROR IS REPORTED.
;* 3) UPON FINDING THAT THE COUNTER HAS CHANGED, IT IS CHECKED
;* IF IT INCREMENTED CORRECTLY. IF IT DID NOT AN ERROR IS REPORTED
;* 4) IF IT INCREMENTED CORRECTLY, THE PROGRAM AGAIN WAITS IN A
;* LOOP TILL THE COUNTER CHANGES. (STEPS 2,3,4 ARE REPEATED
;* TILL THE COUNTER COUNTS UP TO 13)

```

```

TST7: SCOPE
      CNT.RESET

```

```

;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10

```

```

MOV    DRIVAD,DRKDA
MOV    RKDS,R0
CLR    INDX1
CLR    R5
MOV    #-14,R4
MOV    #1,R3
CLR    INDX2
INC    INDX1
BEQ    6$
INC    INDX2
BEQ    7$
MOV    @R0,R1
BIT    #400,R1
BEQ    2$
CMP    @R0,R1
BNE    1$
BIC    #177760,R1
BNE    1$
INC    R4
BEQ    TST10
INC    R5
BEQ    8$
MOV    @R0,R2
BIT    #400,R2

```

```

;INITIALIZE
;'COUNT' - TO TIME 'ERROR 35'
;INITIALIZE 'COUNT' - TO TIME
;'ERROR 36' (WAIT LOOP)
;INITIALIZE 'COUNT' - FOR THE 12 SECTORS.
;R3 CONTAINS THE 'NEXT' COUNT OF SEC-CNTR
;R1 CONTAINS THE 'PREVIOUS' COUNT OF SEC-CNTR
;R2 CONTAINS THE 'PRESENT' COUNT OF SEC-CNTR
;INITIALIZE 'COUNT' - TO TIME
;(WAIT LOOP) 'ERROR 34'
;KEEP TIMING FOR 'ERROR 35'
;BRANCH & REPORT ERROR IF WAITED LONG?
;KEEP TIMING FOR 'ERROR 34'
;BRANCH & REPORT ERROR IF WAITED LONG?
;GET RKDS
;IS 'SOK' SET?
;NO, WAIT FOR IT TO SET
;MAKE SURE THAT 2 CONSECUTIVE
;READINGS OF SEC-CNTR ARE SAME
;YES, MASK OUT NON-SEC CNTR BITS
;IS IT SECTOR 0, IF NOT LOOP BACK &
;WAIT FOR SECTOR 0
;KEEP TRACK OF SECTORS CHECKED
;EXIT IF ALL SECTORS CHKD
;KEEP TIMING FOR 'ERROR 36'
;BR & REPORT ERROR IF WAITED LONG
;GET RKDS
;IS SOK SET?

```



```

2486 005636 001772      BEQ      4$      ;NO WAIT FOR SOK
2487 005640 021002      CMP      JRO,R2  ;MAKE SURE THAT 2 CONSECUTIVE
2488 005642 001370      BNE      4$      ;READINGS OF SEC-CNTR ARE SAME
2489 005644 042702 177760     BIC      #177760,R2 ;MASK NON-SEC-CNTR BITS
2490 005650 020201      CMP      R2,R1  ;HAS SEC CNTR INCREMENTED?
2491 005652 001764      BEQ      4$      ;NO, WAIT FOR IT TO CHANGE
2492 005654 020203      CMP      R2,R3  ;YES, DID IT INCREMENT CORRECTLY?
2493 005656 001023      BNE      9$      ;NO - REPORT ERROR
2494
2495 005660 005203      5$: INC      R3      ;INCREMENT "NEXT COUNT"
2496 005662 005201      INC      R1      ;INCREMENT "PREVIOUS COUNT"
2497 005664 005005      CLR      R5      ;INITIALIZE AGAIN FOR TIMING 'ERROR 36'
2498 005666 000754      BR       3$      ;GO & CHECK THE NEXT SECTOR COUNT
2499
2500 005670 010137 001162     6$: MOV      R1,$REGO ;GET 'SEC CNTR'
2501 005674 104012      ERROR   12      ;WAITED LONG, BUT SECTOR COUNTER
2502                                     ;DID NOT COUNT TO 0
2503 005676 000421      BR       TST10   ;;EXIT
2504
2505 005700 017737 173422 001162  7$: MOV      JRKDS,$REGO ;GET RKDS
2506 005706 104011      ERROR   11      ;WAITED LONG, BUT 'SOK' BIT DID
2507                                     ;NOT SET
2508 005710 000414      BR       TST10   ;;EXIT
2509
2510 005712 010237 001162     8$: MOV      R2,$REGO ;GET SEC CNTR (PRESENT COUNT)
2511 005716 010337 001164     MOV      R3,$REG1 ;GET "NEXT COUNT"
2512 005722 104013      ERROR   13      ;WAITED LONG, BUT THE SECTOR
2513                                     ;COUNTER DID NOT INCREMENT FROM
2514                                     ;THE PRESENT COUNT TO THE NEXT COUNT
2515 005724 000406      BR       TST10   ;;EXIT
2516
2517 005726 010337 001162     9$: MOV      R3,$REGO ;GET 'NEXT COUNT' (SEC CNTR SHOULD BE THIS)
2518 005732 010237 001164     MOV      R2,$REG1 ;GET PRESENT COUNT (WHAT SEC CNTR WAS)
2519 005736 104014      ERROR   14      ;SEC CNTR INCREMENTED WRONG, DID
2520                                     ;NOT INCREMENT FROM PRESENT COUNT
2521                                     ;TO NEXT COUNT
2522 005740 000747      BR       5$
2523 ;
2524
2525 ;*****
2526 ;*TEST 10 CHECK THAT SC=SA CAN BE GENERATED
2527 ;* THIS TEST CHECKS THAT SC=SA CAN BE GENERATED FOR
2528 ;* EVERY SECTOR
2529 ;*****
2530 005742 000004     †ST10: SCOPE
2531 005744 104413     CNT.RESET      ;GO, DO CONTROL RESET
2532                                     ;THIS IS A CALL FOR THE 'CNTRL-
2533                                     ;RESET' ROUTINE. A CONTROL RESET IS
2534                                     ;ISSUED AND AFTER A CERTAIN TIME
2535                                     ;IF THE 'CNTRL RDY' DOES NOT SET
2536                                     ;AN ERROR IS REPORTED. NOTE THAT
2537                                     ;THE PC IN ERROR MESSAGE IS THE
2538                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
2539                                     ;THIS IS A VERY BASIC ERR & IF IT
2540                                     ;OCCURS GO BACK TO TEST 10
2541 005746 013704 001350     MOV      DRIVAD,R4

```



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2606 006110 013700 001332          MOV    RKCS,R0
2607 006114 005004          CLR    R4
2608 006116 013777 001350 173214  MOV    DRIVAD,DRKDA
2609 006124 012710 000015          MOV    #15,DR0
2610 006130 104412          CHKCRDY
2611
2612 006132 104021          ERROR  21
2613
2614
2615
2616 006134 012705 177776 25:      MOV    #-2,R5
2617 006140 032777 000100 173160 65:      BIT    #100,DRKDS
2618 006146 001402          BEQ
2619 006150 000137 006172          JMP    3$
2620 006154 005204          INC    R4
2621 006156 001370          BNE   6$
2622 006160 005205          INC    R5
2623 006162 001366          BNE   6$
2624 006164 004737 020770          JSR    PC,GT4RG
2625 006170 104026          ERROR  26
2626
2627
2628 006172 032777 001000 173126 3$:      BIT    #1000,DRKDS
2629 006200 001403          BEQ    5$
2630 006202 004737 020770          JSR    PC,GT4RG
2631 006206 104001          ERROR  1
2632
2633 006210 032710 140000 5$:      BIT    #140000,DR0
2634 006214 001403          BEQ    4$
2635 006216 004737 020770          JSR    PC,GT4RG
2636 006222 104022          ERROR  22
2637
2638 006224 022710 000214 4$:      CMP    #214,DR0
2639
2640 006230 001406          BEQ    TST13
2641 006232 012737 000214 001162  MOV    #214,$REG0
2642 006240 011037 001164          MOV    DR0,$REG1
2643 006244 104024          ERROR  24
2644
2645
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2653 006246 000004          TST13: SCOPE

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```

;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10
;INITIALIZ COUNT - TO TIME ERROR
;ADDRESS THE DRIVE
;'DRIVE RESET' GO
;GO CHECK IF CONTROL RDY IS SET
;IF SO, SKIP THE EROR MESSAGE.
;CNTRL RDY DID NOT SET AFTER
;SENDING CYL ADDR TO THE DRIV.
;'ADD ACK' SHOULD HAVE COME BACK
;FROM DRIVE. THEREUPON SETTING 'CN RDY'
;SET UP DELAY COUNTER
;CHECK FOR R/W/S READY
;GO, GET RKCS, ER, DS, DA
;R/W/S RDY DID NOT SET AFTER
;DRIVE RESET
;DID SIN SET?
;NO, BRANCH
;GO, GET RKCS, ER, DS, DA
;SIN SET, AFTER A
;DRIVE RESET.
;WAS 'ERR' BIT OR 'HE' BIT SET?
;NO
;GO, GET RKCS, ER, DS, DA
;'ERR' OR 'HE' BIT SET WHILE DOING
;DRIVE RESET
;DOES RKCS STILL CONTAIN THE
;'DRIV RES' BITS
;YES, EXIT
;GET EXPCTD RKCS
;GET RKCS, RECVD
;NO - RKCS SHOULD CONTAIN THE 'DRIV RES'
;FUNCTION, ERROR IF DIFFERENT.

```

```

;*****
;*TEST 13 CHECK 'SEEK' TO CYLINDER 0
;* THIS TEST CHECKS THE SEEK LOGIC DOING SEEK TO CYLINDER 0.
;* NOTE THAT SINCE THE HEADS ARE ALREADY ON CYLINDER 0, NO
;* HEAD MOVEMENT IS INVOLVEDN AND THE STRESS IS ON THE 'BASIC SEEK
;* LOGIC.
;*****

```

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2654 006250 104413          CNT.RESET          ;GO, DO CONTROL RESET
2655                                     ;THIS IS A CALL FOR THE 'CNTRL-
2656                                     ;RESET' ROUTINE. A CONTROL RESET IS
2657                                     ;ISSUED AND AFTER A CERTAIN TIME
2658                                     ;IF THE 'CNTRL RDY' DOES NOT SET
2659                                     ;AN ERROR IS REPORTED. NOTE THAT
2660                                     ;THE PC IN ERROR MESSAGE IS THE
2661                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
2662                                     ;THIS IS A VERY BASIC ERR & IF IT
2663                                     ;OCCURS GO BACK TO TEST 10
2664 006252 104421          TST.SIN          ;GO CHECK IF SIN SET. IF SET
2665                                     ;A DO DRIVE RESET TO CLEAR IT
2666 006254 013700 001332    MOV          RKCS,RO
2667 006260 013777 001350 173052 MOV          DRIVAD,ARKDA ;ADDRESS THE DRIVE
2668                                     ;
2669 006266 012710 000011    MOV          #11,ARD
2670 006272 104412          CHKCRDY          ;'SEEK' GO
2671                                     ;GO CHECK IF CONTROL RDY IS SET
2672 006274 104021          ERROR          21 ;IF SO, SKIP THE EROR MESSAGE.
2673                                     ;'CNTRL RDY' DID NOT SET AFTER SENDING
2674                                     ;CYL ADDR TO THE DRIVE, 'ADD ACK'
2675                                     ;SHOULD HAVE COME BACK FROM THE
2676                                     ;DRIVE, THEREUPON SETTING 'CNTRL RDY'
2676 006276 005005          2$: CLR          R5
2677 006300 032777 000100 173020 BIT          #100,ARKDS ;DID R/W/S RDY BIT SET?
2678 006306 001005          BNE          3$ ;YES, BRANCH
2679 006310 005205          INC          R5 ;WAITED LONG ENOUGH?
2680 006312 001372          BNE          2$+2 ;IF NOT, LUP BAK & WAIT
2681 006314 004737 020770    JSR          PC,GT4RG ;GO, GET RKCS, ER, DS, DA
2682 006320 104026          ERROR          26 ;R/W/S RDY DID NOT SET AFTER SEEK
2683 006322 032777 001000 172776 3$: BIT          #1000,ARKDS ;DID SIN SET?
2684 006330 001403          BEQ          6$ ;NO, BRANCH
2685 006332 004737 020770    JSR          PC,GT4RG ;GO, GET RKCS, ER, DS, DA
2686 006336 104001          ERROR          1 ;SIN SET ON DOING SEEK
2687                                     ;TO CYL 0 NOTE THIS IS THE
2688                                     ;FIRST TIME THE HEADS HAVE
2689                                     ;BEEN MOVED
2690
2691 006340 032710 140000          6$: BIT          #140000,ARD ;WAS 'ERR' OR 'HE' BIT SET?
2692 006344 001403          BEQ          4$
2693
2694 006346 004737 020770    JSR          PC,GT4RG ;GO, GET RKCS, ER, DS, DA
2695 006352 104022          ERROR          22 ;'ERR' OR 'HE' BIT SET WHILE DOING 'SEEK'
2696
2697 006354 005777 172750          4$: TST          ARKER ;WAS ANY BIT IN RKER SET?
2698 006360 001403          BEQ          5$ ;NO
2699 006362 004737 020776    JSR          PC,GT3RG ;GO, GET RKCS, ER, DS
2700 006366 104023          ERROR          23 ;RKER SHOWS AN ERROR BIT, CHECK
2701
2702 006370 022710 000210          5$: CMP          #210,ARD ;DOES RKCS STILL CONTAIN 'SEEK' FUNCTION
2703 006374 001406          BEQ          TST14 ;: YES, EXIT
2704 006376 012737 000210 001162 MOV          #210,$REG0 ;GET EXPCTD RKCS
2705 006404 011037 001164    MOV          ARD,$REG1 ;GET RKCS RECVD
2706 006410 104024          ERROR          24 ;NO, RKCS SHOULD BE STILL CONTAINING
2707                                     ;'SEEK' FUNCTION ERROR - IF IT CHANGED
2708
2709                                     ;:*****

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2719 006412 000004
2720 006414 104413
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2730 006416 104421
2731
2732 006420 004737 021500
2733 006424 104026
2734
2735 006426 005005
2736 006430 013777 001350 172702
2737 006436 052777 000100 172674
2738 006444 013701 001326
2739 006450 012777 000011 172654
2740 006456 032711 000100 1\$:
2741 006462 001405
2742 006464 005205
2743 006466 100373
2744 006470 004737 020776
2745 006474 104025
2746
2747
2748 006476 004737 021432 2\$:
2749 006502 104016
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;*TEST 14 CHECK R/W/S RDY IS CLEAR WHEN HEADS ARE IN MOTION
;*THIS TEST CHECKS THAT R/W/S DOES GET CLEARED
;*WHEN THE HEADS ARE IN MOTION. SINCE 'MOVE L' ON
;*M7700 (RK05) GENERATES THIS SIGNAL, ABSENCE OF
;*R/W/S RDY-CLEAR COULD MEAN A FAULT ON M7702
;*WHERE 'MOVE L' IS GENERATED.
;*NOTE THIS IS THE FIRST TIME HEADS ARE MADE 'TO MOVE BY SEEKING
;*TO CYLINDER 2.

::*****

TST14: SCOPE
CNT.RESET

;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10
;GO CHECK IF SIN IS SET
;IF SET DO DRV-RESET TO CLR IT
;MAKE SURE HEADS R ON CYL 0
;R/W/S RDY DIDN'T SET
;AFTER THE ABOVE DRV RESET

TST.SIN

JSR PC,DRESET
ERROR 26
CLR R5
MOV DRIVAD,ARKDA
BIS #100,ARKDA
MOV RKDS,R1
MOV #11,ARKCS
BIT #100,ARI
BEQ 2\$
INC R5
BPL 1\$
JSR PC,GT3RG
ERROR 25

;SEEK CYLINDER 2

;SEEK, GO
;DID R/W/S RDY CLR?
;YES, BRANCH

;R/W/S RDY WAS NOT CLEAR WHEN HEADS
;WERE SEEKING TO CYLINDER 2

;GO, WAIT FOR R/W/S RDY TO SET
;R/W/S RDY DID NOT SET AFTER SEEK
;WAS TRIED TO CYLINDER 2 (ABOVE).
;NOTE THIS WAS THE FIRST TIME A SEEK
;WAS TRIED TO A CYLINDER OTHER THAN
;0.

::*****

;*TEST 15 CHECK 'WRITE' FORMAT FUNCTION-CYLINDER 0, SECTOR 0
;*THIS TEST CHECKS THE LOGIC INVOLVED IN THE WRITE FMT
;*FUNCTION. ON ISSUING A WRT FMT, THE FOLLOWING IS CHECKED
;*1) CNTRL RDY WAS CLEARED AS GO WAS SET.
;*2) CNTRL RDY SETS WITHIN A CERTAIN TIME ON COMPLETION OF FUNCTION
;*3) IF 'HE' OR 'ERR' BIT SET?
;*4) IF RKDA INCREMENTED CORRECTLY FROM 0 TO 1?

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2773 006504 000004
2774 006506 104413
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2784 006510 104421
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2786 006512 012703 033336
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2800 006516 012700 000001
2801
2802 006522 010023
2803 006524 010013
2804 006526 005423
2805 006530 005200
2806 006532 022700 000200
2807 006536 001371
2808 006540 005023
2809 006542 012713 125252
2810
2811 006546 012703 033336
2812 006552 013701 001332
2813 006556 013702 001336
2814 006562 010312
2815 006564 012777 177400 172542
2816 006572 013777 001350 172540
2817 006600 012711 002003
2818
2819 006604 105711
2820 006606 100003
2821 006610 004737 020776

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;*5) IF RKWC OVERFLOWED CORRECTLY TO 0?
;*6) IF RKBA INCREMENTED CORRECTLY BY 2?
;*7) IF ANY BIT IN RKER SET?
;*8) IF THE 'WRT FMT' FUNCTION BITS ARE STILL IN THE RKCS?
;*NOTE THAT ONE WORD '125252' WAS WRITTEN ON SECTOR
;*0 & IT WILL BE CHECKED IN THE NEXT TESTS.
*****
†ST15: SCOPE
CNT.RESET
;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10
;GO CHECK IF SIN IS SET
;IF SET, DO DRIVE RESET TO CLR IT
;THIS CODE SETS UP A 256 WORD BUFFER
;WHICH WILL BE USED TO WRITE 1 SECTOR
;ON THE DISK
;1ST WORD 000001
;2ND WORD 177777 2'S COMPLEMENT
;3RD WORD 000002 OF ABOVE
;4TH WORD 177776
;253RD WORD 000177
;254TH WORD 177601
;255TH WORD 000000
;256TH WORD 125252
MOV #1, R0 ;SET COUNT
9$: MOV R0, (R3)+ ;SET UP DATA WORDS
MOV R0, (R3)
NEG (R3)+
INC R0
CMP #200, R0 ;DONE?
BNE 9$
CLR (R3)+ ;SET 255TH WORD TO 0
MOV #125252, R3 ;SET 256TH WORD
MOV #OUTBUF, R3 ;RESET POINTER TO OUTBUF
MOV RKCS, R1
MOV RKBA, R2
MOV R3, R2
MOV #-400, R1 ;FROM HERE-SET UP CURRENT ADDRESS
MOV DRIVAD, R1 ;SET UP WORD COUNT 400 WORDS
MOV DRIVAD, R1 ;SET UP DISK ADDR, SECTOR 0, CYLINDER 0
MOV #2003, R1 ;WRITE FORMAT, GO
1$: TSTB R1 ;WAS 'CNTRL RDY' CLEARED AS GO WAS SET?
BPL 2$ ;YES, BRANCH
JSR PC, GT3RG ;GO, GET RKCS, ER, DS

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2822 006614 104030          ERROR 30          ;'CNTRL RDY' DIDN'T CLEAR AS GO
2823                                     ;WAS SET TO 'WRITE FORMAT'
2824 006616 005000          2$: CLR      RO          ;WAS 'CNTRL RDY' SET ON COMPLETION OF WRITE?
2825 006620 105711          TSTB   DR1          ;YES, BRANCH
2826 006622 100411          BMI     3$          ;NO, HAVE U WAITED LONG ENOUGH?
2827 006624 005200          INC     RO          ;IF NOT, LOOP BACK & WAIT
2828 006626 001374          BNE    2$+2        ;IF YES, REPORT ERROR
2829                                     ;GO, GET RKCS, ER, DS, DA
2830 006630 004737 020770    JSR     PC,GT4RG
2831 006634 013737 001350 001202  MOV    DR1VAD,$REG10
2832 006642 104416          BRKDA4
2833                                     ;GO TO 'BDAY' & BREAK CONTENTS OF
2834 006644 104031          ERROR 31          ;$REG10 INTO DR # CYL,SUR,SEC BITS
2835                                     ;'CNTRL RDY' DIDN'T SET ON COMPLETION
2836                                     ;OF WRITE FORMAT
2837                                     ;WRT FMT WAS DONE STARTING AT <DSK-ADRES>
2838 006646 004737 021230          3$: JSR     PC,CHKHE ;INDICATED IN EROR MSGE.
2839                                     ;GO CHECK IF 'HE' OR 'ERR' BIT SET,
2840                                     ;IF YES, SAVE RKCS, ER, DS, DA.
2841 006652 104032          ERROR 32          ;RETURN HERE IF ERROR.
2842                                     ;'HE' OR 'ERR' BIT SET WHILE DOING
2843                                     ;A WRITE FORMAT
2844                                     ;WRT FMT WAS DONE STARTING AT <DSK-ADRES>
2845 006654 004737 021256          4$: JSR     PC,CHKDA ;INDICATED IN EROR MSGE.
2846                                     ;GO CHECK IF RKDA INCREMENTED CORRECTLY
2847 006660 104033          ERROR 33          ;IF NOT, RETURN HERE.
2848                                     ;RKDA SHOULD HAVE INCREMENTED BY
2849 006662 004737 021312          5$: JSR     PC,CHKWC ;1 SECTOR, IT DID NOT
2850                                     ;CHECK IF WORD COUNT OVERFLOWED, IF
2851 006666 104034          ERROR 34          ;NOT RETURN HERE.
2852                                     ;RKWC DID NOT OVERFLOW TO 0, AFTER
2853 006670 022712 034336          6$: CMP     #OUTBUF+1000,DR2 ;XFER ON WRITE FORMAT
2854 006674 001406          BEQ    7$          ;DID RKBA INCREMENT CORRECTLY?
2855 006676 012737 034336 001162  MOV    #OUTBUF+1000,$REG0 ;YES, BRANCH
2856 006704 011237 001164          MOV    DR2,$REG1  ;GET EXPCTD RKBA
2857 006710 104035          ERROR 35          ;GET ACTUAL RKBA
2858                                     ;RKBA DIDN'T INCREMENT BY 1000 AFTER
2859 006712 004737 021336          7$: JSR     PC,CHKER ;WRITE FORMAT OF 400 WORDS
2860                                     ;CHECK IOF ANY BIT IN RKER SET,
2861 006716 104036          ERROR 36          ;IF YES RETURN HERE.
2862                                     ;RKER BIT SET ON DOING 1 WORD
2863 006720 022711 002202          8$: CMP     #2202,DR1 ;WRITE FORMAT
2864 006724 001406          BEQ    TST16       ;DOES RKCS STILL HAVE 'WRT FMT' BITS?
2865 006726 012737 002202 001162  MOV    #2202,$REG0 ;YES, EXIT
2866 006734 011137 001164          MOV    DR1,$REG1  ;GET EXPCTD RKCS
2867 006740 104024          ERROR 24          ;GET ACTUAL RKCS
2868                                     ;RKCS DIDN'T CONTAIN 'WRT FMT' BITS
2869                                     ;AFTER THE FUNCTION WAS COMPLETED

```

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2870 ;*****
2871 ;*TEST 16 CHECK 'READ FORMAT' FUNCTION-CYLINDER 0, SECTOR 0
2872 ;*THIS TEST CHECKS THE LOGIC INVOLVED IN THE WRITE FMT
2873 ;*FUNCTION. ON ISSUING A WRT FMT, THE FOLLOWING IS CHECKED
2874 ;*1) CNTRL RDY WAS CLEARED AS GO WAS SET.
2875 ;*2) CNTRL RDY SETS WITHIN A CERTAIN TIME ON COMPLETION OF FUNCTION
2876 ;*3) IF 'HE' OR 'ERR' BIT SET?
2877 ;*4) IF RKDA INCREMENTED CORRECTLY FROM 0 TO 1?

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2891 006742 000004
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2893 006746 104413
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2903 006750 104421
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2905 006752 013701 001332
2906 006756 013702 001336
2907 006762 012703 033336
2908 006766 010312
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2910 006770 012777 177777 172336
2911 006776 013777 001350 172334
2912 007004 012711 002005
2913
2914 007010 105711
2915 007012 100003
2916 007014 004737 020776
2917 007020 104030
2918
2919 007022 005000
2920 007024 105711
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2922 007026 100411
2923 007030 005200
2924 007032 001374
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2926 007034 004737 020770
2927 007040 013737 001350 001202
2928 007046 104416
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2930 007050 104045
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;*5) IF RKWC OVERFLOWED CORRECTLY TO 0?
;*6) IF RKBA INCREMENTED CORRECTLY BY 2?
;*7) IF ANY BIT IN RKER SET?
;*8) IF THE CORRECT HEADER WAS RECEIVED?
;*9) FOR RK11C, AFTER RD FMT RKDB CONTAINS THE CHECKSUM
;*FOR THAT SECTOR. (125252 IN THIS CASE, BECAUSE THE
;*FIRST WORD IN SEC 0 WAS WRITTEN AS 125252 IN
;*THE PREVIOUS TEST)
;*10) FOR RK11D, AFTER RD FMT RKDB SHOULD CONTAIN
;*A ZERO
;*11) IF THE RD FMT FUNCTION BITS ARE STILL IN
;*THE RKCS?

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↑TST16: SCOPE
CLR RD
CNT.RESET

```

```

;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10
;GO CHECK IF SIN IS SET
;IF SET, DO DRIVE RESET TO CLR IT

```

TST.SIN

```

MOV RKCS,R1
MOV RKBA,R2
MOV #OUTBUF,R3
MOV R3,R2
MOV #-1,RKWC
MOV DRIVAD,RKDA
MOV #2005,R1

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```

;SETUP ADRS WHERE HEADER WORD IS TO BE
;X-FERRED
;SET UP WORD COUNT
;SET UP DISK ADRS, SECTOR 0, CYLINDER 0
;READ FORMAT, GO

```

```

1$: TSTB R1
BPL 2$
JSR PC,GT3RG
ERROR 30

```

```

;WAS 'CNTRL RDY' CLEARED AS GO WAS SET?
;YES, BRANCH
;GO, GET RKCS, RKER
;CNTRL RDY DIDN'T CLEAR AS GO WAS
;SET TO 'READ FORMAT'

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```

2$: CLR RD
TSTB R1

```

```

;WAS 'CNTRL RDY' SET ON COMPLETION OF
;TRANSFER
;YES, BRANCH
;NO, HAVE U WAITED LONG ENOUGH?
;IF NOT, LOOP BACK & WAIT
;IF YES, REPORT ERROR
;GO, GET RKCS, ER, DS,DA

```

```

BMI 3$
INC RD
BNE 2$+2
JSR PC,GT4RG
MOV DRIVAD,$REG10
BRKDA4

```

```

;GO TO 'BDAY' & BREAK CONTENTS OF
;$REG10 INTO DR #,CYL,SUR,SEC BITS
;'CNTRL RDY' DIDN'T SET ON COMPLETION
;OF READ FORMAT
;READ FMT WAS DONE STARTING AT <DSK-ADRES>
;INDICATED IN EROR MESGE

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ERROR 45

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2934 007052 004737 021230 3$: JSR PC,CHKHE ;CHECK IF 'ERR' OR 'HE' BIT SET, IF
2935 ;YES RETURN HERE.
2936 007056 104046 ERROR 46 ;'HE' OR 'ERR' BIT SET WHILE
2937 ;DOING A 'READ FORMAT'
2938 ;READ FMT WAS DONE STARTING AT <DSK-ADRES>
2939 ;INDICATED IN EROR MESGE
2940 007060 004737 021256 4$: JSR PC,CHKDA ;CHECK IF RKDA INCREMENTED CORRECTLY
2941 ;IF NOT, RETURN HERE.
2942 007064 104040 ERROR 40 ;RKDA SHOULD HAVE INCREMENTED
2943 ;BY 1 SECTOR, IT DID NOT
2944
2945 007066 004737 021312 5$: JSR PC,CHKWC ;CHECK IF RKWC OVERFLOWED TO 0, IF
2946 ;NOT RETURN HERE.
2947 007072 104041 ERROR 41 ;RKWC DID NOT OVERFLOW TO 0
2948 ;AFTER XFER ON READ FORMAT
2949 007074 022712 033340 6$: CMP #OUTBUF+2,AR2 ;DID RKBA INCREMENT TO NXT WORD ADDRS?
2950 007100 001406 BEQ 7$ ;YES, BRANCH
2951 007102 012737 033340 001162 MOV #OUTBUF+2,$REG0 ;GET EXPCTD RKBA
2952 007110 011237 001164 MOV AR2,$REG1 ;GET ACTUAL RKBA
2953 007114 104042 ERROR 42 ;RKBA DIDN'T INCREMENT BY 2 AFTER
2954 ;'READ FORMAT' OF 1 WORD
2955 007116 004737 021336 7$: JSR PC,CHKER ;CHECK IF ANY BIT IN RKER SET, IF
2956 ;YES RETURN HERE.
2957 007122 104036 ERROR 36 ;RKER BIT SET ON DOING
2958 ;1 WORD READ FORMAT
2959 007124 005713 8$: TST AR3 ;DOES OUTBUF CONTAIN THE HEADER
2960 ;WORD-0
2961 007126 001407 BEQ 9$ ;YES, BRANCH
2962 007130 005037 001162 CLR $REG0 ;GET SECTOR NO.
2963 007134 005037 001164 CLR $REG1 ;EXPCTD HEADER
2964 007140 011337 001166 MOV AR3,$REG2 ;GET HEADER RECVD
2965 007144 104043 ERROR 43 ;CORRECT HEADER WORD-0-WAS
2966 ;NOT RECEIVED ON READ FORMAT
2967 007146 022711 002204 9$: CMP #2204,AR1 ;DOES RKCS HAVE THE 'RDFMT' BITS?
2968 007152 001406 BEQ TST17 ;YES, BRANCH
2969 007154 012737 002204 001162 MOV #2204,$REG0 ;GET EXPCTD RKCS
2970 007162 011137 001164 MOV AR1,$REG1 ;GET ACTUAL RKCS
2971 007166 104024 ERROR 24 ;RKCS DIDN'T CONTAIN 'RD FMT'
2972 ;BITS AFTER FUNCTION WAS
2973 ;COMPLETED
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*****
; *TEST 17 CHECK 'READ' FUNCTION-CYLINDER 0, SECTOR 0
; *THIS IS THE FIRST TIME A PURE READ IS PREFORMED IN THIS
; *TEST SEQUENCE. THE FOLLOWING IS CHECKED
; *1) CNTRL RDY CLEARS AS GO IS SET
; *2) CNTRL RDY SETS WITHIN A CERTAIN TIME ON COMPLETION
; *OF FUNCTION
; *3) IF 'HE' OR 'ERR' BIT SET?
; *4) IF RKDA INCREMENTED CORRECTLY?
; *5) IF RKWC OVERFLOWED TO 0?
; *6) IF RKBA INCREMENTED CORRECTLY?
; *7) IF ANY RKER BIT SET?
; *8) IF THE CORRECT PSUEDO-HEADER (FIRST WORD) WAS
    
```

```

2990 ;*READ FROM SECTOR 0
2991 ;*9) IF THE 'READ' FUNCTION BITS ARE STILL IN RKCS
2992 ;*****
2993 007170 000004
2994 007172 104413
2995
2996
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2998
2999
3000
3001
3002
3003
3004 007174 104421          TST.SIN
3005
3006 007176 013701 001332    MOV      RKCS,R1
3007 007202 005000          CLR      R0
3008 007204 013702 001336    MOV      RKBA,R2
3009 007210 012703 033336    MOV      #OUTBUF,R3
3010 007214 010312          MOV      R3,R2
3011
3012 007216 012777 177400 172110  MOV      #-400,DRKWC
3013 007224 013777 001350 172106  MOV      DRIVAD,DRKDA
3014 007232 012711 000005          MOV      #5,DR1
3015
3016 007236 105711          1$:      TSTB   DR1
3017 007240 100003          BPL     2$
3018 007242 004737 020776    JSR     PC,GT3RG
3019 007246 104030          ERROR  30
3020
3021 007250 005000          2$:      CLR     R0
3022 007252 105711          TSTB   DR1
3023
3024 007254 100411          BMI     3$
3025 007256 005200          INC     R0
3026 007260 001374          BNE     2$+2
3027
3028 007262 004737 020770    JSR     PC,GT4RG
3029 007266 013737 001350 001202  MOV      DRIVAD,$REG10
3030 007274 104416          BRKDA4
3031
3032 007276 104045          ERROR  45
3033
3034
3035
3036
3037 007300 004737 021230    3$:      JSR     PC,CHKHE
3038
3039 007304 104046          ERROR  46
3040
3041
3042
3043 007306 004737 021256    4$:      JSR     PC,CHKDA
3044
3045 007312 104040          ERROR  40

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;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10
;GO CHECK IF SIN IS SET
;IF SET, DO DRIVE RESET TO CLR IT

;SET UP ADDRS WHERE DATA WORD IS
;TO BE X-FERRED
;SET UP WORD COUNT
;SET UP DISK ADRS, SECTOR 0, CYLINDER 0
;READ, GO

;WAS 'CNTRL RDY' CLEARED AS GO WAS SET?
;YES, BRANCH
;GO, GET RKCS, ER
;CNTRL RDY DID NOT CLEAR AS GO
;WAS SET TO 'READ'

;WAS CNTRL RDY SET ON COMPLETION
;OF TRANSFER?
;YES, BRANCH
;NO, HAVE U WAITED LONG ENOUGH?
;IF NOT, LOOP BACK & WAIT
;IF YES, REPORT ERROR
;GO, GET RKCS, ER, DS,DA

;GO TO 'BDA4' & BREAK CONTENTS OF
;$REG10 INTO DR #,CYL,SUR,SEC BITS
;CNTRL RDY DID NOT SET ON
;COMPLETION OF READ
;READ WAS DONE STARTING AT <DSK-ADRES>
;INDICATED IN EROR MESGE

;CHECK IF 'ERR' OR 'HE' BIT IS SET
;IF YES, RETURN HERE.
;'HE' OR 'ERR' BIT SET WHILE
;DOING A READ.
;READ WAS DONE STARTING AT <DSK-ADRES>
;INDICATED IN EROR MESGE
;CHECK IF RKDA INCREMENTED CORRECTLY.
;IF NOT RETURN HERE.
;RKDA DID NOT INCREMENT

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3046
3047 007314 004737 021312 5$: JSR PC,CHKWC ;BY 1 (SECTOR)
3048 ;CHECK IF RKWC OVERFLOWED TO 0,
3049 007320 104041 ERROR 41 ;IF NOT RETURN HERE.
3050 ;RKWC DID NOT OVERFLOW TO 0,
3051 007322 022712 034336 6$: CMP #OUTBUF+1000,AR2 ;AFTER X-FER ON READ
3052 007326 001406 BEQ 7$ ;DID RKBA INCREMENT CORRECTLY?
3053 007330 012737 034336 001162 MOV #OUTBUF+1000,$REG0 ;YES, BRANCH
3054 007336 011237 001164 MOV AR2,$REG1 ;GET EXPCTD RKBA
3055 007342 104042 ERROR 42 ;GET ACTUAL RKBA
3056 ;RKBA DID NOT INCREMENT BY 2
3057 007344 004737 021336 7$: JSR PC,CHKER ;AFTER 'READ' OF 1 WORD
3058 ;CHECK IF ANY BIT IN RKER SET,
3059 007350 104036 ERROR 36 ;IF YES RETURN HERE.
3060 ;RKER BIT SET ON DOING 1
3061 007352 022713 000001 8$: CMP #1,AR3 ;WORD 'READ'
3062 ;DOES OUTBUF CONTAIN THE RIGHT
3063 007356 001411 BEG 9$ ;DATA WORD
3064 007360 012737 000001 001162 MOV #1,$REG0 ;YES BRANCH
3065 007366 011337 001164 MOV (R3),$REG1 ;GET EXPCTD DATA WORD
3066 007372 013737 001350 001166 MOV DRIVAD,$REG2 ;GET RECD DATA WORD
3067 007400 104044 ERROR 44 ;GET DISK ADRS FROM WHICH READ WAS DONE
3068 ;DID NOT READ THE CORRECT
3069 ;DATA WORD--FROM DISK ADRES.
3070 ;SEC 0, CYL 0, SUR 0
3071
3072 ;AFTER 1 SECTOR READ RKDB CONTAINS
3073 ;FOR RK11C
3074 ;THE CHECKSUM FOR THAT SECTOR
3075 ;FOR RK11D
3076 ;THE LAST WORD TRANSFERRED TO MEMORY
3077
3078 ;IT SO HAPPENS THAT WITH THE SECTOR
3079 ;THAT WAS READ, RKDB CONTAINS THE
3080 ;SAME INFORMATION FOR BOTH RK11C
3081 ;AND RK11D
3082 007402 022777 125252 171732 9$: CMP #125252,ARKDB ;DOES RKDB CONTAIN THE EXPCTD WORD?
3083 007410 001407 BEQ 10$ ;YES, BRANCH
3084 007412 012737 125252 001162 MOV #125252,$REG0 ;GET EXPCTD RKDB
3085 007420 017737 171716 001164 MOV ARKDB,$REG1 ;GET RECD RKDB
3086 007426 104037 ERROR 37 ;RKDB DOES NOT CONTAIN THE
3087 ;EXPCTD WORD AFTER A READ OF SEC 0
3088 ;CYL 0
3089 007430 022711 000204 10$: CMP #204,AR1 ;DOES RKCS HAVE THE 'READ' BITS?
3090 007434 001406 BEQ 11$ ;YES, BRANCH
3091 007436 012737 000204 001162 MOV #204,$REG0 ;GET EXPCTD RKCS
3092 007444 011137 001164 MOV AR1,$REG1 ;GET RECD RKCS
3093 007450 104024 ERROR 24 ;RKCS DID NOT CONTAIN 'READ'
3094 ;FUNCTION BITS AFTER OPERATION
3095 ;WAS COMPLETED
3096 007452 104413 11$: CNT.RESET ;GO DO CONTROL RESET
3097 007454 005777 171662 TST ARKDB ;DID CONTROL RESET CLEAR RKDB?
3098 007460 001407 BEQ TST20 ;YES, EXIT
3099 007462 013737 001342 001164 MOV RKDB,$REG1 ;GET ADRES OF RKDB
3100 007470 017737 171646 001164 MOV ARKDB,$REG1 ;GET CONTENTS OF RKDB
3101 007476 104102 ERROR 102 ;CONTROL RESET DIDN'T CLR RKDB

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007500 000004
007502 013703 001332
007506 012702 177764
007512 013704 001340
007516 013701 001350
007522 010105
007524 005205
007526 012737 007534 001110
007534 104413
007536 104421
007540 005000
007542 010137 033336
007546 012777 033336 171562
007554 012777 177777 171552
007562 010114
007564 012713 002003
007570 105777 171536
007574 100410
007576 005200
007600 001373
007602 004737 020770
007606 010137 001202

```
*****  
: *TEST 20 CHECK 'WRITE FORMAT' -CYLINDER 0, SECTOR 0-13  
: *THIS TEST GOES ONE STEP FURTHER & PERFORMS A WRT  
: *FMT ON CYLINDER 0 & CHECKS THE FOLLOWING  
: *1) IF CNTRL RDY SET WITHIN A CERTAIN TIME ON COMPLETION  
: *OF THE FUNCTION  
: *2) IF 'HE' OR 'ERR' BIT SET?  
: *3) IF THE RKDA INCREMENTS CORRECTLY?  
: *4) IF THE RKDB IS CLEAR?  
: *WRT FMT IS DONE ONE SECTOR AT A TIME  
: *THE FIRST WORD OF EVERY SECTOR IS WRITTEN AS A  
: *PSUEDO-HEADER CONSISTING OF DRIVE #, CYLINDER #, SURFACE  
: * & SECTOR #. THIS WILL BE READ & CHECKED IN THE FOLLOWING TEST.  
*****  
TST20: SCOPE  
MOV RKCS,R3  
MOV #-14,R2 ;SET UP COUNT FOR 12 SECTORS  
MOV RKDA,R4  
MOV DRIVAD,R1 ;GET DRIVE ADDRESS  
MOV R1,R5 ;STORE IT  
INC R5  
MOV #1$, $LPERR ;SET RETURN ADRES FOR LUPING  
ON ERROR (SW 9)  
GO DO CONTROL RESET  
THIS IS A CALL FOR THE 'CNTRL-  
RESET' ROUTINE. A CONTROL RESET IS  
ISSUED AND AFTER A CERTAIN TIME  
IF THE 'CNTRL RDY' DOES NOT SET  
AN ERROR IS REPORTED. NOTE THAT  
THE PC IN ERROR MESSAGE IS THE  
PC WHERE 'CNT.RESET' IS LOCATED.  
THIS IS A VERY BASIC ERR & IF IT  
OCCURS GO BACK TO TEST 10  
GO CHECK IF SIN IS SET  
IF SET, DO DRIVE RESET TO CLR IT  
TST.SIN  
CLR R0  
MOV R1,OUTBUF ;THIS WORD TO BE X-FERRED. FIRST  
;WORD OF EACH SECTOR WILL BE THE  
;ACTUAL DRIVE-ADDRS CONSISTING OF  
;DRIVE NO, CYL ADDR, SURFACE  
;SECTOR NO.  
MOV #OUTBUF,ARKBA ;ADRS FROM WHICH DATA WORD IS TO  
;X-FERRED  
MOV #-1,ARKWC ;SET UP WORD COUNT  
MOV R1,AR4 ;ADRS THE DRIVE, CYL 0, & CORRECT SECTOR  
MOV #2003,AR3 ;WRITE FORMAT, GO  
2$: TSTB ARKCS ;DID 'CNTRL RDY' SET?  
BMI 3$ ;YES, BRANCH  
INC R0 ;NO, HAVE U WAITED LONG?  
BNE 2$ ;IF NOT, LOOP BACK & WAIT  
;IF YES, REPORT ERROR  
JSR PC,GT4RG ;GO GET RKCS, ER, DS, DA  
MOV R1,$REGIO ;GET DISK ADRS (UNIT,CYL,SUR,SEC) TO WHICH  
;WRITE FORMAT WAS DONE
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3158 007612 104416          BRKDA4          ;GO TO 'BDAY' & BREAK CONTENTS OF
3159                                ;$REGIO INTO DR #, CYL, SUR, SEC BITS
3160 007614 104031          ERROR 31        ;'CNTRL RDY' DID NOT SET ON COMPLETION
3161                                ;OF 'WRITE FORMAT'
3162                                ;WRT FMT WAS DONE STARTING AT <DSK-ADRES>
3163                                ;INDICATED IN EROR MSGE.
3164 007616 004737 021222    3$:  JSR      PC,CHKHE1 ;CHECK IF 'ERR' OR 'HE' BIT IS SET,
3165                                ;IF YES RETURN HERE.
3166 007622 104032          ERROR 32        ;'HE' OR 'ERR' BIT SET WHILE DOING
3167                                ;WRITE FORMAT ON CYLINDER 0,
3168                                ;SECTOR IN ERROR IS AS SHOWN IN
3169                                ;DISK-ADRES BITS 0-3
3170                                ;WRT FMT WAS DONE STARTING AT <DSK-ADRES>
3171                                ;INDICATED IN EROR MSGE.
3172
3173 007624 004737 021264    4$:  JSR      PC,CHKDA1 ;CHECK IF RKDA INCREMENTED CORRECTLY?
3174
3175 007630 104033          ERROR 33        ;RKDA DID NOT INCREMENT CORRECT
3176                                ;AFTER 1 WORD 'WRITE FORMAT' ON
3177                                ;CYLINDER 0, SECTOR IN ERROR IS 1
3178                                ;LESS THAN THAT SHOWN IN EXPCTD RKDA
3179 007632 005777 171504    5$:  TST      @RKDB    ;CHECK THAT RKDB DOES CONTAIN A 0
3180                                ;AFTER WRT BECAUSE LAST WORD WRITTEN
3181                                ;WAS SERIALLY SHIFTED OUT TO THE DISK
3182 007636 001406          BEQ      6$      ;YES, BRANCH
3183 007640 005037 001162    CLR      $REGO   ;THIS IS WHAT RKDB SHOULD CONTAIN
3184 007644 017737 171472 001164  MOV      @RKDB,$REGI ;GET RKDB
3185 007652 104037          ERROR 37        ;RKDB SHOULD BE 0 AFTER WRT SINCE THE
3186                                ;LAST WORD WRITTEN WAS SERIALLY SHIFTED
3187                                ;OUT OF RKDB
3188 007654 005201          6$:  INC      R1      ;INCREMENT DRIVE ADDRS TO NXT SECTOR
3189 007656 005205          INC      R5
3190 007660 122705 000014    CMPB    #14,R5   ;R U GOING TO CHECK THE LAST SECTOR?
3191 007664 001002          BNE     .+6     ;IF NOT, BRANCH
3192 007666 062705 000004    ADD     #4,R5   ;IF YES, INCREMENT R5 CORRECTLY TO 'EXPCTD RKDA'
3193                                ;AFTER HAVING CHECKED THE LAST SECTOR
3194 007672 005202          INC      R2      ;HAVE U FORMATTED ALL 12 SECTORS?
3195 007674 001317          BNE     1$      ;IF NOT, BRANCH BACK & LOOP
3196                                ;IF YES, EXIT
3197

```

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3198 ::*****
3199 ;*TEST 21          CHECK 'READ FORMAT'-CYLINDER 0, SECTOR 0-13
3200 ;*THIS TEST PERFORMS A RD FMT ON THE 12 SECTORS OF CYLINDER 0
3201 ;*THE FOLLOWING IS CHECKED
3202 ;*1) IF CNTRL RDY SET WITHIN A CERTAIN TIME ON COMPLETION
3203 ;*OF THE FUNCTION
3204 ;*2) IF 'HE' OR 'ERR' BIT SET?
3205 ;*3) IF THE RKDA INCREMENTS CORRECTLY?
3206 ;*4) RKBA INCREMENTED CORRECTLY BY 30 (OCTAL)
3207 ;*5) RKWC OVERFLOWED TO 0 FROM -14 (OCTAL)
3208 ;*6) CORRECT HEADER WAS RECEIVED FROM ALL 12 SECTORS.
3209 ;*7) RKCS STILL CONTAINS THE 'RD FMT' FUNCTION BITS.
3210 ;*IF THERE IS A READ ERROR IN THIS TEST OR ANY
3211 ;*OTHER TESTS THE USER SHOULD MAKE SURE THAT
3212 ;*IT IS AN IRRECOVERABLE ERROR AND NOT A TRANSIENT
3213 ;*ONE. THIS CAN BE DONE BY LOOPING ON THE TEST

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3217 007676 000004
3218 007700 005005
3219 007702 104413
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3229 007704 104421
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3231 007706 013701 001332
3232 007712 012700 177764
3233 007716 013702 001340
3234 007722 013712 001350
3235 007726 012704 033336
3236 007732 010477 171400
3237 007736 012777 177764 171370
3238 007744 012777 002005 171360
3239
3240 007752 105777 171354 1$:
3241 007756 100411
3242 007760 005205
3243 007762 001373
3244
3245 007764 004737 020770
3246 007770 013737 001350 001202
3247 007776 104416
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3249 010000 104045
3250
3251
3252
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3254 010002 004737 021230 2$:
3255 010006 104046
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3259
3260 010010 013705 001350 3$:
3261 010014 062705 000020
3262
3263 010020 004737 021264
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3265 010024 104040
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; *IN QUESTION. USUALLY A TRANSIENT ERROR
; *DISAPPEARS ON RETRIES, WHEREAS A LOGIC ERROR DOES NOT.
; *****
TST21: SCOPE
        CLR      R5
        CNT.RESET

; GO, DO CONTROL RESET
; THIS IS A CALL FOR THE 'CNTRL-
; RESET' ROUTINE. A CONTROL RESET IS
; ISSUED AND AFTER A CERTAIN TIME
; IF THE 'CNTRL RDY' DOES NOT SET
; AN ERROR IS REPORTED. NOTE THAT
; THE PC IN ERROR MESSAGE IS THE
; PC WHERE 'CNT.RESET' IS LOCATED.
; THIS IS A VERY BASIC ERR & IF IT
; OCCURS GO BACK TO TEST 10
; GO CHECK IF SIN IS SET
; IS SET, DO DRIVE RESET TO CLR IT

; SET UP COUNT FOR 12 SECTORS
; ADDRESS THE DRIVE
; ADRS TO WHICH X-FER DATA FROM DSK
; SET UP WORD COUNT FOR 12 HEADERS TO BREAD
; READ FORMAT, GO

; DID CNTRL RDY SET ON COMPLETION?
; YES, BRANCH
; NO, WAIT FOR IT TO SET
; IF WAITED LONG ENOUGH REPORT
; ERROR. OTHERWISE LOOP BACK & WAIT
; GO, GET RKCS, ER, DS, DA

; GO TO 'BDAY' & BREAK CONTENTS OF
; $REGIO INTO DR#, CYL, SUR, SEC BITS
; CNTRL RDY DID NOT SET ON COMPLETION
; OF READ FORMAT-OF CYLINDER 0,
; SECTORS 0-13
; READ FMT WAS DONE STARTING AT (DSK-ADRES)
; INDICATED IN EROR MESGE
; CHECK IF 'ERR' OR 'HE' BIT IS SET,
; IF YES RETURN HERE.
; 'ERR' OR 'HE' BIT SET ON DOING
; READ FMT-OF CYLINDER 0, SEC 0-13
; READ FMT WAS DONE STARTING AT (DSK-ADRES)
; INDICATED IN EROR MESGE

; RKDA SHOULD HAVE INCREMENTD TO (R2)

; CHECK IF RKDA INCREMENTED CORRECTLY.
; IF NOT, RETURN HERE.
; RKDA DID NOT INCREMENT BY 12
; AFTER A 'RD FMT' OF 12 HEADERS OF
; CYLINDER 0, SECTORS 0-13
; RKBA SHOULD INCREMENT BY 24 BYTES
; AT THE END OF X-FER

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3270 010026 022777 033366 171302 4$: CMP #OUTBUF+30, @RKBA ; DID RKBA INCREMENT CORRECTLY?
3271 010034 001407 BEQ 5$ ; YES, BRANCH
3272 010036 012737 033366 001162 MOV #OUTBUF+30, $REG0 ; GET EXPCTD RKBA
3273 010044 017737 171266 001164 MOV @RKBA, $REG1 ; GET ACTUAL RKBA
3274 010052 104042 ERROR 42 ; RKBA DID NOT INCREMENT CORRECTLY
3275 ; AFTER READ FORMAT OF 12 HEADERS
3276 010054 004737 021312 5$: JSR PC, CHKWC ; GO CHECK IF RKWC OVERFLOWED TO 0
3277 ; IF NOT RETURN HERE.
3278 010060 104041 ERROR 41 ; RKWC DID NOT OVERFLOW TO 0
3279 ; AFTER 'RD FMT' OF 12 HEADERS
3280 ; OF CYLINDER 0
3281 010062 005724 6$: TST (R4)+ ; WAS THE CORRECT HEADER RECIEVED?
3282 010064 001413 BEQ 7$ ; YES, BRANCH
3283 010066 010037 001162 MOV RD, $REG0 ; GET SECTOR FOR WHICH THE HEADER
3284 010072 062737 000014 001162 ADD #14, $REG0 ; COULD NOT BE READ CORRECT
3285 010100 005037 001164 CLR $REG1 ; EXPCTD HEADER-0, FOR CYL 0
3286 010104 014437 001166 MOV -(R4), $REG2 ; GET WRONG HEADER RECVD
3287 010110 104043 ERROR 43 ; HEADER WAS NOT READ RIGHT FOR
3288 ; SECTOR (AS IN ER MSGE), & CYL 0
3289 010112 005724 TST (R4)+ ; WAS THE CORRECT HEADER RECVD?
3290 010114 005200 7$: INC RD ; YES, HAVE U CHECKED FOR ALL 12 SECTORS?
3291 010116 001361 BNE 6$ ; IF NOT, LOOP BACK & CHK HDR FRM NXT SECTR
3292
3293 010120 004737 021336 JSR PC, CHKER ; CHECK IF ANY BIT IN RKER IS SET,
3294 ; IF YES, RETURN HERE.
3295 010124 104036 ERROR 36 ; RKER BIT SET ON DOING RD FMT
3296 ; OF CYL 0, SECTORS 0-13
3297 010126 022711 002204 8$: CMP #2204, @R1 ; DOES RKCS STILL CONTAIN FUNCTION BITS?
3298 010132 001406 BEQ TST22 ; YES, EXIT
3299 010134 012737 002204 001162 MOV #2204, $REG0 ; GET EXPCTD RKCS
3300 010142 011137 001164 MOV @R1, $REG1 ; GET ACTUAL RKCS
3301 010146 104024 ERROR 24 ; RKCS DID NOT CONTAIN 'RD FMT'
3302 ; FUNCTION BITS ON COMPETION OF
3303 ; THE FUNCTION
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*****
;*TEST 22 CHECK 'READ', CYLINDER 0, SECTORS 0 TO 13
;*THIS TEST PERFORMS A READ OF ALL THE SECTORS OF CYLINDER 0
;*8 CHECKS THE FOLLOWING
;*1) CNTRL RDY SETS WITHIN A CERTAIN TIME ON COMPLETION
;*OF THE FUNCTION
;*2) IF 'HE' OR 'ERR' BIT SET?
;*3) IF THE CORRECT PSUEDO-HEADER (FIRST WORD OF EVERY)
;*SECTOR, WRITTEN IN A PREVIOUS TEST) WAS RECEIVED.
;*4) IF RKDS CONTAINS THE CORRECT WORD.
;*4) IF RKDA INCREMENTED CORRECTLY.
;*5) IF REST OF THE (377) WORDS IN EACH SECTOR ARE '0' , NOTE
;*PREVIOUSLY ONE WORD WAS WRITTEN PER SECTOR.
;*6) IF RKCS STILL CONTAINS THE 'READ' FUNCTION BITS
;*7) IF CONTROL RESET CLEARS RKDB.
;* IF TESTING IS BEING DONE ON A SIMULATOR ONLY LAST SECTOR(13)
;*IS READ BECAUSE THE SIMULATOR CAN STORE ONLY 1 SECTOR (256 WORDS).
;*HENCE ONLY THE DATA WRITTEN LAST CAN BE READ BACK.
*****

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3494
3495 010520 000004
3496 010522 012737 000001 001206
3497 010530 012737 010560 001110
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3499 010536 005003
3500
3501 010540 012704 177465
3502 010544 012702 177764
3503 010550 013701 001350
3504 010554 010105
3505 010556 005205
3506 010560 104413
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3514
3515
3516 010562 104421
3517
3518 010564 005037 001362
3519 010570 010137 033336
3520
3521
3522
3523 010574 012777 033336 170534
3524 010602 012777 177777 170524
3525 010610 010177 170524
3526
3527 010614 012777 002003 170510
3528
3529 010622 105777 170504
3530 010626 100411
3531 010630 005237 001362
3532 010634 001372
3533
3534 010636 004737 020770
3535 010642 010137 001202
3536 010646 104416
3537
3538 010650 104031
3539
3540
3541
3542
3543 010652 032777 001000 170446
3544 010660 001405
3545 010662 004737 020776
3546 010666 010137 001170
3547 010672 104001
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*****
TST23: SCOPE
MOV #1,$TIMES
MOV #1,$SLPERR
CLR R3
MOV #-313,R4
MOV #-14,R2
MOV DRIVAD,R1
MOV R1,R5
INC R5
1$: CNT.RESET
TST.SIN
7$: CLR COUNT
MOV R1,OUTBUF
MOV #OUTBUF,$RKBA
MOV #-1,$RKWC
MOV R1,$RKDA
MOV #2003,$RKCS
2$: TSTB $RKCS
BMI 3$
INC COUNT
BNE 2$
JSR PC,GT4RG
MOV R1,$REG10
BRKDA4
ERROR 31
3$: BIT #1000,$RKDS
BEQ 4$
JSR PC,GT3RG
MOV R1,$REG3
ERROR 1

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:DO 1 ITERATION
:SET RETURN ADRES FOR LUPING
:ON ERROR (SW 9)
:(R3)=0, SURFACE 0 BEING WRITTEN
:(R3)-1, SURFACE 1 BEING WRITTEN
:SET UP COUNT FOR 203 CYLINDERS
:SET UP COUNT FOR 12 SECTORS
:GET DRIVE ADRES
:STORE IT
:GO, DO CONTROL RESET
:THIS IS A CALL FOR THE 'CNTRL-
:RESET' ROUTINE. A CONTROL RESET IS
:ISSUED AND AFTER A CERTAIN TIME
:IF THE 'CNTRL RDY' DOES NOT SET
:AN ERROR IS REPORTED. NOTE THAT
:THE PC IN ERROR MESSAGE IS THE
:PC WHERE 'CNT.RESET' IS LOCATED.
:THIS IS A VERY BASIC ERR & IF IT
:OCCURS GO BACK TO TEST 10
:GO CHECK IF SIN IS SET
:IF SET, DO DRIVE RESET TO CLR IT
:THIS WORD TO BE WRITTEN. THE FIRST
:WORD OF EACH SECTOR WILL BE THE ACTUAL
:DISK-ADRES, CONSISTING OF THE DRIVE NO.
:CYL ADRES, SURFACE BIT SECTOR ADRES
:ADRES FROM WHICH WORD IS TO B X-FERRED
:SET UP WORD COUNT
:ADRES THE DRIVE, WITH CORRECT CYL
:& SECTOR ADRES
:WRITE FORMAT, GO
:DID CNTRL RDY SET
:YES, BRANCH
:NO, HAVE U WAITED LONG ENOUGH?
:IF NOT, LOOP BACK & WAIT
:IF YES, REPORT ERROR
:GO, GET RKCS, ER, DS DA
:GET DISK ADRES, WHERE ERROR OCCURED
:GO TO 'BD44' & BREAK CONTENTS OF
:$REG10 INTO DR #.CYL SUR,SEC BITS
:CNTRL RDY DID NOT SET ON COMPLETION
:OF 'WRITE FORMAT', ON SECTOR AS
:SHOWN IN <DSK-ADRES>
:WRT FMT WAS DONE STARTING AT <DSK-ADRES>
:INDICATED IN EROR MSGE.
:DID SIN BIT SET?
:NO, BRANCH
:GO, GET RKCS, ER, DS
:GET, DISK-ADRES WHERE ERROR OCCURED
:SIN SET WHILE DOING WRT FMT
:TO DISK-ADRES (AS IN $REG3)

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3550	010674	004737	021222	4\$:	JSR	PC,CHKHE1	:CHECK IF 'ERR' OR 'HE' BIT IS SET
3551							:IF YES, RETURN HERE.
3552	010700	104032			ERROR	32	:HE OR ERR SET WHILE DOING WRITE
3553							:FORMAT ON SECTOR AS INDICATED IN
3554							:<DSK-ADRES>
3555							:WRT FMT WAS DONE STARTING AT <DSK-ADRES>
3556							:INDICATED IN EROR MSGE.
3557	010702	004737	021264	5\$:	JSR	PC,CHKDA1	:CHECK IF RKDA INCREMENTED CORRECTLY,
3558							:IF NOT, RETURN HERE.
3559	010706	104033			ERROR	33	:RKDA DID NOT INCREMENT CORRECTLY
3560							:AFTER 'WRITE FORMAT' WAS DONE
3561							:TO THE SECTOR PREVIOUS TO THAT
3562							:INDICATED IN 'EXPCTD' RKDA
3563	010710	005201		6\$:	INC	R1	:INCREMENT TO THE NXT SECTOR
3564	010712	005205			INC	R5	:INCREMENT R5, TO WHAT RKDA WILL INCREMENT
3565	010714	022702	177776		CMP	#-2,R2	:R U GOING TO FORMAT THE LAST SECTOR
3566							:IN THE CYLINDER ?
3567	010720	001002			BNE	.+6	:IF NOT, BRANCH
3568	010722	062705	000004		ADD	#4,R5	:INCREMENT R5 CORRECTLY TO 'EXPCTD RKDA'
3569	010726	005202			INC	R2	:HAVE U FORMATTED ALL 12 SECTORS
3570							:ON THIS CYLINDER
3571	010730	001313			BNE	1\$:IF NOT, LOOP BACK & FORMAT THE
3572							:NEXT SECTOR
3573							:YES
3574	010732	012702	177764		MOV	#-14,R2	:RESET THE COUNT FOR 12 SECTORS
3575	010736	042701	000037		BIC	#37,R1	:CLEAR THE SEC ADRES BITS
3576	010742	005703			TST	R3	:SURFACE 1?
3577	010744	001006			BNE	8\$:YES, BRANCH
3578	010746	005203			INC	R3	:NO, SET FLAG
3579	010750	062701	000020		ADD	#20,R1	:INCREMENT TO THE NXT SURFACE
3580	010754	010105			MOV	R1,R5	:THIS IS WHAT RKDA SHOULD
3581	010756	005205			INC	R5	:INCREMENT TO.
3582	010760	000677			BR	1\$:GO, DO NXT SURFACE
3583	010762	062701	000040	8\$:	ADD	#40,R1	:INCREMENT TO NXT CYL
3584	010766	010105			MOV	R1,R5	:POSITION FOR
3585	010770	005205			INC	R5	:EXPCTD RKDA
3586	010772	005003			CLR	R3	
3587	010774	005204			INC	R4	:HAVE U FORMATTED ALL 203 CYLINDERS
3588	010776	001270			BNE	1\$:IF NOT, LOOP BACK & FORMAT THE
3589							:NEXT CYLINDER

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:*****
: *TEST 24 CHECK 'READ FORMAT' FOR THE ENTIRE DISK
: *THIS TEST READ FORMATS THE ENTIRE DISK, WHICH WAS WRT
: *FORMATTED IN THE PREVIOUS TEST. THE FOLLOWING CHECKING
: *IS DONE
: *1. CNTRL RDY SETS WITHIN A CERTAIN TIME ON COMPLETION
: *OF FUNCTION
: *2. IF 'SIN' OCCURRED?
: *3. IF 'HE' OR 'ERR' OCCURRED?
: *4. RKDA INCREMENTED CORRECTLY.
: *5. IF THE CORRECT HEADER WAS READ.
: *6. IF RKWC OVERFLOWED CORRECTLY.
: *12 SECTORS (1 CYLINDER) ARE READ AT A TIME. IF 'SIN'

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011000 000004
011002 012737 000001 001206
011010 012737 011074 001110
011016 005037 001356
011022 013701 001350
011026 010102
011030 005737 001344
011034 001410
011036 052701 014533
011042 052702 014540
011046 012737 177777 001370
011054 000407
011056 012705 177465
011062 012737 177764 001370
011070 062702 000020
011074 104413
011076 104421
011100 012703 033336
011104 005037 001360
011110 010377 170222
011114 013777 001370 170212
011122 010177 170212
011126 012777 002005 170176
011134 105777 170172
011140 100411

TST24: SCOPE
MOV #1,STIMES
MOV #1\$,SLPERR
CLR INDX1
MOV DRIVAD,R1
MOV R1,R2
TST SIMUL
BEQ 12\$
BIS #14533,R1
BIS #14540,R2
MOV #-1,EFLG1
BR 1\$
MOV #-313,R5
MOV #-14,EFLG1
ADD #20,R2
1\$: CNT.RESET
TST.SIN
11\$: MOV #OUTBUF,R3
CLR INDX2
MOV R3,ARKBA
MOV EFLG1,ARKWC
MOV R1,ARKDA
MOV #2005,ARKCS
2\$: TSTB ARKCS
BMI 3\$

;*OCCURS A DRIVE RESET IS DONE BEFORE READING THE NEXT
;*SECTOR. READING IS DONE IN THIS ORDER CYL 0-SUR 0;
;*CYL 0-SUR 1; CYL 1-SUR 0; CYL 1-SUR 1; CYL 2-SUR 0;
;*CYL 2-SUR 1;-----CYL 312-SUR 1. IF TESTING ON SIMULATOR, ONLY
;*THE LAST CYLINDER (312), LAST SECTOR (13), SURFACE 1 IS READ.
;DO 1 ITERATION
;SET RETURN ADRES FOR LUPING
;ON ERROR (SW 9)
;INDX1=0, SURFACE 0 BEING READ
;INDX1=1, SURFACE 1 BEING READ
;GET DRIVE ADRES
;TESTING ON SIMULATOR?
;NO BRANCH
;SET BITS FOR CYL 312, SEC 13, SUR 1
;ON SIMULATOR, CHECK ONLY CYL 312.
;SECTOR 13, SURFACE 1
;RKDA SHOULD INCRMNT TO THIS AFTR
;RD FMT OF 1 SECTOR
;SET COUNT FOR READING HDR
;FROM 1 SECTOR ONLY
;SET UP COUNT FOR 203 CYLINDERS
;SET COUNT FOR 12 HDRS TO BE
;READ FROM EACH CYLINDER
;THIS IS WHAT RKDA SHOULD INCREMENT
;BY, AFTER 'RD FMT' OF EACH CYLINDER
;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10
;CHECK IF SIN IS SET
;IF SET DO DRV-RESET TO CLR IT
;STORE ADRES OF BUFFER
;ADRES TO WHICH DATA IS TO BE X-FERRED
;FROM THE DISK
;SET UP WORD COUNT FOR 12 HEADERS
;TO BE READ OFF EACH CYLINDER
;(ONLY 1 FOR SIMULATOR)
;ADRES THE DRIVE WITH CORRECT
;CYLINDER & SECTOR ADRES
;READ FORMAT, GO
;DID CNTRL RDY SET?
;YES, BRANCH


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3718 011304 005723      8$:  TST      (R3)+      ; INCREMENT POINTER TO THE NXT WORD
3719                                ; IN MEMORY WHERE THE RECD HDR IS STORED
3720 011306 005200      INC      R0              ; HAVE U CHECKED ALL 12 HEADERS?
3721 011310 001361      BNE      7$              ; IF NOT, LOOP BACK & CHK THE NXT.
3722                                ; YES, ALL HEADERS FOR THIS CYLINDER
3723                                ; CHECKED.
3724 011312 004737 021312 JSR      PC,CHKWC        ; CHECK IF RKWC OVERFLOWED TO 0, IF
3725                                ; NOT RETURN HERE.
3726 011316 104041      ERROR    41          ; RKWC DID NOT OVERFLOW AFTER DOING
3727                                ; RDFMT OF 12 SECTORS ON THE CYLINDER
3728                                ; NOTE THAT 'RKDA' IS THE INCREMENTED
3729                                ; RKDA AFTER THE RDFMT
3730 011320 005737 001344 9$:  TST      SIMUL          ; TSTING ON SIMULATOR?
3731 011324 001031      BNE      TST25         ; IF YES, EXIT
3732                                ; NO
3733 011326 005737 001356      TST      INDX1          ; DOING SURFACE 1
3734 011332 001011      BNE      10$           ; YES, BRANCH
3735 011334 005237 001356      INC      INDX1          ; NO
3736 011340 062701 000020      ADD      #20,R1         ; INCREMENT DRIV ADRES TO THE NXT SURFACE
3737 011344 010102      MOV      R1,R2
3738 011346 062702 000020      ADD      #20,R2
3739                                ; THIS IS WHAT RKDA SHOULD INCREMENT
3740 011352 000137 011074      JMP      1$              ; TO, AFTER READ FMT OF THE CYLINDER
3741 011356 005037 001356 10$: CLR      INDX1          ; GO RD FMT THE NXT SURFACE
3742 011362 042701 000037      BIC      #37,R1         ; CLR SEC, SURFACE BITS
3743 011366 062701 000040      ADD      #40,R1         ; INCREMENT TO NXT CYL
3744 011372 010102      MOV      R1,R2         ; THIS IS WHAT RKDA SHOULD BE
3745 011374 062702 000020      ADD      #20,R2         ; AFTER RD FMT OF CYLINDER
3746 011400 005205      INC      R5              ; HAVE U DONE ALL CYLINDERS?
3747 011402 001402      BEQ      TST25         ; EXIT
3748 011404 000137 011074      JMP      1$              ; IF NOT, LOOP BACK & READ FMT FROM
3749                                ; THE NXT CYLINDER
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:*****
:*TEST 25      CHECK 'READ' OF THE ENTIRE DISK
;*READ OF THE ENTIRE DISK (ONE WORD PER SECTOR) IS DONE
;*IN THIS TEST.  IN A PREVIOUS TEST THE FIRST WORD OF
;*EVERY SECTOR WAS WRITTEN LIKE A PSUEDO-HEADER (DRIVE #,
;*CYLINDER #, SURFACE & SECTOR #).  THESE PSUEDO HEADERS
;*WILL BE READ & CHECKED IN THIS TEST, PROVING THAT ANY
;*SECTOR CAN BE ACCESSED AND READ.
;*THE FOLLOWING CHECKING IS DONE
;*1. CNTRL RDY SETS WITHIN A CERTAIN TIME ON COMPLETION
;*OF FUNCTION.
;*2. IF 'SIN' OCCURRED?
;*3. IF 'HE' OR 'ERR' OCCURRED?
;*4. THE CORRECT FIRST WORD FROM EVERY SECTOR
;*WAS RECEIVED.  THIS WORD REFLECTS THE ABSOLUTE
;*DISK ADDRESS (DRV #, CYL #, SUR, SEC#) OF THAT SECTOR.
;*5. IF RKDB CONTAINED THE CORRECT WORD.
;*IF 'SIN' OCCURS DRIVE RESET IS DONE BEFORE READING
;*THE NEXT SECTOR.  READ IS DONE IN THIS ORDER SEC 0-11
;*CYL 0 SUR 0 -> SEC 0-11 CYL 0 SUR 1 -> SEC 0-11 CYL 1.....
;*IF TESTING ON SIMULATOR ONLY LAST CYLINDER (312).  LAST
;*SECTOR (13), SURFACE 1 IS READ.
    
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3774
3775 011410 000004
3776 011412 012737 000001 001206
3777 011420 012737 011464 001110
3778
3779 011426 012703 033336
3780 011432 005004
3781
3782 011434 013701 001350
3783 011440 005737 001344
3784 011444 001403
3785 011446 052701 014533
3786 011452 000404
3787 011454 012700 177764
3788 011460 012705 177465
3789
3790 011464 104413
3791
3792
3793
3794
3795
3796
3797
3798
3799
3800 011466 104421
3801
3802 011470 005037 001356
3803 011474 010377 167636
3804
3805 011500 012777 177777 167626
3806 011506 010177 167626
3807
3808 011512 012777 000005 167612
3809
3810 011520 105777 167606
3811 011524 100411
3812 011526 005237 001356
3813 011532 001372
3814
3815 011534 004737 020770
3816 011540 010137 001202
3817 011544 104416
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3819 011546 104045
3820
3821
3822
3823
3824
3825 011550 032777 001000 167550
3826 011556 001405
3827 011560 004737 020776
3828 011564 010137 001170
3829 011570 104001

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↑T25: SCOPE
MOV #1,\$TIMES ;DO 1 ITERATION
MOV #1,\$SLPERR ;SET RETURN ADRES FOR
;LOOPING ON ERROR (SW9)
MOV #OUTBUF,R3
CLR R4 ;FLAG, CLEAR WHEN READING SURFACE 0
;SET WHEN READING SURFACE 1
MOV DRIVAD,R1 ;GET DRIVE ADDRESS
TST SIMUL ;TSTING ON SIMULATOR?
BEQ 10\$;IF NOT BRANCH
BIS #14533,R1 ;SET ADRES BITS FOR LAST CYL (312)
BR 1\$;LAST SECTOR (13), SURFACE 1
10\$: MOV #-14,R0 ;SET COUNT FOR 12 SECTORS
MOV #-313,R5 ;SET UP COUNT FOR 203 CYLINDERS
1\$: CNT.RESET ;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10
;GO CHECK SIN, IF SET DO
;DRIVE RESET TO CLR IT
TST.SIN
8\$: CLR INDX1
MOV R3,ARKBA ;ADRES TO WHICH DATA IS TO B X-FERRED
;FROM THE DISK
MOV #-1,ARKWC ;SET UP WORD COUNT
MOV R1,ARKDA ;ADRES THE DRIVE WITH CORRECT
;CYLINDER & SECTOR ADRES
MOV #5,ARKCS ;READ, GO
2\$: TSTB ARKCS ;DID CNTRL RDY SET?
BMI 3\$;YES, BRANCH
INC INDX1 ;NO, HAVE U WAITED LONG ENOUGH
BNE 2\$;IF NOT, LOOP BACK & WAIT FOR IT
;IF YES, REPORT ERROR
JSR PC,GT4RG ;GO, GET RKCS, ER, DS,DA
MOV R1,\$REG10 ;GET DISK-ADRES WHERE ERROR OCCURED
BRKDAY ;GO TO 'BDAY' & BREAK CONTENTS OF
;SREG10 INTO DR #,CYL,SUR,SEC BITS
ERROR 45 ;CNTRL RDY DID NOT SET AFTER DOING
;A 1 WORD READ FROM ADRES AS
;INDICATED IN <DISK-ADRES>
;'RKDA' IN EROR MSGE GIVES THE
;CONTENTS OF RKDA AT THE TIME OF ERROR
3\$: BIT #1000,ARKDS ;DID 'SIN' SET?
BEQ 4\$;NO, BRANCH
JSR PC,GT3RG ;GO, GET RKCS, ER, DS
MOV R1,\$REG3 ;GET DISK-ADRES WHERE SIN OCCURED3
ERROR 1 ;'SIN' ERROR ON DOING READ FROM

H06

3830
3831 011572 004737 021222
3832
3833 011576 104046
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3838 011600 020113
3839 011602 001407
3840 011604 010137 001162
3841 011610 011337 001164
3842 011614 010137 001166
3843 011620 104044
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3852 011622 020177 167514
3853 011626 001406
3854 011630 010137 001162
3855 011634 017737 167502 001164
3856 011642 104037
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3861
3862
3863
3864 011644 005737 001344
3865 011650 001022
3866 011652 005201
3867 011654 005200
3868 011656 001302
3869
3870 011660 012700 177764
3871 011664 042701 000037
3872 011670 005704
3873 011672 001004
3874 011674 005204
3875 011676 062701 000020
3876 011702 000670
3877 011704 005004
3878 011706 062701 000040
3879 011712 005205
3880 011714 001263
3881
3882
3883
3884
3885

4\$: JSR PC,CHKHE1
ERROR 46

5\$: CMP R1,(R3)
BEQ 6\$
MOV R1,\$REG0
MOV (R3),\$REG1
MOV R1,\$REG2
ERROR 44

6\$: CMP R1,ARKDB
BEQ 7\$
MOV R1,\$REG0
MOV ARKDB,\$REG1
ERROR 37

7\$: TST SIMUL
BNE TST26
INC R1
INC R0
BNE 1\$

9\$: MOV #-14,R0
BIC #37,R1
TST R4
BNE 9\$
INC R4
ADD #20,R1
BR 1\$
CLR R4
ADD #40,R1
INC R5
BNE 1\$

:DISK-ADRES INDICATED IN \$REG3
:CHECK IF 'ERR' OR 'HE' BIT IS SET,
:IF YES, RETURN HERE.
:'HE' OR 'ERR' ON DOING A READ OF
:1 WORD FROM ADRES AS INDICATED
:IN <DISK-ADRES>
:'RKDA' IN EROR MSGE GIVES THE
:CONTENTS OF RKDA AT THE TIME OF EROR
:WAS THE CORRECT DATA WORD RECVD?

:GET EXPCTD DATA WORD
:GET DATA WORD RECVD
:GET DISK-ADRES
:DID NOT RECIEVE THE CORRECT
:DATA WORD FROM DISK ON DOING
:1 WORD READ FROM 'DISK-ADRES'
:AS INDICATED BY 'EXPCTD' DATA WORD
:NOTE THAT IN A PREVIOUS TEST THE
:FIRST WORD OF EACH SECTOR IS UNIQUELY
:WRITTEN WITH A WORD GIVING THE
:ABSOLUTE ADDRESS OF THAT SECTOR IN
:TERMS OF, DRIV #, CYL ADRES, SUR, SEC ADRS.
:DOES RKDB CONTAIN CORRECT WORD
:YES, BRANCH
:NO, GET EXPCTD RKDB
:GET RKDB RECVD
:RKDB ERROR ON READ.
:FOR RK11C, AFTER A READ RKDB
:CONTAINS CHECKSUM FOR THE SECTOR
:READ.
:WHEREAS FOR RK11D, AFTER READ
:RKDB CONTAINS THE LAST WORD
:READ FROM THAT SECTOR &
:X-FERRED TO MEMORY
:TESTING ON SIMULATOR?
:IF YES, EXIT
:INCREMENT TO ADRES NEXT SECTOR
:HAVE U CHKD ALL 12 SECTORS?
:IF NOT, LUP BAK & CHK THE NXT
:IF YES...
:RESET THE COUNT FOR 12 SECTORS
:CLEAR SECTOR, SURFACE BITS
:DOING SURFACE 1?
:YES, BRANCH
:NO
:INCREMENT THE ADRES TO NXT SURFACE
:GO READ SURFACE 1

:INCREMENT TO NXT CYL
:HAVE U CHKD ALL 203 CYLINDERS
:IF NOT, LOOP BACK & CHK THE NXT CYLINDER
:YES

::*****
:*TEST 26 CHECK 'SEEK' FUNCTION, WITH DIFFERENT VELOCITY MODES

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3892 011716 000004
3893 011720 012737 000005 001206
3894 011726 012703 001372
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3896 011732 005037 001356
3897
3898 011736 013700 001332
3899 011742 013701 001326
3900 011746 013702 001330
3901 011752 012737 011760 001110
3902
3903 011760 000240 1$:
3904 011762 104413 2$:
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3914 011764 104421
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3918 011766 013704 001350
3919 011772 051304
3920 011774 010477 167340
3921 012000 012710 000011
3922
3923 012004 104412
3924
3925 012006 104021
3926
3927
3928
3929 012010 005005 4$:
3930 012012 032711 000100 5$:
3931 012016 001005
3932 012020 005205
3933 012022 001373
3934 012024 004737 020770
3935 012030 104026
3936
3937 012032 032711 001000 6$:
3938 012036 001403
3939 012040 004737 020770
3940 012044 104001
3941 012046 032710 140000 7$:

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*****
TST26: SCOPE
MOV #5,$TIMES ;DO 5 ITERATIONS
MOV #SEEK0,R3 ;INITIALIZE POINTER TO THE FIRST
;SEEK ADDRESS
CLR INDX1 ;INDX1, WHEN 0 INDICATES SEEK IN FWD DIRECTION
; WHEN 1 INICATES SEEK IN REV DIRECTION
MOV RKCS,R0
MOV RKDS,R1
MOV RKER,R2
MOV #1,$LPERR ;SET RETURN ADRES FOR LUPING ON
;EROR (SW 9)
NOP
CNT.RESET ;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10
;GO, CHECK IF SIN IS SET, IF SET
;DO DRV-RESET TO CLEAR IT

TST.SIN

MOV DRIVAD,R4 ;GET DRIV-ADRES
BIS (R3),R4 ;SET CYLINDER BITS
MOV R4,DRKDA ;ADDRS THE DRIVE
MOV #11,DR0 ;SET 'SEEK', 'GO'

CHKCRDY ;GO CHECK IF CONTROL RDY IS SET
;IF SO, SKIP THE EROR MESSAGE.
; 'CNTRL RDY' DID NOT SET AFTER
;SENDING CYL ADD TO THE DRIV, 'ADD ACK'
;FROM DRIVE SHLD HAVE COME BACK
;THEREUPON SETTING 'CNTRL RDY'

CLR R5 ;DID R/W/S RDY SET?
BIT #100,DR1 ;YES, BRANCH
BNE 6$ ;NO, WAIT
INC R5 ;WAITED LONG?
BNE 5$ ;GO, GET RKCS, ER, DS, DA
JSR PC,GT4RG ;R/W/S RDY DID NOT SET ON
;COMPLETION OF SEEK
ERROR 26 ;DID SIN SET?

BIT #1000,DR1 ;NO, BRANCH
BEQ 7$ ;GO, GET RKCS, ER, DS, DA
JSR PC,GT4RG ;SIN SET ON DOING SEEK
ERROR 1 ;DID 'HE' OR 'ERR' SET?
BIT #140000,DR0

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3942 012052 001403 BEQ 8$ ;YES
3943 012054 004737 020770 JSR PC,GT4RG ;GO GET RKCS, ER, DS, DA
3944 012060 104022 ERROR 22 ;EAR OF 'HE' BIT SET WHEN
3945 ;SEEKING TO CYL AS INDICATED
3946 ;IN RKDA
3947
3948 012062 022710 000210 8$: CMP #210,ARO ;DOES RKCS STILL CONTAIN THE 'SEEK' FNCTION
3949 012066 001406 BEQ 9$ ;YES - EXIT
3950 012070 011037 001164 MOV ARO,$REG1 ;NO, GET RKCS RECVD
3951 012074 012737 000210 001162 MOV #210,$REG0 ;GET EXPCTD RKCS
3952 012102 104024 ERROR 24 ;RKCS SHOULD CONTAIN THE 'SEEK' BITS
3953 ;IF NOT, ERROR
3954
3955 012104 020477 167230 9$: CMP R4,ARKDA ;DID RKDA CHANGE?
3956 012110 001406 BEQ 10$ ;NO
3957 012112 010437 001162 MOV R4,$REG0 ;YES, GET EXPCTD?
3958 012116 017737 167216 001164 MOV ARKDA,$REG1 ;GET RKDA
3959 012124 104027 ERROR 27 ;RKDA CHANGED AFTER DOING SEEK
3960
3961 012126 010477 167206 10$: MOV R4,ARKDA ;ADRES THE DRIVE, SEC 0
3962 012132 012777 033336 167176 MOV #OUTBUF,ARKBA ;READ ONE HEADER INTO THIS
3963 012140 012777 177777 167166 MOV #-1,ARKWC ;BUS ADRES
3964 012146 012710 002005 MOV #2005,ARO ;GO READ FORMAT
3965 012152 104414 CNT.RDY ;WAIT FOR CNTRL RDY
3966 012154 021337 033336 CMP (R3),OUTBUF ;WAS THE CORRECT READE4R READ (FROM
3967 012160 001410 BEQ 11$ ;CYLINDER TO WHICH SEEK WAS DONE BEFORE)
3968 012162 005037 001162 CLR $REG0 ;STORE SEC # FROME WHERE HDR WAS RD (0)
3969 012166 011337 001164 MOV (R3),$REG1 ;GET EXPCTD HEADER
3970 012172 013737 033336 001166 MOV OUTBUF,$REG2 ;GET HDR RECVD
3971 012200 104043 ERROR 43 ;WRONG HDR WAS RECVD FROM CYLINDER (ADRES
3972 ;IN ER MSGE). NOTE THAT A PURE SEEK WAS
3973 ;DONE TO THIS CYL BEFORE READING HDR
3974 ;USING READ FORMAT
3975 012202 005737 001356 11$: TST INDX1 ;SEEK IN REVRSE DIRECTION?
3976 012206 001007 BNE 12$ ;YES, BRANCH
3977 012210 005723 TST (R3)+ ;NO, INCREMENT PTR TO NXT SEEK ADRES
3978 012212 022703 001400 CMP #SEEK2+2,R3 ;DONE WITH ALL SKS IN FWD DIR?
3979 012216 001260 BNE 1$ ;NO, GO & DO NXT ONE
3980 012220 005237 001356 INC INDX1 ;SET FLAG INDICATING SK IN REVRSE
3981 012224 005743 TST -(R3)
3982 012226 005743 12$: TST -(R3) ;POSITION PTR TO NXT SK IN REV
3983 012230 022703 001370 CMP #SEEK0-2,R3 ;DONE WITH ALL?
3984 012234 001251 BNE 1$ ;IF NOT, DO NXT ONE
3985
3986
3987
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3990
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3995
3996 012236 000004 ;*****
3997 012240 012737 000005 001206 ;*TEST 27 CHECK DRIVE RESET FROM LAST CYLINDER
; *THE HEADS ARE POSITIONED ON THE LAST CYLINDER (DOING
; *AN IMPLIED SEEK-READ). THEN A DRIVE RESET IS ISSUED.
; *IT'S CHECKED IF THE HEADS WERE BROUGHT BACK TO 0 BY
; *DOING A 1 WORD READ & CHECKING THAT THE CORRECT WORD
; *WAS RECEIVED. IF TESTING ON SIMULATOR THIS TEST IS SKIPPED.
;*****
; *ST27: SCOPE
MOV #5,$TIMES ;:DO 5 ITERATIONS

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3998 012246 005737 001344      IST      SIMUL      ;R U ON A SIMULATOR?
3999 012252 001124              BNE      TST30      ;:YES, EXIT
4000 012254 013701 001332      MOV      RKCS,R1
4001 012260 104413              CNT.RESET          ;GO DO CONTROL RESET
4002                                  ;THIS IS A CALL FOR THE 'CNTRL-
4003                                  ;RESET' ROUTINE. A CONTROL RESET IS
4004                                  ;ISSUED AND AFTER A CERTAIN TIME
4005                                  ;IF THE 'CNTRL RDY' DOES NOT SET
4006                                  ;AN ERROR IS REPORTED. NOTE THAT
4007                                  ;THE PC IN ERROR MESSAGE IS THE
4008                                  ;PC WHERE 'CNT.RESET' IS LOCATED.
4009                                  ;THIS IS A VERY BASIC ERR & IF IT
4010                                  ;OCCURS GO BACK TO TEST 10
4011 012262 005000              CLR      R0
4012 012264 012703 033336      MOV      #OUTBUF,R3
4013 012270 013704 001350      MOV      DRIVAD,R4
4014 012274 010405              MOV      R4,R5
4015 012276 052705 014500      BIS      #14500,R5
4016 012302 010577 167032      MOV      R5,DRKDA
4017 012306 012777 177777 167020  MOV      #-1,DRKWC
4018 012314 010377 167016      MOV      R3,DRKBA
4019
4020 012320 012711 000005      MOV      #5,DR1
4021
4022 012324 005000              CLR      R0
4023 012326 104414              CNT.RDY          1$:
4024                                  ;THIS IS A CALL FOR CN.RDY ROUTINE
4025                                  ;WHICH WAITS FOR CNTRL RDY TO SET.
4026                                  ;A RETURN IS MADE AFTER CNTRL RDY
4027                                  ;SETS. IF WITHIN A CERTAIN TIME
4028                                  ;CNTRL RDY DOESN'T SET AN ERROR
4029                                  ;MESSAGE IS GIVEN. WAITING TIME
4030                                  ;883 MS FOR 11/20, 175 MS FOR 11/45
4031 012330 020513              CMP      R5,DR3
4032 012332 001407              BEQ      3$
4033 012334 010537 001162      MOV      R5,$REG0
4034 012340 011337 001164      MOV      DR3,$REG1
4035 012344 010537 001166      MOV      R5,$REG2
4036 012350 104044              ERROR     44
4037                                  ;WAS THE CORRECT WORD READ?
4038                                  ;YES, SEEK TO 312 WAS DONE CORRECTLYS.
4039                                  ;GET EXPCD WORD
4040                                  ;GET WORD RECVD
4041                                  ;GET DSK-ADRES FROM WHERE WORD WAS READ
4042                                  ;DID NOT READ BACK CORRECT WORD FROM
4043                                  ;LAST CYL, SEC 0. IF TEST 45 & 46
4044                                  ;WERE SUCCESSFULLY DONE THIS
4045                                  ;ERROR MEANS THAT IMPLIED SEEK
4046                                  ;TO CYL 312 COULD NOT B DONE
4047                                  ;DRIVE RESET, GO
4048 012352 012711 000015      MOV      #15,DR1
4049 012356 104414              CNT.RDY          3$:
4050                                  ;THIS IS A CALL FOR CN.RDY ROUTINE
4051                                  ;WHICH WAITS FOR CNTRL RDY TO SET.
4052                                  ;A RETURN IS MADE AFTER CNTRL RDY
4053                                  ;SETS. IF WITHIN A CERTAIN TIME
4054                                  ;CNTRL RDY DOESN'T SET AN ERROR
4055                                  ;MESSAGE IS GIVEN. WAITING TIME
4056                                  ;883 MS FOR 11/20, 175 MS FOR 11/45
4057                                  ;DID R/W/S RDY SET?
4058                                  ;YES, BRANCH
4059                                  ;IF U R ON A SLOWER MACHINE
4060                                  ;& DO NOT NEED SUCH A LARGE MACHINE
4061                                  ;TIME LOOP, CHANGE THESE 3
4058 012360 005000              CLR      R0
4059 012362 032777 000100 166736 4$:
4060 012370 001011              BIT      #100,DRKDS
4061 012372 012702 177763      BNE      5$
4062 012376 005202              MOV      #-15,R2
4063 012400 001376              INC      R2
4064                          BNE      .-2

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012402 005200
 012404 001366
 012406 004737 020770
 012412 104026
 012414 032711 140000
 012420 001403
 012422 004737 020770
 012426 104022
 012430 005205
 012432 020577 166702
 012436 001406
 012440 010537 001162
 012444 017737 166670 001164
 012452 104054
 012454 012777 177777 166652
 012462 010377 166650
 012466 010477 166646
 012472 012711 000005
 012476 005000
 012500 104414
 012502 020413
 012504 001407
 012506 010437 001162
 012512 011337 001164
 012516 010437 001166
 012522 104044

INC R0
 BNE 45
 JSR PC,GT4RG
 ERROR 26
 5\$: BIT #140000,AR1
 BEQ 65
 JSR PC,GT4RG
 ERROR 22
 6\$: INC R5
 CMP R5,ARKDA
 BEQ 75
 MOV R5,\$REG0
 MOV ARKDA,\$REG1
 ERROR 54
 7\$: MOV #-1,ARKWC
 MOV R3,ARKBA
 MOV R4,ARKDA
 MOV #5,AR1
 8\$: CLR R0
 CNT.RDY
 9\$: CMP R4,AR3
 BEQ TST30
 MOV R4,\$REG0
 MOV AR3,\$REG1
 MOV R4,\$REG2
 ERROR 44

; INSTRUCTIONS TO 'NOP' THE
 ; LOOP TIME WILL BE REDUCED
 ; TO 1100 MS
 ;
 ; THE TOTAL TIME FOR THE ABOVE
 ; LOOPS (W/O PUTTING 'NOP'S) IS
 ; 5304 MS FOR 11/20 AND
 ; 1061 MS FOR 11/45 WITH MOS
 ; OR BIPOLAR MEMORY
 ; WAITED LONG?
 ; IF NOT, LUP BAK & WAIT
 ; IF YES, ERROR
 ; GET RKCS,ER,DS,DA
 ; R/W/S RDY DID NOT SET AFTER
 ; DOING DRIVE RESET
 ; DID HE OR ERR BIT SET?
 ; IF NOT, BRANCH
 ;
 ; GET RKCS,ER,DS,DA FOR ERROR MESSAGE
 ; HE OR ERR BIT SET ON DOING DRIVE
 ; RESET FROM LAST CYLINDER
 ; POSITION R5 TO EXPCTD RKDA
 ; DID THE CYL ADRES BITS IN RKDA GET CHANGED?
 ; NO, BRANCH
 ; GET EXPCTD RKDA
 ; GET RKDA RECVD
 ; CYLINDER ADRES BITS IN RKDA
 ; GOT CHANGED AFTER
 ; DRIVE RESET, FROM LAST CYLINDER
 ; READ 1 WORD
 ; INTO THIS ADRES
 ; FROM THIS DSK ADRES-CYL 0, SEC 0
 ;
 ; READ, GO
 ;
 ; THIS IS A CALL FOR CN.RDY ROUTINE
 ; WHICH WAITS FOR CNTRL RDY TO SET.
 ; A RETURN IS MADE AFTER CNTRL RDY
 ; SETS. IF WITHIN A CERTAIN TIME
 ; CNTRL RDY DOESN'T SET AN ERROR
 ; MESSAGE IS GIVEN. WAITING TIME
 ; 883 MS FOR 11/20, 175 MS FOR 11/45
 ; WAS THE CORRECT WORD READ?
 ; YES, EXIT
 ; GET EXPCTD WORD
 ; GET WORD RECVD
 ; GET DISK ADRES WHERE ERROR OCCURED
 ; DID NOT READ CORRECT WORD FROM
 ; CYL 0, SEC 0. IF TEST 45 & 46
 ; WERE SUCCESSFULLY DONE THIS
 ; ERROR COULD MEAN THAT DRIVE-RESET
 ; DID NOT BRING HEADS BACK TO 0.

;*****

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4117 012524 000004
4118 012526 104413
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4128 012530 104421
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4130 012532 013704 001332
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4145 012536 012700 033336
4146 012542 012701 177401
4147 012546 012702 177400
4148 012552 012703 177400
4149
4150 012556 010320
4151 012560 005202
4152 012562 060103
4153 012564 010320
4154 012566 005202
4155 012570 001374
4156
4157 012572 012777 177400 166534
4158 012600 012777 033336 166530
4159 012606 013777 001350 166524
4160
4161 012614 012714 000003
4162
4163 012620 105714
4164 012622 100003
4165 012624 004737 020776

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;*TEST 30 'WRITE' - 256 WORD BLOCK ON SECTOR 0, CYLINDER 0
;THE TEST BELOW SHOULD BE CONSIDERED AS A SET UP PHASE FOR
;THE FOLLOWING TEST. IT WRITES A BLOCK OF 256 WORDS IN
;SECTOR 0, CYLINDER 0 WITH A SPECIFIC PATTERN AND THIS WRITTEN
;BLOCK WILL BE MADE USE OF IN THE NEXT TEST TO CHECK
;OUT 'WRITE-CHECK' AND 'READ CHECK' FUNCTIONS.
;*****
†TST30: SCOPE
CNT.RESET
;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR& IF IT
;OCCURS GO BACK TO TEST 10
;CHECK IF SIN IS SET, IF SET
;DO DRIVE RESET TO CLEAR IT

TST.SIN
MOV RKCS,R4
;THE FOLLOWING CODE IS FOR SETTING
;UP THE I/O BUFFER IN MEMORY (STARTING AT
;OUTBUF), WITH A PARTICULAR 256 WORD PATTERN.
;STARTING FROM THE FIRST WORD IN THE BUFFER
;THE LO BYTE WILL BE A COUNT PATTERN
;FROM 0 TO 255 (DECIMAL), WHEREAS THE
;HI-BYTE WILL BE THE COMPLEMENT OF LO BYTE,
;A DECREASING COUNT PATTERN FROM 255 TO 0.
;I.E.THE BUFFER WILL LOOK LIKE:
;OUTBUF (1 111 111 1 00 000 000)
;OUTBUF+2 (1 111 111 0 00 000 001)
;LAST WORD (0 000 000 0 11 111 111)

MOV #OUTBUF,R0
MOV #177401,R1 ;PATTERN GENERATING NUMBER
MOV #-400,R2 ;SET UP COUNT FOR 256 WORDS
MOV #177400,R3 ;SET UP THE FIRST PATTERN TO B WRITTEN

MOV R3,(R0)+ ;SET UP FIRST WORD IN I/O BUFFER
INC R2 ;INCREMENT COUNT
1$: ADD R1,R3 ;SET UP NEXT WORD PATTERN
MOV R3,(R0)+ ;WRITE IT IN NXT I/O BUFFER WORD
INC R2 ;HAVE U WRITTEN ALL 256 WORDS
BNE 1$ ;IF NOT GO & WRITE NEXT PATTERN

MOV #-400,AR4 ;WRITE 256 WORDS
MOV #OUTBUF,AR4BA ;STARTING FROM THIS BUS ADRES
MOV DRIVAD,AR4DA ;TO THIS DISK ADRES, CYL 0, SEC 0

MOV #3,AR4 ;WRITE, GO

2$: TSTB AR4 ;WAS CNTRL RDY CLEARED AS GO WAS SET?
BPL 3$-2 ;YES, BRANCH
JSR PC,GT3RG ;GET RKCS, ER, DS

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4166 012630 104030          ERROR 30          ;CNTRL RDY DID NOT CLEAR AS GO WAS SET
4167                                     ;TO 'WRITE'
4168
4169 012632 005002          CLR      R2          ;DID CNTRL RDY SET?
4170 012634 105777 166472 3$:  TSTB   @RKCS        ;YES, BRANCH
4171 012640 100411          BMI     4$          ;WAITED LONG ENOUGH?
4172 012642 005202          INC     R2          ;IF NOT, LUP BAK & WAIT
4173 012644 001373          BNE    3$          ;IF YES, ERROR
4174
4175 012646 004737 020770  JSR     PC,GT4RG    ;GO, GET RKCD, ER, DS, DA
4176 012652 013737 001350 001202 MOV    DRIVAD,$REG10 ;GET THE STARING ADRES
4177 012660 104416          BRKDA4
4178
4179 012662 104031          ERROR 31          ;BREAK CONTENTS OF $REG10 INTO
4180                                     ;DRV #, CYL, SUR, SEC #
4181                                     ;CNTRL RDY DID NOT SET ON COMPLETION
4182                                     ;OF WRITE OF 256 WORDS ON CYL 0, SEC 0
4183                                     ;'RKDA' IN EROR MSGE GIVES THE
4184                                     ;CONTENTS OF RKDA AT THE TIME OF EROR
4185 012664 004737 021230 4$:  JSR     PC,CHKHE    ;WRITE WAS DONE STARTING AT <DSK-ADRES>
4186                                     ;INDICATED IN EROR MSGE
4187 012670 104032          ERROR 32          ;CHECK IF 'ERR' OR 'HE' BIT IS SET,
4188                                     ;IF YES, RETURN HERE
4189                                     ;HE OR ERR BIT SET ON DOING WRITE OF
4190                                     ;256 WORDS ON CYL 0, SEC 0
4191                                     ;WRITE WAS DONE STARTING AT <DSK-ADRES>
4192                                     ;INDICATED IN EROR MSGE
4193 012672 020077 166440 5$:  CMP     R0,@RKBA    ;'RKDA' IN EROR MSGE GIVES THE
4194 012676 001406          BEQ    6$          ;CONTENTS OF RKDA AT THE TIME OF EROR
4195 012700 010037 001162          MOV    R0,$REG0    ;DID RKBA INCREMENT CORRECTLY?
4196 012704 017737 166426 001164 MOV    @RKBA,$REG1 ;YES, BRANCH
4197 012712 104035          ERROR 35          ;GET EXPCTD RKBA
4198                                     ;GET RKBA RECVD
4199                                     ;RKBA DID NOT INCREMENT CORRECTLY
4200 012714 004737 021312 6$:  JSR     PC,CHKWC    ;(BY 1000 OCTAL BYTES) AFTER WRITE
4201                                     ;OF 400 (OCTAL) WORDS ON SEC 0, CYL 0
4202 012720 104034          ERROR 34          ;CHECK IF RKWC OVERFLOWED TO 0,
4203                                     ;IF NOT RETURN HERE.
4204 012722 004737 021256 7$:  JSR     PC,CHKDA    ;RKWC DID NOT OVERFLOW, AFTER A
4205                                     ;WRITE OF 256 WORDS ON CYL 0, SEC 0
4206 012726 104033          ERROR 33          ;CHECK IF RKDA INCREMENTED CORRECTLY,
4207                                     ;IF NOT RETURN HERE
4208 012730 004737 021336 8$:  JSR     PC,CHKER    ;RKDA DID NOT INCREMENT BY 1 AFTER
4209                                     ;A WRITE OF 256 WORDS IN CYL 0, SEC 0
4210 012734 104036          ERROR 36          ;CHECK IF ANY BIT RKER IS SET
4211                                     ;IF YES RETURN HERE.
4212 012736 022714 000202 9$:  CMP     #202,@R4    ;RKER BIT SET ON DOING WRITE ON
4213 012742 001406          BEQ    TST31        ;CYLINDER 0, SECTOR 0
4214 012744 012737 000202 001162 MOV    #202,$REG0   ;DOES RKCS STILL CONTAIN THE WRITE BITS?
4215 012752 011437 001164          MOV    @R4,$REG1   ;YES, EXIT
4216 012756 104024          ERROR 24          ;GET EXPECTED RKCS
4217                                     ;GET RKCS RECVD
4218                                     ;RKCS DID NOT CONTAIN THE 'WRITE'
4219                                     ;BITS AFTER THE FUNCTION WAS DONE.
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; *TEST 31      CHECK THAT WRITE WAS DONE CORRECTLY

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012760 000004
012762 104413

012764 104421

012766 012700 177400
012772 012701 033336
012776 005021
013000 005200
013002 001375
013004 005000
013006 012777 177400 166320
013014 012777 033336 166314
013022 013777 001350 166310

013030 012777 000005 166274

013036 105777 166270
013042 100411
013044 005200
013046 001373

013050 004737 020770
013054 013737 001350 001202
013062 104416

013064 104045

013066 032777 001000 166232
013074 001033
013076 012701 177400
013102 012702 177777
013106 012703 033336
013112 012705 177773
013116 062702 177401

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; *THIS TEST CHECKS IF THE 'WRITE' OF 256 WORDS DONE IN PPREVIOUS
; *TEST IS GOOD. THE SEQUENCE OF OPERATIONS IS AS FOLLOWING:
; *1) DO A READ OF 256 WORDS FROM SECTOR 0, CYLINDER 0
; * INTO A BUFFER STARTING AT 'OUTBUF'
; *2) COMPARE & CHECK THE DATA THAT IS READ (STARTING AT 'OUTBUF')
; * WITH THE DATA THAT WAS GENERATED PREVIOUSLY
; *3) REPORT AN ERROR IF THE DATA READ BACK FROM DISK DOES
; * NOT COMPARE WITH DATA THAT WAS SUPPOSE TO HAVE BEEN WRITTEN
;*****
†T31: SCOPE
      CNT.RESET
      GO, DO CONTROL RESET
      THIS IS A CALL FOR THE 'CNTRL-
      RESET' ROUTINE. A CONTROL RESET IS
      ISSUED AND AFTER A CERTAIN TIME
      IF THE 'CNTRL RDY' DOES NOT SET
      AN ERROR IS REPORTED. NOTE THAT
      THE PC IN ERROR MESSAGE IS THE
      PC WHERE 'CNT.RESET' IS LOCATED.
      THIS IS A VERY BASIC ERR& IF IT
      OCCURS GO BACK TO TEST 10
      CHECK IF SIN IS SET, IF SET
      DO DRIVE RESET TO CLEAR IT
      SET COUNT FOR 400 WORDS
      TO BE CLEARED IN THE BUFFER
      CLR THE 400 WORD BUFFER
      STARTING AT 'OUTBUF'
      READ 256 WORDS
      INTO THIS ADRES
      STARTING FROM THIS DISK ADRES
      READ, GO
      DID CNTRL RDY SET?
      YES, BRANCH
      WAITED LONG ENOUGH?
      IF NOT, LUP BAK & WAIT
      ERROR, IF YES
      GO, GET RKCD, ER, DS, DA
      GET THE STARTING ADRES
      GO TO 'BD44' & BREAK CONTENTS OF
      $REG10 INTO DRV #, CYL, SUR, SEC BITS
      CNTRL RDY DID NOT SET AFTER READ
      OF 400 WORDS FROM CYL 0, SEC 0
      'RKDA' IN EROR MSGE GIVES THE
      CONTENTS OF RKDA AT THE TIME OF EROR
      READ WAS DONE STARTING AT (DSK-ADRES)
      INDICATED IN EROR MESGE
      IS SIN SET?
      IF YES, EXIT
      BIT #1000,ARKDS
      BNE TST32
      MOV #-400,R1
      MOV #177777,R2
      MOV #OUTBUF,R3
      MOV #-5,R5
      ADD #177401,R2
      TST.SIN
      8$: MOV #-400,R0
      MOV #OUTBUF,R1
      CLR (R1)+
      INC R0
      BNE 8$
      CLR R0
      MOV #-400,ARKWC
      MOV #OUTBUF,ARKBA
      MOV DRIVAD,ARKDA
      1$: TSTB ARKCS
      BMI 2$
      INC R0
      BNE 1$
      JSR PC,GT4RG
      MOV DRIVAD,$REG10
      BRKDA4
      ERROR 45
      2$: BIT #1000,ARKDS
      BNE TST32
      5$: MOV #-400,R1
      MOV #177777,R2
      MOV #OUTBUF,R3
      MOV #-5,R5
      6$: ADD #177401,R2
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4278 013122 020213
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4280 013124 001414
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4282 013126 010137 001162
4283 013132 062737 000401 001162
4284 013140 010237 001164
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4286 013144 011337 001166
4287 013150 104055
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4291 013152 005205
4292 013154 001403
4293 013156 005723
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4295 013160 005201
4296 013162 001355
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4305 013164 000004
4306 013166 104413
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4316 013170 104421
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4318 013172 013701 001332
4319 013176 013702 001334
4320 013202 013703 001340
4321 013206 013704 001336
4322 013212 012737 052525 033336
4323 013220 012712 177400
4324 013224 013713 001350
4325 013230 012714 033336
4326 013234 012711 000013
4327
4328 013240 105711
4329 013242 100003
4330 013244 004737 020776
4331 013250 104030
4332 013252 104412
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CMP      R2,(R3) ;WAS THE READ WORD SAME AS THE WORD
          ;THAT WAS SUPPOSE TO BE WRITTEN
BEQ      7$      ;YES, BRANCH
          ;NO, ERROR
MOV      R1,$REG0 ;GET THE # OF WORD
ADD      #401,$REG0 ;THAT IS IN ERROR (EXAMPLE=1,2--376,377,400)
MOV      R2,$REG1 ;GET EXPCD WORD (THAT WAS SUPPOSED TO
          ;BE WRITTEN)
MOV      (R3),$REG2 ;GET WORD RECVD (THAT WAS READ BAK)
ERROR    55      ;DID NOT READ BACK WORD THAT WAS SUPPOSED
          ;TO HAVE BEEN WRITTEN PREVIOUSLY. POSITION
          ;OF WORD IN ERROR IS AS INDICATED BY
          ;WORD # ($REG0), SEC 0, CYL 0

          ;:EXIT
7$:      BEQ      TST32
          TST      (R3)+ ;INCREMENT POINTER TO NXT WORD (THAT
          ;WAS READ BACK)
          INC      R1      ;HAVE U CHKD ALL 256 WORDS?
          BNE     6$      ;IF NOT, LUP BAK & CHK THE NXT WORD
          ;IF YES, EXIT

;:*****
;:TEST 32      CHECK 'READ CHECK' FUNCTION - CYLINDER 0, SECTOR 0
;:THIS TEST CHECKS OUT THE BASIC 'READ CHECK' LOGIC, USING THE DATA BLOCK
;:('CYLINDER, SECTOR 0) WRITTEN IN A PREVIOUS TEST. HENCE THE TEST WHICH
;:WRITES THE DATA BLOCK SHOULD BE DONE PRIOR TO THIS TEST.
;:*****
TST32:   SCOPE
          CNT.RESET      ;GO, DO CONTROL RESET
          ;THIS IS A CALL FOR THE 'CNTRL-
          ;RESET' ROUTINE. A CONTROL RESET IS
          ;ISSUED AND AFTER A CERTAIN TIME
          ;IF THE 'CNTRL RDY' DOES NOT SET
          ;AN ERROR IS REPORTED. NOTE THAT
          ;THE PC IN ERROR MESSAGE IS THE
          ;PC WHERE 'CNT.RESET' IS LOCATED.
          ;THIS IS A VERY BASIC ERR& IF IT
          ;OCCURS GO BACK TO TEST 10
          ;CHECK IF SIN IS SET, IF SET
          ;DO DRIVE RESET TO CLEAR IT

          TST.SIN

MOV      RKCS,R1
MOV      RKWC,R2
MOV      RKDA,R3
MOV      RKBA,R4
MOV      #52525,OUTBUF
MOV      #-400,R2      ;READ CHECK 256 WORDS
MOV      DRIVAD,R3     ;STARTING FROM CYL 0, SECTOR 0
MOV      #OUTBUF,R4
MOV      #13,R1        ;READ CHECK, GO

1$:      TSTB     R1      ;DID CNTRL RDY GET CLEARED AS GO WAS SET?
          BPL     2$      ;YES, BRANCH
          JSR     PC,GT3RG ;GET RKCS, ER, DS
          ERROR  30      ;CNTRL RDY DID NOT CLEAR AS GO
2$:      CHKCRDY ;GO CHECK IF CONTROL RDY IS SET
          ;IF SO, SKIP THE EROR MESSAGE.

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4334
4335 013254 104056          ERROR 56          ; WAS SET TO 'READ CHECK'
4336
4337 013256 032711 140000    3$: BIT #140000, R1    ; CNTRL RDY DID NOT SET ON DOING
4338 013262 001403          BEQ 4$          ; 'READ CHECK' FROM CYL 0, SEC 0
4339 013264 004737 020776    JSR PC,GT3RG    ; DID 'ERR' OR 'HE' BIT SET?
4340 013270 104057          ERROR 57          ; NO, BRANCH
4341
4342 013272 032777 000002 166030 4$: BIT #2, RKER    ; GO, GET RKCS, ER DS FOR ERROR MESSAGE
4343 013300 001404          BEQ 5$          ; 'ERR' OR 'HE' BIT SET ON DOING
4344 013302 017737 166022 001162 MOV RKER, $REG0 ; 'READ CHECK' ON CYLINDER 0, SEC 0
4345 013310 104060          ERROR 60          ; DID 'CSE' BIT SET IN RKER?
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4347
4348 013312 005712          5$: TST R2        ; NO, BRANCH
4349 013314 001405          BEQ 6$          ; GET RKER
4350 013316 011237 001162    MOV R2, $REG0   ; SOFT ERROR - CSE - ON DOING 'READ
4351 013322 011137 001164    MOV R1, $REG1   ; CHECK' ON CYLINDER 0, SECTOR 0
4352 013326 104061          ERROR 61          ; U SHOULD HAVE GOT ERROR 102 ALSO
4353
4354 013330 013702 001350    6$: MOV DRIVAD, R2 ; DID WORD COUNT OVERFLOW TO 0?
4355 013334 005202          INC R2          ; YES, BRANCH
4356 013336 020213          CMP R2, R3     ; GET RKWC
4357 013340 001405          BEQ 7$          ; GET RKCS
4358 013342 010237 001162    MOV R2, $REG0   ; WORD COUNT DID NOT OVERFLOW
4359 013346 011337 001164    MOV R3, $REG1   ; ON DOING 'READ CHK' ON CYL 0, SEC 0
4360 013352 104062          ERROR 62          ; RKDA SHOULD INCREMENT
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4363 013354 022714 033336    7$: CMP #OUTBUF, R4 ; TO THIS AFTER 'RD CHK' IS DONE
4364 013360 001406          BEQ 8$          ; DID RKDA INCREMENT CORRECTLY?
4365 013362 012737 033336 001162 MOV #OUTBUF, $REG0 ; GET EXPCTD RKDA
4366 013370 011437 001164    MOV R4, $REG1   ; GET RKDA RECVD
4367 013374 104063          ERROR 63          ; RKDA DID NOT INCREMENT CORRECTLY
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4370 013376 022737 052525 033336 8$: CMP #52525, OUTBUF ; (BY 1) ON DOING 'READ CHK' ON
4371
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4374 013404 001412          BEQ TST33      ; CYL 0, SEC 0
4375
4376 013406 012737 033336 001162 MOV #OUTBUF, $REG0 ; DID RKBA GET CHANGED?
4377 013414 012737 052525 001164 MOV #52525, $REG1 ; NO, BRANCH (RKBA WON'T CHANGE, NO NPR'S)
4378 013422 013737 033336 001166 MOV OUTBUF, $REG2 ; GET EXPCTD RKBA
4379 013430 104064          ERROR 64          ; GET RKBA RECVD
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*****
; *TEST 33 CHECK THE 'WRITE CHECK' FUNCTION - ON CYLINDER 0, SECTOR 0
; *THIS TEST CHECKS OUT THE BASIC 'WRITE CHECK' LOGIC, USING THE 256
; *WORD DATA BLOCK (SECTOR 0, CYLINDER 0) WRITTEN IN A PREVIOUS
; *TEST. THE BUFFER IN MEMORY, USED FOR COMPARISON OF DATA, IS THE

```

:*ONE STARTING AT 'OUTBUF'. HENCE THE TEST WHICH WRITES THE
:*256 WORD BLOCK ON THE DISK (AS WELL AS CREATING THE 256
:*256 WORD MEMORY BUFFER) SHOULD BE DONE BEFORE THIS TEST.

::*****

†T33: SCOPE
CNT.RESET

:GO, DO CONTROL RESET
:THIS IS A CALL FOR THE 'CNTRL-
:RESET' ROUTINE. A CONTROL RESET IS
:ISSUED AND AFTER A CERTAIN TIME
:IF THE 'CNTRL RDY' DOES NOT SET
:AN ERROR IS REPORTED. NOTE THAT
:THE PC IN ERROR MESSAGE IS THE
:PC WHERE 'CNT.RESET' IS LOCATED.
:THIS IS A VERY BASIC ERR& IF IT
:OCCURS GO BACK TO TEST 10
:CHECK IF SIN IS SET, IF SET
:DO DRIVE RESET TO CLEAR IT

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013432 000004
013434 104413

013436 104421

013440 013701 001332
013444 012700 177400
013450 012702 033336
013454 012703 177777
013460 062703 177401
013464 010322
013466 005200
013470 001373
013472 012777 177400 165634
013500 012777 033336 165630
013506 013777 001350 165624
013514 012711 000007

TST.SIN

1\$:

MOV RKCS,R1
MOV #-400,R0
MOV #OUTBUF,R2
MOV #177777,R3
ADD #177401,R3
MOV R3,(R2)+
INC R0
BNE 1\$
MOV #-400,DRKWC
MOV #OUTBUF,DRKBA
MOV DRIVAD,DRKDA
MOV #7,DR1

:WRITE CHECK 256 WORDS
:STARTING AT THIS BUS PDRES
:WITH THIS DISK DATA BLOCK (CYL 0, SEC 0)
:WRITE CHECK, GO

2\$:

CLR R0
TSTB DR1
BPL 3\$
JSR PC,GT3RG
ERROR 30

:GIVE SOME TIME
:DID CNTRL RDY CLEAR AS GO WAS SET?
:YES BRANCH
:GET RKCS, ER, DS
:CNTRL RDY DID NOT CLEAR AS GO WAS
:SET TO DO WRITE CHECK

3\$:

CHKCRDY
ERROR 65

:GO CHECK IF CONTROL RDY IS SET
:IF SO, SKIP THE EROR MESSAGE.
:CNTRL RDY DID NOT SET AFTER
:COMPLETING WRITE CHECK ON
:CYLINDER 0, SECTOR 0

4\$:

BIT #140000,DR1
BEQ 5\$
JSR PC,GT3RG
ERROR 66

:DID HE OR ERR BIT SET
:NO, BRANCH
:GO GET RKCS ER DS FOR ERROR MESSAGE
:HE OR ERR BIT SET ON DOING WRITE
:CHK ON CYLINDER 0, SEC 0

5\$:

BIT #1,DRKER
BEQ 6\$
JSR PC,GT3RG
ERROR 67

:DID WCE SET IN RKER?
:NO, BRANCH
:YES GET RKCS, ER, DS
:WCE ON WRITE CHECK OF CYL 0, SEC 0
:NOTE THAT IF A PREVIOUS TEST
:& THEN COMPARED WITH MEMORY BUFFER
:TO SEE IF IT WAS WRITTEN CORRECT WAS
:DONE RIGHT BEFORE, THIS ERROR SHOULD NOT
:HAPPEN UNLESS THERE IS A FAULT IN THE
:COMPARING LOGIC OF 'WRT CHK'

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4446 013572 005777 165536      6$:  TST      @RKWC      ;DID RKWC OVERFLOW?
4447 013576 001406      7$:  BEQ      7$        ;YES, BRANCH
4448 013600 017737 165530 001162  MOV      @RKWC,$REG0 ;NO, GET RKWC
4449 013606 011137 001164      MOV      @R1,$REG1  ;GET RKCS
4450 013612 104061      ERROR    61        ;RKWC DID NOT OVERFLOW AFTER
4451                                ;WRITE CHECK ON CYL 0, SEC 0
4452 013614 013704 001350      7$:  MOV      DRIVAD, R4 ;RKDA SHOULD INCREMENT
4453 013620 005204      INC      R4         ;TO THIS AFTER WRT CHK
4454 013622 020477 165512      CMP      R4,@RKDA   ;DID RKDA INCREMENT CORRECTLY?
4455 013626 001406      BEQ      8$        ;YES, BRANCH
4456 013630 010437 001162      MOV      R4,$REG0   ;NO, GET EXPCTD RKDA
4457 013634 017737 165500 001164  MOV      @RKDA,$REG1 ;GET RKDA RECVD
4458 013642 104070      ERROR    70        ;RKDA DID NOT INCREMENT CORRECTLY
4459                                ;(BY 1 SECTOR) AFTER WRT CHK ON SEC 0, CYL 0
4460 013644 022777 034336 165464  8$:  CMP      @OUTBUF+1000,@RKBA ;DID RKBA INCREMENT CORRECTLY?
4461 013652 001407      BEQ      9$        ;YES, EXIT
4462 013654 012737 034336 001162  MOV      @OUTBUF+1000,$REG0 ;GET EPCTD RKBA
4463 013662 017737 165450 001164  MOV      @RKBA,$REG1 ;GET RKBA RECVD
4464 013670 104071      ERROR    71        ;RKBA DID NOT INCREMENT CORRECTLY
4465                                ;(BY 1000 BYTES) AFTER A WRT CHK
4466                                ;OF 256 WORDS ON CYL 0, SEC 0
4467 013672 022711 000206      9$:  CMP      #206,@R1   ;DOES RKCS STILL CONTAIN THE WRT CHK BITS?
4468 013676 001406      BEQ      TST34     ;YES, BRANCH
4469 013700 012737 000206 001162  MOV      #206,$REG0 ;NO, GET EXPCTD RKCS
4470 013706 011137 001164      MOV      @R1,$REG1 ;GET RKCS RECVD
4471 013712 104024      ERROR    24        ;RKCS BITS CHANGED AFTER WRT CHK
4472                                ;WAS DONE
4473                                ;*****
4474                                ;*TEST 34 CHECK THAT IBA INHIBITS INCREMENTING OF RKBA
4475                                ;*THIS TEST CHECKS THAT THE BUS ADDRESS DOES NOT INCREMENT WHEN
4476                                ;*THE IBA BIT IS SET. SEQUENCE OF OPERATIONS:
4477                                ;*1) CLEAR OUT 256 WORD BUFFER IN MEMORY (OUTBUF),
4478                                ;*2) READ FROM SECTOR 0, CYLINDER 0 THE 256 WORD BLOCK THAT WAS
4479                                ;*WRITTEN IN A PREVIOUS TEST (NOTE: THAT TEST SHOULD HAVE BEEN
4480                                ;*DONE BEFORE THIS). IBA BIT IS SET DURING READ BACK.
4481                                ;*3) CHECK THAT RKBA DID NOT INCREMENT
4482                                ;*4) CHECK THAT THE ENTIRE BLOCK WAS READ INTO THE SAME MEMORY
4483                                ;*WORD (OUTBUF) & THE REST OF THE WORDS IN THAT BUFFER ARE 0
4484                                ;*AS PREVIOUSLY CLEARED OUT.
4485                                ;*****
4486 013714 000004      TST34: SCOPE
4487 013716 104413      CNT.RESET
4488                                ;GO, DO CONTROL RESET
4489                                ;THIS IS A CALL FOR THE 'CNTRL-
4490                                ;RESET' ROUTINE. A CONTROL RESET IS
4491                                ;ISSUED AND AFTER A CERTAIN TIME
4492                                ;IF THE 'CNTRL RDY' DOES NOT SET
4493                                ;AN ERROR IS REPORTED. NOTE THAT
4494                                ;THE PC IN ERROR MESSAGE IS THE
4495                                ;PC WHERE 'CNT.RESET' IS LOCATED.
4496                                ;THIS IS A VERY BASIC ERRS IF IT
4497 013720 104421      TST.SIN
4498                                ;OCCURS GO BACK TO TEST 10
4499                                ;CHECK IF SIN IS SET, IF SET
4500 013722 013701 001332      MOV      RKCS,R1
4501 013726 012700 177400      MOV      #-400,R0   ;SET UP COUNT FOR 256 WORDS
4501 013732 012702 033336      MOV      @OUTBUF,R2

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4502	013736	010203			MOV	R2,R3	
4503							
4504	013740	005023			1\$: CLR	(R3)+	;CLEAR OUT THE 256
4505	013742	005200			INC	RO	;WORD MEMORY BUFFER STARTING
4506	013744	001375			BNE	1\$;AT 'OUTBUF'
4507	013746	012777	177400	165360	MOV	#-400,ARKWC	;READ BACK 256 WORDS
4508	013754	010277	165356		MOV	R2,ARKBA	;INTO THIS BUS ADRES (IBA WILL B SET)
4509	013760	013777	001350	165352	MOV	DRIVAD,ARKDA	;FROM THIS DSK ADRES (SEC 0, CYL 0)
4510							;NOTE: SEC 0 HAS BEEN WRITTEN IN A
4511							;PREVIOUS TEST WITH A UNIQUE PATTERN
4512	013766	012711	004005		MOV	#4005,AR1	;READ, GO, IBA SET
4513							
4514	013772	005037	001362		CLR	COUNT	
4515	013776	105711			2\$: TSTB	AR1	;DID CNTRL RDY SET?
4516	014000	100412			BMI	3\$;YES, BRANCH
4517	014002	005237	001362		INC	COUNT	;WAITED LONG ENOUGH?
4518	014006	001373			BNE	2\$;IF NOT, LUP BAK & WAIT
4519	014010	004737	020770		JSR	PC,GT4RG	;GO, GET RKCS, ER, DS, DA
4520	014014	013737	001350	001202	MOV	DRIVAD,\$REG10	;GET THE STARTING ADRES
4521	014022	104416			BRKDA4		;BREAK CONTENTS OF \$REG10
4522							;INTO DR #, CYL, SUR, SEC
4523	014024	104045			ERROR	45	;CNTRL RDY DID NOT SET AFTER DOING
4524							;READ
4525	014026	004737	021230		3\$: JSR	PC,CHKHE	;CHECK IF 'ERR' OR 'HE' BIT IS SET.
4526							;IF YES, RETURN HERE.
4527	014032	104046			ERROR	46	;ERR BIT SET ON DOING READ FROM SEC 0,
4528							;CYL 0 (INDICATED IN <DSK-ADRES>)
4529							; 'RKDA' IN EROR MSGE GIVES THE
4530							;CONTENTS OF RKDA AT THE TIME OF EROR
4531							
4532	014034	020277	165276		4\$: CMP	R2,ARKBA	;DID RKBA INCREMENT?
4533	014040	001406			BEQ	5\$;OK IF NOT, BRANCH
4534	014042	010237	001162		MOV	R2,\$REG0	;GET EXPCTD RKBA
4535	014046	017737	165264	001164	MOV	ARKBA,\$REG1	;GET RKBA RECVD
4536	014054	104072			ERROR	72	;RKBA INCREMNTED WHEN IBA BIT WAS
4537							;SET, SHOULD NOT HAVE
4538	014056	032777	001000	165242	5\$: BIT	#1000,ARKDS	;IS SIN SET?
4539	014064	001042			BNE	TST35	;IF YES, EXIT
4540	014066	012700	177400		MOV	#-400,RO	
4541	014072	022712	000377		CMP	#377,AR2	;CHECK THAT THE FIRST WORD IN
4542							'OUTBUF' IS 377 (LAST WORD OF SEC 0,
4543							CYL 0). NOTE THAT READ WAS DONE
4544	014076	001411			BEQ	6\$;INTO THIS SAME WRD WITH IBA SET
4545	014100	012737	000377	001162	MOV	#377,\$REG0	;GET EXPCTD WORD (LAST WORD OF THE BUFFER
4546	014106	011237	001164		MOV	(R2),\$REG1	;GET WORD RECVD (LAST WRD FROM SEC 0)
4547	014112	013737	001350	001166	MOV	DRIVAD,\$REG2	;DISK ADRES WHERE ERROR OCCURED
4548							(SEC 0, CYL 0 LAST WORD)
4549							;DATA ERROR
4550	014120	104044			ERROR	44	;THE FIRST WORD IN MEM BUFFER (OUTBUF)
4551							;SHOULD BE NON-ZERO & SHOULD CONTAIN
4552							;THE LAST WORD READ BACK FROM SEC 0
4553							;CYL 0, THIS DID NOT HAPPEN IF THE ERROR OCCURS
4554	014122	005722			6\$: TST	(R2)+	;INCREMENT POINTER TO THE NXT WORD
4555	014124	012705	177773		MOV	#-5,R5	;ALLOW ONLY 5 MESAGES FOR ERR 116
4556	014130	005200			7\$: INC	RO	;CHKD ALL 256 WORDS IN THE BUFFER?
4557	014132	001417			BEQ	TST35	::YES, EXIT

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4558 014134 005722      TST      (R2)+      ;IS THIS WORD 0?
4559 014136 001774      BEQ      7$         ;YES, LUP BAK & CHK THE NXT WORD?
4560 014140 005037 001164 CLR      $REG1      ;ERROR. GET EXPCTED WORD - 0
4561 014144 014237 001166 MOV      -(R2), $REG2 ;GET WORD THAT WAS FOUND IN THE BUFFER
4562 014150 010004      MOV      R0, R4
4563 014152 062704 000401 ADD      #401, R4
4564 014156 010437 001162 MOV      R4, $REG0  ;THIS 'WORD #' IN MEMORY BUFFER
4565                                     ;SHOULD HAVE BEEN ZERO
4566 014162 104073      ERROR     73       ;THE 256 WORD BUFR (STARTING AT
4567                                     ;OUTBUF) WAS CLEARED BEFORE READING
4568                                     ;BAK SEC 0 INTO IT. SINCE THE IBA
4569                                     ;BIT WAS SET DURING THE READ, ONLY
4570                                     ;THE FIRST WORD OF (OUTBUF) SHOULD
4571                                     ;HAVE CHANGED. THE REST OF THE WORDS
4572                                     ;SHOULD BE STILL 0. IF THIS ERROR
4573                                     ;OCCURS, 'WORD #' (OF THE BUFFER) AS
4574                                     ;INDICATED IN THE EROR MESSAGE) GOT
4575                                     ;CHANGED WHEN READ WAS DONE FROM
4576                                     ;THE DISK, INDICATING THAT WITH IBA
4577                                     ;SET X-FER WAS NOT DONE INTO THE
4578                                     ;SAME MEMORY LOCATION. 'WORD #'
4579                                     ;IS OCTAL & SPECIFIES THE POSITION
4580                                     ;IN THE BUFFER (FIRST WORD IS 'WORD #' 1)
4581 014164 005205      INC      R5
4582 014166 001401      BEQ      TST35     ;EXIT
4583 014170 000757      BR       7$
4584
4585 ;*****
4586 ;*TEST 35 CHECK THAT RK11 INTERRUPTS WHEN IDE IS SET
4587 ;*THIS TEST CHECKS IF RK11 INTERRUPTS TO ITS DESIGNATED VECTOR
4588 ;*ADDRESS WHEN IDE BIT IS SET, WITH CONTROL READY SET & GO CLEAR.
4589 ;* IT IS NORMALLY 220, UNLESS IT HAS BEEN CHANGED. IF IT HAS BEEN
4590 ;*CHANGED RK11 WILL INTERRUPT TO 'RKVEC'. NOTE 'RKVEC' HAS
4591 ;*TO BE SET UP BY THE USER.
4592 ;*****
4593 014172 000004      TST35: SCOPE
4594 014174 104413      CNT.RESET        ;GO, DO CONTROL RESET
4595                                     ;THIS IS A CALL FOR THE 'CNTRL-
4596                                     ;RESET' ROUTINE. A CONTROL RESET IS
4597                                     ;ISSUED AND AFTER A CERTAIN TIME
4598                                     ;IF THE 'CNTRL RDY' DOES NOT SET
4599                                     ;AN ERROR IS REPORTED. NOTE THAT
4600                                     ;THE PC IN ERROR MESSAGE IS THE
4601                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
4602                                     ;THIS IS A VERY BASIC ERR& IF IT
4603                                     ;OCCURS GO BACK TO TEST 10
4604 014176 104421      TST.SIN        ;CHECK IF SIN IS SET, IF SET
4605                                     ;DO DRIVE RESET TO CLEAR IT
4606 014200 012746 000340 MOV      #340, -(SP)
4607 014204 012746 014212 MOV      #64$, -(SP)
4608 014210 000002      RTI
4609 014212      64$:
4610 014212 013701 001332 MOV      RKCS, R1
4611 014216 013700 001402 MOV      RKVEC, R0 ;GET POINTER TO RK VECTOR ADRES
4612 014222 012720 014256 MOV      #1$, (R0)+ ;SET UP INTERRUPT VECTOR FOR RK11
4613 014226 012710 000340 MOV      #340, (R0) ;SET PSW ON INTERRUPT

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4614 014232 105711          TSTB   2R1          ;WAIT FOR CNTRL RDY TO SET
4615 014234 100376          BPL    -2          ;
4616 014236 012711 000100  MOV    #100,2R1    ;SET IDE BIT IN RKCS
4617 014242 104420 000005  WAT.INT ,5        ;WAIT FOR INTERRUPT, ATLEAST
4618                                ;37 US FOR 11/20, 7 US FOR 11/45
4619 014246 011137 001162  MOV    2R1,$REGD   ;GET RKCS
4620 014252 104074          ERROR  74         ;RK11 DID NOT INTERRUPT WHEN IDE
4621                                ;WAS SET, WITH CNTRLE RDY SET & GO
4622                                ;CLEAR
4623 014254 000400          BR     1$         ;
4624 014256 022626          1$:  CMP    (SP)+,(SP)+ ;RK11 INTERRUPTED CORRECTLY TO
4625                                ;THIS. RESTORE STACK POINTER
4626                                ;(FROM RK11 INTERRUPT)
4627 014260 022626          CMP    (SP)+,(SP)+ ;RESTORE STACK POINTER
4628                                ;(FROM WAT.INT)
4629 014262 012777 014276 165112 MOV    #2$,2RKVEC ;IF THERE IS FAULTY POLLING OR INTERRUPT
4630                                ;LOGIC SECOND INTERRUPT MIGHT OCCUR
4631 014270 104420 000005  WAT.INT ,5        ;WAIT FOR INTERRUPT IF ANY
4632                                ;DUE TO FAULTY LOGIC
4633
4634 014274 000403          BR     3$         ;
4635
4636 014276 022626          2$:  CMP    (SP)+,(SP)+ ;RESTORE STACK PTR (FROM RK11 INTRUPT)
4637 014300 022626          CMP    (SP)+,(SP)+ ;RESTORE STACK PTR (FROM WAT.INT)
4638 014302 104020          ERROR  20         ;AN UNEXPECTED RK11 INTERRUPT
4639                                ;OCCURED. THERE SHOULD HAVE BEEN
4640                                ;ONLY 1 INTERRUPT (TO 1$ ABOVE)
4641 014304 012777 004600 165070 3$:  MOV    #BADINT,2RKVEC ;RESTORE VECTOR ADRES FOR
4642                                ;UNEXPECTED RK11 INTERRUPT.
4643 014312 012746 000340  MOV    #340,-(SP)
4644 014316 012746 014324  MOV    #65$,-(SP)
4645 014322 000002          RTI
4646 014324          65$:
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4657
4658 014324 000004          ;:*****
4659 014326 012737 000005 001206 †ST36: SCOPE          ;*****
4660 014334 104413          MOV    #5,$TIMES ;DO 5 ITERATIONS
4661                                ;GO, DO CONTROL RESET
4662                                ;THIS IS A CALL FOR THE 'CNTRL-
4663                                ;RESET' ROUTINE. A CONTROL RESET IS
4664                                ;ISSUED AND AFTER A CERTAIN TIME
4665                                ;IF THE 'CNTRL RDY' DOES NOT SET
4666                                ;AN ERROR IS REPORTED. NOTE THAT
4667                                ;THE PC IN ERROR MESSAGE IS THE
4668                                ;PC WHERE 'CNT.RESET' IS LOCATED.
4669                                ;THIS IS A VERY BASIC ERR3 IF IT
4670                                ;OCCURS GO BACK TO TEST 10

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;:*****
;*TEST 36 CHECK THAT WITH IDE SET RK11 INTERRUPTS AFTER INITIATION & COMPLETION OF
; *THIS TEST CHECKS THAT AN INTERRUPT FROM RK11 OCCURS AFTER
; *A SEEK IS INITIATED WITH 'IDE' BIT SET, AND THEN A SECOND
; *INTERRUPT OCCURS AFTER THE SEEK IS DONE. IT ALSO CHECKS THAT
; *AFTER THE FIRST INTERRUPT 'SCP' BIT IS NOT SET, WHEREAS AFTER
; *THE SECOND INTERRUPT 'SCP' IS SET.
; *THIS TEST ALSO CHECKS A PART OF THE POLLING LOGIC.
;:*****

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4670 014336 013700 001332      MOV      RKCS,RO
4671 014336 013700 001350 164770  MOV      DRIVAD,ARKDA ;ADRES THE DRIVE
4672 014350 004737 021500      JSR      PC,DRESET    ;GO, DO DRIVE RESET
4673 014354 104026                ERROR    26           ;R/W/S RDY DIDN'T SET AFTER DOING
4674                                ;ABOVE DRIVE RESET
4675 014356 013701 001402      2$:     MOV      RKVEC,R1
4676 014362 012721 014426      MOV      #3$, (R1)+   ;SET UP VECTOR ADRES FOR RK11 INTERUPT
4677 014366 012711 000340      MOV      #340, (R1)   ;SET UP PSW ON INTERRUPT
4678 014372 052777 000040 164740  BIS      #40,ARKDA    ;ADRES CYLINDER #1
4679 014400 012710 000111      MOV      #111,AR0    ;SEEK, GO WITH IDE SET
4680 014404 104420 000300      WAT.INT ,300        ;WAIT FOR THE DRIVE TO
4681                                ;INTERRUPT AFTER ADRES WAS RECVD
4682                                ;WAITING TIME= 1.4 MS FOR 11/20
4683                                ;280 US FOR 11/45
4684                                ;ERROR, IF INTERUPT DID NOT OCCUR
4685                                ;BY NOW
4686 014410 012777 004600 164764  MOV      #BADINT,ARKVEC ;RESTORE UNEXPECTED RK11 INTERRUPT
4687 014416 011037 001162      MOV      AR0,$REGO    ;GET RKCS
4688 014422 104075                ERROR    75           ;INTERRUPT DID NOT OCCUR AFTER
4689                                ;SEEK WAS INITIATED WITH IDE SET
4690 014424 000402                BR       3$+4
4691 014426 022626      3$:     CMP      (SP)+,(SP)+  ;OK, IF RK11 INTERRUPTED TO THIS
4692                                ;RESTORE STACK POINTER (FROM RK11 INTERRUPT)
4693 014430 022626      CMP      (SP)+,(SP)+  ;RESTORE STACK POINTER (FROM
4694                                ;WAT.INT)
4695 014432 012777 014476 164742  MOV      #5$,ARKVEC   ;SET UP NEW VECTOR ADRES FOR RK11
4696 014440 032710 020000      BIT      #20000,AR0  ;IS SCP CLEAR
4697 014444 001403                BEQ     4$            ;YES, BRANCH
4698 014446 011037 001162      MOV      AR0,$REGO    ;GET RKCS
4699 014452 104076                ERROR    76           ;SCP SET BEFORE SEEK TO LAST
4700                                ;CYLINDER WAS DONE
4701 014454 104420 056700      4$:     WAT.INT ,56700  ;WAIT FOR DRIVE TO INTERRUPT
4702                                ;AFTER SEEK WAS COMPLETED
4703                                ;WAITING TIME=180 MS FOR 11/20
4704                                ;36 MS FOR 11/45
4705 014460 012777 004600 164714  MOV      #BADINT,ARKVEC ;IT'S AN ERROR IF BY THIS TIME
4706                                ;INTERRUPT HAS NOT OCCURED
4707 014466 004737 020776      JSR      PC,GT3RG     ;GO GET RKCS, ER, DS
4708 014472 104077                ERROR    77           ;RK11 DID NOT INTERRUPT AFTER SEEK (TO
4709                                ;LAST CYLINDER) WAS DONE WITH IDE SET
4710 014474 000401                BR       5$+2
4711 014476 022626      5$:     CMP      (SP)+,(SP)+  ;OK, IF RK11 INTERRUPTED TO THIS AFTER
4712                                ;SEEK WAS COMPLETED. RESTORE
4713                                ;STACK POINTER (FROM RK11 INTERRUPT)
4714 014500 022626      CMP      (SP)+,(SP)+  ;RESTORE STACK POINTER (FROM
4715                                ;WAT.INT)
4716 014502 012777 004600 164672  MOV      #BADINT,ARKVEC ;RESTORE RK11 INTERRUPT VECTOR ADRES
4717                                ;FOR UNEXPECTED INTERUTS
4718 014510 032710 020000      BIT      #20000,AR0  ;DID SCP BIT SET?
4719 014514 001003                BNE     6$            ;YES, BRANCH
4720 014516 011037 001162      MOV      AR0,$REGO    ;GET RKCS
4721 014522 104053                ERROR    53           ;SCP DID NOT SET AFTER RK11 INTERRUPTED
4722                                ;INDICATING SEEK WAS DONE
4723 014524 017701 164576      6$:     MOV      ARKDS,R1 ;GET RKDS
4724 014530 042701 017777      BIC      #17777,R1   ;MASK NON-ID BITS IN RKDS
4725 014534 020137 001350      CMP      R1,DRIVAD   ;CORRECT ID BITS IN RKDS?

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4726 014540 001414      BEQ      7$          ;YES, BRANCH
4727
4728 014542 013746 001350  MOV      DRIVAD, -(SP) ;PUSH DRV ADRES ON THE STACK
4729 014546 004737 021174  JSR      PC, SHFTRT    ;GO, SHIFT RIGHT DRV #
4730 014552 012637 001162  MOV      (SP)+, $REGO  ;GET EXPCTD DRV #
4731 014556 010146      MOV      R1, -(SP)    ;PUSH ID BITS ON THE STACK
4732 014560 004737 021174  JSR      PC, SHFTRT    ;GO SHIFT THEM RIGHT
4733 014564 012637 001164  MOV      (SP)+, $REG1  ;POP THE RECVD ID BITS
4734 014570 104047      ERROR    47          ;WRONG ID BITS WERE RECVD IN
4735                                     ;RKDS AFTER SEEK WAS DONE (INTRUPT
4736                                     ;MODE). 'EXPCT' INDICATES THE DRIVE
4737                                     ;# THAT SHOULD HAVE BEEN IN THE
4738                                     ;ID BITS, 'RECVD' INDICATES THE
4739                                     ;DRIVE # THAT WAS RECVD IN THE ID BITS
4740
4741 014572      7$:
4742 014572 012746 000340  MOV      #340, -(SP)
4743 014576 012746 014604  MOV      #64$, -(SP)
4744 014602 000002      RTI
4745 014604      64$:
4746 014604 104413      CNT.RESET ;GO DO CONTROL RESET
4747 014606 013777 001350 164524  MOV      DRIVAD, @RKDA ;ADRES THE DRIVE
4748 014614 032777 160000 164504  BIT      #160000, @RKDS ;DID CNTRL RESET CLEAR DRIVE ID BITS?
4749 014622 001404      BEQ      8$          ;YES, BRANCH
4750 014624 017737 164476 001162  MOV      @RKDS, $REGO  ;GET RKDS
4751 014632 104050      ERROR    50          ;CNTRL RESET DIDN'T CLEAR THE
4752                                     ;DRIVE ID BITS (13-15) IN RKDS
4753
4754
4755 014634 022710 000200  8$:  CMP      #200, @R0    ;WAS SCP BIT CLEARED BY CNTRL RESET?
4756 014640 001403      BEQ      TST37       ;YES, EXIT
4757 014642 011037 001162  MOV      @R0, $REGO   ;GET RKCS
4758 014646 104100      ERROR    100        ;CNTRL RESET DID NOT CLEAR SCP BIT
4759
4760 ;:*****
4761 ;*TEST 37      CHECK THAT WITH IDE SET RK11 INTERRUPTS WHEN READ IS DONE
4762 ;*THIS TEST CHECKS THAT WHEN A DATA TRANSFER FUNCTION IS DONE
4763 ;*WITH IDE BIT SET, RK11 INTERRUPTS WHEN THE FUNCTION IS COMPLETED
4764 ;*FUNCTION USED IN THIS TEST IS READ.
4765 ;:*****
4766 014650 000004  TST37: SCOPE
4767 014652 104413      CNT.RESET ;GO, DO CONTROL RESET
4768                                     ;THIS IS A CALL FOR THE 'CNTRL-
4769                                     ;RESET' ROUTINE. A CONTROL RESET IS
4770                                     ;ISSUED AND AFTER A CERTAIN TIME
4771                                     ;IF THE 'CNTRL RDY' DOES NOT SET
4772                                     ;AN ERROR IS REPORTED. NOTE THAT
4773                                     ;THE PC IN ERROR MESSAGE IS THE
4774                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
4775                                     ;THIS IS A VERY BASIC ERR& IF IT
4776                                     ;OCCURS GO BACK TO TEST 10
4777 014654 104421      TST.SIN  ;CHECK IF SIN IS SET, IF SET
4778                                     ;DO DRIVE RESET TO CLEAR IT
4779
4780 014656 013700 001332  MOV      RKCS, R0
4781 014662 013702 001340  MOV      RKDA, R2

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4782 014666 013704 001336          MOV      RKBA,R4
4783 014672 013701 001350          MOV      DRIVAD,R1
4784 014676 052701 000013          BIS      #13,R1          ;SET BITS FOR SEC 13
4785 014702 012777 177600 164424      MOV      #-200,AR4WC      ;READ 200 (OCTAL WORDS)
4786 014710 010112          MOV      R1,AR2          ;FROM THIS DISK ADRES (CYL 0, SEC 13)
4787 014712 012714 033336          MOV      #OUTBUF,AR4      ;INTO THIS BUS ADRES
4788 014716 013705 001402          MOV      RKVEC,R5
4789 014722 012725 014760          MOV      #1$, (R5)+      ;SET UP VECTOR ADRES FOR RK11 TO INTRUPT
4790 014726 012715 000340          MOV      #340, (R5)      ;SET PSW ON INTERUPT
4791 014732 012710 000105          MOV      #105,AR0        ;READ, GO, IDE SET
4792 014736 104420 127710          WAT.INT ,127710         ;WAIT FOR RK11 TO INTERRUPT ON
4793                                     ;COMPLETION OF READ
4794                                     ;WAITING TIME= 337 MS FOR 11/20
4795                                     ;67 MS FOR 11/45
4796 014742 012777 004600 164432      MOV      #BADINT,AR4VEC  ;RESTORE UNEXPCTED INTERRUPT VECTOR ADRES
4797 014750 011037 001162          MOV      AR0,$REG0      ;GET RKCS
4798 014754 104101          ERROR    101           ;RK11 DID NOT INTERRUPT AFTER READ
4799                                     ;WAS DONE, IDE BIT SET.
4800 014756 000404          BR      1$+10
4801 014760 022626 1$:          CMP      (SP)+,(SP)+    ;OK, IF RK11 INTERRUPTED TO THIS
4802                                     ;RESTORE STACK POINTER (FROM RK11 INTERRUPT)
4803 014762 022626          CMP      (SP)+,(SP)+    ;RESTORE STACK POINTER (FROM WAT.INT)
4804 014764 012777 004600 164410      MOV      #BADINT,AR4VEC  ;RESTORE UNEXPECTED RK11 INTERRUPT
4805                                     ;VECTOR ADRES
4806 014772 004737 021336          JSR      PC,CHKER        ;CHECK IF ANY BIT IN RKER IS SET.
4807                                     ;IF YES, RETURN HERE.
4808 014776 104036          ERROR    36           ;RKER SET ON DOING READ FROM SEC 0,
4809                                     ;CYL 13 IN INTERRUPT MODE
4810 015000 062701 000005 4$:          ADD      #5,R1          ;RKDA SHOULD HAVE INCREMENTED TO THIS
4811 015004 020112          CMP      R1,AR2          ;DID RKDA INCREMENT CORRECTLY?
4812 015006 001405          BEQ     2$              ;YES BRANCH
4813 015010 010137 001162          MOV      R1,$REG0       ;GET EXPCTD RTDA
4814 015014 011237 001164          MOV      AR2,$REG1      ;GET RKDA RECVD
4815 015020 104040          ERROR    40           ;RKDA INCREMENTED WRONG ON DOING
4816                                     ;A READ ON CYL 0, SEC 13
4817 015022 004737 021312 2$:          JSR      PC,CHKWC       ;CHECK THAT RKWC OVERFLOWED TO 0.
4818                                     ;IF NOT RETURN HERE.
4819 015026 104041          ERROR    41           ;RKWC DIDN'T OUFLO AFTER
4820                                     ;A READ OF 200 WORDS
4821
4822 015030 3$:
4823 015030 012746 000340          MOV      #340,-(SP)
4824 015034 012746 015042          MOV      #64$,-(SP)
4825 015040 000002          RTI
4826 015042 64$:
4827 015042 022714 033736          CMP      #OUTBUF+400,AR4 ;DID RKBA INCREMENT CORRECTLY?
4828 015046 001406          BEQ     TST40           ;;YES, EXIT
4829 015050 012737 033736 001162      MOV      #OUTBUF+400,$REG0 ;GET EXPCT RKBA
4830 015056 011437 001164          MOV      AR4,$REG1      ;GET RKBA RECVD
4831 015062 104042          ERROR    42           ;RKBA DID NOT INCREMENT CORRECTLY
4832                                     ;AFTER A READ OF 200 WORDS
4833
4834 ;*****
4835 ;*TEST 40 CHECK THAT RK11 INTERRUPTS AT BR5 ONLY
4836 ;*THIS TEST CHECKS THAT RK11 CAN ITERRUPT AT BR5 ONLY. IF IT
4837 ;*INTERRUPTS AT A LEVEL HIGHER THAN BR5 AN ERROR IS INDICATED.

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4844 015064 000004
4845 015066 104413
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4853
4854
4855 015070 104421
4856
4857 015072 012737 015126 001110
4858
4859 015100 013700 001332
4860 015104 013777 001350 164226
4861 015112 012701 000007
4862 015116 012702 000340
4863 015122 013703 001400
4864
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4866
4867 015126 013704 001402
4868 015132 012724 015240
4869 015136 012714 000340
4870 015142 010246
4871 015144 012746 015152
4872 015150 000002
4873 015152
4874 015152 012710 000100
4875 015156 012705 177760
4876 015162 005205
4877 015164 001376
4878 015166 020203
4879 015170 003005
4880
4881
4882 015172 010137 001162
4883 015176 011037 001164
4884 015202 104103
4885
4886
4887 015204 005010
4888 015206 062702 177740
4889
4890 015212 005301
4891 015214 001344
4892
4893 015216 012777 004600 164156

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TST40: SCOPE
CNT.RESET
TST.SIN
MOV #1$, $LPERR
MOV RKCS, R0
MOV DRIVAD, DRKDA
MOV #7, R1
MOV #340, R2
MOV RKPRI, R3
1$: MOV RKVEC, R4
MOV #3$, (R4)+
MOV #340, (R4)
MOV R2, -(SP)
MOV #4$, -(SP)
RTI
4$: MOV #100, DR0
MOV #-20, R5
INC R5
BNE -.2
CMP R2, R3
BGT 2$
MOV R1, $REG0
MOV DR0, $REG1
ERROR 103
2$: CLR DR0
ADD #-40, R2
DEC R1
BNE 1$
MOV #BADINT, DRKVEC

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; *IF IT DOES NOT INTERRUPT AT BR5 OR LOWER THEN ALSO AN
; *ERROR IS INDICATED. IF FOR SOME REASON THE INTERRUPT
; *LEVEL IS CHANGED FROM BR5, THEN CONTENTS OF RKPRI WILL
; *HAVE TO BE CHANGED ACCORDINGLY AND STILL TEXT WILL
; *CHECK FOR THIS BR LEVEL.
; GO, DO CONTROL RESET
; THIS IS A CALL FOR THE 'CNTRL-
; RESET' ROUTINE. A CONTROL RESET IS
; ISSUED AND AFTER A CERTAIN TIME
; IF THE 'CNTRL RDY' DOES NOT SET
; AN ERROR IS REPORTED. NOTE THAT
; THE PC IN ERROR MESSAGE IS THE
; PC WHERE 'CNT.RESET' IS LOCATED.
; THIS IS A VERY BASIC ERR& IF IT
; OCCURS GO BACK TO TEST 10
; CHECK IF SIN IS SET, IF SET
; DO DRIVE RESET TO CLEAR IT
; SET RETURN ADRES FOR LUPING
; ON ERROR (SW 9)
; PRIORITY LEVEL 7
; BR LEVEL 7 FOR PSW
; NOTE, IF RK11 INTERRUPT LEVEL IS
; CHANGED FROM 5 TO ANY OTHER LEVEL
; THEN CHANGE CONTENTS OF 'RKPRI'
; ACCORDINGLY
; SET UP ADRES FOR RK11 TO INTERUPT
; SET UP PSW ON INTERUPT
; SET PROCESSOR PRIORITY LEVEL AS
; INDICATED BY R2
; SET THE IDE BIT
; WAIT FOR THE RK11 INTERRUPT
; WAITING TIME=78 US FOR 11/20
; 13 US FOR 11/45
; WAS THE CPU PRIORITY LEVEL LESS THAN
; THE RK11 LEVEL? IF YES, RK11
; SHOULD HAVE INTERRUPTED. ERROR,
; IF IT DID NOT
; GET CPU BR LEVEL
; GET RKCS
; THOUGH CPU LEVEL WAS LESS THAN
; THE RK11 LEVEL (5), RK11 DID NOT
; INTERRUPT
; CLEAR RKCS
; DECREASE THE PRIORITY LEVEL (FOR
; CPU) BY 1
; CPU WILL B AT THIS LEVEL
; LUP BAK & CHK FOR THIS BR LEVEL.
; DONE WITH CHKING FOR ALL LEVELS.
; RESTORE UNEXPECTED RK11 INTERRUPT

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4894                                     ;VECTOR
4895 015224 012746 000340                MOV    #340,-(SP)
4896 015230 012746 015236                MOV    #64$,-(SP)
4897 015234 000002                        RTI
4898 015236                                64$:
4899 015236 000414                        BR     TST41                ;;EXIT,TO NXT TST
4900
4901 015240 022626                        3$:  CMP    (SP)+,(SP)+        ;RESTORE STACK POINTER
4902 015242 012777 004600 164132        MOV    #BADINT,ARKVEC    ;RESTORE UNEXPECTED RK11 INTERRUPT
4903                                     ;VECTOR
4904 015250 020203                        CMP    R2,R3            ;IF THIS INTERRUPT OCCURED WHEN
4905 015252 003754                        BLE    2$                ;CPU LEVEL WAS LESS THAN THE
4906                                     ;RK11 PRIORITY LEVEL (5) THEN IT IS
4907                                     ;OK. IF NOT SO, ERROR
4908 015254 010137 001162                MOV    R1,$REG0        ;GET CPU BR LEVEL
4909 015260 011037 001164                MOV    AR0,$REG1      ;GET RKCS
4910 015264 104104                        ERROR  104            ;RK11 INTERRUPTED WHEN THE CPU
4911                                     ;LEVEL (AS POINTED BY R1) WAS
4912                                     ;HIGHER OR SAME AS THE RK11
4913                                     ;LEVEL (5)
4914 015266 000746                        BR     2$                ;GO BACK & CHK THE NXT LEVEL
4915
4916 ;:*****
4917 ;*TEST 41 SIMULATE & CHECK 'OVR' ERROR
4918 ;*THIS TEST SIMULATES OVERRUN ERROR AND CHECKS IF THE OVR
4919 ;*BIT IN RKER GETS SET. THEN IT IS CLEARED USING CNTRL RESET
4920 ;*& CHECKED THAT IT WAS CLEARED. OVR CONDITION IS SIMULATED
4921 ;*BY TRYING TO READ 401(OCTAL) WORDS FROM LAST CYLINDER(312),
4922 ;*LAST SECTOR (13), SURFACE 1.
4923 ;:*****
4924 015270 000004                        TST41: SCOPE
4925 015272 104413                        CNT.RESET                ;GO, DO CONTROL RESET
4926                                     ;THIS IS A CALL FOR THE 'CNTRL-
4927                                     ;RESET' ROUTINE. A CONTROL RESET IS
4928                                     ;ISSUED AND AFTER A CERTAIN TIME
4929                                     ;IF THE 'CNTRL RDY' DOES NOT SET
4930                                     ;AN ERROR IS REPORTED. NOTE THAT
4931                                     ;THE PC IN ERROR MESSAGE IS THE
4932                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
4933                                     ;THIS IS A VERY BASIC ERR& IF IT
4934                                     ;OCCURS GO BACK TO TEST 10
4935 015274 104421                        TST.SIN                ;CHECK IF SIN IS SET, IF
4936                                     ;SET, DO DRIVE RESET TO CLR IT
4937 015276 013701 001350                MOV    DRIVAD,R1        ;GET ADRES OF DRIVE
4938 015302 052701 014533                BIS    #14533,R1       ;SET BITS FOR LAST CYLINDER (312),
4939                                     ;SUR 1, LAST SECTOR (13)
4940 015306 012777 177377 164020        MOV    #-401,ARKWC     ;READ 401 WORDS
4941 015314 012777 033336 164014        MOV    #OUTBUF,ARKBA   ;INTO THIS MEMORY BUFFER
4942 015322 010177 164012                MOV    R1,ARKDA        ;FROM THIS DSK ADRES, LAST CYL.
4943                                     ;LAST SEC, SURFACE 1
4944 015326 012777 000005 163776        MOV    #5,ARKCS        ;READ, GO
4945
4946 015334 005002                        1$:  CLR    R2
4947 015336 105777 163770                TSTB  ARKCS            ;DID CNTRL RDY SET?
4948 015342 100410                        BMI    2$                ;YES, BRANCH
4949 015344 005202                        INC    R2                ;NO, WAIT FOR IT

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015456 104421
015460 013701 001330
015464 013777 001350 163646
015472 012777 002011 163632
015500 104414
015502 032711 004000
015506 001006
015510 012737 004000 001166
015516 004737 021004
015522 104105
015524 022777 142210 163600 1\$:
015532 001403
015534 004737 021004
015540 104106
015542 104413 2\$:
015544 004737 021352
015550 104102
015552 004737 021376 3\$:
015556 104102

TST.SIN
MOV RKER,R1
MOV DRIVAD,DRKDA
MOV #2011,DRKCS
CNT.RDY
BIT #4000,DR1
BNE 1\$
MOV #4000,\$REG2
JSR PC,GT2RG
ERROR 105
CMP #142210,DRKCS
BEQ 2\$
JSR PC,GT2RG
ERROR 106
CNT.RESET
JSR PC,CHKECLR
ERROR 102
JSR PC,CHKCCLR
ERROR 102

: IF THE 'CNTRL RDY' DOES NOT SET
: AN ERROR IS REPORTED. NOTE THAT
: THE PC IN ERROR MESSAGE IS THE
: PC WHERE 'CNT.RESET' IS LOCATED.
: THIS IS A VERY BASIC ERR& IF IT
: OCCURS GO BACK TO TEST 10
: GO CHECK IF SIN IS SET, IF
: SET DO DRIVE RESET TO CLR IT
:
: ADRES THE DRIVE, CYLINDER 0
:
: SEEK, GO WITH FMT SET
: THIS IS A PGE SIMULATION
: THIS IS A CALL FOR 'CN.RDY'
: ROUTINE WHICH WAITS FOR CNT
: RDY TO SET. IF CNTRL RDY DOES
: NOT SET WITHIN 883 MS/ 11-20
: (176 MS FOR 11-45 WITH BIPOLAR)
: AN ERROR IS REPORTED
: DID PGE BIT IN RKER SET?
: YES, BRANCH
: THIS BIT IN RKER (PGE) DID NOT SET
: GO GET RKCS, ER FOR MESSAGE
: PGE BIT DID NOT SET IN RKER
: ON SIMULATION OF PGE CONDITION
: \$REG2 CONTAINS THE RKER BIT (PGE)
: THAT SHOULD HAVE SET.
: DID HE & ERR BITS SET?
: YES, BRANCH
: GO, GET RKCS, ER
: HE OR ERR BIT DID NOT SET WHEN
: PGE SET IN RKER.
: CLEAR PGE, HE, ERR BITS
: GO, DO CONTROL RESET
: THIS IS A CALL FOR THE 'CNTRL-
: RESET' ROUTINE. A CONTROL RESET IS
: ISSUED AND AFTER A CERTAIN TIME
: IF THE 'CNTRL RDY' DOES NOT SET
: AN ERROR IS REPORTED. NOTE THAT
: THE PC IN ERROR MESSAGE IS THE
: PC WHERE 'CNT.RESET' IS LOCATED.
: THIS IS A VERY BASIC ERR& IF IT
: OCCURS GO BACK TO TEST 10
: CHECK IF 'PGE' BIT GOT CLEARED BY
: CONTROL RESET, IF NOT RETURN HERE.
: CNTRL RESET DID NOT CLEAR
: PGE BIT IN RKER
: CHECK IF 'ERR' BIT GOT CLEARED BY
: CON.RESET, IF NOT RETURN HERE.
: RKCS BITS HE OR ERR DID NOT
: GET CLEARED BY CNTRL RESET

::*****
:*TEST 43 SIMULATE & CHECK NXM ERROR
:*THIS TEST SIMULATES A NON-EXISTENT MEMORY ERROR (NXM) AND
:*CHECKS IF IT IS DETECTED BY NXM BIT OR RKER.LOCATION 760000

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5067 015560 000004
5068 015562 104413
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5078 015564 104421
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5080 015566 005002
5081 015570 013700 001332
5082 015574 012777 177777 163532
5083 015602 012777 160000 163526
5084 015610 013777 001350 163522
5085 015616 012710 000067
5086 015622 105777 163504
5087 015626 100410
5088 015630 005202
5089 015632 001373
5090 015634 004737 021004
5091 015640 017737 163470 001166
5092 015646 104113
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5096 015650 032777 002000 163452 2\$:
5097 015656 001006
5098 015660 004737 021004
5099 015664 012737 002000 001166
5100 015672 104105
5101
5102 015674 022710 140266 3\$:
5103 015700 001403
5104 015702 004737 021004
5105 015706 104106
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5108 015710 104413 4\$:
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:*IS REFERENCED & IT HAPPENS TO BE A NON EXISTENT LOCATION
:*(FOR DIAGNOSTIC PURPOSES LIKE THIS). IT IS ALSO CHECKED
:*IF HE & ERR BITS ALSO SET AND ALL 3 BITS CAN BE CLEARED
:* BY CONTROL RESET.
:*****
TST43: SCOPE
CNT.RESET
TST.SIN
CLR R2
MOV RKCS, R0
MOV #-1, ARKWC
MOV #160000, ARKBA
MOV DRIVAD, ARKDA
MOV #67, AR0
1\$: TSTB ARKCS
BMI 2\$
INC R2
BNE 1\$
JSR PC, GT2RG
MOV ARKWC, \$REG2
ERROR 113
WRITE CHECK 1 WORD
AT THIS BUS ADRES
WITH THIS DISK ADRES (CYL 0, SEC 0)
WRT CHK, GO, MEX BITS SET
DID CNTRL R0Y SET AS A RESULT OF HE?
YES, BRANCH
WAITED LONG ENOUGH?
IF NOT LUP BAK & WAIT
GET RKCS, ER
GET RKWC
CNTRL R0Y DID NOT SET ON DOING
A WRT CHK WITH A NXM LOCATION.
THIS HE SHOULD HAVE SET THE
CNTRL R0Y BIT IN RKCS
DID NXM BIT IN RKER SET?
YES, BRANCH
GO GET RKCS, RKER
THIS BIT (NXM) DID NOT SET IN RKER
NXM BIT DID NOT SET IN RKER ON
SIMULATING NXM CONDITION.
DID HE & ERR BIT SET?
YES, BRANCH
GO, GET RKCS, RKER
HE OR ERR BIT DID NOT SET WHEN
NXM ERROR WAS SIMULATED
CLEAR NXM, HE, ERR BITS
GO, DO CONTROL RESET
THIS IS A CALL FOR THE 'CNTRL-
RESET' ROUTINE. A CONTROL RESET IS
ISSUED AND AFTER A CERTAIN TIME
IF THE 'CNTRL R0Y' DOES NOT SET
AN ERROR IS REPORTED. NOTE THAT
THE PC IN ERROR MESSAGE IS THE
PC WHERE 'CNT.RESET' IS LOCATED.
THIS IS A VERY BASIC ERR& IF IT
OCCURS GO BACK TO TEST 10
2\$: BIT #2000, ARKER
BNE 3\$
JSR PC, GT2RG
MOV #2000, \$REG2
ERROR 105
3\$: CMP #140266, AR0
BEQ 4\$
JSR PC, GT2RG
ERROR 106
4\$: CNT.RESET

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S118 015712 004737 021352 JSR PC,CHKECLR ;CHECK IF 'NXM' BIT GOT CLEARED BY
S119 ;CON.RESET, IF NOT RETURN HERE.
S120 015716 104102 ERROR 102 ;CNTRL RESET DID NOT CLEAR
S121 ;NXM BIT IN RKER
S122 015720 004737 021376 S$: JSR PC,CHKCCLR ;CHECK IF 'HE' & 'ERR' BITS GOT CLEARED
S123 ;BY CON.RESET, IF NOT RETURN HERE.
S124 015724 104102 ERROR 102 ;CNTRL RESET DID NOT CLEAR
S125 ;HE OR ERR BIT IN RKCS.
S126 015726 004737 021432 S$: JSR PC,TSTRWS ;GO CHECK IF R/W/S RDY IS SET &
S127 ;WAIT FOR IT. SKIP ERROR IF IT IS SET
S128 015732 104016 ERROR 16 ;R/W/S RDY IS NOT SET
S129
S130 ;*****
S131 ;*TEST 44 SIMULATE & CHECK NXD ERROR
S132 ;*THIS TEST SIMULATES NON-EXISTENT DISK ERROR & CHECKS IF
S133 ;*IT IS DETECTED BY NXD BIT OF RKER. IF ALL EIGHT ARE PRESENT
S134 ;*THEN THIS TEST IS ABORTED FOR SIMULATION CANNOT BE DONE.
S135 ;*****
S136 015734 000004 †ST44: SCOPE
S137 015736 104413 CNT.RESET ;GO, DO CONTROL RESET
S138 ;THIS IS A CALL FOR THE 'CNTRL-
S139 ;RESET' ROUTINE. A CONTROL RESET IS
S140 ;ISSUED AND AFTER A CERTAIN TIME
S141 ;IF THE 'CNTRL RDY' DOES NOT SET
S142 ;AN ERROR IS REPORTED. NOTE THAT
S143 ;THE PC IN ERROR MESSAGE IS THE
S144 ;PC WHERE 'CNT.RESET' IS LOCATED.
S145 ;THIS IS A VERY BASIC ERR& IF IT
S146 ;OCCURS GO BACK TO TEST 10
S147 015740 104421 TST.SIN ;CHECK IF SIN IS SET, IF SET
S148 ;DO DRV RESET TO CLR IT
S149 015742 013700 001332 MOV RKCS,R0
S150 015746 012702 160000 MOV #160000,R2 ;ADRES DRIVE 7 TO FIND
S151 ;IF IT IS PRESENT
S152 015752 010277 163362 1$: MOV R2,DRKDA ;ADRES DRIVE # POINTED TO BY R2
S153 015756 104417 000001 DELAY ,1 ;TIME DELAY, 7.5 US ON 11/20.
S154 ;1.5 US ON 11/45
S155 015762 105777 163340 TSTB DRKDS ;IS IT PRESENT?
S156 015766 100004 BPL 2$ ;NO, BRANCH
S157 015770 062702 160000 ADD #-20000,R2 ;ADRES THE NXT DRIVE IN THE
S158 ;REVERSE ORDER. I.E. 7,6...
S159 015774 001366 BNE 1$ ;LUP BAK & TRY TO FIND A DRIVE
S160 ;THAT'S NOT PRESENT
S161 015776 000435 BR TST45 ;EXIT TO THE NXT TST
S162
S163 016000 012710 000015 2$: MOV #15,DR0 ;DRIVE RESET, ON A NX DRIVE
S164 016004 104417 000106 DELAY ,106 ;TIME DELAY, 525 US ON 11/20
S165 ;105 US ON 11/45
S166 016010 105777 163314 TSTB DRKER ;DID NXD BIT IN RKER SET?
S167 016014 001006 BNE 3$ ;YES, BRANCH
S168 016016 004737 021004 JSR PC,GT2RG ;GET RKCS, RKER
S169 016022 012737 000200 001166 MOV #200,$REG2 ;THIS BIT (NXD) IN RKER DID NOT SET
S170 016030 104105 ERROR 105 ;NXD BIT DID NOT SET ON TRYING
S171 ;TO PERFORM A FUNCTION ON A
S172 ;NON-EXISTENT DRIVE
S173 ;CHECK THAT THE JUMPER CARD CONTAINING

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016032 022710 140214
016036 001403
016040 004737 021004
016044 104106
016046 104413
016050 004737 021352
016054 104102
016056 004737 021376
016062 104102
016064 004737 021432
016070 104016
000004
013700 001332
012737 177773 001362
013702 001350
016112 052702 014540
016116 012737 016124 001110
016124 104413

3\$: CMP #140214, R0
BEQ 4\$
JSR PC, GT2RG
ERROR 106
4\$: CNT.RESET
JSR PC, CHKECLR
ERROR 102
5\$: JSR PC, CHKCLR
ERROR 102
JSR PC, TSTRWS
ERROR 16
TEST45: SCOPE
MOV RKCS, R0
MOV #-5, COUNT
MOV DRIVAD, R2
BIS #14540, R2
MOV #3\$, \$LPERR
3\$: CNT.RESET

: JUMPERS FOR DRIVES PRESENT IS PROPERLY
: CONNECTED
: NOTE THAT ON RK11C IF A DRIVE
: IS OFFLINE BUT PHYSICALLY PRESENT
: (IE. DRY IS CLR FOR THAT DRIVE)
: & A FUNCTION IS INITIATED ON THAT
: DRIVE NXD WON'T SET, BUT U WILL
: GET ONLY A DRE, HE & ERR.
: DID HE & ERR SET WHEN NXD SET?
: YES BRANCH
: HE OR ERR BIT DID NOT SET
: WHEN NXD WAS SIMULATED
: CLEAR NXD, HE, ERR BITS
: GO, DO CONTROL RESET
: THIS IS A CALL FOR THE 'CNTRL-
: RESET' ROUTINE. A CONTROL RESET IS
: ISSUED AND AFTER A CERTAIN TIME
: IF THE 'CNTRL RDY' DOES NOT SET
: AN ERROR IS REPORTED. NOTE THAT
: THE PC IN ERROR MESSAGE IS THE
: PC WHERE 'CNT.RESET' IS LOCATED.
: THIS IS A VERY BASIC ERR& IF IT
: OCCURS GO BACK TO TEST 10
: CHECK IF 'NXD' BIT WAS CLEARED BY
: CON.RESET. IF NOT, RETURN HERE.
: CNTRL RESET DID NOT CLEAR
: NXD BIT IN RKER
: CHECK IF 'HE' & 'ERR' BITS WERE CLEARED
: BY CON.RESET. IF NOT RETURN HERE.
: CNTRL RESET DID NOT CLEAR
: HE OR ERR BIT IN RKCS
: GO CHECK & WAIT FOR R/W/S RDY
: TO SET. IF SET SKIP ERROR
: R/W/S SHOULD BE SET, IT'S
: NOT
: *****
: *TEST 45 SIMULATE & CHECK NXC ERROR
: *THIS TEST SIMULATES THE NON-EXISTENT CYLINDER ERROR & CHECKS
: *IF IT IS DETECTED BY THE NXC BIT OF RKER, HE & ERR BITS
: *OF RKCS. IT IS CHECKED IF THEY CAN BE CLEARED BY CONTROL
: *RESET
: *****
: ALLOW 'ERROR 133' ONLY 5 TIMES
: GET ADRES OF DRIVE
: SET BITS FOR CYL 313
: SET RETURN ADRES FOR
: LUPING ON EROR (SW9)
: GO, DO CONTROL RESET
: THIS IS A CALL FOR THE 'CNTRL-
: RESET' ROUTINE. A CONTROL RESET IS
: ISSUED AND AFTER A CERTAIN TIME
: IF THE 'CNTRL RDY' DOES NOT SET
: AN ERROR IS REPORTED. NOTE THAT

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5234 016126 004737 021432 JSR PC,TSTRWS
5235
5236 016132 104016 ERROR 16
5237 016134 104421 TST.SIN
5238
5239 016136 010277 163176 MOV R2,ARKDA
5240 016142 012710 000011 MOV #11,ARO
5241 016146 104412 CHKCRDY
5242
5243 016150 104021 ERROR 21
5244
5245
5246
5247 016152 032777 000100 163150 9$: BIT #100,ARKER
5248 016160 001020 BNE 4$
5249 016162 004737 021004 JSR PC,GT2RG
5250 016166 017737 163146 001166 MOV ARKDA,$REG2
5251 016174 104110 ERROR 110
5252
5253
5254 016176 004737 021432 JSR PC,TSTRWS
5255
5256 016202 104016 ERROR 16
5257 016204 104413 CNT.RESET
5258 016206 004737 021500 JSR PC,DRESET
5259 016212 104026 ERROR 26
5260
5261
5262 016214 005237 001362 INC COUNT
5263 016220 001405 BEQ 5$
5264 016222 062702 000040 4$: ADD #40,R2
5265 016226 032702 017740 BIT #17740,R2
5266 016232 001334 BNE 3$
5267
5268 016234 032710 140000 5$: BIT #140000,ARO
5269 016240 001003 BNE 6$
5270 016242 004737 021004 JSR PC,GT2RG
5271 016246 104106 ERROR 106
5272
5273
5274 016250 104413 6$: CNT.RESET
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5284 016252 004737 021352 JSR PC,CHKECLR
5285

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;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR& IF IT
;OCCURS GO BACK TO TEST 10
;GO CHECK & WAIT FOR R/W/S RDY
;TO SET. IF SET SKIP ERROR BELOW
;R/W/S RDY IS NOT SET
;CHECK IF SIN IS SET, IF SET
;DO DRIVE RESET TO CLR IT
;ADRES DRIVE, NXC CYLINDER
;SEEK, GO TO NXC CYL
;GO CHECK IF CONTROL RDY IS SET
;IF SO, SKIP THE EROR MESSAGE.
;SEEK WAS TRIED TO A NON EXISTENT
;CYLINDER, NXC SHOULD HAVE OCCURED
;SETTING CNTRL RDY. BUT CNTRL RDY
;DID NOT SET.
;DID NXC SET?
;YES, BRANCH
;GO GET RKCS, ER
;GET RKDA
;NXC DID NOT SET WHEN SEEK
;WAS TRIED TO CYLINDER AS INDICATED
;IN RKDA
;CHECK & WAIT FOR R/W/S RDY.
;IF SET SKIP ERROR
;R/W/S SHOULD BE SET
;GO DO CONTROL RESET
;GO DO DRIVE RESET
;NXC DID NOT SET AND DRIVE MAY
;HAVE TRIED TO DO A SEEK, AFTER
;WHICH R/W/S RDY DID NOT SET
;ALLOW ONLY 5 MESSAGES FOR
;ERROR 133
;ADRES THE NXT CYL(IN NON-EXISTENT ZONE)
;CHKD FOR ALL NXC'S?
;IF NOT, LUP BAK & CHK THE NXT NXC
;DID HE & ERR BIT SET WHEN NXC BIT SET?
;YES, BRANCH
;GET RKCS, ER
;HE OR ERR BIT DID NOT SET IN RKCS
;WHEN NXC ERROR WAS SIMULATED
;CLEAR HE, ERR, NXC BITS
;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR& IF IT
;OCCURS GO BACK TO TEST 10.
;CHECK IF 'NXC' BIT WAS CLEARED BY
;CON.RESET. IF NOT, RETURN HERE.

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5286 016256 104102          ERROR 102          ;CNTRL RESET DID NOT CLEAR
5287                                ;NXC BIT IN RKER
5288 016260 032710 140000    7$: BIT #140000,AR0    ;DID HE & ERR BITS GET CLEARED?
5289 016264 001405          BEQ TST46          ;YES, EXIT
5290 016266 010037 001162    MOV R0,$REG0      ;GET ADRES OF RKCS
5291 016272 011037 001164    MOV AR0,$REG1     ;GET RKCS CONTENTS
5292 016276 104102          ERROR 102          ;CNTRL RESET DID NOT CLEAR
5293                                ;HE OR ERR BIT IN RKCS
5294
5295 ;:*****
5296 ;*TEST 46 SIMULATE & CHECK NXS ERROR
5297 ;*THIS TEST SIMULATES NON-EXISTENT SECTOR ERROR & CHECKS THAT
5298 ;*IT IS DETECTED BY NXS BIT OF RKER. IT IS CHECKED THAT
5299 ;*WHEN NXS SETS HE & ERR OF RKER ALSO SETS, AND ALL THREE
5300 ;*CAN BE CLEARED BY CONTROL RESET.
5301 ;:*****
5302 016300 000004          TST46: SCOPE
5303 016302 104413          CNT.RESET
5304                                ;GO, DO CONTROL RESET
5305                                ;THIS IS A CALL FOR THE 'CNTRL-
5306                                ;RESET' ROUTINE. A CONTROL RESET IS
5307                                ;ISSUED AND AFTER A CERTAIN TIME
5308                                ;IF THE 'CNTRL RDY' DOES NOT SET
5309                                ;AN ERROR IS REPORTED. NOTE THAT
5310                                ;THE PC IN ERROR MESSAGE IS THE
5311                                ;PC WHERE 'CNT.RESET' IS LOCATED.
5312                                ;THIS IS A VERY BASIC ERR& IF IT
5313                                ;OCCURS GO BACK TO TEST 10
5313 016304 013700 001332    MOV RKCS,R0
5314 016310 013777 001350 163022  MOV DRIVAD,ARKDA ;GET ADRES OF DRIVE
5315 016316 052777 000014 163014  BIS #14,ARKDA    ;SET BITS FOR SECTOR 12 (DECIMAL)
5316 016324 012777 177777 163002  MOV #-1,ARKWC   ;READ 1 WORD
5317 016332 012777 033336 162776  MOV #OUTBUF,ARKBA ;INTO THIS BUS ADRES
5318 016340 012710 000005  MOV #5,AR0      ;READ, GO (FROM NX SECTOR)
5319 016344 104414          CNT.RDY          ;THIS IS A CALL FOR 'CN.RDY'
5320                                ;ROUTINE WHICH WAITS FOR CNT
5321                                ;RDY TO SET. IF CNTRL RDY DOES
5322                                ;NOT SET WITHIN 883 MS/ 11-20
5323                                ;(176 MS FOR 11-45 WITH BIPOLAR)
5324                                ;AN ERROR IS REPORTED
5325                                ;NXS ERROR SHOULD OCCUR NOW
5326 016346 017702 162756    MOV ARKER,R2
5327 016352 032702 000040    BIT #40,R2       ;DID NXS BIT SET IN RKER?
5328 016356 001006          BNE 1$          ;YES, BRANCH
5329 016360 004737 021004    JSR PC,GT2RG     ;GO GET RKCS, RKER
5330 016364 012737 000040 001166  MOV #40,$REG2    ;THIS BIT (NXS) IN RKER DID NOT SET
5331 016372 104105          ERROR 105       ;NXS BIT DID NOT SET ON SIMULATING
5332                                ;NXS ERROR
5333 016374 042702 000040    1$: BIC #40,R2     ;MASK NXS BIT
5334 016400 001407          BEQ 2$          ;CHECK IF ANY OTHER
5335                                ;RKER BIT SET
5336 016402 012737 000040 001162  MOV #40,$REG0    ;GET EXPCTD RKER
5337 016410 017737 162714 001164  MOV ARKER,$REG1 ;GET RKER RECVD
5338 016416 104107          ERROR 107       ;ONLY 'NXS' SHOULD BE SET
5339                                ;IN RKER. ANOTHER RKER BIT
5340                                ;WAS SET. (NOTE 'NXS' WAS
5341                                ;SIMULATED)

```

```

5342 016420 022710 140204 2$: CMP #140204,R0 ;DID HE & ERR BITS SET?
5343 016424 001403 BEQ 3$ ;YES, BRANCH
5344 016426 004737 021004 JSR PC,GT2RG ;GO GET RKCS, RKER
5345 016432 104106 ERROR 106 ;HE OR ERR BIT DID NOT SET WHEN
5346 ;NXS ERROR OCCURED
5347 ;CLEAR NXS, HE, ERR BITS
5348 - 016434 104413 3$: CNT.RESET ;GO, DO CONTROL RESET
5349 ;THIS IS A CALL FOR THE 'CNTRL-
5350 ;RESET' ROUTINE. A CONTROL RESET IS
5351 ;ISSUED AND AFTER A CERTAIN TIME
5352 ;IF THE 'CNTRL RDY' DOES NOT SET
5353 ;AN ERROR IS REPORTED. NOTE THAT
5354 ;THE PC IN ERROR MESSAGE IS THE
5355 ;PC WHERE 'CNT.RESET' IS LOCATED.
5356 ;THIS IS A VERY BASIC ERR& IF IT
5357 ;OCCURS GO BACK TO TEST 10
5358 016436 004737 021352 JSR PC,CHKECLR ;CHECK IF 'NXS' BIT WAS CLEARED BY
5359 ERROR 102 ;CON.RESET. IF NOT, RETURN HERE.
5360 016442 104102 ;CNTRL RESET DID NOT CLEAR
5361 ;NXS BIT IN RKER
5362 016444 004737 021376 4$: JSR PC,CHKCCLR ;CHECK IF 'HE' & 'ERR' BITS WERE CLEARED
5363 ;BY CON.RESET. IF NOT, RETURN HERE.
5364 016450 104102 ERROR 102 ;RKCS BITS ERR OR HE WERE NOT
5365 ;CLEARED BY CNTRL RESET
5366
5367 ;:*****
5368 ;*TEST 47 SIMULATE & CHECK WCE
5369 ;*THIS TEST SIMULATES A WRITE CHECK ERROR AND CHECKS THAT IT
5370 ;*IS DETECTED BY WCE BIT OF RKER. FOR COMPARISON IT USES
5371 ;*THE 256 WORDS DATA BLOCK WRITTEN ON SECTOR 0, CYLINDER 0
5372 ;*IN A PREVIOUS TEST. THIS BLOCK IS COMPARED WITH THE 256 WORDS
5373 ;*MEMORY BUFFER STARTING AT 'OUTBUF'. WCE IS SIMULATED BY
5374 ;*DROPPING A BIT FROM ONE OF THE WORDS IN THE MEMORY BUFFER.
5375 ;:*****
5376 016452 000004 TST47: SCOPE
5377 016454 013700 001332 MOV RKCS,R0
5378 016460 104413 CNT.RESET
5379 ;GO, DO CONTROL RESET
5380 ;THIS IS A CALL FOR THE 'CNTRL-
5381 ;RESET' ROUTINE. A CONTROL RESET IS
5382 ;ISSUED AND AFTER A CERTAIN TIME
5383 ;IF THE 'CNTRL RDY' DOES NOT SET
5384 ;AN ERROR IS REPORTED. NOTE THAT
5385 ;THE PC IN ERROR MESSAGE IS THE
5386 ;PC WHERE 'CNT.RESET' IS LOCATED.
5387 ;THIS IS A VERY BASIC ERR& IF IT
5388 016462 104421 TST.SIN ;OCCURS GO BACK TO TEST 10
5389 ;CHECK IF SIN IS SET, IF
5390 016464 012701 033336 MOV #OUTBUF,R1 ;SET DO DRV-RESET TO CLR IT
5391 016470 012702 177400 MOV #-400,R2 ;THIS CODE SETS UP A MEMORY
5392 016474 012703 177777 MOV #177777,R3 ;BUFFER OF 256 WORDS STARTING
5393 ;AT OUTBUF
5394 ;FIRST WORD 177400
5395 016500 062703 177401 1$: ADD #177401,R3 ;SECOND 177001
5396 016504 010321 MOV R3,(R1)+ ;LAST WORD 000377
5397 016506 005202 INC R2 ;HAVE U GENERATED ALL 256 WORDS?

```



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5454
5455
5456 016634 104421
5457
5458 016636 013700 001332
5459 016642 012737 170007 033354
5460
5461
5462
5463 016650 013701 001350
5464 016654 012777 177000 162452
5465 016662 012777 033336 162446
5466 016670 010177 162444
5467 016674 012710 000407
5468 016700 104412
5469
5470 016702 104065
5471
5472
5473
5474 016704 022777 000001 162416 2$:
5475
5476 016712 001407
5477 016714 012737 000001 001162
5478 016722 017737 162402 001164
5479 016730 104107
5480
5481
5482
5483 016732 005201 3$:
5484 016734 020177 162400
5485
5486
5487 016740 001406
5488 016742 010137 001162
5489 016746 017737 162366 001164
5490 016754 104070
5491
5492
5493
5494
5495
5496
5497
5498
5499
5500
5501
5502
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5505
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```

TST.SIN
MOV RKCS,RO
MOV #170007,OUTBUF+16
MOV DRIVAD,R1
MOV #-1000,ARKWC
MOV #OUTBUF,ARKBA
MOV R1,ARKDA
MOV #407,ARO
CHKCRDY
ERROR 65
CMP #1,ARKER
BEQ 3$
MOV #1,$REGO
MOV ARKER,$REG1
ERROR 107
INC R1
CMP R1,ARKDA
BEQ TST51
MOV R1,$REGO
MOV ARKDA,$REG1
ERROR 70

```

```

;THIS IS A VERY BASIC ERR& IF IT
;OCCURS GO BACK TO TEST 10
;CHECK IF SIN IS SET, IF
;SET DO DRIVE RESET TO CLR IT
;WCE IS SIMULATED BY DROPPING A BIT
;IN THE EIGHTH WORD (WHICH IS ACTUALLY
;174007). NOTE THAT 256 WORD MEMORY
;BUFFER IS CREATED IN THE PREVIOUS TEST.
;WRT CHK 1000 (OCTAL) WORDS, 2 SECTORS
;FROM THIS BUS ADRES
;WITH THIS DISK ADRES, SEC 0, CYL 0
;WRT CHK, GO, SSE
;GO CHECK IF CONTROL RDY IS SET
;IF SO, SKIP THE EROR MESSAGE.
;CNTRL RDY DID NOT SET AFTER WRT
;CHK. A SOFT ERROR (WCE) IN
;SECTOR 0 SHOULD HAVE STOPPED
;ALL CONTROL ACTION.
;CHECK ONLY 'WCE' BIT SHOULD
;BE SET?
;YES, BRANCH
;GET EXPCTD RKER
;GET RKER RECVD
;ONLY BIT 'WCE' OF RKER
;SHOULD BE SET (WCE WAS
;SIMULATED ABOVE). ERROR
;IF IT'S NOT
;CHECK THAT RKDA INCREMENTED BY
;1 SECTOR ONLY IMPLYING THAT
;CNTRL ACTION DID STOP AFTER
;SOFT ERROR IN SECTOR 0
;YES, EXIT
;GET EXPCTD RKDA
;GET RKDA RECVD
;RKDA SHOULD HAVE INCRMNTD
;BY 1 SECTOR ONLY, IT DIDN'T.
;WCE WAS SIMULATED IN THE
;FIRST SECTOR & A WRT CHK
;OF 2 SECTORS WAS ISSUED.
;CONTROLLER SHOULD STOP AFTER
;DETECTING WCE IN THE FIRST
;SECTOR. HENCE RKDA SHOULD
;INCREMENT BY 1 SECTOR ONLY

```

```

;*****
;*TEST 51 CHECK THAT RK11 INTERRUPTS ON SOFT ERROR WHEN SSE & IDE ARE SET
; *THIS TEST CHECKS WHEN SSE BIT IS SET WITH IDE SET AND A SOFT
; *ERROR OCCURS, THEN ALL CONTROL ACTION WILL STOP AND A BUS
; *REQUEST (INTERRUPT) WILL OCCUR AT THE END OF THE CURRENT
; *SECTOR. SOFT ERROR IS SIMULATED BY WCE AS IN PREVIOUS
; *TEST. PREREQUISITES FOR THIS TEST ARE THE SAME AS THOSE
; *FOR THE PREVIOUS TEST.
;*****

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5566
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5576 017130 000004
5577 017132 013700 001332
5578 017136 012701 177774
5579 017142 005002
5580 017144 012737 017152 001110
5581
5582 017152 104417 000142
5583 017156 004737 021432
5584 017162 104016
5585 017164 104413
5586
5587
5588
5589
5590
5591
5592
5593
5594
5595 017166 010210
5596 017170 012777 177777 162136
5597 017176 013777 001350 162134
5598 017204 012777 177776 162124
5599
5600 017212 052710 000007
5601
5602
5603
5604 017216 104412
5605
5606 017220 104065
5607 017222 010205
5608 017224 062705 000020
5609 017230 042705 000100
5610 017234 011004
5611 017236 042704 177717
5612 017242 020504
5613 017244 001405
5614 017246 010537 001162
5615 017252 010437 001164
5616 017256 104112
5617
5618
5619
5620
5621 017260 017703 162044

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*****
; *TEST 52 CHECK THE MEX BITS IN RKCS
; *THIS TEST CHECKS OUT THE EXTENDED MEMORY BITS OF THE RKCS.
; *THE RKBA IS SET TO 177776 AND A ONE WORD WRITE CHECK IS TRIED.
; *THIS COULD GIVE RISE TO NXM ERROR, BUT EVEN THEN THE RKBA
; *SHOULD OVERFLOW INTO THE MEX BITS. SIMILIARLY IT IS CHECKED
; *THAT THE OVERFLOWING BIT CAN MAKE THE MEX BITS COUNT
; *01,10,11,00.
*****
†ST52: SCOPE
MOV RKCS,R0
MOV #-4,R1 ;SET UP THE COUNT
CLR R2 ;INITIALIZE MEX BITS TO B SET IN RKCS
MOV #1$, $LPERR ;SET RETURN ADRES FOR
;LUPING ON EROR (SW9)
1$: DELAY 142 ;TIME DELAY
JSR PC,TSTRWS ;WAIT FOR R/W/S RDY
ERROR 16 ;R/W/S RDY IS NOT SET
CNT.RESET ;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR& IF IT
;OCCURS GO BACK TO TEST 10
MOV R2,DR0 ;SET MEX BITS (AS IN R2) IN RKCS
MOV #-1,DRKWC ;WRT CHK 1 WORD
MOV DRIVAD,DRKDA ;THIS DISK ADRES, SEC 0, CYL 0
MOV #177776,DRKBA ;THIS BUS ADRES. NOTE THIS BA
;IN CONJUCTION WITH MEX BITS OF RKCS
BIS #7,DR0 ;WRT CHK, GO
;THERE MAY BE A NXM OR WCE BUT
;WHATEVER THE CASE RKBA SHOULD
;OVERFLOW MAKING THE MEX BITS COUNT
;GO CHECK IF CONTROL RDY IS SET
;IF SO, SKIP THE EROR MESSAGE.
;CNTRL RDY DID NOT SET AFTER WRT CHK
3$: ERROR 65
MOV R2,R5 ;MEX BITS SHOULD INCREMENT BY 1 TO THIS
ADD #20,R5 ;MASK OUT IDE BIT POSITION, IF SET
BIC #100,R5 ;GET RKCS
MOV DR0,R4 ;MASK OUT ALL BITS EXCEPT MEX
BIC #177717,R4 ;DID MEX BITS INCREMENT CORRECTLY?
CMP R5,R4 ;YES, BRANCH
BEQ 4$ ;GET EXPCTD MEX BITS
MOV R5,$REG0 ;GET MEX BITS RECVD
MOV R4,$REG1 ;MEX BITS DID NOT INCREMENT AS
ERROR 112 ;'EXPCTD' WHEN RKBA OVERFLOWED.
;NOTE THAT BIT POSITION 4 & 5
;REFLECT MEX BITS 0 & 1 IN THE
;ERROR MESSAGE.
4$: MOV DRKER,R3 ;GET RKER

```



```

5622 017264 010305
5623 017266 042703 003001
5624 017272 001410
5625 017274 042705 177776
5626 017300 010537 001162
5627 017304 017737 162020 001164
5628 017312 104107
5629
5630
5631 017314 062702 000020
5632 017320 005201
5633 017322 001313
5634
5635
5636
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5641
5642
5643
5644
5645 017324 000004
5646 017326 012737 000001 001206
5647
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5651
5652
5653 017334 012737 017356 000004
5654 017342 005737 177700
5655 017346 012737 004534 000004
5656
5657 017354 000520
5658 017356 022626
5659 017360 012737 004534 000004
5660 017366 012746 000340
5661 017372 012746 017400
5662 017376 000002
5663 017400
5664 017400 013700 001332
5665 017404 104413
5666
5667
5668
5669
5670
5671
5672
5673
5674
5675 017406 012701 033336
5676 017412 013704 001336
5677 017416 012711 000100

```

```

MOV R3,R5
BIC #3001,R3 ;MASK WCE,DLT,NXM BIT, IF SET
BEQ 5$ ;BRANCH IF REST OF RKER CLR
BIC #177776,R5 ;MASK NON-WCE BITS
MOV R5,$REG0 ;THIS IS THE EXPCTD RKER
MOV @RKER,$REG1 ;GET RKER RECVD
ERROR 107 ;ERROR IN RKER. IT SHOULD
;BE AS EXPECTED IN
;ERROR MESSAGE
5$: ADD #20,R2 ;INCREMENT TO NXT MEX BIT
INC R1 ;HAVE U CHKD THE MEX BITS 4 TIMES?
BNE 1$ ;IF NOT, LUP BACK

;*****
;*TEST 53 TRANSFER FROM DISK TO TTY
;* THIS TEST CHECKS THE HIGH ORDER BITS OF THE ADDRESS
;* LINES. FIRST A ONE WORD (100) IS WRITTEN ON SECTOR,
;* 2, CYL 0. THEN IT IS READ BACK, BUT THE NPR IS DONE
;* NOT TO THE MEMORY, BUT THE TELETYPE BUFFER (TKS 177560)
;* AND IT CHECKED THAT THE WORD WAS RECIEVED CORRECTLY.
;* IF IT IS NOT, AN ERROR IS REPORTED. THIS TEST IS
;* SKIPPED ON AN 11/05.
;*****
†ST53: SCOPE
MOV #1,$TIMES ;DO 1 ITERATION
;THIS CODE FINDS OUT IF THE CPU
;IS AN 11/05 OR ELSE.
;ON AN 11/05, RO (177700) CAN BE
;ADDRESSED AS A MEMORY LOCATION, BUT
;ON ANY OTHER CPU IF 177700 IS REFERENCED
;A TIME OUT WILL OCCUR.
;SET UP TIME OUT VECTOR
TST @#177700 ;REFERENCE RO
MOV #BADTMO,@#4 ;RO WAS REFERENCED W/O TIMEOUT
;HENCE 11/05
BR TST54 ;SKIP THIS TEST
5$: CMP (SP)+,(SP)+ ;RESTORE STACK POINTER
MOV #BADTMO,@#4 ;RESTORE TIMEOUT VECTOR
MOV #340,-(SP)
MOV #64$,-(SP)
RTI
64$: MOV RKCS,RO
CNT.RESET ;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR& IF IT
;OCCURS GO BACK TO TEST 10
MOV #OUTBUF,R1
MOV RKBA,R4
MOV #100,@R1 ;WRITE THIS WORD

```

```

5678 017422 012777 177777 161704      MOV      #-1,ARKWC      ;WRITE 1 WORD
5679 017430 013702 001350      MOV      DRIVAD,R2
5680 017434 052702 000002      BIS      #2,R2        ;ON CYL 0, SEC 2
5681 017440 010277 161674      MOV      R2,ARKDA
5682 017444 010114      MOV      R1,AR4       ;FROM THIS MEMORY LOC
5683 017446 012710 000003      MOV      #3,AR0       ;WRITE, GO
5684 017452 005003      CLR      R3
5685 017454 105710      1$: TSTB   AR0
5686 017456 100410      BMI     2$
5687 017460 005203      INC     R3
5688 017462 001374      BNE     1$
5689 017464 004737 020770      JSR     PC,GT4RG      ;GET RKCS, ER, DS
5690 017470 010237 001202      MOV     R2,$REG10    ;GET THE STARTING ADRES
5691 017474 104416      BRKDAY ;BREAK IT INTO DRV #, CYL, SUR, SEC #
5692 017476 104031      ERROR  31           ;CNTRL RDY DID NOT SET AFTER
5693                                     ;WRITE OF 1 WORD ON CYL 0, SEC 2
5694 017500 012777 177777 161626 2$: MOV     #-1,ARKWC      ;READ 1 WORD
5695 017506 010277 161626      MOV     R2,ARKDA     ;FROM SEC 2, CYL 0
5696 017512 013714 001144      MOV     $TKS,AR4     ;INTO TTY STAU REGISTER
5697 017516 005077 161422      CLR     $STKS        ;CLEAR TTY KEY BRD STATUS REG
5698
5699 017522 012710 000065      MOV     #65,AR0     ;READ, MEX BITS SET
5700 017526 005003      CLR     R3
5701 017530 105710      3$: TSTB   AR0
5702 017532 100410      BMI     4$
5703 017534 005203      INC     R3
5704 017536 001374      BNE     3$
5705 017540 004737 020770      JSR     PC,GT4RG
5706 017544 010237 001202      MOV     R2,$REG10    ;GET THE STARTING ADRES
5707 017550 104416      BRKDAY ;BREAK IT INTO DR#, CYL, SUR, SEC#
5708 017552 104045      ERROR  45           ;CNTRL RDY DIDN'T SET AFTER
5709                                     ;READ OF 1 WORD FROM CYL 0, SEC 2.
5710                                     ;IN EROR MSGE, <DSK-ADRES> GIVES
5711                                     ;ADRES WHERE READ BEGAN. 'RKDA'
5712                                     ;GIVES CONTENTS OF RKDA AT TIME OF EROR
5713 017554 032737 000100 001144 4$: BIT     #100,$TKS    ;WAS THE CORRECT WORD READ INTO
5714                                     ;THE TTY STATUS REGISTER?
5715 017562 001015      BNE     TST54        ;;YES, EXIT
5716 017564 017705 161354      MOV     $STKS,R5     ;GET THE WORD RECVD FROM DISK
5717 017570 010537 001164      MOV     R5,$REG1
5718 017574 052705 000100      BIS     #100,R5      ;THIS WORD WAS EXPCTD
5719 017600 010537 001162      MOV     R5,$REG0    ;STORE EXPCTD WORD
5720 017604 011437 001166      MOV     AR4,$REG2   ;GET RKBA
5721 017610 011037 001170      MOV     AR0,$REG3   ;GET RKCS
5722 017614 104115      ERROR  115         ;DATA ERROR. A ONE WORD (100)
5723                                     ;NPR WAS TRIED FROM DISK TO
5724                                     ;TTY KEYBOARD STATUS REGISTER
5725                                     ;(17756) BIT 6 SHOULD HAVE BEEN
5726                                     ;SET AS RESULT OF THIS
5727                                     ;BUT IT WAS NOT
5728
5729
5730
5731
5732
5733

```

```

::*****
:*TEST 54      CHECK THAT RKBA CAN COUNT CORRECTLY
:*THIS TEST CHECKS THAT RKBA CAN COUNT CORRECTLY. IT IS SET
:*TO THE DESIRED INITIAL VALUE. THEN A ONE WORD WRITE CHECK

```

```

5734                                     ;*IS TRIED, WITH MEX (MEMORY EXTENSION) BITS SET. IF THERE IS
5735                                     ;*NO MEMORY PRESENT (FOR CERTAIN BUS ADDRESSES), THERE
5736                                     ;*WILL BE AN NXM ERROR STOPPING CONTROLLER ACTION. BUT RKBA
5737                                     ;*SHOULD HAVE INCREMENTED BY 1 FROM ITS INITIAL VALUE. IF IT
5738                                     ;*HAS NOT, AN ERROR IS REPORTED.
5739                                     ;*****
5740 017616 000004                               T54: SCOPE
5741 017620 012737 000005 001206             MOV #5, $TIMES ;DO 5 ITERATIONS
5742 017626 104421                               TST.SIN ;CHECK IF SIN SET, IF SET DRV RESET
5743 017630 005001                               CLR R1 ;INITIALIZE (VALUE OF RKBA)
5744 017632 012702 000002                       MOV #2, R2 ;INITIALIZE (INCMNTD VALUE OF RKBA)
5745
5746 017636 012737 017650 001110             MOV #1$, $LPERR ;SET RETURN ADRES FOR LUPING
5747 ;ON EROR
5748
5749 017644 013705 001336                               MOV RKBA, R5
5750 017650 004737 021432                               JSR PC, T$TRWS ;WAIT FOR R/W/S RDY
5751 017654 104016                               ERROR 16 ;R/W/S RDY IS NOT SET
5752 017656 104413                               CNT.RESET ;DO CONTROL RESET
5753 017660 012777 177777 161446             MOV #-1, $RKWC ;WRITE CHK 1 WORD
5754 017666 010115                               MOV R1, $RS ;THIS BUS ADRES
5755 017670 013777 001350 161442             MOV $DRVAD, $RKDA ;SET DISK ADRES
5756 017676 012777 000067 161426             MOV #67, $RKCS ;WRITE CHECK, GO, MEX BITS SET
5757 017704 104412                               CHKCRDY ;GO CHECK IF CONTROL RDY IS SET
5758 ;IF SO, SKIP THE EROR MESSAGE.
5759 017706 104065                               ERROR 65 ;CNTRL RDY DID NOT SET AFTER
5760 ;WRT CHK WAS TRIED TO NXM LOC
5761 ;U MIGHT WANT TO USE TESTS
5762 ;CHECKING MEX BITS & NXM.
5763 017710 005237 001356                               INC INDX1 ;ALLOW ONLY 5 ERRORS OF ABOVE KIND
5764 017714 001417                               BEQ 5$
5765
5766 017716 020215                               3$: CMP R2, $RS ;DID RKBA INCREMENT BY 1 FROM
5767 ;ITS INITIAL VALUE?
5768 017720 001410                               BEQ 4$ ;YES, BRANCH
5769 017722 010137 001162                       MOV R1, $REG0 ;GET EXPCTD RKBA
5770 017726 011537 001164                       MOV $RS, $REG1 ;GET RKBA RECVD
5771 017732 104017                               ERROR 17 ;RKBA DID NOT INCREMENT BY
5772 ;1 FROM ITS INITIAL VALUE.
5773 ;ONE WORD WRT CHK WAS TRIED
5774 ;TO A NXM LOCATION. THERE
5775 ;WILL BE AN NXM ERROR,
5776 ;BUT STILL RKBA SHOULD
5777 ;INCREMENT BY 1 FROM ITS
5778 ;INITIAL VALUE.
5779 017734 005237 001360                               INC INDX2 ;ALLOW ONLY 5 ERRORS OF
5780 017740 001405                               BEQ 5$ ;THE ABOVE KIND
5781 017742 060201                               4$: ADD R2, R1 ;SET NXT VALUE OF RKBA
5782 017744 010102                               MOV R1, R2
5783 017746 062702 000002                       ADD #2, R2 ;SET EXPCTD VALUE OF RKBA
5784 017752 001336                               BNE 1$ ;ALL DONE?
5785
5786 017754                               5$: ;DUMMY EXIT POINT
5787
5788
5789 ;*****

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5791
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5795 017754 000004
5796 017756 012737 000001 001206
5797 017764 005737 001404
5798 017770 001403
5799 017772 004537 025154
5800 017776 104120
5801
5802 020000
5803
5804
5805
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5809
5810
5811
5812 020000 000004
5813 020002 012737 000001 001206
5814 020010 005237 001352
5815
5816 020014 004737 021500
5817 020020 104026
5818 020022 023737 001412 001352 BTEOP:
5819
5820 020030 001405
5821 020032 062737 020000 001350
5822 020040 000137 005040
5823
5824 020044 005037 001112
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5827
5828
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5843 020050 000004
5844 020052 012737 000005 001206
5845 020060 005237 001440

```

:*TEST 55      CHECK FOR RK-05F
; *THIS TEST CHECKS RK-05F TYPE DRIVES
; *TO INSURE THAT IF SEEKS ARE ISSUED ON ONE
; *DRIVE, THE OTHER DRIVE BECOMES BUSY
; *****
†ST55: SCOPE
MOV      #1,$TIMES      ;; DO 1 ITERATION
TST      FFLAG          ;; SEE IF RK-05F
BEQ      IS             ;; NOT F
JSR      R5,FCHECK      ;; SEE IF OTHER GOES BUSY
ERROR    120

IS:

; *****
:*TEST 56      END OF PROGRAM
; *THIS IS NOT A TEST, BUT A LINKAGE PROVIDED TO PERFORM
; *THE ABOVE SUB-TESTS FOR ALL DRIVES THAT ARE PRESENT.
; *NOTE THAT THE NEXT TEST- HARDWARE POLLING LOGIC-
; *IS DONE USING ALL THE DRIVES THAT ARE INDICATED PRESENT.
; *DO NOT LOOP ON THIS 'TEST'.
; *****
†ST56: SCOPE
MOV      #1,$TIMES      ;; DO 1 ITERATION
INC      DRVDON          ;; INCREMENT THE COUNT FOR THE NUMBER
                        ;; OF DRIVES THAT ARE CHECKED
JSR      PC,DRESET      ;; RESET THE DRIVE
ERROR    26             ;; R/W/S DIDN'T SET AFTER DRIVE RESET
BTEOP:  CMP      DRIVS,DRVCON ;; HAVE U TESTED ALL THE DRIVES
                        ;; THAT ARE PRESENT?
BEQ      IS             ;; IF YES, EXIT
ADD      #20000,DRIVAD   ;; ADRES THE NXT POSSIBLE DRIVE
JMP      NUDRV          ;; GO BACK AND TEST THE NEXT
                        ;; DRIVE PRESENT

IS:      CLR      $ERTTL

; *****
:*TEST 57      CHECK HARDWARE POLLING LOGIC
; *THIS TEST CHECKS THE HARDWARE POLL LOGIC, USING ALL THE DRIVES
; *PRESENT ON THE RK11. ATLEAST TWO DRIVES SHOULD BE PRESENT
; *TO DO A MEANINGFUL HARDWARE POLL. SEQUENCE OF OPERATIONS IS
; *AS FOLLOWING:
; *1) NUMBER OF DRIVES ON THE RK11 IS ASCERTAINED.
; *2) HAVING LOCKED OUT ALL INTERRUPTS (CPU PR 7), SEEK IS INITIATED
; *FOR ONE DRIVE AT A TIME, ONLY WHEN 'CNTRL RDY' IS SET.
; *3) CPU PRIORITY IS DROPPED TO 4 SO THAT RK11 CAN INTERRUPT. THE INCOMING
; *INTERRUPT IS PROCESSED TO CHECK IF IT WAS DUE TO 'SEEK DONE' BY
; *ONE OF THE DRIVES.
; *4) IF BY THE END OF THE SET TIME A DRIVE HAS NOT INTERRUPTED
; *AN ERROR MESSAGE IS GIVEN INDICATING WHICH DRIVE DID NOT
; *INTERRUPT AFTER SEEK WAS DONE.
; *****
†ST57: SCOPE
MOV      #5,$TIMES      ;; DO 5 ITERATIONS
INC      SI2YET         ;; FOUNR RK05F YET?

```


5902	020252	032711	000001		BIT	#BIT0,(R1)	: IS THIS DRIVE PRESENT?
5903	020256	001433			BEQ	4\$: IF NOT, BRANCH
5904	020260	005711			TST	(R1)	: RK06F?
5905	020262	100012			BPL	17\$: NO, CONTINUE
5906	020264	032702	020000		BIT	#BIT13,R2	: DRIVE EVEN?
5907	020270	001404			BEQ	16\$: YES
5908	020272	005737	001406		TST	0DDEVN	: DO WE WANT ODD?
5909	020276	001423			BEQ	4\$: NO, SO DO NOT TEST
5910	020300	000403			BR	17\$: ADD THIS DRIVE TO LIST
5911	020302	005737	001406	16\$:	TST	0DDEVN	: DO WE WANT EVEN?
5912	020306	001017			BNE	4\$: NO, SO SKIP
5913	020310	010215		17\$:	MOV	R2,(R5)	: SET UP THIS WORD IN THE
5914							: INDICATOR AREA SHOWING THAT THIS
5915							: DRIVE (AS IN BITS 13-15 OF R2)
5916							: IS PRESENT
5917	020312	042725	017777		BIC	#17777,(R5)+	: MASK OUT UNWANTED BITS (CYL,SUR,SEC BITS)
5918	020316	005004			CLR	R4	
5919	020320	105710		2\$:	TSTB	3R0	: IS CNTRL RDY SET?
5920	020322	100405			BMI	3\$: YES, BRANCH
5921	020324	005204			INC	R4	: NO, WAIT FOR IT
5922	020326	001374			BNE	2\$: IF WAITED LONG REPORT ERROR
5923	020330	004737	020770		JSR	PC,GT4RG	: GO, GET RKCS,ER,DS,DA
5924	020334	104021			ERROR	21	: CNTRL RDY DID NOT SET AFTER ACCEPTING
5925							: ADRES FROM PREVIOUS SEEK
5926	020336	010277	160776	3\$:	MOV	R2,3RKDA	: ADRES THIS DRIVE, CYL 0 OR CYL 4
5927							: (WHICHEVER THE CASE MAY BE)
5928	020342	012710	000111		MOV	#111,3R0	: SEEK,GO,IDE SET
5929	020346	005721		4\$:	TST	(R1)+	: NEXT DRIVE DATA
5930	020350	062702	020000		ADD	#20000,R2	: INCREMENT DRIVE ADRES (BITS 15,14,13)
5931	020354	005203			INC	R3	: TO NEXT ONE
5932	020356	001330			BNE	1\$: BRANCH BACK IF ALL DRIVES ARE
5933							: NOT CHECKED TO SEE IF THE NEXT
5934							: DRIVE IS PRESENT (& IF SO ISSUE A
5935							: SEEK TO IT)
5936							: BY NOW SEEKS HAVE BEEN ISSUED
5937							: TO ALL DRIVES PRESENT & POLLING
5938							: HAS BEGUN
5939	020360	005004			CLR	R4	
5940	020362	013702	001402	5\$:	MOV	RKVEC,R2	
5941	020366	012722	020420		MOV	#6\$, (R2)+	: SET ADRES FOR RK11 TO INTERRUPT
5942	020372	012712	000340		MOV	#340,(R2)	: SET PSW ON INTERRUPT
5943	020376	013746	001400		MOV	RKPRI,-(SP)	: DROP CPU PRIORITY TO 4 SO THAT
5944	020402	012746	020410		MOV	#18\$,-(SP)	: RK11 CAN INTERRUPT
5945	020406	000002			RTI		
5946	020410	000240		18\$:	NOP		: THIS IS A TIME LOOP DURING
5947	020412	005204			INC	R4	: WHICH ALL DRIVES PRESENT SHOULD
5948	020414	001375			BNE	18\$: INTERRUPT
5949	020416	000452			BR	11\$: BRANCH AND CHECK IF ALL AVAILABLE
5950							: DRIVES INTERRUPTED CORRECTLY
5951	020420	022626		6\$:	CMP	(SP)+,(SP)+	: RESTORE STACK POINTER
5952	020422	005737	001360		TST	INDX2	: WAS THIS FIRST INTERRUPT
5953							: DUE TO 'ADRES ACK' AFTER INITIATION
5954							: OF SEEK?
5955	020426	001021			BNE	9\$: IF YES, CHECK THE FOLLOWING
5956							
5957	020430	032710	020000		BIT	#20000,3R0	: CHECK THAT SCP IS NOT SET


```

6014 020556 001006      BNE 13$      ;YES, BRANCH
6015 020560 011546      MOV (R5),-(R6) ;GET THIS DRIVE #
6016 020562 004737 021174 JSR PC,SHFRT  ;SHIFT IT TO THE RIGHT
6017 020566 012637 001162 MOV (R6)+,$REGO ;THIS DRIVE # DID NOT INTERRUPT
6018                                     ;DURING H'WARE POLL
6019 020572 104052      ERROR 52      ;DRIVE # (AS IN $REGO) DID NOT
6020                                     ;INTERRUPT DURING HARDWARE POLL
6021 020574 062705 000002 13$: ADD #2,R5    ;INCREMENT POINTER TO THE NEXT FLAG
6022 020600 005303      DEC R3      ;CHKD FOR ALL DRIVES?
6023 020602 001364      BNE 14$    ;IF NOT LUP BACK
6024                                     ;
6025 020604 005737 001356 TST INDX1   ;DONE POLLING FOR SEEKS TO CYL 312?
6026 020610 001004      BNE TSTEND ;IF YES, EXIT
6027 020612 005237 001356 INC INDX1   ;IF NOT, INCREMENT FLAG
6028 020616 000137 020162 JMP 15$    ;GO DO IT
6029                                     ;
6030                                     ;INDICATOR TABLE
6031                                     ;THE 8-WORD INDICATOR TABLE USED IN
6032                                     ;THE FORMER PART OF THIS SUB-TEST
6033                                     ;IS LOCATED STARTING AT 'OUTBUF'.
6034                                     ;WORDS ARE SET UP TO INDICATE
6035                                     ;PRESENCE OF A DRIVE EG: IF
6036                                     ;DRIVES 0,1,2 ARE PRESENT, IT WILL
6037                                     ;LOOK LIKE
6038                                     ;OUTBUF:          000000 BITS 13,14,15
6039                                     ;                   020000 CONTAIN THE
6040                                     ;                   040000 DRIVE NO.
6041                                     ;                   000000 REST 0'S
6042                                     ;WHEN A DRIVE INTERRUPTS AFTER SEEK
6043                                     ;IS DONE BIT 0 OF THE CORRESPONDING
6044                                     ;INDICATOR WORD IS SET. THUS FOR THE
6045                                     ;ABOVE EXAMPLE IF ALL DRIVES INTERRUPTED
6046                                     ;CORRECTLY THEN IT WILL LOOK LIKE:
6047                                     ;                   12$: 000001 BIT 0 SET
6048                                     ;                   020001 TO INDICATE
6049                                     ;                   040001 DR INTERRUPTED
6050                                     ;                   000000 REST 0'S
6051                                     ;
6052                                     ;
6053 020622 005237 001406 TSTEND: INC 0DDEVN ;NOW ODD IF RK05F
6054 020626 022737 000002 001406 CMP #2,0DDEVN ;SEE IF DONE
6055 020634 001402      BEQ 21$      ;ALL DONE
6056 020636 000137 020134 JMP T56     ;TEST AGAIN
6057 020642 005037 001434 21$: CLR T56FLG
6058
6059
6060 .SBTTL END OF PASS ROUTINE
6061
6062 ;*****
6063 ;*INCREMENT THE PASS NUMBER ($PASS)
6064 ;*INDICATE END-OF-PROGRAM AFTER 1 PASSES THRU THE PROGRAM
6065 ;*TYPE "END PASS #XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)
6066 ;*IF THERES A MONITOR GO TO IT
6067 ;*IF THERE ISN'T JUMP TO ST4
6068
6069 020646 $EOP:
    
```



```

6070 020646 000004
6071 020650 005037 001102
6072 020654 005037 001206
6073 020660 005237 001100
6074 020664 042737 100000 001100
6075 020672 005327
6076 020674 000001
6077 020676 003022
6078 020700 012737
6079 020702 000001
6080 020704 020674
6081 020706 104401 020753
6082 020712 013746 001100
6083 020716 104405
6084 020720 104401 020750
6085 020724 013700 000042
6086 020730 001405
6087 020732 000005
6088 020734 004710
6089 020736 000240
6090 020740 000240
6091 020742 000240
6092 020744
6093 020744 000137
6094 020746 004456
6095 020750 377 377 000
6096 020753 015 042412 042116
6097 020760 050040 051501 020123
6098 020766 000043
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```

SCOPE
CLR $TSTNM ;; ZERO THE TEST NUMBER
CLR $TIMES ;; ZERO THE NUMBER OF ITERATIONS
INC $PASS ;; INCREMENT THE PASS NUMBER
BIC #100000,$PASS ;; DON'T ALLOW A NEG. NUMBER
DEC (PC)+ ;; LOOP?
$EOPCT: .WORD 1
BGT $DOAGN ;; YES
MOV (PC)+,a(PC)+ ;; RESTORE COUNTER
$ENDCT: .WORD 1
$EOPCT
TYPE $ENDMG ;; TYPE "END PASS #"
MOV $PASS,-(SP) ;; SAVE $PASS FOR TYPEOUT
TYPDS ;; GO TYPE--DECIMAL ASCII WITH SIGN
TYPE $ENULL ;; TYPE A NULL CHARACTER
$GET42: MOV a#42,R0 ;; GET MONITOR ADDRESS
BEQ $DOAGN ;; BRANCH IF NO MONITOR
RESET ;; CLEAR THE WORLD
$ENDAD: JSR PC,(R0) ;; GO TO MONITOR
NOP ;; SAVE ROOM
NOP ;; FOR
NOP ;; ACT11
$DOAGN:
JMP a(PC)+ ;; RETURN
$RTNAD: .WORD ST4
$ENULL: .BYTE -1,-1,0 ;; NULL CHARACTER STRING
$ENDMG: .ASCIZ <15><12>/END PASS #/

```

.SBTTL GT2RG: ROUTINE FOR GETTING RKCS,RKER

```

;SUBROUTINE FOR TRANSFERRING THE CONTENTS OF RKCS, RKER
;TO $REG0,$REG1 RESPECTIVELY BEFORE TYPING OUT AN ERROR MESSAGE.
;CALL: JSR PC,GT2RG

```

.SBTTL GT3RG: ROUTINE FOR GETTING RKCS, RKER, RKDS

```

;GT3RG
;SUBROUTINE FOR TRANSFERRING THE CONTENTS OF RKCS, RKER, RKDS
;TO $REG0,$REG1,$REG2 RESPECTIVELY BEFORE TYPING OUT AN
;ERROR MESSAGE.
;CALL: JSR PC,GT3RG

```

.SBTTL GT4RG: ROUTINE FOR GETTING RKCS, RKER, RKDS, RKDA

```

;GT4RG
;SUBROUTINE FOR TRANSFERRING CONTENTS OF RKCS, RKER, RKDS
;RKDA TO $REG0,$REG1,$REG2,$REG3 RESPECTIVELY BEFORE
;TYPING OUT AN ERROR MESSAGE.

```

```

6126 ;CALL: JSR PC,GT4RG
6127
6128 020770 017737 160344 001170 GT4RG: MOV BRKDA,$REG3 ;GET RKDA
6129 020776 017737 160324 001166 GT3RG: MOV BRKDS,$REG2 ;GET RKDS
6130 021004 017737 160320 001164 GT2RG: MOV BRKER,$REG1 ;GET RKER
6131 021012 017737 160314 001162 MOV BRKCS,$REG0
6132 021020 000207 RTS PC
6133
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6142
6143
6144
6145
6146 021022
6147 021022 104401 021030
6148 021026 000406
6149
6150 021044
6151 021044 010346
6152 021046 104402
6153 021050 000207
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6177
6178 021052 010046
6179 021054 012700 001172
6180 021060 000403
6181

```

.SBTTL TYERM: SPECIAL ERROR MESSAGE ROUTINE

```

:TYERM
:THIS ROUTINE TYPES OUT 'EROR AT PC=X'
:X IS THE PC WHERE THE EXPLANATION AS TO WHAT HAPPENED IS GIVEN. THIS ROUTINE
:IS USED ONLY FOR NON-MANUAL MODE OF THE PROGRAM.
:CALL: JSR TYERM

```

```

TYERM:
        TYPE      65$      ;;TYPE ASCIZ STRING
        BR        64$      ;;GET OVER THE ASCIZ
;;65$: .ASCIZ <15><12>/EROR,PC=/
64$:   MOV        R3,-(SP)
        TYPOC
        RTS       PC

```

.SBTTL BDAO, BDA4: BREAK DISK ADDRESS INTO SEC, SUR, CYL, DRIVE

```

:BDAO, BDA4
:THIS ROUTINE BREAKS A DISK ADDRESS (BITS 0-15) INTO DRIVE #,
:CYLINDER #, SURFACE, SECTOR #. THE ROUTINE IS CALLED BY USING EITHER
:BRKDAO OR BRKDA4, BOTH BEING 'TRAP' INSTRUCTIOS WITH THEIR LOWER BYTES
:ENCODED TO PROVIDE INDEXING TO 'BDAO' OR 'BDA4'. BEFORE CALLING
:THE ROUTINE THE DISK ADDRESS WHICH IS TO BE BROKEN AS ABOVE
:IS DEPOSITED IN $REG10.
:'BRKDAO' PUTS THE
:DRIVE # INTO $REG0
:CYLINDER # INTO $REG1
:SURFACE # INTO $REG2
:SECTOR # INTO $REG3
:CALL: BRKDAO
BRKDA4 PUTS THE
DRIVE # INTO $REG4
CYLINDER # INTO $REG5
SURFACE # INTO $REG6
SECTOR # INTO $REG7
BRKDA4

```

```

BDAO:  MOV        R0,-(SP)      ;PUSH R0 ONTO THE STACK
        MOV        #$REG3+2,R0 ;SET UP POINTER
        BR        BDA4

```

```

6182 021062 010046          BDA4:  MOV    R0,-(SP)      ;PUSH R0 ONTO THE STACK
6183 021064 012700 001202    MOV    #SREG7+2,R0    ;SET UP POINTER
6184
6185 021070 032777 020000 160042 BDA4:  BIT    #20000,ASWR    ;INHIBIT TYPEOUT?
6186 021076 001034          BNE    2$            ;YES, BRANCH TO EXIT POINT
6187
6188 021100 010146          MOV    R1,-(SP)      ;PUSH R1 ON STACK
6189 021102 010246          MOV    R2,-(SP)      ;PUSH R2 ON STACK
6190 021104 013701 001202    MOV    $REG10,R1     ;GET THE ADDRESS WHICH
6191          ;HAS TO BE BROKEN
6192 021110 042701 177760    BIC    #177760,R1    ;EXTRACT SECTOR BITS 0-3
6193 021114 010140          MOV    R1,-(R0)     ;MOVE SECTOR BITS TO $REG3 OR $REG7
6194 021116 013701 001202    MOV    $REG10,R1     ;GET THE DSK-ADRES TO BE BROKEN
6195 021122 006201          ASR    R1            ;SHIFT RIGHT 4 TIMES
6196 021124 006201          ASR    R1
6197 021126 006201          ASR    R1
6198 021130 006201          ASR    R1
6199 021132 010102          MOV    R1,R2        ;STORE THIS
6200 021134 042702 177776    BIC    #177776,R2    ;EXTRACCT THE SURFACE BIT
6201 021140 010240          MOV    R2,-(R0)     ;MOVE SURFACE BIT TO $REG3 OR $REG6
6202 021142 006201          ASR    R1
6203 021144 010102          MOV    R1,R2        ;STORE IT
6204 021146 042702 177400    BIC    #177400,R2    ;EXTRACT THE CYLINDER BITS
6205 021152 010240          MOV    R2,-(R0)     ;MOVE CYLINDER BITS TO $REG1 OR $REG5
6206 021154 000301          SWAB   R1            ;SWAB HI-LO BYTES
6207 021156 042701 177770    BIC    #177770,R1    ;EXTRACT THE DRIVE #
6208 021162 010140          MOV    R1,-(R0)     ;MOVE DRIVE # TO $REG0 OR $REG4
6209
6210 021164 012602          MOV    (SP)+,R2     ;RESTORE R2
6211 021166 012601          MOV    (SP)+,R1     ;RESTORE R1
6212 021170 012600 2$:    MOV    (SP)+,R0     ;RESTORE R0 FROM THE STACK
6213 021172 000002          RTI                ;RETURN FROM INTERRUPT, EXIT THIS
6214          ;ROUTINE
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6226 021174 012737 177763 021220 SHFTRT: MOV    #-15,2$    ;SET UP A COUNT OF 13
6227 021202 000241          CLC                ;CLEAR THE C BIT
6228 021204 006066 000002 1$:    ROR    2(R6)        ;ROTATE RIGHT THE WORD TO B SHFTD
6229 021210 005237 021220    INC    2$          ;SHIFTED 13 TIMES?
6230 021214 001373          BNE    1$          ;IF NOT LUP BAK & SHIFT
6231 021216 000207          RTS                ;EXIT FROM THIS SUBROUTINE
6232 021220 000000 2$:    0
6233
6234
6235
6236
6237

```

.SBTTL SHFTRT: SHIFT RIGHT ROUTINE

```

;SHFTRT
;THIS ROUTINE SHIFTS A WORD TO THE RIGHT 13 TIMES. THE WORD TO BE SHIFTED
;IS PUT ON THE STACK BEFORE ENTERING THIS ROUTINE AND IT IS POPPED UP
;FROM THE STACK AFTER THE SHIFT HAS BEEN DONE.
;CALL: JSR PC,SHFTRT

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6292
6293.SBTTL CHKHE: CHECK FOR 'ERR'OR
.SBTTL CHKHE1: CHECK FOR 'ERR'OR

```

;CHKHE
;THIS ROUTINE CHECKS IF 'HE' OR 'ERR' BITS IN RKCS ARE SET. IF ANY OF THE
;TWO BITS ARE SET, THE CONTENTS OF RKCS, ER, DS, AND DA ARE SAVED AND A
;RETURN IS MADE TO THE ERROR MESSAGE FOLLOWING THE 'JSR' CALL.
;AT THE TIME OF ENTRY 'DRIVAD' CONTAINS THE DISK ADDRESS WHICH IS TO
;BE BROKEN DOWN INTO DRIVE #, CYLINDER, SURFACE AND SECTOR #. THIS INFORMATION
;IS SAVED TO BE USED LATER FOR ERROR REPORTING. IF THE BITS ARE NOT SET,
;RETURN IS MADE TO SKIP THE ERROR MESSAGE.

```

```

;CHKHE1
;THIS ROUTINE CHECKS IF 'HE' OR 'ERR' BITS IN RKCS ARE SET. IF ANY OF THE
;TWO BITS ARE SET, THE CONTENTS OF RKCS, ER, DS, AND DA ARE SAVED AND A
;RETURN IS MADE TO THE ERROR MESSAGE FOLLOWING THE 'JSR' CALL.
;AT THE TIME OF ENTRY R1 CONTAINS THE DISK ADDRESS WHICH IS TO BE BROKEN
;DOWN INTO DRIVE #, CYLINDER, SURFACE AND SECTOR #. THIS INFORMATION IS
;SAVED TO BE USED LATER FOR ERROR REPORTING. IF THE BITS ARE NOT SET,
;RETURN IS MADE TO SKIP THE ERROR MESSAGE.

```

```

CHKHE1: MOV    R1,$REGIO    ;SAVE THE DISK ADRES
        BR     CHE1
CHKHE:  MOV    DRIVAD,$REGIO ;SAVE THE DISK ADRES
CHE1:   BIT    #!40000,$RKCS ;IS 'HE' OR 'ERR' BIT SET?
        BEQ   CRETRN        ;NO
        JSR   PC,$GTARG     ;GET RKCS, ER, DS, DA
        BRKDA4              ;GO TO 'BD4' & BREAK CONTENTS OF
                           ;$REGIO INTO DR#, CYL, SUR, SEC BITS
        RTS    PC           ;RETURN TO THE ERROR MESSAGE

```

.SBTTL CHKDA: CHECK IF RKDA INCREMENTED CORRECTLY

```

;CHKDA
;THIS ROUTINE CHECKS IF RKDA INCREMENTED CORRECTLY. IF RKDA INCREMENTED
;CORRECTLY RETURN IS MADE TO SKIP THE ERROR MESSAGE.
;IF RKDA DID NOT INCREMENT CORRECTLY, THE EXPECTED AND RECIEVED VALUES
;OF RKDA ARE SAVED AND A RETURN IS MADE TO THE ERROR MESSAGE FOLLOWING THE
;'JSR' CALL.

```

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CHKDA:  MOV    DRIVAD,R5    ;RKDA SHOULD INCREMENT TO THIS
        INC    R5          ;AFTER DATA TRANSFER IS DONE
CHKDA1: CMP    R5,$RKDA    ;DID RKDA INCREMENT CORRECTLY?
        BEQ   CRETRN        ;IF YES, BRANCH
        MOV    R5,$REGIO   ;IF NOT, REPORT ERROR
        BRKDA0              ;GET EXPCTD RKDA
                           ;GO TO 'BD4' & BREAK CONTENTS OF
                           ;$REGIO INTO DR #, CYL, SUR, SEC BITS
        MOV    $RKDA,$REGIO ;GET ACTUAL RKDA
        BRKDA4              ;GO TO 'BD4' & BREAK CONTENTS OF
                           ;$REGIO INTO DR #, CYL, SUR, SEC BITS
        RTS    PC           ;RETURN TO THE ERROR MESSAGE

```

.SBTTL CHKWC: CHECK IF RKWC OVERFLOWED

6294
6295
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6300 021312 005777 160016
6301 021316 001442
6302
6303 021320 017737 160010 001162
6304 021326 017737 160006 001164
6305 021334 000207
6306
6307
6308
6309
6310
6311
6312
6313
6314 021336 005777 157766
6315 021342 001430
6316
6317 021344 004737 020776
6318
6319 021350 000207
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6327 021352 005777 157752
6328 021356 001422
6329 021360 013737 001330 001162
6330 021366 017737 157736 001164
6331 021374 000207
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6338 021376 022777 000200 157726
6339 021404 001407
6340 021406 013737 001332 001162
6341 021414 017737 157712 001164
6342 021422 000207
6343
6344 021424 062716 000002
6345 021430 000207
6346
6347
6348
6349

;CHKWC
;THIS ROUTINE CHECKS IF RKWC OVERFLOWED TO 0. IF IT DID A RETURN IS MADE
;TO SKIP THE ERROR MESSAGE. IF NOT, THE CONTENTS OF RKWC AND RKDA ARE SAVED
;AND A RETURN IS MADE TO THE ERROR MESSAGE FOLLOWING THE 'JSR' CALL.

CHKWC: TST @RKWC ;DID WORD COUNT OVERFLOW TO 0?
BEQ CRETRN ;IF YES, BRANCH
;IF NOT, ERROR
MOV @RKWC, \$REG0 ;GET RKWC
MOV @RKDA, \$REG1 ;GET RKDA
RTS PC ;RETURN TO THE ERROR MESSAGE

.SBTTL CHKER: CHECK RKER CONTENTS

;CHKER
;THIS ROUTINE CHECKS IF ANY BIT IN RKER SET. IF NOT RETURN IS MADE TO SKIP
;THE ERROR MESSAGE. IF ANY BIT IS SET THE CONTENTS OF RKCS, RKER, RKDS ARE
;SAVED AND A RETURN IS MADE TO THE ERROR MESSAGE.

CHKER: TST @RKER ;DID ANY BIT IN RKER SET?
BEQ CRETRN ;NO, BRANCH
;YES, ERROR
JSR PC, GT3RG ;GO, GET RKCS, ER, DS
RTS PC ;RETURN TO THE ERROR MESSAGE

;CHKECLR
;THIS ROUTINE CHECKS THAT RKER IS CLEAR. IF NOT, THE CONTENTS OF RKER
;ARE SAVED AND A RETURN IS MADE TO THE ERROR MESSAGE FOLLOWING THE "JSR"
;CALL. IF RKER IS CLEAR THE ERROR MESSAGE IS SKIPPED ON RETURN.

CHKECLR: TST @RKER ;ANY BIT IN RKER SET?
BEQ CRETRN ;NO
MOV @RKER, \$REG0 ;GET ADRES OF RKER
MOV @RKER, \$REG1 ;GET CONTENTS OF RKER
RTS PC ;RETURN TO THE ERROR MESSAGE

;CHKCCLR
;THIS ROUTINE CHECKS THAT RKCS IS CLEAR. IF NOT, THE CONTENTS OF RKCS ARE
;SAVED AND A RETURN IS MADE TO THE ERROR MESSAGE. IF RKCS IS CLEAR THE
;ERROR MESSAGE IS SKIPPED ON RETURN.

CHKCCLR: CMP #200, @RKCS ;IS RKCS CLEAR?
BEQ CRETRN ;YES
MOV @RKCS, \$REG0 ;SAVE ADRES OF RKCS
MOV @RKCS, \$REG1 ;SAVE THE CONTENT OF RKCS
RTS PC ;RETURN TO THE ERROR MESSAGE

CRETRN: ADD #2, (SP) ;SKIP ERROR MESSAGE ON
RTS PC ;RETURN

.SBTTL TSTRWS: WAIT FOR R/W/S RDY ROUTINE

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6350 ;TSTRWS
6351 ;THIS ROUTINE WAITS FOR R/W/S RDY TO SET. WHEN IT SETS, THE RETURN PC
6352 ;IS INCREMENTED SO THAT ON RETURN (TO THE MAIN PROGRAM) THE ERROR
6353 ;MESSAGE FOLLOWING THE 'JSR' CALL IS SKIPPED. IF R/W/S RDY DOES NOT SET
6354 ;THEN A RETURN IS MADE TO THE ERROR MESSAGE (FOLLOWING THE 'JSR' CALL).
6355 ;WAITING TIME IS APPROX. 1040 MS FOR 11/20, APPROX. 208 MS FOR 11/45
6356 ;CALL: JSR TSTRWS
6357
6358 021432 013777 001350 157700 TSTRWS: MOV DRIVAD, @RKDA ;ADRES THE DRIVE
6359 021440 005037 001366 CLR TIMER ;INITIALIZE COUNT
6360 021444 032777 000100 157654 1$: BIT #100, @RKDS ;DID R/W/S RDY SET?
6361 021452 001007 BNE 2$ ;YES, BRANCH
6362 021454 005237 001366 INC TIMER ;WAIT FOR R/W/S RDY
6363 021460 001371 BNE 1$ ;ERROR IF IT'S NOT SET BY NOW
6364 021462 017737 157640 001162 MOV @RKDS, $REGO ;GET RKDS
6365 021470 000207 RTS PC ;EXIT (TO ERROR FOOLOWING 'JSR TSTRWS')
6366
6367 021472 062716 000002 2$: ADD #2, (SP) ;ADJUST RETURN ADRES TO SKIP OVER
6368 ;ERROR (FOLLOWING 'JSR TSTRWS')
6369 021476 000207 RTS PC ;EXIT
6370
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6375 .SBTTL DRESET: DRIVE RESET ROUTINE
6376
6377 ;DRESET
6378 ;THIS ROUTINE DOES A DRIVE RESET ON THE DRIVE WHOOSE ADDRESS IS IN
6379 ;RKDA. MULTIPLE RETURN ADDRESSES FOR THIS ROUTINE ARE PROVIDED.
6380 ;IF THERE IS NO ERROR (R/W/S RDY SETS WITHIN CERTAIN TIME), THEN BEFORE
6381 ;EXITNG FROM THIS ROUTINE THE RETURN ADDRESS IS INCREMENTED BY 2, TO SKIP
6382 ;THE ERROR MESSAGE ON RETURN. IF THERE IS AN ERROR, THE 3 REGISTERS (CS, ER, DS)
6383 ;ARE STORED AND THEN A NORMAL EXIT IS MADE FROM THIS ROUTINE TO THE
6384 ;ERROR MESSAGE FOLLOWING THE CALL FOR THIS ROUTINE.
6385 ;CALL: JSR PC, DRESET
6386
6387
6388 021500 005037 001364 DRESET: CLR COUNT1 ;INITIALIZE THE COUNT
6389 021504 013777 001350 157626 MOV DRIVAD, @RKDA ;ADRES THE DRIVE
6390 021512 012777 000015 157612 MOV #15, @RKCS ;DRIVE RESET, GO
6391 021520 104414 CNT.RDY ;THIS IS A CALL FOR 'CN.RDY'
6392 ;ROUTINE WHICH WAITS FOR CNT
6393 ;RDY TO SET. IF CNTRL RDY DOES
6394 ;NOT SET WITHIN 883 MS/ 11-20
6395 ; (176 MS FOR 11-45 WITH BIPOLAR)
6396 ;AN ERROR IS REPORTED
6397 021522 032777 000100 157576 1$: BIT #100, @RKDS ;DID R/W/S RDY SET?
6398 021530 001013 BNE 2$
6399 021532 012746 177770 MOV #-10, -(SP) ;PUSH COUNT ON SP
6400 021536 005216 INC (SP) ;COUNT IT DOWN
6401 021540 001376 BNE .-2
6402 021542 005726 TST (SP)+ ;POP UP $P
6403 021544 005237 001364 INC COUNT1 ;IF NOT WAIT
6404 021550 001364 BNE 1$ ;WAITED LONG?
6405 021552 004737 020770 JSR PC, GT4RG
    
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6406 021556 000402
6407 021560 062716 000002
6408 021564 000207
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BR 2\$+4
2\$: ADD #2,DR6
RTS PC

.SBTTL TSTSIN: CHECK 'SIN' ROUTINE

:TSTSIN
:THIS ROUTINE CHECKS IF 'SIN' IS SET. IF IT IS SET A
:DRIVE RESET IS DONE TO CLEAR 'SIN' AND INITIALIZE POSITIONER.
:CALL: TST.SIN
:IF ON DOING DRIVE RESET R/W/S RDY DOES NOT SET A MESSAGE
: ERROR PC=XXXXXX IS GIVEN.
:XXXXXX=PC IN THE MAIN PROGRAM WHERE 'TST.SIN' CALL IS LOCATED.

6423 021566 013777 001350 157544
6424 021574 032777 001000 157524
6425 021602 001403
6426 021604 004737 021500
6427 021610 000401
6428 021612 000002
6429 021614 032777 020000 157316
6430 021622 001373
6431 021624 104401 021632
6432 021630 000406
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6434 021646
6435 021646 011646
6436 021650 062716 177776
6437 021654 104402
6438 021656 000755
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TSTSIN: MOV DRIVAD,DRKDA ;ADRES THE DRIVE
BIT #1000,DRKDS ;IS SIN SET?
BEQ 1\$
JSR PC,DRESET ;GO DO DRIVE RESET, SIN SET
BR 2\$;REPORT ERROR
1\$: RTI
2\$: BIT #SW13,DSWR ;INHIBIT TYPEOUT?
BNE 1\$;IF YES, SKIP TYPEOUT
TYPE 65\$;TYPE ASCIZ STRING
BR 64\$;GET OVER THE ASCIZ
65\$: .ASCIZ /ERROR PC= /
64\$: MOV (SP),-(SP)
ADD #-2,(SP) ;GET THE PC WHERE 'TST.SIN' IS LOCATED
TYPOC ;GO TYPE OUT PC
BR 1\$

.SBTTL DELAY: TIME DELAY ROUTINE

:DELAY
:THIS ROUTINE PROVIDES A VARIABLE TIME DELAY. THE CALL FOR THIS
:ROUTINE IS AN ENCODED 'TRAP' INSTRUCTION.
:CALL: DELAY ,N N IS ANY OCTAL NO. FROM 1 TO 177777
:THE DELAY PROVIDED IS 7.5N US (CONVERT N TO DECIMAL) FOR 11/20
:1.5N US FOR 11/45
:IF THE USER WANTS TO CHANGE THE DELAY TIME (EXMP: SHORTER DELAY TO
:GET A TIGHTER SCOPE LOOP) THE VARIABLE 'N' FOLLOWING 'DELAY' SHOULD
:BE CHANGED TO SUIT THE INDIVIDUAL NEED.

6454 021660 017637 000000 001366
6455 021666 062716 000002
6456
6457 021672 005337 001366
6458 021676 001375
6459
6460 021700 000002
6461

DELAY: MOV 2(SP),TIMER ;GET 'AMOUNT' (N) FOR WHICH
ADD #2,(SP) ;DELAY IS TO BE PROVIDED
1\$: DEC TIMER ;ADJUST STACK POINTER TO SKIP OVER 'N'
BNE 1\$;COUNT DOWN TO 0
RTI ;RETURN TO MAIN PROGRAM

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.SBTTL WAT.INT: WAIT FOR INTERRUPT ROUTINE

;WAT.INT
 ;THIS ROUTINE PROVIDES A VARIABLE TIME WAIT LOOP DURING WHICH AN INTERRUPT
 ;FROM RK11 CAN OCCUR. THE CALL IS AN ENCODED 'TRAP' INSTRUCTION.

;CALL: WAT.INT ,N N IS ANY OCTAL NO. FROM 1 TO 177777

;WAIT LOOP TIME= APPROX. 7.5N US (CONVERT N TO DECIMAL) FOR 11/20
 ;APPROX. 1.5N US FOR 11/45
 ;UPON ENTERING THE ROUTINE THE CPU PRIORITY IS DROPPED SO THAT
 ;RK11 CAN INTERRUPT. NOTE THAT WHEN RK11 INTERRUPTS THIS ROUTINE
 ;IS EXITED WITHOUT POPPING THE STACK, THIS POPPING IS DONE AFTER GETTING
 ;TO RK11 INTERRUPT HANDLER.
 ;IF FOR ANY REASON THE WAIT LOOP TIME HAS TO BE CHANGED IT CAN BE DONE
 ;BY SIMPLY CHANGING THE VARIABLE 'N' FOLLOWING THE 'WAT.INT'.

021702	017637	000000	001366	WATINT: MOV	2(SP),TIMER	;GET 'AMOUNT' (N) FOR WHICH
021710	062716	000002		ADD	#2,(SP)	;WAITING IS TO BE DONE
021714	013746	001400		MOV	RKPRI, -(SP)	;ADJUST STACK POINTER FOR CORRECT RETURN
021720	012746	021726		MOV	#1\$, -(SP)	;DROP CPU PRIORITY SO THAT RK11 CAN
021724	000002			RTI		; INTERRUPT
021726	005337	001366	1\$:	DEC	TIMER	;WAIT FOR RK11 TO INTERRUPT
021732	001375			BNE	1\$	
021734	000002			RTI		;IF INTERRUPT HAS NOT OCCURED BY NOW
						;RETURN AND REPORT ERROR
						;EXIT

;WATIME

021736	005000		WATIME: CLR	RO
021740	005001		CLR	R1
021742	005200	1\$:	INC	RO
021744	001376		BNE	1\$
021746	105201		INCB	R1
021750	001374		BNE	1\$
021752	000207		RTS	PC

.SBTTL CHKCRDY: CHECK CONTROL READY

;:CH.CRDY
 ;THIS ROUTINE WAITS FOR THE CONTROL READY TO SET. IF THE CONTROL READY BIT
 ;DOES NOT SET WITHIN A CERTAIN TIME, THEN THE CONTENTS OF RKCS, RKER, RKDS
 ;AND RKDA ARE SAVED AND AN EXIT MADE TO THE ERROR MESSAGE FOLLOWING THE
 ;'JSR' CALL FOR THIS ROUTINE.
 ;IF CONTROL READY SETS THEN THE RETURN ADDRESS IS ADJUSTED TO SKIP THE
 ;ERROR MESSAGE ON RETURN.
 ;CALL: CHKCRDY
 ; ERROR ;RETURN HERE IF ERROR


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6520 021754 005037 001366
6521 021760 105777 157346
6522 021764 100406
6523 021766 005237 001366
6524 021772 001372
6525 021774 004737 020770
6526 022000 000002
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6528 022002 062716 000002
6529 022006 000002
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6557 022010 012777 000001 157314
6558 022016 012737 177500 001170
6559 022024 000402
6560 022026 005037 001170
6561 022032 105777 157274
6562 022036 100435
6563 022040 005237 001170
6564 022044 001372
6565 022046 032777 020000 157064
6566 022054 001026
6567 022056 104401
6568 022060 001245
6569 022062 104401 022070
6570 022066 000403
6571
6572 022076
6573 022076 011646

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```

; --- ;RETURN HERE IF NO ERROR
CH.CRDY: CLR TIMER
1$: TSTB @RKCS ;CNTRL RDY SET?
BMI 2$ ;YES
INC TIMER
BNE 1$ ;NO, WAIT
JSR PC,GT4RG ;SAVE RKCS, ER, DS, DA
RTI

2$: ADD #2,(SP) ;ADJUST RETURN ADDRESS TO
RTI ;SKIP ERROR MESSAGE ON RETURN

.SBTTL CON.RESET: CONTROL REST ROUTINE

;CON.RESET
;THIS ROUTINE ISSUES A CONTROL RESET AND WAITS FOR
;THE 'CNTRL RDY' FLAG TO SET. WHEN THE FLAG SETS
;AN EXIT IS MADE OUT OF THE ROUTINE. IF 'CNTRL-RDY'
;DOES NOT SET WITHIN A CERTAIN TIME AN ERROR MESSAGE
; CNT RDY DIDN'T SET
; PC=XXXXXX RKCS=YYYYYY
; IS GIVEN. NOTE THAT XXXXXX IS THE PC WHERE 'CNT.RESET' OR 'CNT.RDY'
; IS CALLED.

;CALL: CNT.RESET

.SBTTL CNT.RDY: WAIT FOR CONTROL READY ROUTINE

;CN.RDY
;THIS ROUTINE WAITS FOR THE CONTROL READY BIT TO SET AND WHEN IT
;SETS EXITS OUT. IF WITHIN A CERTAIN TIME CNTRL RDY DOES
;NOT SET AN ERROR IS REPORTED. WAITING TIME IS 983 MS FOR 11/20
;175 MS FOR 11/45 WITH BIPOLAR MEMORY.
;CALL: CNT.RDY
CN.RST: MOV #1,@RKCS ;ISSUE A CONTROL RESET
MOV #-300,$REG3 ;SET UP COUNT
BR CN.RDY+4 ;SKIP OVER CN.RDY

CN.RDY: CLR $REG3
1$: TSTB @RKCS ;DID CNTRL-RDY SET?
BMI 3$ ;YES, EXIT
INC $REG3 ;WAITED LONG?
BNE 1$ ;IF NOT, GO BAK & WAIT
2$: BIT #SW13,@SWR ;INHIBIT TYPEOUT?
BNE 3$ ;IF YES, SKIP TYPEOUT

3$: TYPE MSG3
TYPE 65$ ;:TYPE ASCIZ STRING
BR 64$ ;:GET OVER THE ASCIZ

65$: .ASCIZ <15><12>/PC=/
64$: MOV (SP),-(SP)

```

```

6574 022100 162716 000002      SUB      #2, (SP)
6575 022104 104402              TYPOC              ;GO TYPE PC IN THE MAIN PROGRAM,
6576                                ; WHERE ERROR OCCURRED
6577 022106 104401 022114      TYPE      67$      ;: TYPE ASCIZ STRING
6578 022112 000404              BR      66$        ;: GET OVER THE ASCIZ
6579                                ;: 67$: .ASCIZ / RKCS=/
6580 022124                                66$:
6581 022124 017746 157202      MOV      @RKCS, -(SP) ;GET RKCS
6582 022130 104402              TYPOC              ;GO TYPE IT
6583
6584 022132 000002      3$:      RTI              ;RETURN FROM THIS
6585                                ;ROUTINE TO THE MAIN
6586                                ;PROGRAM
6587
6588
6589                                ;THIS PART OF THE PROGRAM CONTAINS THE COMMON ROUTINES CALLED
6590                                ;FROM THE SYSMAC.SML PACKAGE
6591                                ;
6592                                ;
6593                                ;.SBTTL SCOPE HANDLER ROUTINE
6594
6595                                ;:*****
6596                                ;:THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
6597                                ;:AND LOAD THE TEST NUMBER($STNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
6598                                ;:AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
6599                                ;:THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
6600                                ;:SW14=1      LOOP ON TEST
6601                                ;:SW11=1      INHIBIT ITERATIONS
6602                                ;:SW09=1      LOOP ON ERROR
6603                                ;:SW08=1      LOOP ON TEST IN SWR<7:0>
6604                                ;:CALL
6605                                ;:*      SCOPE              ;:SCOPE=IOT
6606
6607                                ;SCOPE:
6608 022134 104407              CKSWR
6609 022136 032777 040000 156774 1$:      BIT      #BIT14, @SWR      ;:TEST FOR CHANGE IN SOFT-SWR
6610 022144 001111              BNE      $OVER          ;:LOOP ON PRESENT TEST?
6611                                ;:YES IF SW14=1
6612 022146 000416              ;:*****START OF CODE FOR THE XOR TESTER*****
6613                                ;:XTSTR: BR      65$      ;: IF RUNNING ON THE "XOR" TESTER CHANGE
6614                                ;:THIS INSTRUCTION TO A "NOP" (NOP=240)
6615 022150 013746 000004              MOV      @ERRVEC, -(SP) ;:SAVE THE CONTENTS OF THE ERROR VECTOR
6616 022154 012737 022174 000004      MOV      #5$, @ERRVEC ;:SET FOR TIMEOUT
6617 022162 005737 177060              TST      @177060      ;:TIME OUT ON XOR?
6618 022166 012637 000004              MOV      (SP)+, @ERRVEC ;:RESTORE THE ERROR VECTOR
6619 022172 000463              BR      $$VLAD        ;:GO TO THE NEXT TEST
6620 022174 022626              5$:      CMP      (SP)+, (SP)+ ;:CLEAR THE STACK AFTER A TIME OUT
6621 022202 000423              MOV      (SP)+, @ERRVEC ;:RESTORE THE ERROR VECTOR
6622 022204              BR      7$          ;:LOOP ON THE PRESENT TEST
6623 022204 032777 000400 156726 6$: ; *****END OF CODE FOR THE XOR TESTER*****
6624 022212 001404              BIT      #BIT08, @SWR ;:LOOP ON SPEC. TEST?
6625 022214 127737 156720 001102      BEQ      2$          ;:BR IF NO
6626 022222 001462              CMPB    @SWR, $STNM   ;:ON THE RIGHT TEST? SWR<7:0>
6627 022224 105737 001103              BEQ      $OVER      ;:BR IF YES
6628 022230 001421              TSTB   $ERFLG       ;:HAS AN ERROR OCCURRED?
6629 022232 123737 001115 001103      BEQ      3$          ;:BR IF NO
6629                                ;:MAX. ERRORS FOR THIS TEST OCCURRED?

```



```
6686 022500 004737 022730 JSR PC, @SERRTYP ;GO TO USER ERROR ROUTINE
6687 022504 104401 001213 TYPE $CRLF
6688 022510 023737 000042 000046 2$: CMP @#42, @#46 ;ARE WE IN ACT11 AUTO MODE?
6689 022516 001403 BEQ .+10 ;YES, HALT ON ERROR
6690 022520 005777 156414 TST @SWR ;HALT ON ERROR?
6691 022524 100002 BPL 3$ ;SKIP IF CONTINUE
6692 022526 000000 HALT ;HALT ON ERROR!
6693 022530 104407 CKSWR ;CHECK FOR SOFTWARE SWITCH REGIATER REQUEST
6694 022532 032777 010000 156400 3$: BIT #SW12, @SWR ;SW 12 SET?
6695 022540 001402 BEQ .+6 ;NO, BRANCH
6696 022542 013716 001106 MOV $LPADR, (SP) ;ADJUST RETURN ADRES FOR SW12
6697 022546 032777 001000 156364 BIT #SW09, @SWR ;LOOP ON ERROR SWITCH SET?
6698 022554 001402 BEQ 4$ ;BR IF NO
6699 022556 013716 001110 MOV $LPERR, (SP) ;FUDGE RETURN FOR LOOPING
6700 022562 005737 001210 4$: TST $ESCAPE ;CHECK FOR AN ESCAPE ADDRESS
6701 022566 001402 BEQ 5$ ;BR IF NONE
6702 022570 013716 001210 MOV $ESCAPE, (SP) ;FUDGE RETURN ADDRESS FOR ESCAPE
6703 022574 000002 5$: RTI ;RETURN
6704
6705 022576 005737 001434 8$: TST T56FLG ;IF EROR WAS IN LAST TEST (POLL)
6706 ;DROP ALL THE DRIVES
6707 022602 001407 BEQ 10$
6708 022604 104401 001303 TYPE MSG5
6709 022610 005037 001412 CLR DRIVS
6710 022614 022626 CMP (SP)+, (SP)+
6711 022616 000137 020646 JMP $EOP
6712 022622 013746 001354 10$: MOV DRVPT, -(SP) ;DROP THE DRIVE FROM THE
6713 022626 162716 000002 SUB #2, (SP) ;SELECTION LIST
6714 022632 013746 001350 MOV DRIVAD, -(SP) ;DRIVE ADDR TO STACK
6715 022636 004737 021174 JSR PC, SHFRT ;RIGHT JUSTIFY
6716 022642 042716 000001 BIC #1, (R6) ;MAKE EVEN
6717 022646 062716 001414 ADD #DRIVO, (SP) ;POINTS TO TABLE FOR EVEN DRIVE
6718 022652 042776 100000 000000 BIC #BIT15, @ (R6) ;TEST REMAINING DRIVE AS RK05E
6719 022660 062716 000002 ADD #2, (R6) ;POINT TO ODD
6720 022664 042736 100000 BIC #BIT15, @ (SP)+ ;TEST AS RK-05E
6721 022670 012736 010000 MOV #BIT12, @ (SP)+ ;INDICATE THIS DRIVE DROPPED
6722 022674 104401 001272 TYPE MSG4
6723 022700 013746 001350 MOV DRIVAD, -(R6) ;PUSH DRIVE # ON STACK
6724 022704 004737 021174 JSR PC, SHFRT ;SHIFT IT BEFORE TYPING
6725 022710 104402 TYPOC ;TYPE OUT DRIVE #
6726 022712 104401 001315 TYPE MSG6
6727 022716 005337 001412 DEC DRIVS ;DECREMENT # OF DRIVES PRESNT
6728 022722 022626 9$: CMP (SP)+, (SP)+ ;RESTORE STACK
6729 022724 000137 020022 JMP BTEOP ;GO BACK TO THE END OF PROGRAM
;LINKAGE.
```

.SBTTL ERROR MESSAGE TYPEOUT ROUTINE

```
*****
;THIS ROUTINE USES THE "ITEM CONTROL BYTE" ($ITEMB) TO DETERMINE WHICH
;ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" ($ERRTB),
;AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.
```

```
$ERRTYP:
6739 022730 TYPE $CRLF ;;"CARRIAGE RETURN" & "LINE FEED"
6740 022730 104401 001213 MOV RD, -(SP) ;;SAVE RD
6741 022734 010046
```

```

6742 022736 005000 CLR RO ;;PICKUP THE ITEM INDEX
6743 022740 153700 001114 BISB 2(S)ITEMB,RO
6744 022744 001004 BNE 1S ;; IF ITEM NUMBER IS ZERO, JUST
6745 022746 013746 001116 MOV $ERRPC,-(SP) ;;TYPE THE PC OF THE ERROR
6746 022752 104402 TYPOC ;;SAVE $ERRPC FOR TYPEOUT
6747 022754 000426 BR 6S ;;ERROR ADDRESS
6748 022756 005300 1S: DEC RO ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
6749 022760 006300 ASL RO ;;GET OUT
6750 022762 006300 ASL RO ;;ADJUST THE INDEX SO THAT IT WILL
6751 022764 006300 ASL RO ;; WORK FOR THE ERROR TABLE
6752 022766 062700 001442 ADD #SERRTB,RO ;;FORM TABLE POINTER
6753 022772 012037 023002 MOV (RO)+,2S ;;PICKUP "ERROR MESSAGE" POINTER
6754 023000 104401 BEQ 3S ;;SKIP TYPEOUT IF NO POINTER
6755 023002 000000 2S: TYPE 0 ;;TYPE THE "ERROR MESSAGE"
6756 023004 104401 001213 TYPE $SCRLF ;;"ERROR MESSAGE" POINTER GOES HERE
6757 023010 012037 023020 3S: MOV (RO)+,4S ;;"CARRIAGE RETURN" & "LINE FEED"
6758 023014 001404 BEQ 5S ;;PICKUP "DATA HEADER" POINTER
6759 023016 104401 TYPE 0 ;;SKIP TYPEOUT IF 0
6760 023020 000000 4S: .WORD 0 ;;TYPE THE "DATA HEADER"
6761 023022 104401 001213 TYPE $SCRLF ;;"DATA HEADER" POINTER GOES HERE
6762 023026 011000 5S: MOV (RO),RO ;;"CARRIAGE RETURN" & "LINE FEED"
6763 023030 001004 BNE 7S ;;PICKUP "DATA TABLE" POINTER
6764 023032 012600 6S: MOV (SP)+,RO ;;GO TYPE THE DATA
6765 023034 104401 001213 TYPE $SCRLF ;;RESTORE RO
6766 023040 000207 RTS PC ;;"CARRIAGE RETURN" & "LINE FEED"
6767 023042 013046 7S: MOV 2(RO)+,-(SP) ;;RETURN
6768 023044 104402 TYPOC ;;SAVE 2(RO)+ FOR TYPEOUT
6769 023046 005710 TST (RO) ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
6770 023050 001770 BEQ 6S ;;IS THERE ANOTHER NUMBER?
6771 023052 104401 023060 8S: TYPE 8S ;;BR IF NO
6772 023056 000771 BR 7S ;;TYPE TWO(2) SPACES
6773 023060 020040 000 .ASCIZ / / ;;LOOP
6774 023064 .EVEN ;;TWO(2) SPACES

.SBTTL TYPE ROUTINE

*****
*ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
*NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
*NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
*NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
*
*CALL:
*1) USING A TRAP INSTRUCTION
* TYPE ,MESADR ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
*OR
* TYPE
* MESADR
*
6797 023064 105737 001157 $TYPE: TSTB $TPFLG ;;IS THERE A TERMINAL?

```

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6798 023070 100002      BPL      1$          ;; BR IF YES
6799 023072 000000      HALT                    ;; HALT HERE IF NO TERMINAL
6800 023074 000407      BR      3$          ;; LEAVE
6801 023076 010046      1$:     MOV      RO,-(SP)  ;; SAVE RO
6802 023100 017600 000002  MOV      2(SP),RO  ;; GET ADDRESS OF ASCIZ STRING
6803 023104 112046      2$:     MOVVB   (RO)+,-(SP)  ;; PUSH CHARACTER TO BE TYPED ONTO STACK
6804 023106 001005      BNE     4$          ;; BR IF IT ISN'T THE TERMINATOR
6805 023110 005726      TST    (SP)+      ;; IF TERMINATOR POP IT OFF THE STACK
6806 023112 012600      60$:    MOV      (SP)+,RO  ;; RESTORE RO
6807 023114 062716 000002  3$:     ADD      #2,(SP)  ;; ADJUST RETURN PC
6808 023120 000002      RTI                    ;; RETURN
6809 023122 122716 000011  4$:     CMPB   #HT,(SP)  ;; BRANCH IF <HT>
6810 023126 001430      BEQ    8$          ;; BRANCH IF NOT <CRLF>
6811 023130 122716 000200  CMPB   #CRLF,(SP)
6812 023134 001006      BNE    5$          ;; POP <CR><LF> EQUIV
6813 023136 005726      TST    (SP)+      ;; TYPE A CR AND LF
6814 023140 104401      TYPE
6815 023142 001213      $CRLF
6816 023144 105037 023300  CLRB   $CHARCNT  ;; CLEAR CHARACTER COUNT
6817 023150 000755      BR      2$          ;; GET NEXT CHARACTER
6818 023152 004737 023234  5$:     JSR    PC,$TYPEC  ;; GO TYPE THIS CHARACTER
6819 023156 123726 001156  6$:     CMPB   $FILLC,(SP)+  ;; IS IT TIME FOR FILLER CHARS.?
6820 023162 001350      BNE    2$          ;; IF NO GO GET NEXT CHAR.
6821 023164 013746 001154  MOV     $NULL,-(SP)  ;; GET # OF FILLER CHARS. NEEDED
6822                                ;; AND THE NULL CHAR.
6823 023170 105366 000001  7$:     DECB   1(SP)  ;; DOES A NULL NEED TO BE TYPED?
6824 023174 002770      BLT    6$          ;; BR IF NO--GO POP THE NULL OFF OF STACK
6825 023176 004737 023234  JSR    PC,$TYPEC  ;; GO TYPE A NULL
6826 023202 105337 023300  DECB   $CHARCNT  ;; DO NOT COUNT AS A COUNT
6827 023206 000770      BR      7$          ;; LOOP

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;HORIZONTAL TAB PROCESSOR

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6829
6830
6831 023210 112716 000040  8$:     MOVVB   #' (SP)  ;; REPLACE TAB WITH SPACE
6832 023214 004737 023234  9$:     JSR    PC,$TYPEC  ;; TYPE A SPACE
6833 023220 132737 000007 023300  BITB   #7,$CHARCNT  ;; BRANCH IF NOT AT
6834 023226 001372      BNE    9$          ;; TAB STOP
6835 023230 005726      TST    (SP)+      ;; POP SPACE OFF STACK
6836 023232 000724      BR      2$          ;; GET NEXT CHARACTER
6837 023234 105777 155710  $TYPEC: TST    2$TSPS  ;; WAIT UNTIL PRINTER IS READY
6838 023240 100375      BPL    $TYPEC
6839 023242 116677 000002 155702  MOVVB   2(SP),2$TPB  ;; LOAD CHAR TO BE TYPED INTO DATA REG.
6840 023250 122766 000015 000002  CMPB   #CR,2(SP)  ;; IS CHARACTER A CARRIAGE RETURN?
6841 023256 001003      BNE    1$          ;; BRANCH IF NO
6842 023260 105037 023300  CLRB   $CHARCNT  ;; YES--CLEAR CHARACTER COUNT
6843 023264 000406      BR      $TYPEX
6844 023266 122766 000012 000002  1$:     CMPB   #LF,2(SP)  ;; IS CHARACTER A LINE FEED?
6845 023274 001402      BEQ    $TYPEX  ;; BRANCH IF YES
6846 023276 105227      INCB   (PC)+      ;; COUNT THE CHARACTER
6847 023300 000000  $CHARCNT: .WORD 0  ;; CHARACTER COUNT STORAGE
6848 023302 000207  $TYPEX: RTS    PC
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.SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE

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6863 023304
6864 023304 010046
6865 023306 010146
6866 023310 010246
6867 023312 010346
6868 023314 010546
6869 023316 012746 020200
6870 023322 016605 000020
6871 023326 100004
6872 023330 005405
6873 023332 112766 000055 000001
6874 023340 005000 1$:
6875 023342 012703 023520
6876 023346 112723 000040
6877 023352 005002 2$:
6878 023354 016001 023510
6879 023360 160105 3$:
6880 023362 002402
6881 023364 005202
6882 023366 000774
6883 023370 060105 4$:
6884 023372 005702
6885 023374 001002
6886 023376 105716
6887 023400 100407
6888 023402 106316 5$:
6889 023404 103003
6890 023406 116663 000001 177777
6891 023414 052702 000060 6$:
6892 023420 052702 000040 7$:
6893 023424 110223
6894 023426 005720
6895 023430 020027 000010
6896 023434 002746
6897 023436 003002
6898 023440 010502
6899 023442 000764
6900 023444 105726 8$:
6901 023446 100003
6902 023450 116663 177777 177776
6903 023456 105013 9$:
6904 023460 012605
6905 023462 012603
6906 023464 012602
6907 023466 012601
6908 023470 012600
6909 023472 104401 023520

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: *THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
: *SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
: *NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
: *BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
: *REPLACED WITH SPACES.
: *CALL:
: *   MOV    NUM,-(SP)      ;; PUT THE BINARY NUMBER ON THE STACK
: *   TYPDS                ;; GO TO THE ROUTINE
$TYPDS:
MOV    R0,-(SP)           ;; PUSH R0 ON STACK
MOV    R1,-(SP)           ;; PUSH R1 ON STACK
MOV    R2,-(SP)           ;; PUSH R2 ON STACK
MOV    R3,-(SP)           ;; PUSH R3 ON STACK
MOV    R5,-(SP)           ;; PUSH R5 ON STACK
MOV    #20200,-(SP)      ;; SET BLANK SWITCH AND SIGN
MOV    20(SP),R5         ;; GET THE INPUT NUMBER
BPL    1$                 ;; BR IF INPUT IS POS.
NEG    R5                 ;; MAKE THE BINARY NUMBER POS.
MOVB   #'-,1(SP)         ;; MAKE THE ASCII NUMBER NEG.
CLR    R0                 ;; ZERO THE CONSTANTS INDEX
MOV    #SDBLK,R3         ;; SETUP THE OUTPUT POINTER
MOVB   #' ,(R3)+         ;; SET THE FIRST CHARACTER TO A BLANK
CLR    R2                 ;; CLEAR THE BCD NUMBER
MOV    $DTBL(R0),R1      ;; GET THE CONSTANT
SUB    R1,R5              ;; FORM THIS BCD DIGIT
BLT    4$                 ;; BR IF DONE
INC    R2                 ;; INCREASE THE BCD DIGIT BY 1
BR     3$
4$:   ADD    R1,R5         ;; ADD BACK THE CONSTANT
TST    R2                 ;; CHECK IF BCD DIGIT=0
BNE    5$                 ;; FALL THROUGH IF 0
TSTB   (SP)              ;; STILL DOING LEADING 0'S?
BMI    7$                 ;; BR IF YES
5$:   ASLB   (SP)          ;; MSD?
BCC    6$                 ;; BR IF NO
MOVB   1(SP),-1(R3)      ;; YES--SET THE SIGN
BIS    #'0,R2            ;; MAKE THE BCD DIGIT ASCII
BIS    #' ,R2            ;; MAKE IT A SPACE IF NOT ALREADY A DIGIT
MOVB   R2,(R3)+         ;; PUT THIS CHARACTER IN THE OUTPUT BUFFER
TST    (R0)+             ;; JUST INCREMENTING
CMP    R0,#10           ;; CHECK THE TABLE INDEX
BLT    2$                 ;; GO DO THE NEXT DIGIT
BGT    8$                 ;; GO TO EXIT
MOV    R5,R2             ;; GET THE LSD
BR     6$                 ;; GO CHANGE TO ASCII
6$:   TSTB   (SP)+         ;; WAS THE LSD THE FIRST NON-ZERO?
BPL    9$                 ;; BR IF NO
MOVB   -1(SP),-2(R3)    ;; YES--SET THE SIGN FOR TYPING
CLRB   (R3)              ;; SET THE TERMINATOR
MOV    (SP)+,R5          ;; POP STACK INTO R5
MOV    (SP)+,R3          ;; POP STACK INTO R3
MOV    (SP)+,R2          ;; POP STACK INTO R2
MOV    (SP)+,R1          ;; POP STACK INTO R1
MOV    (SP)+,R0          ;; POP STACK INTO R0
TYPE   ,SDBLK           ;; NOW TYPE THE NUMBER

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6966 023644 006105          ROL      R5
6967 023646 010503          MOV      R5,R3
6968 023650 006103          3$:    ROL      R3          ;; GET LSB OF THIS DIGIT
6969 023652 105337 023754    DECB     $OMODE          ;; TYPE THIS DIGIT?
6970 023656 100016          BPL      7$              ;; BR IF NO
6971 023660 042703 177770    BIC      #177770,R3      ;; GET RID OF JUNK
6972 023664 001002          BNE      4$              ;; TEST FOR 0
6973 023666 005704          TST      R4              ;; SUPPRESS THIS 0?
6974 023670 001403          BEQ      5$              ;; BR IF YES
6975 023672 005204          4$:    INC      R4          ;; DON'T SUPPRESS ANYMORE 0'S
6976 023674 052703 000060    BIS      #'0,R3          ;; MAKE THIS DIGIT ASCII
6977 023700 052703 000040    5$:    BIS      #' ,R3      ;; MAKE ASCII IF NOT ALREADY
6978 023704 110337 023750    MOVB     R3,8$           ;; SAVE FOR TYPING
6979 023710 104401 023750    TYPE     8$              ;; GO TYPE THIS DIGIT
6980 023714 105337 023752    7$:    DECB     $OCNT      ;; COUNT BY 1
6981 023720 003347          BGT      2$              ;; BR IF MORE TO DO
6982 023722 002402          BLT      6$              ;; BR IF DONE
6983 023724 005204          INC      R4              ;; INSURE LAST DIGIT ISN'T A BLANK
6984 023726 000744          BR       2$              ;; GO DO THE LAST DIGIT
6985 023730 012605          6$:    MOV      (SP)+,R5      ;; RESTORE R5
6986 023732 012604          MOV      (SP)+,R4      ;; RESTORE R4
6987 023734 012603          MOV      (SP)+,R3      ;; RESTORE R3
6988 023736 016666 000002 000004    MOV      2(SP),4(SP)    ;; SET THE STACK FOR RETURNING
6989 023744 012616          MOV      (SP)+,(SP)
6990 023746 000002          RTI
6991 023750          8$:    .BYTE    0          ;; RETURN
6992 023751          .BYTE    0          ;; STORAGE FOR ASCII DIGIT
6993 023752          .BYTE    0          ;; TERMINATOR FOR TYPE ROUTINE
6994 023753          .BYTE    0          ;; OCTAL DIGIT COUNTER
6995 023754 000000    $OCNT:  .BYTE    0          ;; ZERO FILL SWITCH
6996          $OFILL: .BYTE    0          ;; NUMBER OF DIGITS TO TYPE
6997          $OMODE: .WORD   0
6998          .SBTTL  TTY INPUT ROUTINE
6999          ;;*****
7000          .ENABL  LSB
7001          ;;*****
7002          ;;*SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
7003          ;;*ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
7004          ;;*SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP CALL
7005          ;;*WHEN OPERATING IN TTY FLAG MODE.
7006          $CKSWR: CMP      #SWREG,SWR          ;; IS THE SOFT-SWR SELECTED?
7007 023756 022737 000176 001140    BNE      15$           ;; BRANCH IF NO
7008 023764 001074          TSTB     @TKS          ;; CHAR THERE?
7009 023766 105777 155152    BPL      15$           ;; IF NO, DON'T WAIT AROUND
7010 023772 100071          MOVB     @TKB,-(SP)     ;; SAVE THE CHAR
7011 023774 117746 155146    BIC      #177,(SP)      ;; STRIP-OFF THE ASCII
7012 024000 042716 177600    CMP      #7,(SP)+       ;; IS IT A CONTROL G?
7013 024004 022726 000007    BNE      15$           ;; NO, RETURN TO USER
7014 024010 001062          CMPB     $AUTOB,#1      ;; ARE WE RUNNING IN AUTO-MODE?
7015 024012 123727 001134 000001    BEQ      15$           ;; BRANCH IF YES
7016 024020 001456          15$:
7017          $CNTLG
7018 024022 104401 024643    $GTSWR: TYPE     , $CNTLG          ;; ECHO THE CONTROL-G (↑G)
7019 024026 104401 024650    TYPE     $MSWR          ;; TYPE CURRENT CONTENTS
7020 024032 013746 000176    MOV      SWREG,-(SP)    ;; SAVE SWREG FOR TYPEOUT
7021 024036 104402          TYPOC          ;; GO TYPE--OCTAL ASCII(ALL DIGITS)

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7246 025052 013706 025142      MOV    $$SAVR6,SP      ;; GET SP
7247 025056 005037 025142      CLR    $$SAVR6        ;; WAIT LOOP FOR THE TTY
7248 025062 005237 025142      1$:   INC    $$SAVR6    ;; WAIT FOR THE INC
7249 025066 001375          BNE    1$             ;; OF WORD
7250 025070 012677 154044      MOV    (SP)+, @JSWR   ;; POP STACK INTO @JSWR
7251 025074 012605          MOV    (SP)+, R5      ;; POP STACK INTO R5
7252 025076 012604          MOV    (SP)+, R4      ;; POP STACK INTO R4
7253 025100 012603          MOV    (SP)+, R3      ;; POP STACK INTO R3
7254 025102 012602          MOV    (SP)+, R2      ;; POP STACK INTO R2
7255 025104 012601          MOV    (SP)+, R1      ;; POP STACK INTO R1
7256 025106 012600          MOV    (SP)+, R0      ;; POP STACK INTO R0
7257 025110 012737 024772 000024  MOV    #SPWRDN, @PWRVEC ;; SET UP THE POWER DOWN VECTOR
7258 025116 012737 000340 000026  MOV    #340, @PWRVEC+2 ;; PRIO:7
7259 025124 104401          TYPE                                ;; REPORT THE POWER FAILURE
7260 025126 025144          $PWRMG: .WORD $POWER      ;; POWER FAIL MESSAGE POINTER
7261 025130 012716          MOV    (PC)+, (SP)    ;; RESTART AT PFSTR
7262 025132 004702          $PWRAD: .WORD PFSTR     ;; RESTART ADDRESS
7263 025134 000002          RTI
7264 025136 000000          $ILLUP: HALT          ;; THE POWER UP SEQUENCE WAS STARTED
7265 025140 000776          BR    .-2            ;; BEFORE THE POWER DOWN WAS COMPLETE
7266 025142 000000          $$SAVR6: 0           ;; PUT THE SP HERE
7267 025144 005015 047520 042527  $POWER: .ASCIZ <15><12>"POWER"
7268 025152 000122          .EVEN
7270
7271 025154 004737 021500          FCHECK: JSR    PC, DRESET      ; RESETB DRIVE
7272 025160 104026          ERROR 26
7273 025162 104413          CNT.RESET
7274 025164 013737 001350 025276      MOV    DRIVAD, DRHOLD      ; SAVE DRIVE ADDR
7275 025172 032737 020000 001350      BIT    #20000, DRIVAD      ; SEE IF ODD
7276 025200 001404          BEQ    1$
7277 025202 042737 020000 001350      BIC    #20000, DRIVAD      ; MAKE EVEN
7278 025210 000403          BR    2$
7279 025212 052737 020000 001350 1$:   BIS    #20000, DRIVAD      ; MAKE ODD
7280 025220 013777 001350 154112 2$:   MOV    DRIVAD, @RKDA      ; DRIVE ADDR
7281 025226 012777 000011 154076      MOV    #11, @RKCS         ; DRIVE SEEK
7282 025234 104414          CNT.RDY
7283 025236 013777 025276 154074      MOV    DRHOLD, @RKDA      ; OTHER DRIVE
7284 025244 104414          CNT.RDY
7285 025246 032777 000100 154052      BIT    #100, @RKDS        ; HEADS IN MOTION?
7286 025254 001001          BNE    3$
7287 025256 005725          TST   (R5)+              ; NO SO RK-05J
7288 025260 013737 025276 001350 3$:   MOV    DRHOLD, DRIVAD      ; YES RK-05F
7289 025266 004737 021500          JSR    PC, DRESET        ; RESTORE ADDR
7290 025272 104026          ERROR 26                ; WAIT FOR RESET
7291 025274 000205          RTS    R5
7292 025276 000000          DRHOLD: 0
7293 025300 005037 001350          SIZEF: CLR    DRIVAD      ; START AT DRO
7294 025304 012700 001414          MOV    #DRIVO, R0        ; TABLE OF AVAIL DRIVES
7295 025310 005710          4$:   TST   (R0)              ; THIS DRIVE HERE?
7296 025312 001413          BEQ    2$                ; NO
7297 025314 005760 000002          TST   2(R0)              ; COMPLEMENT HERE?
7298 025320 001410          BEQ    2$                ; NO
7299 025322 004537 025154          JSR    R5, FCHECK        ; SEE IF F MODEL
7300 025326 000405          BR    2$                ; J MODEL
7301 025330 052710 100000          BIS    #100000, (R0)     ; SET SIGN FOR F

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7414	026242	053440	051501	042040		
7415	026250	047117	000105			
7416						
7417	026254	027522	027527	020123	EM50:	.ASCIZ "R/W/S RDY DIDN'T CLEAR"
7418	026262	042122	020131	044504		
7419	026270	047104	052047	041440		
7420	026276	042514	051101	000		
7421						
7422	026303	122	053457	051457	EM51:	.ASCIZ "R/W/S RDY DIDN'T SET AFTR SEEK OR DR RESET"
7423	026310	051040	054504	042040		
7424	026316	042111	023516	020124		
7425	026324	042523	020124	043101		
7426	026332	051124	051440	042505		
7427	026340	020113	051117	042040		
7428	026346	020122	042522	042523		
7429	026354	000124				
7430						
7431	026356	045522	040504	041440	EM52:	.ASCIZ /RKDA CHNGD AFTR SEEK/
7432	026364	047110	042107	040440		
7433	026372	052106	020122	042523		
7434	026400	045505	000			
7435						
7436	026403	103	052116	046122	EM53:	.ASCIZ /CNTRL RDY DIDN'T CLR AS GO WAS SET/
7437	026410	051040	054504	042040		
7438	026416	042111	023516	020124		
7439	026424	046103	020122	051501		
7440	026432	043440	020117	040527		
7441	026440	020123	042523	000124		
7442						
7443	026446	047103	051124	020114	EM54:	.ASCIZ "CNTRL RDY DIDN'T SET ON WRT/FMT STARTING FROM <DSK-ADRES>"
7444	026454	042122	020131	044504		
7445	026462	047104	052047	051440		
7446	026470	052105	047440	020116		
7447	026476	051127	027524	046506		
7448	026504	020124	052123	051101		
7449	026512	044524	043516	043040		
7450	026520	047522	020115	042074		
7451	026526	045523	040455	051104		
7452	026534	051505	000076			
7453						
7454	026540	042510	047440	020122	EM55:	.ASCIZ "HE OR ERR ON WRT/FMT STARTING FROM <DSK-ADRES>"
7455	026546	051105	020122	047117		
7456	026554	053440	052122	043057		
7457	026562	052115	051440	040524		
7458	026570	052122	047111	020107		
7459	026576	051106	046517	036040		
7460	026604	051504	026513	042101		
7461	026612	042522	037123	000		
7462						
7463	026617	122	042113	020101	EM56:	.ASCIZ /RKDA INCRMNTD WRONG ON WRT-FMT/
7464	026624	047111	051103	047115		
7465	026632	042124	053440	047522		
7466	026640	043516	047440	020116		
7467	026646	051127	026524	046506		
7468	026654	000124				
7469						

7470	026656	045522	041527	042040	EM57:	.ASCIZ	/RKWC DIDN'T OVRFLO ON WRT FMT/
7471	026664	042111	023516	020124			
7472	026672	053117	043122	047514			
7473	026700	047440	020116	051127			
7474	026706	020124	046506	000124			
7475							
7476	026714	045522	040502	044440	EM60:	.ASCIZ	/RKBA INCRMNTD WRONG ON WRT FMT/
7477	026722	041516	046522	052116			
7478	026730	020104	051127	047117			
7479	026736	020107	047117	053440			
7480	026744	052122	043040	052115			
7481	026752	000					
7482							
7483	026753	122	042513	020122	EM61:	.ASCIZ	/RKER SET,ON WRT OR RD OR FMT/
7484	026760	042523	026124	047117			
7485	026766	053440	052122	047440			
7486	026774	020122	042122	047440			
7487	027002	020122	046506	000124			
7488							
7489	027010	045522	041104	042440	EM62:	.ASCIZ	/RKDB EROR/
7490	027016	047522	000122				
7491							

7492	027022	045522	040504	044440	EM63:	.ASCIZ	/RKDA INCRMNTD WRONG ON RD OR RD FMT/
7493	027030	041516	046522	052116			
7494	027036	020104	051127	047117			
7495	027044	020107	047117	051040			
7496	027052	020104	051117	051040			
7497	027060	020104	046506	000124			
7498							
7499	027066	045522	041527	042040	EM64:	.ASCIZ	/RKWC DIDN'T OVRFLO ON RD OR RD FMT/
7500	027074	042111	023516	020124			
7501	027102	053117	043122	047514			
7502	027110	047440	020116	042122			
7503	027116	047440	020122	042122			
7504	027124	043040	052115	000			
7505							
7506	027131	122	041113	020101	EM65:	.ASCIZ	/RKBA INCRMNTD WRONG ON RD OR RD FMT/
7507	027136	047111	051103	047115			
7508	027144	042124	053440	047522			
7509	027152	043516	047440	020116			
7510	027160	042122	047440	020122			
7511	027166	042122	043040	052115			
7512	027174	000					
7513							
7514	027175	111	041516	051117	EM66:	.ASCIZ	/INCORRECT HEADER FROM 'SECTOR' /
7515	027202	042522	052103	044040			
7516	027210	040505	042504	020122			
7517	027216	051106	046517	023440			
7518	027224	042523	052103	051117			
7519	027232	000047					
7520							
7521	027234	040504	040524	042440	EM67:	.ASCIZ	/DATA ERROR/
7522	027242	051122	051117	000			
7523							
7524	027247	103	052116	046122	EM70:	.ASCIZ	"CNTRL RDY DIDN'T SET ON RD/FMT STARTING FROM <DSK-ADRES>"
7525	027254	051040	054504	042040			
7526	027262	042111	023516	020124			
7527	027270	042523	020124	047117			
7528	027276	051040	027504	046506			
7529	027304	020124	052123	051101			
7530	027312	044524	043516	043040			
7531	027320	047522	020115	042074			
7532	027326	045523	040455	051104			
7533	027334	051505	000076				
7534							
7535	027340	042510	047440	020122	EM71:	.ASCIZ	"HE OR ERR ON RD/FMT STARTING FROM <DSK-ADRES>"
7536	027346	051105	020122	047117			
7537	027354	051040	027504	046506			
7538	027362	020124	052123	051101			
7539	027370	044524	043516	043040			
7540	027376	047522	020115	042074			
7541	027404	045523	040455	051104			
7542	027412	051505	000076				
7543							
7544	027416	051127	047117	020107	EM72:	.ASCIZ	/WRONG DRIVE ID IN RKDS AFTER SEEK/
7545	027424	051104	053111	020105			
7546	027432	042111	044440	020116			
7547	027440	045522	051504	040440			

7548	027446	052106	051105	051440	
7549	027454	042505	000113		
7550					
7551	027460	051110	053504	042522	EM73: .ASCIZ /HRDWRE POLL-DRV ID BITS(13-15) SHLD BE CLR/
7552	027466	050040	046117	026514	
7553	027474	051104	020126	042111	
7554	027502	041040	052111	024123	
7555	027510	031461	030455	024465	
7556	027516	051440	046110	041104	
7557	027524	020105	046103	000122	
7558					
7559	027532	051110	053504	042522	EM74: .ASCIZ /HRDWRE POLL-INTRUPTING DRIV # NOT PRSNT/
7560	027540	050040	046117	026514	
7561	027546	047111	051124	050125	
7562	027554	044524	043516	042040	
7563	027562	044522	020126	020043	
7564	027570	047516	020124	051120	
7565	027576	047123	000124		
7566					
7567	027602	051104	053111	021440	EM75: .ASCIZ /DRIV # DIDN'T INTRUPT AFTER HRDWRE POLL/
7568	027610	042040	042111	023516	
7569	027616	020124	047111	051124	
7570	027624	050125	020124	043101	
7571	027632	042524	020122	051110	
7572	027640	053504	042522	050040	
7573	027646	046117	000114		
7574					
7575	027652	041523	020120	044504	EM76: .ASCIZ /SCP DIDN'T SET AFTER SEEK WAS DONE/
7576	027660	047104	052047	051440	
7577	027666	052105	040440	052106	
7578	027674	051105	051440	042505	
7579	027702	020113	040527	020123	
7580	027710	047504	042516	000	
7581					
7582	027715	122	042113	020101	EM77: .ASCIZ /RKDA CHANGD AFTER DRIV RESET/
7583	027722	044103	047101	042107	
7584	027730	040440	052106	051105	
7585	027736	042040	044522	020126	
7586	027744	042522	042523	000124	
7587					
7588	027752	040504	040524	042440	EM100: .ASCIZ /DATA EROR AT WORD#/
7589	027760	047522	020122	052101	
7590	027766	053440	051117	021504	
7591	027774	000			
7592					
7593	027775	103	052116	046122	EM101: .ASCIZ /CNTRL RDY DIDN'T SET AFTER RD CHK/
7594	030002	051040	054504	042040	
7595	030010	042111	023516	020124	
7596	030016	042523	020124	043101	
7597	030024	042524	020122	042122	
7598	030032	041440	045510	000	
7599					
7600	030037	105	051122	047440	EM102: .ASCIZ /ERR OR HE ON RD CHK/
7601	030044	020122	042510	047440	
7602	030052	020116	042122	041440	
7603	030060	045510	000		

7716	031074	020122	051447	050103	
7717	031102	000047			
7718					
7719	031104	045522	030461	042040	EM124: .ASCIZ /RK11 DIDN'T INTRUPT AFTER RD DONE/
7720	031112	042111	023516	020124	
7721	031120	047111	051124	050125	
7722	031126	020124	043101	042524	
7723	031134	020122	042122	042040	
7724	031142	047117	000105		
7725					
7726	031146	047103	051124	020114	EM125: .ASCIZ /CNTRL RESET DIDN'T CLR REGISTR/
7727	031154	042522	042523	020124	
7728	031162	044504	047104	052047	
7729	031170	041440	051114	051040	
7730	031176	043505	051511	051124	
7731	031204	000			
7732					
7733	031205	122	030513	020061	EM126: .ASCIZ /RK11 DIDN'T INTRUPT AT CPU LEVEL/
7734	031212	044504	047104	052047	
7735	031220	044440	052116	052522	
7736	031226	052120	040440	020124	
7737	031234	050103	020125	042514	
7738	031242	042526	000114		
7739					
7740	031246	045522	030461	044440	EM127: .ASCIZ /RK11 INTRUPTED AT WRONG CPU LEVEL/
7741	031254	052116	052522	052120	
7742	031262	042105	040440	020124	
7743	031270	051127	047117	020107	
7744	031276	050103	020125	042514	
7745	031304	042526	000114		
7746					
7747	031310	042447	051122	041040	EM130: .ASCIZ /'ERR BIT' DIDN'T SET IN RKER/
7748	031316	052111	020047	044504	
7749	031324	047104	052047	051440	
7750	031332	052105	044440	020116	
7751	031340	045522	051105	000	
7752					
7753	031345	110	020105	051117	EM131: .ASCIZ /HE OR ERR DIDN'T SET/
7754	031352	042440	051122	042040	
7755	031360	042111	023516	020124	
7756	031366	042523	000124		
7757					
7758	031372	045522	051105	042440	EM132: .ASCIZ /RKER EROR/
7759	031400	047522	000122		
7760					
7761	031404	054116	020103	044502	EM133: .ASCIZ /NXC BIT DIDN'T SET/
7762	031412	020124	044504	047104	
7763	031420	052047	051440	052105	
7764	031426	000			
7765					
7766	031427	122	030513	020061	EM134: .ASCIZ /RK11 DIDN'T INTRUPT ON SOFT EROR/
7767	031434	044504	047104	052047	
7768	031442	044440	052116	052522	
7769	031450	052120	047440	020116	
7770	031456	047523	052106	042440	
7771	031464	047522	000122		

7996
7997
7998 033336 000400
7999
8000
8001
8002 000001

;DATA BUFFER

OUTBUF: .BLKW 256.

;THIS 256 WORD BUFFER IS FOR
;DATA TRANSFERS FROM AND
;TO THE DISK.

.END

