

RK11

BASIC LOGIC TEST 2 MD-11-DZRKK-D

EP-DZRKK-D-DL-B
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This page contains a grid of 128 small-scale test diagrams or data tables, arranged in 8 rows and 16 columns. Each cell in the grid represents a single test pattern, which is a miniature version of the patterns described in the header. The patterns consist of various combinations of binary digits (0s and 1s) and are used for testing the logic of the MD-11-DZRKK-D system. The diagrams are organized into a regular grid, with each cell containing a unique test configuration. The patterns are arranged in a way that allows for systematic testing of the system's logic. The grid is the primary content of the document, providing a comprehensive set of test data for the system.

IDENTIFICATION

SEQ 0001

PRODUCT CODE: MAINDEC-11-DZRKK-D-D
PRODUCT NAME: RK11 BASIC LOGIC TEST II
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QUICK LOOK-UP OPERATING INSTRUCTIONS

SEQ 0002

FOR A QUICK REFERENCE, LOOK UP THE FOLLOWING SECTIONS:

1.0 ABSTRACT
 2.0 REQUIREMENTS
 4.1 LOADING AND OPERATOR ACTION
 7.0 SWITCH OPTIONS
 FOR A MORE COMPLETE EXPLANATION REFER TO THE TABLE OF
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1.0 ABSTRACT

THE RK11 LOGIC TESTS CONSIST OF A SERIES OF TESTS AIMED AT CHECKING THE BASIC LOGIC OF THE RK11 CONTROLLER. THIS PROGRAM IS THE SECOND PART OF THE TWO-PART RK11 LOGIC TESTS. IT SHOULD BE NOTED THAT LOGIC TEST I AND LOGIC TEST II TOGETHER CONSTITUTE A COMPLETE PROGRAM AND BOTH OF THEM SHOULD BE RUN.

WHEN USED IN CONJUNCTION WITH A DRIVE IT IS CAPABLE OF DETECTING FAULTS IN THE DRIVE ALSO.

USED CORRECTLY THIS PROGRAM CAN BE AN EFFECTIVE ANALYTIC AND DIAGNOSTIC TOOL.

2.0 REQUIREMENTS

2.1 EQUIPMENT

- A. PDP11 WITH CONSOLE TELETYPE.
- B. 8K OF MEMORY
- C. RK11 OR RKV11 CONTROLLER
- D. 1-8 RK05 OR RK05F DRIVES OR THE RK05 SIMULATOR (DRIVE TYPES MAY BE MIXED)

2.2 PRELIMINARY PROGRAMS

RK11 BASIC LOGIC TEST I (MD-11-DZRKJ)

2.3 EXECUTION TIME

ERROR FREE FIRST PASS ON PDP11/20 WITH CORE MEMORY TAKES APPROXIMATELY TWO MINUTES. CONSIDERABLY LESS FOR FASTER MACHINES OR MEMORIES.

3.0 STARTING ADDRESS

200 FOR ANY MODE OF OPERATION. NORMAL START UP WITH ALL SWITCHES DOWN.

4.0 PROGRAM CONTROL MODES & OPERATOR ACTION

PAPER TAPE LOADING
RKDP DUMP MODE
RKDP CHAIN MODE
ACT11

- 4.1 PAPER TAPE LOADING
- 4.1.1 LOAD PROGRAM INTO MEMORY USING STANDARD PROCEDURE FOR .ABS TAPES.
- 4.1.2 MAKE SURE THAT THE DRIVES TO BE CHECKED ARE LOADED WITH DISKS AND ARE IN 'RUN' 'WRT ENABLE' THEM. CHECK THAT 'WRT PROT' LIGHT ON THESE DRIVES IS OFF. PUT DRIVES THAT ARE NOT TO BE TESTED ON 'LOAD'.
- 4.1.3 LOAD ADDRESS 200
- 4.1.4 SET SWITCHES IF DESIRED (SEE SEC 7.0) IF TESTING ON SIMULATOR PUT SW<10> UP.
- PRESS START.
- 4.1.5 THE PROGRAM IDENTIFIES ITSELF (NAME, MAINDEC NO), THEN THE FOLLOWING QUESTION IS ASKED:

DRIVES TO BE TESTED?

THE USER SHOULD TYPE IN THE DRIVE NUMBERS THAT ARE IN 'RUN' AND TO BE TESTED. CARRIAGE RETURN SHOULD TERMINATE THE STRING. IF AN RK-05F IS TO BE TESTED, TYPE THE SUFFIX 'F' WITH THE FIRST DRIVE OF THE PAIR. FOR EXAMPLE, IF DRIVES 2 AND 3 ARE ON AN RK-05F, TYPE ONLY 2F.

EXMP: DRIVES TO BE TESTED? 0,1,2<CR>

THE DRIVES DO NOT HAVE TO BE IN LOGICAL ORDER.

EXMP: DRIVES TO BE TESTED? 2,4<CR>

IF ANY ONE DRIVE IS TO BE TESTED, TYPE IN THAT NUMBER. IT DOES NOT HAVE TO BE DRIVE 0.

THUS A NORMAL SEQUENCE WITH DRIVES 0,1 WOULD BE:

RK11 LOGIC TEST II
 MAINDEC-11-DZRKK-D
 DRIVES TO BE TESTED? 0,1<CR>

- 4.1.6 THERE IS A "RUBOUT" FEATURE WHICH ALLOWS RUBBING OUT ANY NUMBER OF CHARACTERS THAT WERE TYPED IN WRONG. THE RUBBED OUT CHARACTERS ARE ECHOED BACK WITHIN SLASHES.
- "↑U" DELETES THE ENTIRE LINE
- 4.1.7 IF REPLY TO ANY OF THE ABOVE QUESTION IS IN A WRONG FORMAT (EX: 012<CR>; 0, B<CR>; 0, A<CR>; M<CR> ETC), IT IS AUTOMATICALLY REJECTED, A "???" IS PRINTED OUT;

THE CORRECT ANSWER CAN NOW BE RETYPED AGAIN.

4.1.8 THE DRIVE NUMBER BEING TESTED OUT IS PRINTED:

DRIVE N :N=0,1...7
IF THE DRIVE IS AN RK-DSF, AN F IS APPENDED

AT THE END OF A PASS THE FOLLOWING TYPE-OUT OCCURS

END PASS # X

WHERE X= PASS NUMBER (1,2,3---), CONTROL IS PASSED TO THE BEGINNING OF THE PROGRAM AND RE-EXECUTION BEGINS. NO QUESTIONS ARE TO BE ANSWERED AGAIN.

4.1.9 ERROR FREE PASSES OF THE PROGRAM APPEAR AS SHOWN BELOW.

```
RK11 LOGIC TEST II
MAINDEC-11-DZRKK-D
DRIVES TO BE TESTED?
D,1<CR>
DRIVE 0
DRIVE 1
END PASS # 1
D
DRIVE 1
END PASS # 2
...
...
```

4.2 RKDP DUMP MODE

4.2.1 THE PROGRAM IS LOADED INTO THE MEMORY BY THE RKDP MONITOR

4.2.2 START AS NORMALLY USING SA 200

4.2.3 THE PROGRAM IDENTIFIES ITSELF (NAME MAINDEC NO.). ON FINDING OUT THAT THE LOADING WAS BY RKDP (DUMP MODE), THE FOLLOWING MESSAGE APPEARS:

'TO TEST DRIVE 'N' HALT PROGRAM, REMOVE RKDP PACK AND REPLACE IT WITH A WORK PACK, CLEAR LOCATION 40, AND RESTART PROGRAM'

IF DRIVE 'N' IS TO BE TESTED, THE RKDP PACK ON THAT DRIVE SHOULD BE REPLACED BY ANOTHER PACK, THE DRIVE SHOULD BE PUT ON 'WRT ENABL' (BECAUSE RKDP WRITE PROTECTS THE DRIVE).

IF DRIVE 'N' IS NOT TO BE CHECKED, THEN THE MESSAGE SHOULD BE IGNORED.

AFTER THIS, THE SEQUENCE OF QUESTIONING IS AS EXPLAINED IN SEC 4.1.5.

4.3 RKDP CHAIN MODE

THE PROGRAM IS CHAIN-LOADED FROM THE RKDP PACK ON DRIVE 'N'. AFTER THE PROGRAM IDENTIFIES ITSELF THE FOLLOWING PRINTOUT OCCURS.

'DRIVE 'N' NOT TESTED'

THERE IS NO OPERATOR INTERVENTION REQUIRED. THE PROGRAM FINDS OUT THE NUMBER OF DRIVES PRESENT.

4.4 ACT11 MODE

THE PROGRAM IS LOADED BY THE ACT11 MONITOR. ON STARTING, IDENTIFIES ITSELF, ASCERTAINS THE NUMBER OF DRIVES AND PROCEEDS WITH THE EXECUTION OF THE TESTS AS BEFORE.

5.0 DRIVE SELECTION

IF ANY PARTICULAR DRIVE IS TO BE SELECTED FOR TESTING, PUT THAT DRIVE ON 'RUN', 'WRITE ENABLE'; PUT REST OF THE DRIVES ON 'LOAD', 'WRITE LOCK' AND IN REPLY TO THE QUESTION 'VES TO BE TESTED?' TYPE IN THE DRIVE NUMBER FOLLOWED BY CR. SEE SEC 4.1.5.

6.0 DRIVE-LESS TEST

USE RK11 BASIC LOGIC TEST I, WHICH IS ACTUALLY THE FIRST PART OF THE TWO-PART RK11 BASIC LOGIC TESTS. SEE SEC 1.0, 2.2.

7.0 SWITCH OPTIONS

IF THE PROGRAM IS BEING RUN ON A SWITCHLESS PROCESSOR (I.E. AN 11/34) THE PROGRAM WILL DETERMINE THAT THE HARDWARE SWITCH REGISTER IS NOT PRESENT AND WILL USE A 'SOFTWARE' SWITCH REGISTER. THE 'SOFTWARE' SWITCH REGISTER IS LOCATED AT LOCATION 176 (8). THE SETTINGS OF THE 'SOFTWARE' SWITCHES ARE CONTROLLED THROUGH A KEYBOARD ROUTINE WHICH IS CALLED BY TYPING A 'CONTROL G'. THE PROGRAM WILL RECOGNIZE THE 'CONTROL G' whenever the program enters the scope routine or begins a new test. the 'SOFTWARE' SWITCH VALUES ARE ENTERED AS AN OCTAL NUMBER IN RESPONSE TO THE PROMPT FROM THE SWITCH ENTRY ROUTINE:

'SWR = NNNNNN NEW ='

EACH TIME SWITCH SETTING ARE ENTERED, THE ENTIRE SWITCH REGISTER IMAGE MUST BE ENTERED. LEADING ZEROS ARE NOT REQUIRED. 'RUBOUT' AND 'CONTROL U' FUNCTIONS MAY BE USED TO CORRECT TYPING ERRORS DURING SWITCH ENTRY.

ON PROCESSORS WITH HARDWARE SWITCH REGISTERS, THE 'SOFTWARE' SWITCH REGISTER MAY BE USED. IF THE PROGRAM FINDS ALL 16 SWITCHES IN THE 'UP' POSITION, ALL SWITCH REGISTER REFERENCES WILL BE TO THE 'SOFTWARE' REGISTER AND THE PROCEDURES DESCRIBED ABOVE MUST BE FOLLOWED.

SW<15>=1 HALT ON ERROR
 SW<14>=1 LOOP ON TEST
 SW<13>=1 INHIBIT ERROR PRINTOUTS
 SW<12>=1 CYCLE ON ERROR TO THE PREVIOUS
 'SCOPE' STATEMENT
 SW<11>=1 INHIBIT ITERATIONS
 SW<10>=1 TESTING ON SIMULATOR
 SW<09>=1 LOOP ON SPECIFIC ERROR
 SW<08>=1 LOOP ON TEST AS PER SW<07:00>
 SW<06>=1 DROP THE DRIVE AFTER MAXIMUM
 ALLOWABLE NUMBER OF ERRORS OCCUR

7.1 SW<15>

THE PROGRAM HALTS ON ENCOUNTERING AN ERROR, AFTER TYPING OUT THE ERROR MESSAGE AND PERTINENT INFORMATION. PRESSING "CONTINUE" RESTORES NORMAL OPERATION OF THE PROGRAM.

7.2 SW<14>

THE PROGRAM LOOPS ON THE SUBTEST THAT IS BEING EXECUTED WHEN THE SWITCH IS PUT ON. THIS SWITCH IS USED NORMALLY ALONG SW 15. SEE SEC 8.0.

7.3 SW <13>

THIS SWITCH INHIBITS ALL ERROR MESSAGES. NORMALLY USED WHEN LOOPING ON TEST (SW 14) OR LOOPING ON ERROR (SW 9).

7.4 SW <12>

THIS SWITCH ALLOWS THE PROGRAM TO CYCLE FROM THE POINT OF ERROR TO THE PREVIOUS SCOPE STATEMENT. NOTE THAT IN DOING SO ANY INITIALIZATION BEING DONE AT THE BEGINING OF THE SUBTEST WILL BE DONE AGAIN AND AGAIN. SEE SEC 8.0 FOR DIFFERENT SCOPE LOOPS AVAILABLE.

7.5 SW <11>

EACH SUBTEST WILL BE EXECUTED ONLY ONCE. NORMALLY AF THE FIRST PASS, EACH SUBTEST IS ITERATED A NUMBER OF TIMES (USUALLY 50, 5 IN SOME CASES). SETTING THIS SWITCH INHIBITS ITERATIONS, SO THAT QUICK PASSES CAN BE MADE.

7.6 SW <10>

THIS SWITCH WHEN SET INDICATES THAT TESTING IS BEING DONE ON A SIMULATOR. THE SWITCH SHOULD BE PUT UP BEFORE START- ING THE PROGRAM. NOTE THAT RK11C IS NOT COMPATIBLE WITH THE SIMULATOR.

7.7 SW <09>

THIS SWITCH PROVIDES THE TIGHTEST POSSIBLE SCOPE LOOP. NOTE THAT SW12 THE INITIALIZATION OF PARAMETERS AT THE BEGINNING OF THE SUBTEST MAY NOT BE DONE IN THIS CASE. THIS SWITCH IS HELPFUL WHEN A PARTICULAR PART OF A SUBTEST IS BEING REPEATED USING DIFFERENT PARAMETERS AND YOU WANT TO SCOPE ON THE PARAMETER IN ERROR. (EXAMPLE: RKDA IS BEING WRITTEN AND READ BACK WITH COUNT PATTERNS FROM 1 TO 177777. PATTERN 561 IS GIVING ERROR, YOU MIGHT NOT WANT TO GO THROUGH THE 560 PATTERNS BEFORE HITTING ERROR ON THE 561TH PATTERN. IN THIS CASE SW 9 WILL GIVE YOU A SCOPE LOOP ON THE 561TH PATTERN ONLY

7.8 SW <08>

THIS SWITCH IS USED TO SELECT A PARTICULAR TEST (AS PER SW<00-07>) FOR EXECUTION AND SUBSEQUENT LOOPING. THUS IF TEST 15 IS TO BE SELECTED THE SWITCH SETTING WOULD BE 000415. IT SHOULD BE NOTED THAT BEFORE SELECTING TEST 15, ALL THE PREVIOUS TESTS (1-14) WILL BE EXECUTED.

7.9 SW<06>

THIS SWITCH ALLOWS THE PROGRAM TO DROP A DRIVE FROM THE SELECTION LIST AND TESTING AFTER MAXIMUM ALLOWABLE ERROR COUNT (TOTAL NUMBER OF ERRORS) ON THAT DRIVE IS EXCEEDED. THE MAXIMUM ALLOWABLE ERROR COUNT IS 5, AFTER 5 ERRORS HAVE OCCURED DRIVE IS DROPPED AND A MESSAGE (DRIVE # XXX DROPPED) IS PRINTED.

8.0 SCOPE LOOPS

THERE ARE THREE KINDS OF SCOPE LOOPS AVAILABLE

1. SW14: LOOPING IS DONE FOR THE ENTIRE SUB-TEST
2. SW12: LOOPING IS DONE FROM THE POINT OF ERROR BACK TO THE PREVIOUS 'SCOPE' STATEMENT.
3. SW09: PROVIDE THE TIGHTEST POSSIBLE SCOPE LOOP SEE SEC. 7.7

EXAMPLE:

```
TST1: SCOPE
      :
```

```
      INITIALIZATION
      :
ERROR 1
      :
      ERROR 2
      :
```



```

ERROR 3
:
ERROR 4
:
TST2: SCOPE
    
```

THE SEQUENCE OF LOOPING FOR DIFFERENT CASES IS EXPLAINED BELOW. NOTE THAT 'TST1' AND 'TST2' ARE TAGS WHICH DEFINE THE BOUNDARY OF A TEST, (IN THIS CASE TEST 1). TEST 1 STARTS AT 'TST1' AND ENDS JUST BEFORE 'TST2'.

IN THE ILLUSTRATION BELOW --> INDICATES THE POINT FROM WHERE RETURN IS MADE AND LOOPING IS DONE.

1. ERROR 2 OCCURS, SW 14 SET.

```
TST1..ERROR 2..TST2-->TST1..ERROR 2..TST2-->TST1...
```

2. ERROR 2 OCCURS, SW 12 SET.

```
TST1...ERROR 2-->TST1...ERROR2-->TST1...
```

3. ERROR 2,3; SW 14 SET.

```
TST1..ERROR 2..ERROR 3..TST2-->TST1..ERROR 2..ERROR 3..TST2-->TST1...
```

4. ERROR 2,3; SW 12 SET.

```
TST1...ERROR 2-->TST1...ERROR 2-->TST1....
```

NOTE THAT LOOPING IS DONE FROM THE VERY FIRST ERROR ENCOUNTERED. THE MORE BASIC AND EARLIER IT OCCURS AND IS DETECTED AND SHOULD BE FIXED.

IN THE ABOVE EXAMPLE NO PART OF THE SUB-TEST IS BEING REPEASING DIFFERENT PARAMETERS, HENCE IT SO HAPPENS THAT SW 9 AND 12 GIVE THE SAME KIND OF LOOPS. THE EXAMPLE BELOW WILL DEMONSTRATE THE DIFFERENCE BETWEEN SW 9 AND 12.

```
TST1: SCOPE
:
```

INITIALIZATION

```
:
ERROR 1
:
```

```
MOV    #1$, $LPERR    ; '$LPERR' CONTAINS
                     ; THE ADDRESS TO LOOP
                     ; BACK ON ERROR- SW 9
```

```
1$:
```

```

:
:
ER      I      N REPETITIONS
:
:
I
    
```

TST2: SCOPE ----

1. SW 12 SET, ERROR 2 OCCURS DURING K.TH REPETITIONS

TST1..1,2...K.ERROR 2-->TST1..1,2...K.ERROR 2-->TST1..

2. SW 9 SET, ERROR 2 OCCURS DURING K.TH REPETITION

1S..K..ERROR 2-->1S..K..ERROR 2-->1S...

9.0 PROGRAM STRUCTURE

THERE ARE THREE DISTINCT PARTS OF THE PROGRAM.

SET-UP PHASE
DRIVE-DEPENDENT CONTROLLER TESTS

9.1 SET-UP PHASE

SETTING UP OF INITIAL POINTERS, VECTORS, TABLES IS DONE IN THIS PART. IN THIS SECTION THE DECISION IS MADE ABOUT THE PROGRAM MODE-PAPER TAPE, RKDP DUMP, CHAIN OR ACT11. IF IN A NON-INTERVENTION MODE (CHAIN, ACT11) NUMBER OF DRIVES AND THE TYPE OF CONTROLLER IS FOUND OUT. FLAGS ARE SET TO INDICATE WHICH DRIVES ARE TO BE TESTED, ETC.

9.2 DRIVE DEPENDENT CONTROLLER TESTS

THIS SECTION FORMS A MAJOR PART OF THE PROGRAM WHEREIN MOST OF THE CONTROLLER IS CHECKED.

JUST BEFORE ENTERING THIS SECTION THE PROGRAM FINDS OUT WHICH DRIVE IS TO BE CHECKED. IF IN RKDP CHAIN MODE, DRIVE 'N' IF PRESENT, IS SKIPPED AND THE NEXT AVAILABLE DRIVE IS SELECTED.

THE DRIVE NUMBER BEING TESTED IS PRINTED OUT:

DRIVE N ;N=0,1,2...7

THE TESTING IS DONE IN A LOGICAL HIERCHY, SIMPLER THINGS FIRST, THEN MORE COMPLEX AND SO ON.

IN ONE OF THE TESTS THE ENTIRE DISK PACK IS FORMATTED, CHECKS ARE MADE FOR ERROR CONDITIONS. THE FIRST WORD OF EVERY SECTOR IS WRITTEN AS A PSUEDO-HEADER, REFLECTING THE ABSOLUTE ADDRESS OF THAT SECTOR (DRIVE #, CYLINDER #, SURFACE #, SECTOR #). EXAMPLE: THE PSUEDO-HEADER FOR SECTOR 5, SURFACE 0, CYLINDER 20, DRIVE 0 WOULD BE 001005.

IN THE NEXT TEST THE HEADERS FROM THE ENTIRE PACK ARE READ AND CHECKED FOR CORRECTNESS. IN A SUBSEQUENT TEST ALL THE PSUEDO-HEADERS ARE READ AND VERIFIED.

ALL THE FUNCTIONS ARE CHECKED OUT. 'SEEK' IS CHECKED IN THE THREE DIFFERENT VELOCITY MODES (HIGH, MEDIUM, LOW). VARIOUS ERRORS LIKE 'NXD', 'NXC', ETC. ARE SIMULATED AND CHECKED.

HARDWARE POGIC IS CHECKED USING ALL THE DRIVES THAT HAVE BEEN INDICATED.

AT THE END OF THIS SECTION, A CHECK IS MADE IF ALL INDICATED DRIVES HAVE BEEN TESTED. IF NOT, CONTROL IS TRANSFERRED TO THE BEGINNING OF THIS SECTION.

THUS ONE PASS OF THE PROGRAM INVOLVES DOING

1. SUBTEST #1 ONCE
2. DRIVE-DEPENDENT TESTS FOR ALL THE SELECTED DRIVES.

10.0 ERROR REPORTING

THE ERROR TABLE STARTING AT SERRTB CONTAINS INFORMATION PERTAINING TO EVERY ERROR THAT CAN OCCUR. EACH ITEM IN THE TABLE CONSISTS OF FOUR ENTRIES.

- A. EM - THIS IS A POINTER TO THE ERROR MESSAGE TO BE TYPED OUT WHEN THE ERROR OCCURS.
- B. DH - THIS IS A POINTER TO THE DATA HEADER TO BE TYPED OUT.
- C. DT - THIS IS A POINTER TO THE DATA WHICH IS TO BE TYPED TYPED OUT UNDER THE HEADERS.
- D. D - THIS IS A TERMINATOR SIGNIFYING THE END OF THE ITEM.

THE ERROR CALL IS AN EMT INSTRUCTION WITH ITS LOWER BYTE ENCODED TO INDICATE THE ERROR NUMBER. THUS OR 1" WOULD BE (EMT+1) IE 104001.

EVERY ERROR CORRESPONDS TO AN ITEM IN THE ERROR TABLE. THUS "ERROR 14" WOULD CORRESPOND TO ITEM 14. AS FAR AS POSSIBLE, THE ERROR MESSAGES HAVE BEEN KEPT SHORT, BUT CLARITY IS NOT SACRIFICED FOR BREVITY. INSPITE OF THIS, IF THE USER FINDS A NEED, HE CAN LOOK UP THE ENTIRE ERROR MESSAGE IN THE ERROR ITEMS TABLE FOUND IN THE BEGINNING OF THE LISTINGS. THUS FOR "ERROR 14", "ITEM 14" IN THE ITEM TABLE CAN BE LOOKED UP. WHEN THE ERROR INSTRUCTION IS EXECUTED A TRAP OCCURS TO THE ERROR HA LOCATED AT SERRTB WHICH PROCESSES THE ERROR CALL. SEE SEC 12.3

11.0 ERROR INTERPRETATION

WHENEVER AN ERROR MESSAGE IS PRINTED OUT, ALL REGISTERS AND OTHER DATA PERTAINING TO THE ERROR ARE

ALSO GIVEN. RKDS, RKER...RKBA INDICATE THE CONTENTS OF THE CORRESPONDING REGISTERS AT THE TIME OF ERROR.

EVERY ERROR MESSAGE CONTAINS A PC. THIS PC INDICATES THE POSITION IN PROGRAM WHERE THE ERROR CALL IS LOCATED. THE ERROR MESSAGE, BECAUSE OF PRACTICAL CONSIDERATIONS IS MADE SHORT AND MEANINGFUL. THE USER IS ADVTD TO LOOK UP THE PC IN THE PROGRAM LISTING, WHERE HE WILL FIND MORE INFORMATION ABOUT THE ERROR. IN MANY INSTANCES, A SINGLE FAULT WILL GIVE RISE TO MORE THAN ONE ERROR REPORT. A LITTLE DELIBERATION AND CAREFUL EXAMINATION OF THE DATA GIVEN WILL BE CERTAINLY VERY HELPFUL IN PINPOINTING THE FAULT. A BRIEF EXPLANATION OF WHAT IS BEING CHECKED IN THE SUBTEST IS GIVEN AT THE BEGINNING OF EVERY SUBTEST. ALL THE NUMBERS GIVEN WITH ERROR MESSAGES ARE IN OCTAL.

12.0 HANDLERS AND COMMON ROUTINES

THE COMPOSED ROUTINES USED IN THE PROGRAM ARE CALLED IN TWO WAYS.

- A. AS A SUBROUTINE THROUGH 'JSR' CALL
- B. THROUGH A 'TRAP' HANDLER

12.1 TRAP HANDLER

MANY COMMONLY USED ROUTINES IN THE PROGRAM ARE CALLED USING THE TRAP INSTRUCTION AND THE 'TRAP' HANDLER. THE LOWER BYTE OF THE TRAP INSTRUCTION IS ENCODED DIFFERENTLY FOR DIFFERENT ROUTINES. THE TRAP HANDLER IS LOCATED AT '\$TRAP'. WHEN A CALL FOR A ROUTINE IS EXECUTED, A TRAP OCCURS TO THE HANDLER 'ARAP'. THE HANDLER PICKS UP THE LOWER BYTE OF THE "CALL INSTRUCTION" AND USES IT TO FORM THE STARTING ADDRESS OF THE ROUTINE TO GO TO FOR SERVICE.

12.2 SCOPE HANDLER

THE 'IOT' TRAP IS USED BY THE 'SCOPE' STATEMENT. WHEN 'SCOPE' IS EXECUTED, AN IOT TRAP OCCURS TO MEMORY LOCATION '\$SCOPE'. THE SCOPE HANDLER STARTS AT '\$SCOPE'. DEPENDING ON THE SWITCH SETTINGS THE HANDLER DECIDES TO LOOP ON TEXT, INHIBIT ITERATIONS ETC. THERE ARE CERTAIN POINTERS AND FLAGS WHICH ARE ADJUSTED. THUS, IT IS NOT ADVISABLE START THE PROGRAM AT ANY GIVEN LOCATION SINCE THE VARIOUS POINTERS AND FLAGS MAY NOT BE CORRECTLY ADJUSTED.

12.3 ERROR HANDLER

AN EMT TRAP INSTRUCTION IS USED BY THE ERROR CALL. THE LOWER BYTE IS ENCODED TO GIVE DIFFERENT ERROR CALLS. (EX: ERROR 1 = 104000+1; ERROR 16 = 104000+16). WHEN THE ERROR STATEMENT IS EXECUTED, A

TRAP OCCURS TO MEMORY LOCATION 'SERROR'. THE ERROR HANDLER IS LOCATED AT 'SERROR'. THE HANDLER FORMS THE POINTER TO ERROR TABLE, WHICH IS USED IF AN ERROR MESSAGE IS TO BE TYPED DEPENDING ON THE SWITCH SETTINGS. A DECISION ABOUT HALTING ON ERROR, INHIBITING TYPEOUT, LOOPING ON ERROR ETC. IS MADE. IF AN ERROR MESSAGE IS TO BE TYPED OUT AN EXIT IS MADE TO THE ERROR MESSAGE TYPEOUT ROUTINE LOCATED AT 'SERRTYP'.

12.4 CONTROL RESET ROUTINE

THE CALL FOR THIS ROUTINE IS "CNT.RESET" AND IS AN ENCODED 'TRAP' INSTRUCTION. WHEN "CNT.RESET" IS EXECUTED THE CONTROL RESET ROUTINE STARTING AT "CN.RST" IS ENTERED. A CONTROL RESET IS ISSUED THE PROGRAM WAITS TILL THE CONTROL READY SETS, ON WHICH THE ROUTINE IS EXITED. IF CONTROL READY DOES NOT SET WITHIN A CERTAIN TIME AN ERROR IS REPORTED. THE PC TYPED OUT IS THE LOCATION WHERE THE "CNT.RESET" CALL IS LOCATED. THE WAITING TIME IS 2.8 MS FOR 11/20 AND 560 US FOR 11/45 WITH BIPOLAR MEMORY.

12.5 CONTROL READY ROUTINE

THIS ROUTINE IS CALLED BY "CNT.RDY" (AN ENCODED 'TRAP' INSTRUCTION) AND IS LOCATED AT "CN.RDY". THE ROUTINE WAITS FOR THE CONTROL READY TO SET AND WHEN IT DOES, EXITS IF CONTROL READY DOES NOT SET WITHIN A SPECIFIED TIME AN ERROR MESSAGE IS GIVEN

CNTRL RDY DIDN'T SET
PC = XXXXXX RKCS = YYYYYY

THE PC IS THE LOCATION AT WHICH THE "CNT.RDY" CALL IS LOCATED. THE WAITING TIME IS 949 MS FOR 11/20 AND 189 MS FOR 11/45 WITH BIPOLAR MEMORY.

12.6 DRIVE RESET ROUTINE

THE DRIVE - RESET ROUTINE IS LOCATED AT "DRESET" AND IS CALLED BY A "JSR". IT ISSUES A DRIVE RESET AND WAITS FOR THE R/W/S RDY TO SET, ON WHICH THE ROUTINE IS EXITED. THE WAITING TIME IS 4959 MS FOR 11/20 AND 991 MS FOR 11/45 WITH BIPOLAR MEMORY.

12.7 TIME DELAY ROUTINE

THIS ROUTINE PROVIDES A VARIABLE TIME DELAY. THE CALL IS DELAY ,N WHERE N=1 TO 177777 (OCTAL) TIME DELAY PROVIDED= 7.5 TIMES(X) N MICRO SECS FOR 11/20, 1.5N US FOR 11/45 (N CONVERTED TO DECIMAL BEFORE COMPUTING DELAY) IF THE USER WANTS TO CHANGE THE DELAY AT ANY POINT IT CAN BE DONE BY SIMPLY CHANGING VARIABLE 'N'.

12.8 WAIT FOR INTERRUPT ROUTINE

THIS ROUTINE PROVIDES A VARIABLE TIME LIMIT DURING WHICH RK11 INTERRUPT MAY OCCUR. THE IS
 MAT.INT N N=1 TO 1777777 (OCTAL)
 WAITING TIME=7.5 TIMES(X) N US FOR 11/20, 1.5N US

FOR 11/45 UPON ENTERING THE ROUTINE CPU PRIORITY IS DROPPED SO THAT RK11 CAN INTERRUPT.

12.9 OTHER ROUTINES

THERE ARE OTHER COMMONLY USED ROUTINES AS LISTED BELOW.

STYPE:
 TYPE ROUTINE FOR TYPING OUT ASCII STRINGS.
 LOCATED AT "STYPE"
 CALLED BY "TYPE"

STYPOC:
 ROUTINE FOR TYPING OUT OCTAL NUMBERS.
 LOCATED AT "STYPOC"
 CALLED BY "TYPOC"

STYPDS:
 ROUTINE FOR TYPING OUT DECIMAL NUMBERS.
 LOCATED AT "STYPDS"
 CALLED BY "TYPDS"

SRDLIN:
 ROUTINE FOR INPUTTING ASCII STRINGS FROM TTY.
 LOCATED AT "SRDLIN"
 CALLED BY "RDLIN"

SERRTYP:
 ROUTINE FOR TYPING OUT ERROR MESSAGES.
 LOCATED AT SERRTYP
 CALLED BY "JSR SERRTYP"

SPWRDN:
 ROUTINE FOR HANDLING POWER FAILURE.
 LOCATED AT SPWRDN
 CALLED WHEN THERE IS A POWER FAILURE.

SPWRUP:
 ROUTINE FOR HANDLING POWER UP AFTER A POWER FAIL.
 LOCATED AT SPWRUP
 CALLED WHEN POWER RETURNS AFTER HAVING GONE DOWN.

13.0 UNEXPECTED TIMEOUTS AND RK11 INTERRUPTS

WHEN AN UNEXPECTED TIMEOUT OCCURS, THE PC AT WHICH TIME OUT OCCURED IS TYPED OUT AND THE PROGRAM HALTS.

IF IT IS INTACT, IT CAN BE RESTARTED BY PRESSING CONTINUE.

SEQ 0015

IF AN UNEXPECTED RK11 INTERRUPT OCCURS THE PROGRAM TYPES OUT THE PC AT WHICH THE INTERRUPT CAME IN AND THEN HALTS. PRESSING CONTINUE WOULD RESTART THE PROGRAM FROM BEGINING. SW 9- LOOPING CAITY IS PROVIDED AS A TROUBLE SHOOTING AID.

14.0 QUICK VERIFYING MODE

THE FIRST PASS OF THE PROGRAM IS A QUICK VERIFYING MODE. ALL THE TESTS ARE DONE ONLY ONCE, ON SUBSEQUENT PASSES THE TESTS ARE ITERATED (NORMALLY 50 TIMES, 5 IN SOME CASES). THUS THE FIRST PASS TAKES A SHORTER TIME TO COMPLETE, WHEREAS SUBSEQUENT PASSES TAKE MORE TIME.

23	OPERATIONAL SWITCH SETTINGS
48	BASIC DEFINITIONS
158	TRAP CATCHER
167	STARTING ADDRESS(ES)
169	ACT11 HOOKS
179	COMMON TAGS
332	ERROR POINTER TABLE
965	INITIALIZE THE COMMON TAGS
1002	TYPE PROGRAM NAME
1007	GET VALUE FOR SOFTWARE SWITCH REGISTER
1312	T1 CHECK THAT THE DRIVES THAT ARE NOT SPECIFIED ARE NOT FOUND TO BE PRESENT
1388	T2 FIND OUT NEXT DRIVE TO BE CHECKED
1442	T3 CHECK THAT DRIVE IS SUPPLIED WITH POWER-DPL BIT
1490	T4 CHECK THAT 'DRIVE UNSAFE' IS CLEAR, 'HDEN' IS SET, 'WPS' IS CLEAR
1528	T5 CHECK THAT 'DRIVE READY' IS SET IN RKDS
1549	T6 CHECK THAT 'SOK' BIT CAN SET
1568	T7 CHECK THAT 'SECTOR COUNTER' CAN COUNT FROM 0-13
1666	T10 CHECK THAT SC=SA CAN BE GENERATED
1704	T11 CHECK THAT 'R/W/S RDY' IS SET & 'SIN' IS CLEAR
1729	T12 CHECK 'DRIVE RESET'
1786	T13 CHECK 'SEEK' TO CYLINDER 0
1850	T14 CHECK R/W/S RDY IS CLEAR WHEN HEADS ARE IN MOTION
1900	T15 CHECK 'WRITE' FORMAT FUNCTION-CYLINDER 0, SECTOR 0
2013	T16 CHECK 'READ FORMAT' FUNCTION-CYLINDER 0, SECTOR 0
2121	T17 CHECK 'READ' FUNCTION-CYLINDER 0, SECTOR 0
2248	T20 CHECK 'WRITE FORMAT' -CYLINDER 0, SECTOR 0-13
2344	T21 CHECK 'READ FORMAT' -CYLINDER 0, SECTOR 0-13
2454	T22 CHECK 'READ', CYLINDER 0, SECTORS 0 TO 13
2616	T23 CHECK 'WRITE FORMAT' OF THE DISK
2742	T24 CHECK 'READ FORMAT' FOR THE ENTIRE DISK
2902	T25 CHECK 'READ' OF THE ENTIRE DISK
3035	T26 CHECK 'SEEK' FUNCTION, WITH DIFFERENT VELOCITY MODES
3140	T27 CHECK DRIVE RESET FROM LAST CYLINDER
3262	T30 'WRITE' - 256 WORD BLOCK ON SECTOR 0, CYLINDER 0
3374	T31 CHECK THAT WRITE WAS DONE CORRECTLY
3454	T32 CHECK 'READ CHECK' FUNCTION - CYLINDER 0, SECTOR 0
3541	T33 CHECK THE 'WRITE CHECK' FUNCTION - ON CYLINDER 0, SECTOR 0
3630	T34 CHECK THAT IBA INHIBITS INCREMENTING OF RKBA
3743	T35 CHECK THAT RK11 INTERRUPTS WHEN IDE IS SET
3808	T36 CHECK THAT WITH IDE SET RK11 INTERRUPTS AFTER INITIATION & COMPLETION OF SEEK
3920	T37 CHECK THAT WITH IDE SET RK11 INTERRUPTS WHEN READ IS DONE
3995	T40 CHECK THAT RK11 INTERRUPTS AT BRS ONLY
4078	T41 SIMULATE & CHECK 'OVR' ERROR
4156	T42 SIMULATE & CHECK PGE ERROR
4222	T43 SIMULATE & CHECK NXM ERROR
4295	T44 SIMULATE & CHECK NXD ERROR
4376	T45 SIMULATE & CHECK NXC ERROR
4462	T46 SIMULATE & CHECK NXS ERROR
4535	T47 SIMULATE & CHECK WCE
4602	T50 CHECK THAT SSE STOPS ALL CONTROL ACTION ON SOFT ERROR
4671	T51 CHECK THAT RK11 INTERRUPTS ON SOFT ERROR WHEN SSE & IDE ARE SET
4738	T52 CHECK THE MEX BITS IN RKCS
4807	T53 TRANSFER FROM DISK TO TTY
4903	T54 CHECK THAT RKBA CAN COUNT CORRECTLY
4963	T55 CHECK FOR RK-05F
4979	T56 END OF PROGRAM

5003	T57	CHECK HARDWARE POLLING LOGIC
5237	END OF	PASS ROUTINE
5283	GT2RG:	ROUTINE FOR GETTING RKCS, RKER
5289	GT3RG:	ROUTINE FOR GETTING RKCS, RKER, RKDS
5297	GT4RG:	ROUTINE FOR GETTING RKCS, RKER, RKDS, RKDA
5315	TYERM:	SPECIAL ERROR MESSAGE ROUTINE
5337	BDA0, BDA4:	BREAK DISK ADDRESS INTO SEC, SUR, CYL, DRIVE
5395	SHFTRT:	SHIFT RIGHT ROUTINE
5415	CHKHE:	CHECK FOR 'ERR'OR
5416	CHKHE1:	CHECK FOR 'ERR'OR
5449	CHKDA:	CHECK IF RKDA INCREMENTED CORRECTLY
5471	CHKWC:	CHECK IF RKWC OVERFLOWED
5485	CHKER:	CHECK RKER CONTENTS
5525	TSTRWS:	WAIT FOR R/W/S RDY ROUTINE
5552	DRESET:	DRIVE RESET ROUTINE
5590	TSTSIN:	CHECK 'SIN' ROUTINE
5619	DELAY:	TIME DELAY ROUTINE
5642	WAT.INT:	WAIT FOR INTERRUPT ROUTINE
5684	CHKCRDY:	CHECK CONTROL READY
5709	CON.RESET:	CONTROL REST ROUTINE
5726	CNT.RDY:	WAIT FOR CONTROL READY ROUTINE
5770	SCOPE HANDLER ROUTINE	
5838	ERROR HANDLER ROUTINE	
5908	ERROR MESSAGE TYPEOUT ROUTINE	
5956	TYPE ROUTINE	
6027	CONVERT BINARY TO DECIMAL AND TYPE ROUTINE	
6095	BINARY TO OCTAL (ASCII) AND TYPE	
6173	TTY INPUT ROUTINE	
6341	TRAP DECODER	
6364	TRAP TABLE	
6401	POWER DOWN AND UP ROUTINES	
6488	ERROR MESSAGES	
6986	ERROR DATA POINTERS	
7009	ERROR HEADERS	

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.TITLE MAINDEC-11-DZRKK-D
.*COPYRIGHT (C) 1974,1976
.*DIGITAL EQUIPMENT CORP.
.*MAYNARD, MASS. 01754
.*
.*PROGRAM BY JIM KAPADIA
.*
.*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
.*PACKAGE (MAINDEC-11-DZQAC-C2), SEPT 14, 1976.
.*
.*PROGRAM REVISED BY TOM SAWYER, MARCH, 1976
.*REVISED BY CHUCK MESS, AUGUST, 1976
.SBTTL OPERATIONAL SWITCH SETTINGS
.*
.*      SWITCH          USE
.*      -----          -
.*      15             HALT ON ERROR
.*      14             LOOP ON TEST
.*      13             INHIBIT ERROR TYPEOUTS
.*      12             CYCLE ON ERROR TO PREVIOUS 'SCOPE' STATEMENT
.*      11             INHIBIT ITERATIONS
.*      10             TESTING ON SIMULATOR
.*      9              LOOP ON ERROR
.*      8              LOOP ON TEST IN SWR<7:0>
.*      6              DROP THE DRIVE IF MORE THAN 5 ERRORS

```

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;*****
;YOU ARE ADVISED TO READ THE DOCUMENT BEFORE USING THIS PROGRAM.
;ON GETTING AN ERROR REFER TO THE LISTINGS AT THE PC POINTED
;OUT IN THE ERROR MESSAGE. ADJACENT ERROR MESSAGES IF FOLLOWED
;CAREFULLY COULD LEAD TO AN EASY PINPOINTING OF THE FAULT

```

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;*****
.SBTTL BASIC DEFINITIONS
.*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
STACK= 1100
.EQUIV EMT,ERROR      ;;BASIC DEFINITION OF ERROR CALL
.EQUIV IOT,SCOPE      ;;BASIC DEFINITION OF SCOPE CALL
.*MISCELLANEOUS DEFINITIONS
HT= 11                ;;CODE FOR HORIZONTAL TAB
LF= 12                ;;CODE FOR LINE FEED

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001100

000011
000012

57	000015	CR=	15	:: CODE FOR CARRIAGE RETURN
58	000200	CRLF=	200	:: CODE FOR CARRIAGE RETURN-LINE FEED
59	177776	PS=	177776	:: PROCESSOR STATUS WORD
60		.EQUIV	PS,PSW	
61	177774	STKLMT=	177774	:: STACK LIMIT REGISTER
62	177772	PIRQ=	177772	:: PROGRAM INTERRUPT REQUEST REGISTER
63	177570	DSWR=	177570	:: HARDWARE SWITCH REGISTER
64	177570	DDISP=	177570	:: HARDWARE DISPLAY REGISTER

::*GENERAL PURPOSE REGISTER DEFINITIONS

67	000000	R0=	%0	:: GENERAL REGISTER
68	000001	R1=	%1	:: GENERAL REGISTER
69	000002	R2=	%2	:: GENERAL REGISTER
70	000003	R3=	%3	:: GENERAL REGISTER
71	000004	R4=	%4	:: GENERAL REGISTER
72	000005	R5=	%5	:: GENERAL REGISTER
73	000006	R6=	%6	:: GENERAL REGISTER
74	000007	R7=	%7	:: GENERAL REGISTER
75	000006	SP=	%6	:: STACK POINTER
76	000007	PC=	%7	:: PROGRAM COUNTER

::*PRIORITY LEVEL DEFINITIONS

79	000000	PR0=	0	:: PRIORITY LEVEL 0
80	000040	PR1=	40	:: PRIORITY LEVEL 1
81	000100	PR2=	100	:: PRIORITY LEVEL 2
82	000140	PR3=	140	:: PRIORITY LEVEL 3
83	000200	PR4=	200	:: PRIORITY LEVEL 4
84	000240	PR5=	240	:: PRIORITY LEVEL 5
85	000300	PR6=	300	:: PRIORITY LEVEL 6
86	000340	PR7=	340	:: PRIORITY LEVEL 7

::*"SWITCH REGISTER" SWITCH DEFINITIONS

89	100000	SW15=	100000	
90	040000	SW14=	40000	
91	020000	SW13=	20000	
92	010000	SW12=	10000	
93	004000	SW11=	4000	
94	002000	SW10=	2000	
95	001000	SW09=	1000	
96	000400	SW08=	400	
97	000200	SW07=	200	
98	000100	SW06=	100	
99	000040	SW05=	40	
100	000020	SW04=	20	
101	000010	SW03=	10	
102	000004	SW02=	4	
103	000002	SW01=	2	
104	000001	SW00=	1	
105		.EQUIV	SW09,SW9	
106		.EQUIV	SW08,SW8	
107		.EQUIV	SW07,SW7	
108		.EQUIV	SW06,SW6	
109		.EQUIV	SW05,SW5	
110		.EQUIV	SW04,SW4	
111		.EQUIV	SW03,SW3	
112		.EQUIV	SW02,SW2	

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113      .EQUIV SW01,SW1
114      .EQUIV SW00,SW0
115
116      ;*DATA BIT DEFINITIONS (BIT00 TO BIT15)
117      100000      BIT15= 100000
118      040000      BIT14= 40000
119      020000      BIT13= 20000
120      010000      BIT12= 10000
121      004000      BIT11= 4000
122      002000      BIT10= 2000
123      001000      BIT09= 1000
124      000400      BIT08= 400
125      000200      BIT07= 200
126      000100      BIT06= 100
127      000040      BIT05= 40
128      000020      BIT04= 20
129      000010      BIT03= 10
130      000004      BIT02= 4
131      000002      BIT01= 2
132      000001      BIT00= 1
133
134      .EQUIV BIT09,BIT9
135      .EQUIV BIT08,BIT8
136      .EQUIV BIT07,BIT7
137      .EQUIV BIT06,BIT6
138      .EQUIV BIT05,BIT5
139      .EQUIV BIT04,BIT4
140      .EQUIV BIT03,BIT3
141      .EQUIV BIT02,BIT2
142      .EQUIV BIT01,BIT1
143      .EQUIV BIT00,BIT0
144
145      ;*BASIC "CPU" TRAP VECTOR ADDRESSES
146      000004      ERRVEC= 4          ;; TIME OUT AND OTHER ERRORS
147      000010      RESVEC= 10         ;; RESERVED AND ILLEGAL INSTRUCTIONS
148      000014      TBITVEC=14         ;; "T" BIT
149      000014      TRTVEC= 14         ;; TRACE TRAP
150      000014      BPTVEC= 14         ;; BREAKPOINT TRAP (BPT)
151      000020      IOTVEC= 20         ;; INPUT/OUTPUT TRAP (IOT) **SCOPE**
152      000024      PWRVEC= 24         ;; POWER FAIL
153      000030      EMTVEC= 30         ;; EMULATOR TRAP (EMT) **ERROR**
154      000034      TRAPVEC=34         ;; "TRAP" TRAP
155      000060      TKVEC= 60          ;; TTY KEYBOARD VECTOR
156      000064      TPVEC= 64         ;; TTY PRINTER VECTOR
157      000240      PIRQVEC=240       ;; PROGRAM INTERRUPT REQUEST VECTOR
158      .SBTTL TRAP CATCHER
159
160      .=0
161      ;*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
162      ;*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
163      ;*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS
164      000174      000000      .=174
165      000176      000000      DISPREG: .WORD 0          ;; SOFTWARE DISPLAY REGISTER
166      .SBTTL STARTING ADDRESS(ES)
167      000200      000137      002636      SWREG: .WORD 0          ;; SOFTWARE SWITCH REGISTER
168      .SBTTL STARTING ADDRESS(ES)
169      JMP 2#START ;; JUMP TO STARTING ADDRESS OF PROGRAM
170      .SBTTL ACT11 HOOKS

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000046 020646
000052 000000
000204

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;*****
;HOOKS REQUIRED BY ACT11
$SVPC=. ;SAVE PC
.=46
$ENDAD ;;1)SET LOC.46 TO ADDRESS OF $ENDAD IN .SEOP
.=52
.WORD 0 ;;2)SET LOC.52 TO ZERO
.=$SVPC ;; RESTORE PC

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001100
001100 000000
001102 000
001103 000
001104 000000
001106 000000
001110 000000
001112 000000
001114 000
001115 001
001116 000000
001120 000000
001122 000000
001124 000000
001126 000000
001130 000000
001132 000000
001134 000
001135 000
001136 000000
001140 177570
001142 177570
001144 177560
001146 177562
001150 177564
001152 177566
001154 000
001155 002
001156 012
001157 000
001160 000000
001162 000000
001164 000000
001166 000000
001170 000000
001172 000000
001174 000000
001176 000000
001200 000000
001202 000000
001204 000000
001206 000000
001210 000000
001212 077
001213 015
001214 000012
001216 005015 051104 053111

.SBTTL COMMON TAGS
:*****
: *THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
: *USED IN THE PROGRAM.
.=1100
\$CMTAG: .WORD 0
\$PASS: .WORD 0
\$STNM: .BYTE 0
\$ERFLG: .BYTE 0
\$ICNT: .WORD 0
\$LPADR: .WORD 0
\$LPERR: .WORD 0
\$ERTTL: .WORD 0
\$ITEMB: .BYTE 0
\$ERMAX: .BYTE 1
\$ERRPC: .WORD 0
\$GDADR: .WORD 0
\$BDADR: .WORD 0
\$GDDAT: .WORD 0
\$BDDAT: .WORD 0
\$AUTOB: .BYTE 0
\$INTAG: .BYTE 0
\$SWR: .WORD DSWR
DISPLAY: .WORD DDISP
\$TKS: 177560
\$TKB: 177562
\$TPS: 177564
\$TPB: 177566
\$NULL: .BYTE 0
\$FILLS: .BYTE 2
\$FILLC: .BYTE 12
\$TPFLG: .BYTE 0
\$REGAD: .WORD 0
\$REG0: .WORD 0
\$REG1: .WORD 0
\$REG2: .WORD 0
\$REG3: .WORD 0
\$REG4: .WORD 0
\$REG5: .WORD 0
\$REG6: .WORD 0
\$REG7: .WORD 0
\$REG10: .WORD 0
\$REG11: .WORD 0
\$TIMES: 0
\$ESCAPE: 0
\$QUES: .ASCII /?/
\$CRLF: .ASCII <15>
\$LF: .ASCIZ <12>
MSG1: .ASCIZ <15><12>/DRIVE PRESNT/

;; START OF COMMON TAGS
;; CONTAINS PASS COUNT
;; CONTAINS THE TEST NUMBER
;; CONTAINS ERROR FLAG
;; CONTAINS SUBTEST ITERATION COUNT
;; CONTAINS SCOPE LOOP ADDRESS
;; CONTAINS SCOPE RETURN FOR ERRORS
;; CONTAINS TOTAL ERRORS DETECTED
;; CONTAINS ITEM CONTROL BYTE
;; CONTAINS MAX. ERRORS PER TEST
;; CONTAINS PC OF LAST ERROR INSTRUCTION
;; CONTAINS ADDRESS OF 'GOOD' DATA
;; CONTAINS ADDRESS OF 'BAD' DATA
;; CONTAINS 'GOOD' DATA
;; CONTAINS 'BAD' DATA
;; RESERVED--NOT TO BE USED
;; AUTOMATIC MODE INDICATOR
;; INTERRUPT MODE INDICATOR
;; ADDRESS OF SWITCH REGISTER
;; ADDRESS OF DISPLAY REGISTER
;; TTY KBD STATUS
;; TTY KBD BUFFER
;; TTY PRINTER STATUS REG. ADDRESS
;; TTY PRINTER BUFFER REG. ADDRESS
;; CONTAINS NULL CHARACTER FOR FILLS
;; CONTAINS # OF FILLER CHARACTERS REQUIRED
;; INSERT FILL CHARS. AFTER A "LINE FEED"
;; "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
;; CONTAINS THE ADDRESS FROM
;; WHICH (\$REG0) WAS OBTAINED
;; CONTAINS ((\$REGAD)+0)
;; CONTAINS ((\$REGAD)+2)
;; CONTAINS ((\$REGAD)+4)
;; CONTAINS ((\$REGAD)+6)
;; CONTAINS ((\$REGAD)+10)
;; CONTAINS ((\$REGAD)+12)
;; CONTAINS ((\$REGAD)+14)
;; CONTAINS ((\$REGAD)+16)
;; CONTAINS ((\$REGAD)+20)
;; CONTAINS ((\$REGAD)+22)
;; MAX. NUMBER OF ITERATIONS
;; ESCAPE ON ERROR ADDRESS
;; QUESTION MARK
;; CARRIAGE RETURN
;; LINE FEED

K02

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234 001224 020105 051120 051505
235 001232 052116 000
236 001236 001236
237 001236 005015 047516 042516 MSG2: .EVEN
238 001244 000 .ASCIZ <15><12>/NONE/
239
240 001245 015 041412 052116 MSG3: .ASCIZ <15><12>/CNT RDY DIDN'T SET/
241 001252 051040 054504 042040
242 001260 042111 023516 020124
243 001266 042523 000124
244
245 001272 005015 051104 053111 MSG4: .ASCIZ <15><12>/DRIVE /
246 001300 020105 000
247
248 001303 015 040412 046114 MSG5: .ASCII <15><12>/ALL DRVS/
249 001310 042040 053122 123
250
251 001315 040 051104 050117 MSG6: .ASCIZ / DROPD/<15><12>
252 001322 006504 000012
253
254 .EVEN
255
256 ;RK11 REGISTERS
257 ;IF FOR ANY REASON THE REGISTER ADDRESSES ARE DIFFERENT FROM THESE
258 ; (GIVEN BELOW), THE CONTENTS OF THE APPROPRIATE POINTERS SHOULD BE
259 ; MODIFIED SO THAT THE CORRECT ADDRESS IS USED.
260 ;
261 001326 177400 RKDS: .EVEN 177400
262 001330 177402 RKER: 177402
263 001332 177404 RKCS: 177404
264 001334 177406 RKWC: 177406
265 001336 177410 RKBA: 177410
266 001340 177412 RKDA: 177412
267 001342 177416 RKDB: 177416
268
269
270 ;TAGS AND GENERAL DATA AREA
271 ;
272 ;
273
274 001344 000000 SIMUL: 0 ;FLAG TO BE SET TO 1 WHEN ON SIMULATOR
275 001346 000000 FTITLE: 0 ;FLAG FOR PRINTING PROGRAM TITLE
276 001350 000000 DRIVAD: 0 ;CONTAINS ADDRESS OF THE DRIVE UNDER TEST
277 001352 000000 DRVDON: 0 ;CONTAINS THE NUMBER OF DRIVES CHECKED.
278 ;IT IS INCREMENTED EACH TIME THE TESTS FOR
279 ; A DRIVE IS COMPLETED.
280 001354 000000 DRVPTR: 0 ;CONTAINS THE POINTER TO THE DRIVE FLAG (DRIVED
281 ; -DRIVE?) OF THE DRIVE TO BE CHECKED NEXT.
282 001356 000000 INDX1: 0 ;GENERAL INDEX FOR KEEPING COUNT
283 001360 000000 INDX2: 0 ;GENERAL INDEX
284 001362 000000 COUNT: 0 ;GENERAL COUNT REGISTER
285 001364 000000 COUNT1: 0 ;COUNT REGISTER USED FOR 'DRESET' SUBROUTINE
286 001366 000000 TIMER: 0 ;TIMER REGISTER
287 001370 000000 EFLG1: 0 ;SET, TO INDICATE A PARTICULAR
288 ;ERROR CONDITION
289

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290	001372	000100	SEEK0:	100	; CONTAINS ADDRESS OF CYLINDER 2
291	001374	001000	SEEK1:	1000	; CONTAINS ADDRESS OF CYLINDER 20
292	001376	014500	SEEK2:	14500	; CONTAINS ADDRESS OF CYLINDER 312
293	001400	000200	RKPRI:	200	; CONTAINS THE CPU LEVEL AT WHICH
294					; RK11 NORMALLY INTERRUPTS. THIS WORD
295					; SHOULD BE CHANGED IF RK11 IS DESINGATED
296					; A BR LEVEL OTHER THAN 5. E.G. IF IT IS CHANGED
297					; TO 6, THIS WORD SHOULD BE CHANGED TO 240.
298	001402	000220	RKVEC:	220	; CONTAINS THE NORMAL VECTOR ADDRESS TO WHICH
299					; RK11 INTERRUPTS. IF THIS IS NOT SO, CHANGE
300					; THIS WORD TO CONTAIN MODIFIED VECTOR ADDRESS.
301	001404	000000	FFLAG:	0	
302	001406	000000	ODDEVN:	0	; USED TO DETERMINE WHICH OF RK-05F DRIVES ACTIVE
303					; 0 IF EVEN DRIVE
304					; -1 IF ODD DRIVE
305	001410	000000	DDPCH:	0	; IF PROGRAM LOADED FROM RK05, CONTAINS
306					; ADDRESS OF DRIVE WITH RKDP PACK
307	001412	000000	DRIVS:	0	; CONTAINS THE NUMBER OF DRIVES PRESENT
308					
309					
310					
311					
312					; THE FLAGS BELOW (BIT 0) ARE SET TO 1 TO INDICATE THAT A PARTICULAR DRIVE
313					; IS PRESENT AND IS TO BE TESTED. BIT 12, IF SET, INDICATES THAT THE DRIVE
314					; WAS DROPPED AFTER MAXIMUM ALLOWABLE NUMBER OF ERRORS OCCURED ON THAT
315					; DRIVE (SW 6 SET).
316					; IF MORE THAN 5 ERRORS OCCUR IN THE HARDWARE POLLING TEST (LAST)
317					; THEN ALL DRIVES ARE DROPPED. BUT BIT 12 IS NOT SET.
318					
319	001414	000000	DRIV0:	0	; FLAG SET TO 1 WHEN DRIVE 0 PRESENT
320	001416	000000	DRIV1:	0	; FOR DRIVE 1
321	001420	000000	DRIV2:	0	; FOR DRIVE 2
322	001422	000000	DRIV3:	0	; FOR DRIVE 3
323	001424	000000	DRIV4:	0	; FOR DRIVE 4
324	001426	000000	DRIV5:	0	; FOR DRIVE 5
325	001430	000000	DRIV6:	0	; FOR DRIVE 6
326	001432	000000	DRIV7:	0	; FOR DRIVE 7
327					
328	001434	000000	T56FLG:	0	
329	001436	000000	PHYDRV:	0	
330	001440	000000	SIZYET:	0	

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.SBTTL ERROR POINTER TABLE

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;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
;*LOCATION $ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
;*NOTE1:      IF $ITEMB IS 0 THE ONLY PERTINENT DATA IS ($ERRPC).
;*NOTE2:      EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

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;*      EM      ::POINTS TO THE ERROR MESSAGE
;*      DH      ::POINTS TO THE DATA HEADER
;*      DT      ::POINTS TO THE DATA
;*      DF      ::POINTS TO THE DATA FORMAT

```

\$ERRTB:

```

;THE ERROR ITEMS TABLE CONSISTS OF ALL THE POSSIBLE ERROR MESSAGES
;USED IN THIS PROGRAM. AN ERROR CALL IN THE PROGRAM CORRESPONDS TO
;THE ITEM NUMBER IN THE ERROR TABLE. THUS 'ERROR 1' IN THE
;PROGRAM CORRESPONDS TO 'ITEM 1' IN THE ERROR TABLE.
;'EM###' IS THE POINTER TO THE ERROR MESSAGE WHICH WILL BE TYPED
;OUT IN CASE THAT ERROR WERE TO OCCUR. THUS FOR 'ERROR 1' THE ERROR
;MESSAGE TYPE OUT WILL BE 'TIME OUT ON RK11 REG'.
;'DH###' IS THE POINTER TO THE HEADER BLOCK WHICH WILL BE TYPED OUT
;IMMEDIATELY AFTER THE ERROR MESSAGE.
;'DT###' SERVES AS A POINTER TO THE MEMORY LOCATIONS WHERE
;THE INFORMATION RELEVANT TO THE ERROR TYPE OUTS (LIKE PC, CONTENTS
;OF RKCS ETC.) WILL BE PICKED UP FROM.
;THE LAST ROW CONTAINING '0' SERVES AS A TERMINATOR.
;EXAMPLE:
;IF ON RUNNING THIS PROGRAM A TIMEOUT WERE TO OCCUR ON ADDRESSING RKDS
;(177400), BECAUSE OF SOME FAULT, THE FOLOWING TYPEOUT WOULD
;OCCUR ON THE TELETYPE.

```

```

      TIME OUT ON RK11 REG
      PC      REG
      ##### 177400

```

```

;NOTE THAT ##### WOULD BE THE ACTUAL PC WHERE 'ERROR 1' IS LOCATED.

```

```

;THE ERROR HANDLER IS LOCATED AT '$ERROR'. THE ERROR CALL IS AN 'EMT'
;INSTRUCTION WITH ITS LOWER BYTE ENCODED TO PROVIDE INDEXING TO THE
;ITEMS IN THE ERROR TABLE.
;THUS 'ERROR 1' IS 104001
;'ERROR 103' IS 104126 ETC.

```

;ERROR ITEMS TABLE

001442

443			; ITEM	11			
444					EM34	;	'SOK' DID NOT SET
445	001542	025447			DH34	;	PC RKDS
446	001544	032111			DT1	;	SERRPC \$REGO
447	001546	031622			0		
448	001550	000000					
449							
450			; ITEM	12			
451					EM35	;	'SEC COUNTR' DIDN'T COUNT TO 0
452	001552	025466			DH35	;	PC SEC-CNTR
453	001554	032127			DT1	;	SERRPC \$REGO
454	001556	031622			0		
455	001560	000000					
456							
457			; ITEM	13			
458					EM36	;	'SEC COUNTR' DIDN'T INCREMENT
459	001562	025521			DH36	;	PC PRSNT-COUNT NXT-COUNT
460	001564	032147			DT2	;	SERRPC \$REGO \$REG1
461	001566	031630			0		
462	001570	000000					
463							
464			; ITEM	14			
465					EM37	;	'SECTOR COUNTER' INCREMENTED WRONG
466	001572	025551			DH4	;	PC EXPCTD RECVD
467	001574	031745			DT2	;	SERRPC \$REGO \$REG1
468	001576	031630			0		
469	001600	000000					
470							
471			; ITEM	15			
472					EM40	;	DIDN'T GET SC=SA FOR THIS SECTOR
473	001602	025605			DH40	;	PC SECTOR RKDS
474	001604	032177			DT2	;	SERRPC \$REGO \$REG1
475	001606	031630			0		
476	001610	000000					
477							
478			; ITEM	16			
479					EM41	;	ERROR-'R/W/S RDY' SHOULD BE SET
480	001612	025645			DH34	;	PC RKDS
481	001614	032111			DT1	;	SERRPC \$REGO
482	001616	031622			0		
483	001620	000000					
484							
485			; ITEM	17			
486					EM13	;	RKBA ERROR
487	001622	025313			DH4	;	PC EXPCT RECVD
488	001624	031745			DT2	;	SERRPC \$REGO \$REG1
489	001626	031630			0		
490	001630	000000					
491							
492			; ITEM	20			
493					EM43	;	UNEXPECTED RK11 INTERRUPT
494	001632	025702			DH21	;	PC
495	001634	032046			DT21	;	SERRPC
496	001636	031654			0		
497	001640	000000					
498							

499			:ITEM 21	
500				
501	001642	025734	EM44	: 'CNTRL RDY' DIDN'T SET AFTER SEEK OR DRIVE RESET
502	001644	032225	DH44	: PC RKCS RKER RKDS RKDA
503	001646	031640	DT20	: SERRPC \$REG0 \$REG1 \$REG2 \$REG3.
504	001650	000000	0	
505				
506			:ITEM 22	
507				
508	001652	026010	EM45	: 'ERR' OR 'HE' SET ON SEEK OR DRIVE RESET
509	001654	032225	DH44	: PC RKCS RKER RKDS RKDA
510	001656	031640	DT20	: SERRPC \$REG0 \$REG1 \$REG2 \$REG3
511	001660	000000	0	
512				
513			:ITEM 23	
514				
515	001662	026056	EM46	: RKER BIT, ON SEEK OR DRIVE RESET
516	001664	032053	DH30	: PC RKCS RKER RKDS
517	001666	031660	DT26	: SERRPC \$REG0 \$REG1 \$REG2
518	001670	000000	0	
519				
520			:ITEM 24	
521				
522	001672	026114	EM47	: RKCS CHANGED AFTER FUNCTION WAS DONE
523	001674	031745	DH4	: PC EXPCT RECVD
524	001676	031630	DT2	: SERRPC \$REG0 \$REG1
525	001700	000000	0	
526				
527			:ITEM 25	
528				
529	001702	026156	EM50	: 'R/W/S RDY' DID NOT CLEAR
530	001704	032053	DH30	: PC RKCS RKER RKDS
531	001706	031660	DT26	: SERRPC \$REG0 \$REG1 \$REG2
532	001710	000000	0	
533				
534			:ITEM 26	
535				
536	001712	026205	EM51	: 'R/W/S RDY' DIDN'T SET AFTER SEEK OR DRIVE RESET
537	001714	032225	DH44	: PC RKCS RKER RKDS RKDA
538	001716	031640	DT20	: SERRPC \$REG0 \$REG1 \$REG2 \$REG3
539	001720	000000	0	
540				
541			:ITEM 27	
542				
543	001722	026260	EM52	: RKDA CHANGED AFTER SEEK
544	001724	031745	DH4	: PC EXPCTD REGVD
545	001726	031630	DT2	: SERRPC \$REG0 \$REG1
546	001730	000000	0	
547				
548			:ITEM 30	
549				
550	001732	026305	EM53	: 'CNTRL RDY' DIDN'T CLEAR AS GO WAS SET
551	001734	032053	DH30	: PC RKCS RKER RKDS
552	001736	031660	DT26	: SERRPC \$REG0 \$REG1 \$REG2
553	001740	000000	0	
554				

555			:ITEM 31		
556				EM54	: 'CNTRL RDY' DIDN'T SET ON DOING WRITE/FMT STARTING
557	001742	026350			: FROM <DSK-ADRES>
558				DH54	: PC RKCS RKER RKDS RKDA
559	001744	032272			: DRV# CYL <DSK-ADRES> SUR SECTR
560				DT54	: \$ERRPC \$REG0 \$REG1 \$REG2 \$REG3
561	001746	031672			: \$REG4 \$REG5 \$REG6 \$REG7
562				0	
563	001750	000000			
564			:ITEM 32		
565				EM55	: 'HE' OR 'ERR' ON WRITE/FMT STARTING FROM
566					: <DSK-ADRES>
567	001752	026442		DH54	: PC RKCS RKER RKDS RKDA
568				DT54	: DRV# CYL <DSK-ADRES> SUR SECTR
569	001754	032272			: \$ERRPC \$REG0 \$REG1 \$REG2 \$REG3
570				0	: \$REG4 \$REG5 \$REG6 \$REG7
571	001756	031672			
572			:ITEM 33		
573	001760	000000		EM56	: RKDA INCREMENTED WRONG ON WRITE OR WRITE FORMAT
574				DH56	: PC EXPCT: DRV# CYL SUR SECTR
575				DT54	: RECVD: DRV# CYL SUR SECTR
576				0	: \$ERRPC \$REG0 \$REG1 \$REG2 \$REG3
577	001762	026521			: \$REG4 \$REG5 \$REG6 \$REG7
578	001764	032401			
579			:ITEM 34		
580	001766	031672		EM57	: RKWC DIDN'T OVERFLOW ON WRITE OR WRITE FORMAT
581				DH5	: PC RECVD
582	001770	000000		DT1	: \$ERRPC \$REG0
583				0	
584			:ITEM 35		
585				EM60	: RKBA INCREMENTED WRONG ON WRITE OR WRITE FORMAT
586	001772	026560		DH4	: PC EXPCT RECVD
587	001774	031773		DT2	: \$ERRPC \$REG0 \$REG1
588	001776	031622		0	
589	002000	000000			
590			:ITEM 36		
591				EM61	: RKER SET, ON WRITE/READ/FORMAT
592				DH30	: PC RKCS RKER RKDS
593	002002	026616		DT26	: \$ERRPC \$REG0 \$REG1 \$REG2
594	002004	031745		0	
595	002006	031630			
596	002010	000000			
597			:ITEM 37		
598				EM62	: RKDB ERROR
599				DH4	: PC EXPCT RECVD
600	002012	026655		DT2	: \$ERRPC \$REG0 \$REG1
601	002014	032053		0	
602	002016	031660			
603	002020	000000			
604					
605					
606					
607	002022	026712			
608	002024	031745			
609	002026	031630			
610	002030	000000			

667	002120	000000	0	
668				
669			;ITEM	47
670				
671	002122	027320	EM72	;WRONG DRIVE ID IN RKDS AFTER SEEK
672	002124	031745	DH4	;PC EXPCT RECVD
673	002126	031630	DT2	;SERRPC \$REG0 \$REG1
674	002130	000000	0	
675				
676			;ITEM	50
677				
678	002132	027362	EM73	;HARDWARE POLL, DRIVE ID BITS(13-15) SHOULD BE CLEAR
679	002134	032111	DH34	;PC RKDS
680	002136	031630	DT2	;SERRPC \$REG0
681	002140	000000	0	
682				
683			;ITEM	51
684				
685	002142	027434	EM74	;HARDWARE POLL, INTERRUPTING DRIVE # NOT PRESENT
686	002144	032630	DH74	;PC DRIVE #
687	002146	031622	DT1	;SERRPC \$REG0
688	002150	000000	0	
689				
690			;ITEM	52
691				
692	002152	027504	EM75	; 'DRIVE #' DID NOT INTERRUPT DURING HARDWARE POLL
693	002154	032630	DH74	;PC DRIVE #
694	002156	031622	DT1	;SERRPC \$REG0
695	002160	000000	0	
696				
697			;ITEM	53
698				
699	002162	027554	EM76	;SCP DID NOT SET AFTER WAS DONE
700	002164	033004	DH117	;PC RKCS
701	002166	031622	DT1	;SERRPC \$REG0
702	002170	000000	0	
703				
704			;ITEM	54
705				
706	002172	027617	EM77	;RKDA CHANGED AFTER 'DRIVE RESET'
707	002174	031745	DH4	;PC EXPCT RECVD
708	002176	031630	DT2	;SERRPC \$REG0 \$REG1
709	002200	000000	0	
710				
711			;ITEM	55
712				
713	002202	027654	EM100	;DATA ERROR AT WORD#
714	002204	032651	DH100	;PC WORD# EXPCT RECVD
715	002206	031660	DT26	;SERRPC \$REG0 \$REG1 \$REG2
716	002210	000000	0	
717				
718			;ITEM	56
719				
720	002212	027677	EM101	;CNTRL RDY DID NOT SET AFTER READ CHECK
721	002214	032225	DH44	;PC RKCS RKER RKDS RKDA
722	002216	031640	DT20	;SERRPC \$REG0 \$REG1 \$REG2 \$REG3

723	002220	000000	0	
724				
725			; ITEM	57
726				
727	002222	027741	EM102	; 'ERR' OF 'HE' SET ON READ CHECK
728	002224	032053	DH30	; PC RKCS RKER RKDS
729	002226	031660	DT26	; SERRPC \$REG0 \$REG1 \$REG2
730	002230	000000	0	
731				
732			; ITEM	60
733				
734	002232	027765	EM103	; 'CSE' ON READ CHECK
735	002234	032706	DH103	; PC RKER
736	002236	031622	DT1	; SERRPC \$REG0
737	002240	000000	0	
738				
739			; ITEM	61
740				
741	002242	030003	EM104	; RKWC DID NOT OVERFLOW ON READ CHECK OR WRITE CHECK
742	002244	032722	DH104	; PC RECVD RKCS
743	002246	031630	DT2	; SERRPC \$REG0 \$REG1
744	002250	000000	0	
745				
746			; ITEM	62
747				
748	002252	030054	EM105	; RKDA INCREMENTED WRONG ON READ CHECK
749	002254	031745	DH4	; PC EXPCT RECVD
750	002256	031630	DT2	; SERRPC \$REG0 \$REG1
751	002260	000000	0	
752				
753			; ITEM	63
754				
755	002262	030112	EM106	; RKBA CHANGED AFTER READ CHECK
756	002264	031745	DH4	; PC EXPCT RECVD
757	002266	031630	DT2	; SERRPC \$REG0 \$REG1
758	002270	000000	0	
759				
760			; ITEM	64
761				
762	002272	030143	EM107	; MEMORY WORD CHANGED AFTER READ CHECK
763	002274	032746	DH107	; PC LOC EXPCT RECVD
764	002276	031660	DT26	; SERRPC \$REG0 \$REG1 \$REG2
765	002300	000000	0	
766				
767			; ITEM	65
768				
769	002302	030204	EM110	; CNTRL RDY DID NOT SET AFTER WRITE CHECK
770	002304	032225	DH44	; PC RKCS RKER RKDS RKDA
771	002306	031640	DT20	; SERRPC \$REG0 \$REG1 \$REG2 \$REG3
772	002310	000000	0	
773				
774			; ITEM	66
775				
776	002312	030247	EM111	; HE OR ERR BIT SET AFTER DOING WRITE CHECK
777	002314	032053	DH30	; PC RKCS RKER RKDS
778	002316	031660	DT26	; SERRPC \$REG0 \$REG1 \$REG2

779	002320	000000	0	
780				
781			;ITEM	67
782				
783	002322	030274	EM112	:WRITE CHECK ERROR
784	002324	032053	DH30	:PC RKCS RKER RKDS
785	002326	031660	DT26	:SERRPC \$REG0 \$REG1 \$REG2
786	002330	000000	0	
787				
788			;ITEM	70
789				
790	002332	030315	EM113	:RKDA INCREMENTED WRONG ON WRITE CHECK
791	002334	031745	DH4	:PC EXPCT RECVD
792	002336	031630	DT2	:SERRPC \$REG0 \$REG1
793	002340	000000	0	
794				
795			;ITEM	71
796				
797	002342	030354	EM114	:RKBA INCREMENTED WRONG ON WRITE CHECK
798	002344	031745	DH4	:PC EXPCT RECVD
799	002346	031630	DT2	:SERRPC \$REG0 \$REG1
800	002350	000000	0	
801				
802			;ITEM	72
803				
804	002352	030413	EM115	:RKBA INCREMENTED WITH IBA SET
805	002354	031745	DH4	:PC EXPCT RECVD
806	002356	031630	DT2	:SERRPC \$REG0 \$REG1
807	002360	000000	0	
808				
809			;ITEM	73
810				
811	002362	030447	EM116	:WRONG MEMORY LOCATION CHANGED WITH IBA SET
812	002364	032651	DH100	:PC WORD# EXPCT RECVD
813	002366	031660	DT26	:SERRPC \$REG0 \$REG1 \$REG2
814	002370	000000	0	
815				
816			;ITEM	74
817				
818	002372	030522	EM117	:RK11 DID NOT INTERRUPT WHEN IDE WAS SET
819	002374	033004	DH117	:PC RKCS
820	002376	031622	DT1	:SERRPC \$REG0
821	002400	000000	0	
822				
823			;ITEM	75
824				
825	002402	030567	EM120	:RK11 DID NOT INTERRUPT AFTER SEEK WAS INITIATED
826	002404	033004	DH117	:PC RKCS
827	002406	031622	DT1	:SERRPC \$REG0
828	002410	000000	0	
829				
830			;ITEM	76
831				
832	002412	030642	EM121	:SCP SET BEFORE SEEK COMPLETED
833	002414	033004	DH117	:PC RKCS
834	002416	031622	DT1	:SERRPC \$REG0

835	002420	000000	0	
836				
837			; ITEM	77
838				
839	002422	030700	EM122	; RK11 DID NOT INTERRUPT AFTER SEEK COMPLETED
840	002424	032053	DH30	; PC RKCS RKER RKDS
841	002426	031660	DT26	; SERRPC \$REG0 \$REG1 \$REG2
842	002430	000000	0	
843				
844			; ITEM	100
845				
846	002432	030747	EM123	; CNTRL RESET DID NOT CLEAR 'SCP' BIT
847	002434	033004	DH117	; PC RKCS
848	002436	031622	DT1	; SERRPC \$REG0
849	002440	000000	0	
850				
851			; ITEM	101
852				
853	002442	031006	EM124	; RK11 DID NOT INTERRUPT AFTER READ WAS DONE
854	002444	033004	DH117	; PC RKCS
855	002446	031622	DT1	; SERRPC \$REG0
856	002450	000000	0	
857				
858			; ITEM	102
859				
860	002452	031050	EM125	; CNTRL RESET DID NOT CLEAR REGISTER
861	002454	031716	DH2	; PC REGADD RECVD
862	002456	031630	DT2	; SERRPC \$REG0 \$REG1
863	002460	000000	0	
864				
865			; ITEM	103
866				
867	002462	031107	EM126	; RK11 DID NOT INTERRUPT AT CPU LEVEL
868	002464	033020	DH126	; PC LEVEL RKCS
869	002466	031630	DT2	; SERRPC \$REG0 \$REG1
870	002470	000000	0	
871				
872			; ITEM	104
873				
874	002472	031150	EM127	; RK11 INTERRUPTED AT WRONG CPU LEVEL
875	002474	033020	DH126	; PC LEVEL RKCS
876	002476	031630	DT2	; SERRPC \$REG0 \$REG1
877	002500	000000	0	
878				
879			; ITEM	105
880				
881	002502	031212	EM130	; 'ERR BIT' DID NOT SET IN RKER
882	002504	033046	DH130	; PC RKCS RKER ERR BIT
883	002506	031660	DT26	; SERRPC \$REG0 \$REG1 \$REG2
884	002510	000000	0	
885				
886				
887			; ITEM	106
888				
889	002512	031247	EM131	; HE OR ERR DID NOT SET
890	002514	033105	DH131	; PC RKCS RKER

891	002516	031630	DT2	;	\$ERRPC	\$REGO	\$REG1					
892	002520	000000	0									
893												
894			:ITEM	107								
895												
896	002522	031274	EM132	;	RKER	ERROR						
897	002524	031745	DH4	;	PC	EXPCT	RECVD					
898	002526	031630	DT2	;	\$ERRPC	\$REGO	\$REG1					
899	002530	000000	0									
900												
901			:ITEM	110								
902												
903	002532	031306	EM133	;	NXC	BIT	DID	NOT	SET			
904	002534	033133	DH133	;	PC	RKCS	RKER	RKDA				
905	002536	031660	DT26	;	PC	\$REGO	\$REG1	\$REG2				
906	002540	000000	0									
907												
908			:ITEM	111								
909												
910	002542	031331	EM134	;	RK11	DIDN'T	INTERRUPT	ON	SOFT	ERROR		
911	002544	033105	DH131	;	PC	RKCS	RKER					
912	002546	031630	DT2	;	\$ERRPC	\$REGO	\$REG1					
913	002550	000000	0									
914												
915			:ITEM	112								
916												
917	002552	031372	EM135	;	MEX	BITS	INCREMENTED	WRONG	IN	RKCS		
918	002554	031745	DH4	;	PC	EXPCTD	RECVD					
919	002556	031630	DT2	;	\$ERRPC	\$REGO	\$REG1					
920	002560	000000	0									
921												
922			:ITEM	113								
923												
924	002562	030204	EM110	;	CNTRL	RDY	DID	NOT	SET	AFTER	WRT	CHK
925	002564	032011	DH14	;	PC	RKCS	RKER	RKWC				
926	002566	031660	DT26	;	\$ERRPC	\$REGO	\$REG1	\$REG2				
927	002570	000000	0									
928												
929			:ITEM	114								
930												
931	002572	031427	EM137	;	'WPS'	NOT	CLEAR					
932	002574	032225	DH44	;	PC	RKCS	RKER	RKDS	RKDA			
933	002576	031640	DT20	;	\$ERRPC	\$REGO	\$REG1	\$REG2	\$REG3			
934	002600	000000	0									
935												
936			:ITEM	115								
937												
938	002602	031445	EM140	;	DATA	ERROR	ON	TRANSFER	FROM	DISK	TO	TTY
939	002604	033171	DH140	;	PC	EXPCT	RECVD	RKBA	RKCS			
940	002606	031640	DT20	;	\$ERRPC	\$REGO	\$REG1	\$REG2	\$REG3			
941	002610	000000	0									
942												
943												
944			:ITEM	116								
945												
946	002612	031514	EM141	;	'DRIVE	#'	PRESENT,	BUT	NOT	SPECIFIED		

K03

SEQ 0036

947	002614	032630	DH74	:PC	DRIVE #	
948	002616	031622	DT1	;\$ERRPC	\$REGO	
949	002620	000000	0			
950						
951			;ITEM	117		
952						
953	002622	025266	EM11	;\$RWC	ERROR	
954	002624	031745	DH4	:PC	EXPCT	RECVD
955	002626	031630	DT2	;\$ERRPC	\$REGO	\$REG1
956	002630	000000	0			
957			;ITEM	120		
958	002632	031560	EM142			
959	002634	000000	0			
960						
961						
962						


```

963 002636 000005 START: RESET ;CLEAR THE BUS
964 .SBTTL INITIALIZE THE COMMON TAGS
965 ;;CLEAR THE COMMON TAGS ($CMTAG) AREA
966 002640 012706 001100 MOV $CMTAG,R6 ;FIRST LOCATION TO BE CLEARED
967 002644 005026 CLR (R6)+ ;CLEAR MEMORY LOCATION
968 002646 022706 001140 CMP $SWR,R6 ;;DONE?
969 002652 001374 BNE -6 ;LOOP BACK IF NO
970 002654 012706 001100 MOV $STACK,SP ;SETUP THE STACK POINTER
971 ;;INITIALIZE A FEW VECTORS
972 002660 012737 022046 000020 MOV $$SCOPE,@#IOTVEC ;IOT VECTOR FOR SCOPE ROUTINE
973 002666 012737 000340 000022 MOV #340,@#IOTVEC+2 ;LEVEL 7
974 002674 012737 022320 000030 MOV $ERROR,@#EMTVEC ;EMT VECTOR FOR ERROR ROUTINE
975 002702 012737 000340 000032 MOV #340,@#EMTVEC+2 ;LEVEL 7
976 002710 012737 024574 000034 MOV $STRAP,@#TRAPVEC ;TRAP VECTOR FOR TRAP CALLS
977 002716 012737 000340 000036 MOV #340,@#TRAPVEC+2 ;LEVEL 7
978 002724 012737 024674 000024 MOV $SPWRDN,@#PWRVEC ;POWER FAILURE VECTOR
979 002732 012737 000340 000026 MOV #340,@#PWRVEC+2 ;LEVEL 7
980 002740 005037 001206 CLR $TIMES ;INITIALIZE NUMBER OF ITERATIONS
981 002744 005037 001210 CLR $ESCAPE ;CLEAR THE ESCAPE ON ERROR ADDRESS
982 002750 112737 000001 001115 MOVB #1,$ERMAX ;ALLOW ONE ERROR PER TEST
983 002756 012737 002756 001106 MOV #,$SLPADR ;INITIALIZE THE LOOP ADDRESS FOR SCOPE
984 002764 012737 002764 001110 MOV #,$SLPERR ;SETUP THE ERROR LOOP ADDRESS
985 ;;SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
986 ;;EQUAL TO A "-1" SETUP FOR A SOFTWARE SWITCH REGISTER.
987 002772 013746 000004 MOV @#ERRVEC,-(SP) ;SAVE ERROR VECTOR
988 002776 012737 003032 000004 MOV #64$,@#ERRVEC ;SET UP ERROR VECTOR
989 003004 012737 177570 001140 MOV $DSWR,$SWR ;SETUP FOR A HARDWARE SWICH REGISTER
990 003012 012737 177570 001142 MOV $DDISP,$DISPLAY ;AND A HARDWARE DISPLAY REGISTER
991 003020 022777 177777 176112 CMP #-1,@$SWR ;TRY TO REFERENCE HARDWARE SWR
992 003026 001012 BNE 66$ ;BRANCH IF NO TIMEOUT TRAP OCCURRED
993 ;AND THE HARDWARE SWR IS NOT = -1
994 003030 000403 BR 65$ ;BRANCH IF NO TIMEOUT
995 003032 012716 003040 64$: MOV #65$,(SP) ;SET UP FOR TRAP RETURN
996 003036 000002 RTI
997 003040 012737 000176 001140 65$: MOV $SWREG,$SWR ;POINT TO SOFTWARE SWR
998 003046 012737 000174 001142 MOV $DISPREG,$DISPLAY
999 003054 012637 000004 66$: MOV (SP)+,@#ERRVEC ;RESTORE ERROR VECTOR
1000
1001 .SBTTL TYPE PROGRAM NAME
1002 ;;TYPE THE NAME OF THE PROGRAM IF FIRST PASS
1003 003060 005227 177777 INC #-1 ;FIRST TIME?
1004 003064 001044 BNE 67$ ;BRANCH IF NO
1005 003066 104401 003124 TYPE 68$ ;TYPE ASCIZ STRING
1006 .SBTTL GET VALUE FOR SOFTWARE SWITCH REGISTER
1007 003072 005737 000042 TST @#42 ;ARE WE RUNNING UNDER XXDP/ACT?
1008 003076 001006 BNE 69$ ;BRANCH IF YES
1009 003100 023727 001140 000176 CMP $SWR,$SWREG ;SOFTWARE SWITCH REG SELECTED?
1010 003106 001005 BNE 70$ ;BRANCH IF NO
1011 003110 104406 GTSWR ;GET SOFT-SWR SETTINGS
1012 003112 000403 BR 70$
1013 003114 112737 000001 001134 69$: MOVB #1,$AUTOB ;SET AUTO-MODE INDICATOR
1014 003122 70$:
1015 003122 000425 BR 67$ ;GET OVER THE ASCIZ
1016 ;;68$: .ASCIZ <CRLF>/RK11 LOGIC TEST II/<15><12>/MAINDEC-11-DZRKK-D/<CRLF>
1017 67$:
1018 003176 012700 001410 MOV $DDPCH,R0

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M03

MAINDEC-11-DZRKK-D MACY11 27(1006) 04-OCT-76 16:06 PAGE 21
 DZRKKD.P11 22-SEP-76 08:47 GET VALUE FOR SOFTWARE SWITCH REGISTER

SEQ 0038

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1019 003202 012701 177765          MOV    #-13,R1
1020 003206 005020          1$:   CLR    (R0)+
1021 003210 005201          INC    R1
1022 003212 001375          BNE    1$
1023 003214 005227 177777          INC    #-1          ;FIRST START ?
1024 003220 001020          BNE    START1       ;BR IF NOT
1025 003222 013746 000004          MOV    ERRVEC, -(SP) ;SAVE ERROR VECTOR ADDRESS
1026 003226 012737 003242 000004          MOV    #2$,ERRVEC   ;NEW VECTOR ADDRESS
1027 003234 005737 177776          TST    PS           ;SEE IF PROGRAM CAN REFERENCE THE
1028                                ;PROCESSOR STATUS WORD
1029 003240 000406          BR     3$           ;BR IF REFERENCE DIDN'T CAUSE TRAP
1030 003242 012737 000140 001400 2$:   MOV    #140,RKPRI   ;SETUP INTERRUPTING PRIORITY TO VALUE
1031                                ;WHICH WILL ALLOW INTERRUPT ON AN LSI-11
1032 003250 012716 003256          MOV    #3$, (SP)   ;SETUP RETURN ADDRESS
1033 003254 000002          RTI                    ;RETURN
1034 003256 012637 000004          3$:   MOV    (SP)+,ERRVEC ;RESTORE THE ERROR VECTOR
1035
1036                                ;FIND OUT IF ACT11, 'XXDP' CHAIN OR DUMP MODE
1037
1038 003262 012700 001410          START1: MOV   #DDPCH,R0
1039 003266 012701 177766          MOV   #-12,R1      ;CLEAR OUT DRIVE TABLE AREA
1040 003272 005020          1$:   CLR    (R0)+
1041 003274 005201          INC    R1
1042 003276 001375          BNE    1$
1043 003300 122737 000002 000041          CMPB  #2,41        ;LOADED FROM AN RK05 ?
1044 003306 001166          BNE    ST2          ;BR IF NOT
1045 003310 013737 000040 001410          MOV   40,DDPCH     ;GET DEVICE INDICATOR AND DRIVE ADDRESS OF
1046                                ;LOADING RK05
1047 003316 122737 000010 001410          CMPB  #10,DDPCH    ;VALID DRIVE NUMBER IN BYTE 40 ?
1048 003324 101002          BHI   2$           ;BR IF YES
1049 003326 105037 001410          CLRB  DDPCH        ;MUST BE DRIVE ZERO WHICH LOADED
1050                                ;THIS PROGRAM
1051 003332 005737 000042          2$:   TST   42          ;CHAIN MODE OR ACT11 AUTO ACCEPT ?
1052 003336 001432          BEQ   4$           ;BR IF NEITHER
1053 003340 005737 001410          TST   DDPCH        ;RUNNING FROM AN RK05 ?
1054 003344 001002          BNE   3$           ;BR IF YES
1055 003346 000137 004210          JMP   ST3          ;FIND OUT NUMBER OF DRIVES
1056 003352          3$:
1057 003352 104401 003360          TYPE  65$         ;; TYPE ASCIZ STRING
1058 003356 000413          BR    64$         ;; GET OVER THE ASCIZ
1059                                ;; 55$: .ASCIZ <15><12>/NOT TESTING DRIVE /
1060 003406          64$:
1061 003406 005046          CLR   -(SP)        ;CLEAR WORD ON STACK
1062 003410 113716 001410          MOVB  DDPCH, (SP) ;GET DRIVE ADDRESS
1063 003414 104403          TYPOS ;TYPE THE ADDRESS
1064 003416 001          .BYTE 1           ;ONLY 1 CHARACTER
1065 003417 000          .BYTE 0           ;SUPPRESS LEADING ZEROS
1066 003420 000137 004210          JMP   ST3          ;GET NUMBER OF DRIVES
1067 003424 005227 177777          4$:   INC   #-1        ;FIRST TIME THROUGH HERE ?
1068 003430 001115          BNE   ST2          ;BR IF NOT
1069 003432 104401 003440          TYPE  67$         ;; TYPE ASCIZ STRING
1070 003436 000411          BR    66$         ;; GET OVER THE ASCIZ
1071                                ;; 67$: .ASCIZ <15><12>/TO TEST DRIVE /
1072 003462          66$:
1073 003462 005046          CLR   -(SP)        ;CLEAR WORD ON THE STACK
1074 003464 113716 001410          MOVB  DDPCH, (SP) ;GET DRIVE ADDRESS
  
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N03

MAINDEC-11-DZRKK-D
DZRKKD.P11 22-SEP-76

MACY11 27(1006)
08:47

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GET VALUE FOR SOFTWARE SWITCH REGISTER

SEQ 0039

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1075 003470 104403          TYPOS          ;TYPE THE DRIVE ADDRESS
1076 003472      001        .BYTE 1          ;ONLY 1 CHARACTER
1077 003473      000        .BYTE 0          ;SUPPRESS LEADING ZEROS
1078 003474 104401 003502    TYPE ,69$      ;TYPE ASCIZ STRING
1079 003500 000431          BR 68$        ;GET OVER THE ASCIZ
1080          ;:69$: .ASCIZ / HALT PROGRAM, REMOVE RKDP PACK AND REPLACE IT/<15><12>
1081 003564          ;68$:
1082 003564 104401 003572    TYPE ,71$      ;TYPE ASCIZ STRING
1083 003570 000435          BR 70$        ;GET OVER THE ASCIZ
1084          ;:71$: .ASCIZ /WITH A WORK PACK, CLEAR LOCATION 40, AND RESTART PROGRAM/
1085 003664          ;70$:
1086
1087          ;FIND OUT FROM USER WHICH DRIVES (LOGICAL ADDRESSES) ARE TO BE
1088          ;TESTED (DRIVES TO BE TESTED ?). IN REPLY THE USER SHOULD TYPE IN THE
1089          ;LOGICAL ADDRESSES SEPERATED BY COMMAS. THUS IF 2 DRIVES 0,1 ARE PRESENT:
1090          ; 'DRIVS TO B TSTD?'
1091          ; '0,1<CR>' A CAR. RET. SHOULD BE TYPED TO TERMINATE THE LIST.
1092 003664 012700 001412    ST2: MOV #DRIVS,R0
1093 003670 012701 177767    MOV #-11,R1
1094 003674 005020          13$: CLR (R0)+
1095 003676 005201          INC R1
1096 003700 001375          BNE 13$
1097 003702 104401 003710    TYPE ,65$      ;TYPE ASCIZ STRING
1098 003706 000415          BR 64$        ;GET OVER THE ASCIZ
1099          ;:65$: .ASCIZ <15><12>/DRIVES TO BE TESTED ?/<15><12>
1100          ;64$:
1101 003742 104411          RDLIN
1102 003744 012600          MOV (SP)+,R0 ;GET STARTING ADRES OF ASCII STRING
1103 003746 012701 177770    MOV #-10,R1 ;SET UP COUNT
1104 003752 112002          1$: MOVB (R0)+,R2 ;GET ASCII CHARACTER
1105 003754 042702 177400    BIC #177400,R2 ;MASK UNWANTED BITS
1106 003760 012703 001414    MOV #DRIVO,R3
1107 003764 012704 177770    MOV #-10,R4
1108 003770 012705 000060    MOV #60,R5
1109 003774 020502          2$: CMP R5,R2 ;WAS THE TYPED IN CHARACTER
1110          ;A NUMBER BETWEEN 0-7?
1111 003776 001414          BEQ 3$        ;YES, BRANCH
1112 004000 005205          INC R5        ;NO, INCREMENT
1113 004002 005723          TST (R3)+    ;INCREMENT POINTER TO DRV FLAG
1114 004004 005204          INC R4        ;CHARACTER THAT WAS INPUT
1115 004006 001372          BNE 2$        ;SHOULD BE 0-7, IF ANY OTHER
1116          ;TYPE ?? & AGAIN ASK FOR
1117          ;DRIVS TO BE TSTD?
1118 004010 005702          TST R2        ;IS IT A TERMINATOR?
1119 004012 001461          BEQ 6$        ;YES, EXIT. NO DRIVES INDICATED.
1120          ;4$:
1121 004014 104401 004022    TYPE ,67$      ;TYPE ASCIZ STRING
1122 004020 000402          BR 66$        ;GET OVER THE ASCIZ
1123          ;:67$: .ASCIZ /??/
1124          ;66$:
1125 004026 000716          BR ST2        ;GO, AGAIN ASK QUESTION
1126 004030 005713          3$: TST R3 ;SEE IF ALL READY SELECTED
1127 004032 001370          BNE 4$        ;ERROR IF SELECTED ALL READY
1128 004034 005213          INC R3        ;SET UP FLAG FOR THE DRIVE
1129 004036 005237 001412    INC DRIVS    ;INCREMENT TOTAL NO OF DRIVES PRESENT
1130 004042 111002          11$: MOVB R0,R2 ;GET NEXT CHAR

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1131	004044	042702	177400		BIC	#177400,R2	: CHARACTER ONLY	
1132	004050	022702	000106		CMP	#F,R2	: IS IT F?	
1133	004054	001026			BNE	8\$: NO, GO ON	
1134	004056	052713	100000		BIS	#BIT15,DR3	: SET BIT 15 TO SHOW RK0SF	
1135	004062	032705	000001		BIT	#BIT0,R5	: EVEN DRIVE?	
1136	004066	001407			BEQ	9\$: EVEN DRIVE SO BRANCH	
1137	004070	005763	177776		TST	-2(R3)	: CHECK EVEN DRIVE	
1138	004074	001347			BNE	4\$: EVEN ALL READY SELECTED	
1139	004076	012763	100001	177776	MOV	#BIT15!BIT0,-2(R3)	: SELECT EVEN DRIVE	
1140	004104	000406			BR	10\$: CONTINUE	
1141	004106	005763	000002	9\$:	TST	2(R3)	: CHECK ODD DRIVE	
1142	004112	001340			BNE	4\$: ERROR IF SELECTED BEFORE	
1143	004114	012763	100001	000002	MOV	#BIT15!BIT0,2(R3)	: SELECT ODD DRIVE	
1144	004122	005237	001412	10\$:	INC	DRIVS	: COUNT DRIVES SELECTED	
1145	004126	105720			TSTB	(R0)+	: POINT TO NEXT CHAR	
1146	004130	000744			BR	11\$: CHECK FOR COMMA	
1147	004132	022702	000054	8\$:	CMP	#54,R2	: IS IT A 'COMMA'?	
1148	004136	001403			BEQ	5\$: YES, GO PROCESS NXT WORD	
1149	004140	005702			TST	R2	: NO, IS IT A TERMINATOR?	
1150	004142	001324			BNE	4\$: IF NOT, SOMETHING WRONG	
1151							: GO ASK QUESTION AGAIN	
1152	004144	000404			BR	6\$: EXIT, IF A TERMINATOR	
1153	004146	105720		5\$:	TSTB	(R0)+	: INCREMENT PTR TO NXT BYTE	
1154							: IN INPUT BUFFER	
1155	004150	005201			INC	R1	: THERE SHOULD BE NO MORE THAN	
1156	004152	001277			BNE	1\$: 8 DRIVES, HENCE IF MORE	
1157	004154	000717			BR	4\$: THAN 8 DIFFERENT NOS. TYPED IN, ERROR!	
1158							: GO AGAIN ASK THE QUESTION	
1159								
1160	004156	005037	001440	6\$:	CLR	SIZYET	: NO SIZING NEEDED	
1161	004162	032777	002000	174750	BIT	#SW10,DSWR	: TESTING ON SIMULATOR?	
1162	004170	001003			BNE	7\$: YES, BRANCH	
1163	004172	005037	001344		CLR	SIMUL	: NO, CLR FLAG	
1164	004176	000502			BR	5T4		
1165								
1166	004200	012737	000001	001344	7\$:	MOV	#1,SIMUL	: SET FLAG TO INDICATE SIMULATOR
1167	004206	000476			BR	5T4		
1168								
1169								
1170								
1171							: CHECK NUMBER OF DRIVES	
1172	004210	012737	177777	001440	5T3:	MOV	#-1,SIZYET	: CHECK FOR RK0SF LATER
1173	004216	012737	004370	000004	MOV	#5\$,D#4	: SET UP ADRES FOR TIME-OUT VECTOR	
1174	004224	005777	175076		TST	DRKDS	: REFERENCE RKDS	
1175	004230	005777	175104		TST	DRKDA	: REFERENCE RKDA	
1176	004234	012737	004462	000004	MOV	#BADTMO,D#4		
1177	004242	104401			TYPE			
1178	004244	001216			MSG1			
1179	004246	012700	177770		MOV	#-10,R0	: INITIALIZE COUNT FOR THE 8 DRIVES	
1180	004252	005037	001412		CLR	DRIVS	: INITIALIZE # OF DRIVES PRESENT TO 0	
1181	004256	005001			CLR	R1	: INITIALIZE ADDRESS TO DRIVE 0	
1182	004260	005004			CLR	R4		
1183	004262	012702	001414		MOV	#DRIVO,R2		
1184	004266	010177	175046	1\$:	MOV	R1,DRKDA	: ADDRESS THE DRIVE	
1185	004272	020177	175042		CMP	R1,DRKDA	: CHECK, WAS IT ADDRESSED?	
1186	004276	001405			BEQ	3\$: YES	


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1243 004460 000465          BR      TST1          ;GO TO TEST 1
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1250 004462 011600          BADTMO: MOV     (SP),RO ;SAVE PC WHERE TIME OUT OCCURED
1251 004464 005740          TST     -(RO)
1252 004466 022626          CMP     (SP)+,(SP)+ ;RESTORE STACK POINTER
1253 004470 104401 004476          TYPE   65$          ;:TYPE ASCIZ STRING
1254 004474 000407          BR      64$          ;:GET OVER THE ASCIZ
1255
1256 004514          ;:65$: .ASCIZ <15><12>/TIMEOUT,PC=/
1257 004514 010046          ;:64$: MOV     RO,-(SP) ;SET UP FOR TYPING OUT PC
1258 004516 104402          TYPOC   ;GO TYPE OUT OCTAL PC
1259 004520 000000          HALT
1260 004522 000137 002636          JMP     2#START
1261
1262
1263
1264          ;THIS ROUTINE HANDLES UNEXPECTED INTERRUPTS FROM RK11
1265          ;SW 9 AND 10 FOR LOOPING ON ERROR
1266          ;AND LOOPING ON TEST IN WHICH TIMEOUT
1267          ;OCCURRED, ARE PROVIDED.
1268
1269 004526 011600          BADINT: MOV     (SP),RO ;SAVE PC WHERE INTERRUPT OCCURED
1270 004530 005740          TST     -(RO)
1271 004532 032777 020000 174400          BIT     #20000,2$SWR ;INHIBIT ERROR TYPEOUT?
1272 004540 001014          BNE     1$          ;YES, DON'T TYPE OUT
1273 004542 104401          TYPE
1274 004544 001213          $CRLF
1275 004546 104401          TYPE
1276 004550 025702          EM43          ;TYPE 'UNEXPEXED RK11 INTERRUPT'
1277          ;TYPE ' AT PC='
1278 004552 104401 004560          TYPE   65$          ;:TYPE ASCIZ STRING
1279 004556 000403          BR      64$          ;:GET OVER THE ASCIZ
1280
1281          ;:65$: .ASCIZ /,PC=/
1282          ;:64$: MOV     RO,-(SP) ;SET UP FOR TYPING OUT PC
1283 004566 010046          TYPOC   ;GO TYPE OCTAL PC WHERE BAD
1284 004570 104402          ;INTERUPT OCCURED
1285 004572 032777 001000 174340 1$: BIT     #1000,2$SWR ;LOOP ON ERROR?
1286 004600 001403          BEQ     2$          ;NO, BRANCH
1287 004602 022626          CMP     (SP)+,(SP)+ ;YES, REPOSITION STACK
1288 004604 000177 174276          JMP     2$LPADR    ;GO TO THE STARTING ADDRESS OF
1289          ;THE TEST THAT GAVE UNEXPECTED INTERRUPT
1290 004610 032777 040000 174322 2$: BIT     #40000,2$SWR ;LOOP ON TEST?
1291 004616 001401          BEQ     3$          ;NO, BRANCH
1292 004620 000002          RTI
1293 004622 000000          ;:3$: HALT          ;YES, LOOP. GO BACK WHER U INTERRUPTED FROM.
1294          ;UNEXPEXED INTERRUPT OCCURED AS
1295          ;INDICATED IN THE TYPE OUT.U CAN LOOP
1296          ;ON ERROR, TEST OR INHIBIT TYPEOUT BY
1297 004624 000137 002636          JMP     2#START    ;SETTING APPROPRIATE SWITCHES.
1298          ;GO BACK TO THE START OF THE
1299          ;PROGRAM. THUS PRESSING CONTINUE
    
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:AFTER THE ABOVE HALT WILL
:RESTART THE PROGRAM

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:RESTART AFTER POWER FAIL
:THE PROGRAM WOULD RESTART HERE IF POWER CAME BACK AFTER A FALIURE.

004630 004737 021650 PFSTRT: JSR PC,WATIME ;KILL TIME

:TEST 1 CHECK THAT THE DRIVES THAT ARE NOT SPECIFIED ARE NOT FOUND TO BE PRESENT
: *THIS TEST CHECKS THAT THE DRIVES THAT ARE NOT SPECIFIED
: *(IN RESPONSE TO "DRIVS TO BE TSTD?") ARE NOT FOUND TO BE PRESENT.
: *EVERY DRIVE FROM 0 TO 7 IS ADDRESSED. IF A PARTICULAR DRIVE
: *GIVES 'DRY' (IN RKDS), IT IS CHECKED THAT THIS DRIVE
: *WAS SPECIFIED BY THE USER, IF IT WAS NOT AN ERROR IS
: *REPORTED, GIVING THE DRIVE NUMBER. IT IS LIKELY THAT THE USER
: *MAY HAVE FORGOTTEN TO PUT THE DRIVE (THAT IS NOT SPECIFIED) ON
: *'LOAD'. IF THIS IS THE CASE THEN PUT THIS DRIVE ON 'LOAD'.
: *IF THIS IS NOT THE CASE, THERE IS A GENUINE ERROR. (TWO DIFFERENT
: *DRIVE ADDRESSES MAY BE RESULTING IN THE SELECTION OF THE SAME
: *PHYSICAL DRIVE.)

TST1: SCOPE

004634 000004
004636 012700 001414
004642 005001
004644 005002
004646 005737 001410
004652 001403
004654 120237 001410
004660 001435
004662 010177 174452
004666 105777 174434
004672 100005
004674 005710
004676 001026

MOV #DRIVO,R0 ;INITIALIZE POINTER
CLR R1 ;INITIALIZE DRIVE ADRES 0
CLR R2 ;INITIALIZE DRIVE # 0
15: TST DDPCH ;LOADED FROM AN RKDS ?
BEQ 25 ;B IF NOT
CMPB R2,DDPCH ;LOADED FROM THIS DRIVE ?
BEQ 45 ;BR IF YES
25: MOV R1,DRKDA ;ADRES THE DRIVE
TSTB DRKDS ;DRIVE READY?
BPL 35 ;NO, THIS DRIVE NOT PRESENT
;YES, THIS DRIVE SELECTED
TST DR0 ;WAS THIS DRIVE SPECIFIED BY
;THE USER?
BNE 45 ;YES, OK
;NO, THIS DRIVE # WAS NOT SPECIFIED
;BY THE USER, BUT STILL IS GIVING
;'DRY' WHEN ADRESED. REPORT ERROR.
MOV R2,\$REGO ;GET DRIVE #
ERROR 116 ;THIS DRIVE # WAS NOT SPECIFIED BY
;THE USER, BUT WHEN ADRESED GAVE
;'DRY'. CHECK THAT THIS DRIVE # IF
;PHYSICALLY PRESENT IS ON 'LOAD'. IF
;THIS IS NOT THE CASE, THEN ONE DRIVE
;MAY BE GETTING SELECTED BY TWO DIFFERENT
;LOGICAL ADDRESSES.
35: TST DR0 ;CHECK THAT THIS DRIVE WAS NOT INDICATED
BEQ 45 ;IF IT WAS, & IT IS NOT FOUND TO BE
;PRESENT (DRY CLEAR), REPORT ERROR.

F04

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1355 004712 004737 020702 JSR PC,GT4RG ;GET RKCS, ER, DS, DA
1356 004716 104010 ERROR 10 ;DRIVE # (AS IN RKDA) WAS INDICATED BY
1357 ;THE USER, BUT WAS NOT FOUND TO BE PRESENT.
1358 ;CHECK THAT THE ROTARY DRIVE SELECTION
1359 ;SWITCH ON THE MODULE IS SET TO THE RIGHT
1360 ;DRIVE #.
1361
1362 004720 005010 CLR JRD ;THIS DRIVE IS NOT FOUND TO BE PRESENT
1363 ;HENCE DROP IT FROM THE SELECTION TABLE.
1364 004722 010003 MOV R0,R3 ;DRIVE ADDR
1365 004724 162703 001414 SUB #DRIV0,R3 ;MINUS OFFSET FOR TABLE
1366 004730 042703 000003 BIC #3,R3 ;EVEN DRIVE OF PAIR
1367 004734 062703 001414 ADD #DRIV0,R3 ;POINT TO EVEN OF PAIR IF RK05 F
1368 004740 042723 100000 BIC #100000,(R3)+ ;NOT SPECIFIED AS F MODEL
1369 004744 042713 100000 BIC #100000,(R3) ;SAME
1370 004750 005337 001412 DEC DRIVS ;DECREMENT DRIVE COUNT
1371 004754 005202 4S: INC R2 ;INCRMNT DRIVE #
1372 004756 005720 TST (R0)+ ;INCRMNT POINTER
1373 004760 062701 020000 ADD #20000,R1 ;INCRMNT ADRES TO NXT DRIVE
1374 004764 001330 BNE 1S ;LUP BAK IF NOT DONE

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;THIS PART OF THE PROGRAM IS GOING TO BE REPEATED FOR
;EACH DRIVE PRESENT
;
;'DRIVAD' CONTAINS IN BITS 15,14,13 THE ADDRESS OF THE
;DRIVE BEING CURRENTLY CHECKED.
;

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NUDRV:

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;*****
;TEST 2 FIND OUT NEXT DRIVE TO BE CHECKED
;THIS CODE FINDS OUT THE NEXT DRIVE THAT IS PRESENT AND THEN SETS UP
;THE ADDRESS IN DRIVAD (BITS 13,14,15). THUS THROUGHOUT THE FOLLOWING TESTS
;THE DRIVE TESTED IS THE DRIVE WHOOSE ADDRESS IS IN 'DRIVAD'.
;*****

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1392 004766 000004 TST2: SCOPE
1393 004770 012737 000001 001206 MOV #1,$TIMES ;DO 1 ITERATION
1394 004776 012737 000002 001102 MOV #2,$TSTNM ;RESET POINTER TO THIS TEST
1395 ;NO. CHANGE THIS (2) IN CASE THE
1396 ;TEST NO. CHANGES
1397 005004 005037 001112 CLR $ERTTL ;CLEAR TOTAL ERROR COUNT
1398 005010 005737 001412 TST DRIVS ;R THERE ANY DRIVES PRESENT?
1399 005014 001002 BNE .+6 ;YES, BRANCH
1400 005016 000137 020560 4S: JMP $EOP ;NO, JMP TO THE END
1401 005022 013701 001354 MOV DRVPTR,R1 ;GET THAT POINTER TO THE NEXT
1402 ;DRIVE FLAG
1403 005026 032721 000001 2S: BIT #BIT0,(R1)+ ;IS THIS DRIVE PRESENT?
1404 005032 001005 BNE 1S ;YES
1405 005034 062737 020000 001350 6S: ADD #20000,DRIVAD ;FORM NXT DRIVE ADRES
1406 005042 001371 BNE 2S
1407 005044 000764 BR 4S
1408 005046 005737 001410 1S: TST DDPCH ;PROGRAM LOADED FROM AN RK05 ?
1409 005052 001413 BEQ 3S ;NO, BRANCH
1410 005054 013746 001350 MOV DRIVAD,-(SP) ;PUT TEST DRIVE ADDRESS ON THE STACK

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1411 005060 000316 SWAB (SP) ;SETUP TO RIGHT JUSTIFY THE ADDRESS
1412 005062 006216 ASR (SP) ;RIGHT JUSTIFY THE ADDRESS
1413 005064 006216 ASR (SP) ;RIGHT JUSTIFY THE ADDRESS
1414 005066 006216 ASR (SP) ;RIGHT JUSTIFY THE ADDRESS
1415 005070 006216 ASR (SP) ;RIGHT JUSTIFY THE ADDRESS
1416 005072 006216 ASR (SP) ;RIGHT JUSTIFY THE ADDRESS
1417 005074 122637 001410 CMPB (SP)+,DPTCH ;PROGRAM LOADED FROM THIS DRIVE ?
1418 005100 001755 BEQ 6$ ;BR IF YES, DON'T TEST THE DRIVE
1419 005102 010137 001354 3$: MOV R1,DRVPTR ;STORE POINTER TO THE NEXT
1420 ;DRIVE FLAG
1421 005106 104401 001272 TYPE MSG4
1422 005112 013746 001350 MOV DRIVAD, -(R6) ;GET THE DRIVE ADDRESS
1423 005116 004737 021106 JSR PC,SHFRT ;GO SHIFT IT TO THE RIGHT
1424 005122 005037 001404 CLR FFLAG
1425 005126 011600 MOV (R6),R0 ;DRIVE NUMBER
1426 005130 104403 TYPOS ;GO TYPE THE OCTAL # FOR THE
1427 ;DRIVE THAT IS BEING CHECKED
1428 005132 001 000 .BYTE 1,0
1429 005134 006300 ASL R0 ;INDEX TO TABLE
1430 005136 005760 001414 TST DRIVD(R0) ;SEE IF F
1431 005142 100006 BPL 5$ ;NO
1432 005144 104401 005152 TYPE 65$ ;TYPE ASCIZ STRING
1433 005150 000401 BR 64$ ;GET OVER THE ASCIZ
1434 ;:65$: .ASCIZ /F/
1435 64$:
1436 005154 005237 001404 INC FFLAG ;SET F FLAG
1437 005160 104401 5$: TYPE
1438 005162 001213 SCRLF ;TYPE CR, LF
1439 ;:*****
1440 ;:TEST 3 CHECK THAT DRIVE IS SUPPLIED WITH POWER-DPL BIT
1441 ;:*****
1442 005164 000004 t$T3: SCOPE
1443 005166 104413 CNT.RESET ;GO, DO CONTROL RESET
1444 ;THIS IS A CALL FOR THE 'CNTRL-
1445 ;RESET' ROUTINE. A CONTROL RESET IS
1446 ;ISSUED AND AFTER A CERTAIN TIME
1447 ;IF THE 'CNTRL RDY' DOES NOT SET
1448 ;AN ERROR IS REPORTED. NOTE THAT
1449 ;THE PC IN ERROR MESSAGE IS THE
1450 ;PC WHERE 'CNT.RESET' IS LOCATED.
1451 ;THIS IS A VERY BASIC ERR& IF IT
1452 ;OCCURS GO BACK TO TEST 10
1453 005170 013700 001326 MOV RKDS,R0
1454 005174 013777 001350 174136 MOV DRIVAD,DRKDA ;ADDRESS THE DRIVE UNDER TEST
1455 005202 005710 TST DR0 ;CHECK IF ANY BIT OF RKDS IS SET?
1456 005204 001003 BNE 1$ ;IF SET, BRANCH
1457 005206 011037 001162 MOV DR0,$REGO ;GET RKDS
1458 005212 104004 ERROR 4 ;RKDS ERROR! RKDS IF ADDRESSED
1459 ;CORRECTLY SHOULD BE NON-ZERO
1460 005214 012777 000015 174110 1$: MOV #15,DRKCS ;ISSUE A DRV RESET, IF DRV
1461 ;POWER IS LO, DPL WILL SET
1462 005222 005001 CLR R1
1463 005224 032710 010000 2$: BIT #10000,DR0 ;IS 'DPL' BIT SET?
1464 005230 001003 BNE 3$ ;DPL IS SET, BRANCH
1465 005232 005201 INC R1 ;WAIT FOR SOME TIME TO
1466 005234 001373 BNE 2$ ;SEE IF DPL WOULD SET

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CHECK THAT DRIVE IS SUPPLIED WITH POWER-DPL BIT

SEQ 0046

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1467 005236 000403          BR      45-2          ;OK, DPL NOT SET
1468 005240 004737 020710 3$: JSR    PC,GT3RG      ;GO, GET RKCS, ER, DS
1469 005244 104005          ERROR   5            ;DPL BIT OF RKDS IS SET, CHECK DRIVE POWER
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1472 005246 005001          CLR     R1            ;DID R/W/S RDY BIT SET?
1473 005250 032710 000100 4$: BIT   #100,AR0      ;:YES, EXIT
1474 005254 001010          BNE    TST4          ;:TIME DELAY
1475 005256 104417 000011  DELAY  11           ;WAIT FOR R/W/S RDY
1476 005262 005201          INC    R1
1477 005264 001371          BNE    45
1478 005266 017737 174034 001162 MOV    ARKDS,$REGO   ;GET RKDS
1479 005274 104016          ERROR   16           ;R/W/S RDY DID NOT SET AFTER
1480                          ;DRIVE RESET. DRIVE RESET WAS DONE
1481                          ;TO CHECK 'DPL' BIT. THIS TEST
1482                          ;IS NOT FOR CHECKING DRIVE RESET.
1483                          ;U MIGHT WANT TO USE THE TEST PROVIDED
1484                          ;FOR CHECKING DRIVE RESET.
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;*****
;*TEST 4 CHECK THAT 'DRIVE UNSAFE' IS CLEAR, 'HDEN' IS SET, 'WPS' IS CLEAR
;*****

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1489 005276 000004          TST4: SCOPE
1490 005300 104413          CNT.RESET
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1500 005302 013777 001350 174030 MOV    DRIVAD,ARKDA ;GO, DO CONTROL RESET
1501 005310 017700 174012          MOV    ARKDS,R0     ;THIS IS A CALL FOR THE 'CNTRL-
1502 005314 032700 002000          BIT   #2000,R0     ;RESET' ROUTINE. A CONTROL RESET IS
1503 005320 001403          BEQ   15            ;ISSUED AND AFTER A CERTAIN TIME
1504 005322 004737 020710          JSR   PC,GT3RG      ;IF THE 'CNTRL RDY' DOES NOT SET
1505 005326 104006          ERROR   6           ;AN ERROR IS REPORTED. NOTE THAT
1506                          ;THE PC IN ERROR MESSAGE IS THE
1507                          ;PC WHERE 'CNT.RESET' IS LOCATED.
1508 005330 032700 004000 1$: BIT   #4000,R0     ;THIS IS A VERY BASIC ERR & IF IT
1509 005334 001004          BNE    25           ;OCCURS GO BACK TO TEST 10
1510 005336 017737 173764 001162 MOV    ARKDS,$REGO   ;SET DRIVE ADDRESS
1511 005344 104007          ERROR   7           ;GET RKDS
1512                          ;IS 'DRU' BIT OF RKDS SET?
1513 005346 032777 000040 173752 2$: BIT   #40,ARKDS      ;NO
1514 005354 001403          BEQ   TST5          ;GO, GET RKCS, ER, DS
1515 005356 004737 020702          JSR   PC,GT4RG      ;'DRU' BIT OF RKDS IS SET, CHECK
1516 005362 104114          ERROR   114        ;DRIV BY PUTTING RUN/LOAD SW TO LOAD
1517                          ;THEN BACK TO RUN
1518                          ;IS 'HDEN' BIT SET?
1519                          ;YES, BRANCH
1520                          ;GET RKDS
1521                          ;ERROR, 'RKOS' BIT IS NOT SET
1522

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005364 000004
005366 104413

005370 013777 001350 173742
005376 105777 173724
005402 100403
005404 004737 020702
005410 104010

005412 000004
005414 013777 001350 173716
005422 005001
005424 032777 000400 173674
005432 001006
005434 005201
005436 001372
005440 017737 173662 001162
005446 104011

005450 000004

```
*****  
*TEST 5 CHECK THAT 'DRIVE READY' IS SET IN RKDS  
*****  
TST5: SCOPE ;GO, DO CONTROL RESET  
CNT.RESET ;THIS IS A CALL FOR THE 'CNTRL-  
;RESET' ROUTINE. A CONTROL RESET IS  
;ISSUED AND AFTER A CERTAIN TIME  
;IF THE 'CNTRL RDY' DOES NOT SET  
;AN ERROR IS REPORTED. NOTE THAT  
;THE PC IN ERROR MESSAGE IS THE  
;PC WHERE 'CNT.RESET' IS LOCATED.  
;THIS IS A VERY BASIC ERR & IF IT  
;OCCURS GO BACK TO TEST 10  
MOV DRIVAD,DRKDA ;ADDRS THE DRIVE  
TSTB DRKDS ;IS 'DRY' SET?  
BMI TST6 ;YES, OK  
JSR PC,GT4RG ;GO, GET RKCS, ER, DS, DA  
ERROR 10 ;'DRY' NOT SET  
;  
*****  
*TEST 6 CHECK THAT 'SOK' BIT CAN SET  
;* THIS TEST CHECKS THAT WITHIN A CERTAIN TIME  
;* 'SOK' BIT CAN SET, IF IT DOES NOT AN ERROR IS REPORTED  
*****  
TST6: SCOPE ;ADDRS THE DRIVE  
MOV DRIVAD,DRKDA ;INITIALIZE COUNT FOR TIMING WAIT LOOP  
CLR R1 ;IS SOK SET?  
1$: BIT #400,DRKDS ;EXIT  
BNE TST7 ;NO, WAIT  
INC R1 ;WAITED LONG?  
BNE 1$ ;GET RKDS  
MOV DRKDS,$REGO ;WAITED LONG BUT 'SEC OK' BIT DID NOT  
ERROR 11 ;SET  
;  
*****  
*TEST 7 CHECK THAT 'SECTOR COUNTER' CAN COUNT FROM 0-13  
;* THIS TEST CHECKS THAT THE SECTOR COUNTER CAN COUNT FROM  
;* 0-13  
;* 1) FIRST, FOR INITIALIZING PURPOSES THERE IS A TIMED LOOP  
;* DURING WHICH SECTOR COUNTER SHOULD COUNT DOWN TO 0. IF THIS  
;* IS NOT DONE AN ERROR IS REPORTED  
;* 2) AFTER A COUNT OF 0 IS REACHED, THE PROGRAM WAITS  
;* FOR A CERTAIN TIME, DURING WHICH THE SEC COUNTER  
;* IS SAMPLED. IF THE COUNTER DOES NOT CHANGE WITHIN THIS  
;* TIME PERIOD AN ERROR IS REPORTED.  
;* 3) UPON FINDING THAT THE COUNTER HAS CHANGED, IT IS CHECKED  
;* IF IT INCREMENTED CORRECTLY. IF IT DID NOT AN ERROR IS REPORTED  
;* 4) IF IT INCREMENTED CORRECTLY, THE PROGRAM AGAIN WAITS IN A  
;* LOOP TILL THE COUNTER CHANGES. (STEPS 2,3,4 ARE REPEATED  
;* TILL THE COUNTER COUNTS UP TO 13)  
*****  
TST7: SCOPE
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T7

CHECK THAT 'SECTOR COUNTER' CAN COUNT FROM 0-13

SEQ 0049

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1635                                     ;DID NOT COUNT TO 0
1636 005624 000421                       BR    TST10                       ;;EXIT
1637
1638 005626 017737 173474 001162 7$:    MOV    DRKDS,$REG0           ;GET RKDS
1639 005634 104011                       ERROR  11                   ;WAITED LONG, BUT 'SOK' BIT DID
1640                                     ;NOT SET
1641 005636 000414                       BR    TST10                       ;;EXIT
1642
1643 005640 010237 001162                 8$:    MOV    R2,$REG0           ;GET SEC CNTR (PRESENT COUNT)
1644 005644 010337 001164                 MOV    R3,$REG1           ;GET "NEXT COUNT"
1645 005650 104013                       ERROR  13                   ;WAITED LONG, BUT THE SECTOR
1646                                     ;COUNTER DID NOT INCREMENT FROM
1647                                     ;THE PRESENT COUNT TO THE NEXT COUNT
1648 005652 000406                       BR    TST10                       ;;EXIT
1649
1650 005654 010337 001162                 9$:    MOV    R3,$REG0           ;GET 'NEXT COUNT' (SEC CNTR SHOULD BE THIS)
1651 005660 010237 001164                 MOV    R2,$REG1           ;GET PRESENT COUNT (WHAT SEC CNTR WAS)
1652 005664 104014                       ERROR  14                   ;SEC CNTR INCREMENTED WRONG, DID
1653                                     ;NOT INCREMENT FROM PRESENT COUNT
1654                                     ;TO NEXT COUNT
1655 005666 000747                       BR    5$
1656 ;
1657
1658 ;*****
1659 ;*TEST 10      CHECK THAT SC=SA CAN BE GENERATED
1660 ;* THIS TEST CHECKS THAT SC=SA CAN BE GENERATED FOR
1661 ;* EVERY SECTOR
1662 ;*****
1663 005670 000004                         TST10: SCOPE
1664 005672 104413                         CNT.RESET
1665                                     ;GO, DO CONTROL RESET
1666                                     ;THIS IS A CALL FOR THE 'CNTRL-
1667                                     ;RESET' ROUTINE. A CONTROL RESET IS
1668                                     ;ISSUED AND AFTER A CERTAIN TIME
1669                                     ;IF THE 'CNTRL RDY' DOES NOT SET
1670                                     ;AN ERROR IS REPORTED. NOTE THAT
1671                                     ;THE PC IN ERROR MESSAGE IS THE
1672                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
1673                                     ;THIS IS A VERY BASIC ERR & IF IT
1674                                     ;OCCURS GO BACK TO TEST 10
1675
1676 005674 013704 001350                   MOV    DRIVAD,R4
1677 005700 013700 001326                   MOV    RKDS,R0
1678 005704 012703 177764                   MOV    #-14,R3
1679 005710 010477 173424                   1$:    MOV    R4,DRKDA
1680 005714 005005                           CLR    R5
1681 005716 005205                           2$:    INC    R5
1682 005720 001410                           BEQ    3$
1683 005722 011001                           MOV    DR0,R1
1684 005724 032701 000020                   BIT    #20,R1
1685 005730 001772                           BEQ    2$
1686 005732 005204                           4$:    INC    R4
1687 005734 005203                           INC    R3
1688 005736 001364                           BNE    1$
1689 005740 000406                           BR    TST11
1690
1689 005742 110437 001162                 3$:    MOVB   R4,$REG0           ;GET SECTOR ADDRESS
1690 005746 010137 001164                 MOV    R1,$REG1           ;GET RKDS

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1859 006330 104421          TST.SIN          ;GO CHECK IF SIN IS SET
1860                                ;IF SET DO DRV-RESET TO CLR IT
1861 006332 004737 021412    JSR      PC,DRESET ;MAKE SURE HEADS R ON CYL 0
1862 006336 104026          ERROR      26      ;R/W/S RDY DIDN'T SET
1863                                ;AFTER THE ABOVE DRV RESET
1864 006340 005005          CLR      R5
1865 006342 013777 001350 172770 MOV     DRIVAD,DRKDA
1866 006350 052777 000100 172762 BIS     #100,DRKDA ;SEEK CYLINDER 2
1867 006356 013701 001326          MOV     RKDS,R1
1868 006362 012777 000011 172742 MOV     #11,DRKCS ;SEEK GO
1869 006370 032711 000100 15:   BIT     #100,DR1 ;DID R/W/S RDY CLR?
1870 006374 001405          BEQ     25          ;YES, BRANCH
1871 006376 005205          INC     R5
1872 006400 100373          BPL     15
1873 006402 004737 020710    JSR     PC,GT3RG
1874 006406 104025          ERROR      25      ;R/W/S RDY WAS NOT CLEAR WHEN HEADS
1875                                ;WERE SEEKING TO CYLINDER 2
1876
1877 006410 004737 021344    25:   JSR     PC,TSTRWS ;GO, WAIT FOR R/W/S RDY TO SET
1878 006414 104016          ERROR      16      ;R/W/S RDY DID NOT SET AFTER SEEK
1879                                ;WAS TRIED TO CYLINDER 2 (ABOVE).
1880                                ;NOTE THIS WAS THE FIRST TIME A SEEK
1881                                ;WAS TRIED TO A CYLINDER OTHER THAN
1882                                ;0.
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1902 006416 000004          ;*****
1903 006420 104413          ;*TEST 15 CHECK 'WRITE' FORMAT FUNCTION-CYLINDER 0, SECTOR 0
1904                                ;*THIS TEST CHECKS THE LOGIC INVOLVED IN THE WRITE FMT
1905                                ;*FUNCTION. ON ISSUING A WRT FMT, THE FOLLOWING IS CHECKED
1906                                ;*1) CNTRL RDY WAS CLEARED AS GO WAS SET.
1907                                ;*2) CNTRL RDY SETS WITHIN A CERTAIN TIME ON COMPLETION OF FUNCTION
1908                                ;*3) IF 'HE' OR 'ERR' BIT SET?
1909                                ;*4) IF RKDA INCREMENTED CORRECTLY FROM 0 TO 1?
1910                                ;*5) IF RKWC OVERFLOWED CORRECTLY TO 0?
1911                                ;*6) IF RKBA INCREMENTED CORRECTLY BY 2?
1912                                ;*7) IF ANY BIT IN RKER SET?
1913                                ;*8) IF THE 'WRT FMT' FUNCTION BITS ARE STILL IN THE RKCS?
1914                                ;*NOTE THAT ONE WORD '125252' WAS WRITTEN ON SECTOR
1915                                ;*0 & IT WILL BE CHECKED IN THE NEXT TESTS.
1916                                ;*****
1917                                ;*TST15: SCOPE
1918                                ;CNT.RESET
1919                                ;GO, DO CONTROL RESET
1920                                ;THIS IS A CALL FOR THE 'CNTRL-
1921                                ;RESET' ROUTINE. A CONTROL RESET IS
1922                                ;ISSUED AND AFTER A CERTAIN TIME
1923                                ;IF THE 'CNTRL RDY' DOES NOT SET
1924                                ;AN ERROR IS REPORTED. NOTE THAT
1925                                ;THE PC IN ERROR MESSAGE IS THE
1926                                ;PC WHERE 'CNT.RESET' IS LOCATED.
1927                                ;THIS IS A VERY BASIC ERR & IF IT
1928                                ;OCCURS GO BACK TO TEST 10
1929                                ;GO CHECK IF SIN IS SET
1930                                ;IF SET, DO DRIVE RESET TO CLR IT
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1915	006424	012703	033240		MOV	#OUTBUF,R3			
1916									: THIS CODE SETS UP A 256 WORD BUFFER
1917									: WHICH WILL BE USED TO WRITE 1 SECTOR
1918									: ON THE DISK
1919									: 1ST WORD 000001
1920									: 2ND WORD 177777 2'S COMPLEMENT
1921									: 3RD WORD 000002 OF ABOVE
1922									: 4TH WORD 177776
1923									: 253RD WORD 000177
1924									: 254TH WORD 177601
1925									: 255TH WORD 000000
1926									: 256TH WORD 125252
1927									
1928									
1929	006430	012700	000001		MOV	#1,R0			: SET COUNT
1930									
1931	006434	010023		9\$:	MOV	R0,(R3)+			: SET UP DATA WORDS
1932	006436	010013			MOV	R0,(R3)			
1933	006440	005423			NEG	(R3)+			
1934	006442	005200			INC	R0			
1935	006444	022700	000200		CMP	#200,R0			: DONE?
1936	006450	001371			BNE	9\$			
1937	006452	005023			CLR	(R3)+			: SET 255TH WORD TO 0
1938	006454	012713	125252		MOV	#125252,R3			: SET 256TH WORD
1939									
1940	006460	012703	033240		MOV	#OUTBUF,R3			: RESET POINTER TO OUTBUF
1941	006464	013701	001332		MOV	RKCS,R1			
1942	006470	013702	001336		MOV	RKBA,R2			
1943	006474	010312			MOV	R3,R2			: FROM HERE-SET UP CURRENT ADDRESS
1944	006476	012777	177400	172630	MOV	#-400,RKWC			: SET UP WORD COUNT 400 WORDS
1945	006504	013777	001350	172626	MOV	DRIVAD,RKDA			: SET UP DISK ADDR, SECTOR 0, CYLINDER 0
1946	006512	012711	002003		MOV	#2003,R1			: WRITE FORMAT, GO
1947									
1948	006516	105711		1\$:	TSTB	R1			: WAS 'CNTRL RDY' CLEARED AS GO WAS SET?
1949	006520	100003			BPL	2\$: YES, BRANCH
1950	006522	004737	020710		JSR	PC,GT3RG			: GO, GET RKCS, ER, DS
1951	006526	104030			ERROR	30			: 'CNTRL RDY' DIDN'T CLEAR AS GO
1952									: WAS SET TO 'WRITE FORMAT'
1953	006530	005000		2\$:	CLR	R0			
1954	006532	105711			TSTB	R1			: WAS 'CNTRL RDY' SET ON COMPLETION OF WRITE?
1955	006534	100411			BMI	3\$: YES, BRANCH
1956	006536	005200			INC	R0			: NO, HAVE U WAITED LONG ENOUGH?
1957	006540	001374			BNE	2\$+2			: IF NOT, LOOP BACK & WAIT
1958									: IF YES, REPORT ERROR
1959	006542	004737	020702		JSR	PC,GT4RG			: GO, GET RKCS, ER, DS, DA
1960	006546	013737	001350	001202	MOV	DRIVAD,\$REG10			
1961	006554	104416			BRKDA4				: GO TO 'BDA4' & BREAK CONTENTS OF
1962									: \$REG10 INTO DR #,CYL,SUR,SEC BITS
1963	006556	104031			ERROR	31			: 'CNTRL RDY' DIDN'T SET ON COMPLETION
1964									: OF WRITE FORMAT
1965									: WRT FMT WAS DONE STARTING AT (DSK-ADRES)
1966									: INDICATED IN EROR MSGE.
1967	006560	004737	021142	3\$:	JSR	PC,CHKHE			: GO CHECK IF 'HE' OR 'ERR' BIT SET,
1968									: IF YES, SAVE RKCS, ER, DS, DA.
1969									: RETURN HERE IF ERROR.
1970	006564	104032			ERROR	32			: 'HE' OR 'ERR' BIT SET WHILE DOING


```

1971                                     :A WRITE FORMAT
1972                                     :WRT FMT WAS DONE STARTING AT (DSK-ADRES)
1973                                     :INDICATED IN EROR MSGE.
1974 006566 004737 021170                4$: JSR PC,CHKDA :GO CHECK IF RKDA INCREMENTED CORRECTLY
1975                                     :IF NOT, RETURN HERE.
1976 006572 104033                        ERROR 33 :RKDA SHOULD HAVE INCREMENTED BY
1977                                     :1 SECTOR, IT DID NOT
1978 006574 004737 021224                5$: JSR PC,CHKWC :CHECK IF WORD COUNT OVERFLOWED, IF
1979                                     :NOT RETURN HERE.
1980 006600 104034                        ERROR 34 :RKWC DID NOT OVERFLOW TO 0, AFTER
1981                                     :XFER ON WRITE FORMAT
1982 006602 022712 034240                6$: CMP #OUTBUF+1000,AR2 :DID RKBA INCREMENT CORRECTLY?
1983 006606 001406                        BEQ 7$ :YES, BRANCH
1984 006610 012737 034240 001162        MOV #OUTBUF+1000,$REG0 :GET EXPCTD RKBA
1985 006616 011237 001164                MOV AR2,$REG1 :GET ACTUAL RKBA
1986 006622 104035                        ERROR 35 :RKBA DIDN'T INCREMENT BY 1000 AFTER
1987                                     :WRITE FORMAT OF 400 WORDS
1988 006624 004737 021250                7$: JSR PC,CHKER :CHECK IOF ANY BIT IN RKER SET,
1989                                     :IF YES RETURN HERE.
1990 006630 104036                        ERROR 36 :RKER BIT SET ON DOING 1 WORD
1991                                     :WRITE FORMAT
1992 006632 022711 002202                8$: CMP #2202,AR1 :DOES RKCS STILL HAVE 'WRT FMT' BITS?
1993 006636 001406                        BEQ TST16 :YES, EXIT
1994 006640 012737 002202 001162        MOV #2202,$REG0 :GET EXPCTD RKCS
1995 006646 011137 001164                MOV AR1,$REG1 :GET ACTUAL RKCS
1996 006652 104024                        ERROR 24 :RKCS DIDN'T CONTAIN 'WRT FMT' BITS
1997                                     :AFTER THE FUNCTION WAS COMPLETED

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1998
1999
2000 :*****
2001 :*TEST 16 CHECK 'READ FORMAT' FUNCTION-CYLINDER 0, SECTOR 0
2002 :*THIS TEST CHECKS THE LOGIC INVOLVED IN THE WRITE FMT
2003 :*FUNCTION. ON ISSUING A WRT FMT, THE FOLLOWING IS CHECKED
2004 :*1) CNTRL RDY WAS CLEARED AS GO WAS SET.
2005 :*2) CNTRL RDY SETS WITHIN A CERTAIN TIME ON COMPLETION OF FUNCTION
2006 :*3) IF 'HE' OR 'ERR' BIT SET?
2007 :*4) IF RKDA INCREMENTED CORRECTLY FROM 0 TO 1?
2008 :*5) IF RKWC OVERFLOWED CORRECTLY TO 0?
2009 :*6) IF RKBA INCREMENTED CORRECTLY BY 2?
2010 :*7) IF ANY BIT IN RKER SET?
2011 :*8) IF THE CORRECT HEADER WAS RECEIVED?
2012 :*9) FOR RK11C, AFTER RD FMT RKDB CONTAINS THE CHECKSUM
2013 :*FOR THAT SECTOR. (125252 IN THIS CASE, BECAUSE THE
2014 :*FIRST WORD IN SEC 0 WAS WRITTEN AS 125252 IN
2015 :*THE PREVIOUS TEST)
2016 :*10) FOR RK11D, AFTER RD FMT RKDB SHOULD CONTAIN
2017 :*A ZERO
2018 :*11) IF THE RD FMT FUNCTION BITS ARE STILL IN
2019 :*THE RKCS?

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2020 006654 000004 :*****
2021 006656 005000 †TST16: SCOPE
2022 006660 104413 CLR RD
2023 :CNT.RESET :GO, DO CONTROL RESET
2024 :THIS IS A CALL FOR THE 'CNTRL-
2025 :RESET' ROUTINE. A CONTROL RESET IS
2026 :ISSUED AND AFTER A CERTAIN TIME
:IF THE 'CNTRL RDY' DOES NOT SET

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E05

MAINDEC-11-DZRKK-D MACY11 27(1006) 04-OCT-76 16:06 PAGE 39
 DZRKKD.P11 22-SEP-76 08:47 T16 CHECK 'READ FORMAT' FUNCTION-CYLINDER 0, SECTOR 0

SEQ 0056

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2027                                     ;AN ERROR IS REPORTED. NOTE THAT
2028                                     ;THE PC IN ERROR MESSAGE IS THE
2029                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
2030                                     ;THIS IS A VERY BASIC ERR & IF IT
2031                                     ;OCCURS GO BACK TO TEST 10
2032 006662 104421                       TST.SIN                               ;GO CHECK IF SIN IS SET
2033                                     ;IF SET, DO DRIVE RESET TO CLR IT
2034 006664 013701 001332                MOV      RKCS,R1
2035 006670 013702 001336                MOV      RKBA,R2
2036 006674 012703 033240                MOV      #OUTBUF,R3
2037 006700 010312                       MOV      R3,R2                               ;SETUP ADRS WHERE HEADER WORD IS TO BE
2038                                     ;X-FERRED
2039 006702 012777 177777 172424         MOV      #-1,RKWC                             ;SET UP WORD COUNT
2040 006710 013777 001350 172422         MOV      DRIVAD,RKDA                          ;SET UP DISK ADRS, SECTOR 0, CYLINDER 0
2041 006716 012711 002005               MOV      #2005,R1                             ;READ FORMAT, GO
2042
2043 006722 105711                       1$:    TSTB   R1                               ;WAS 'CNTRL RDY' CLEARED AS GO WAS SET?
2044 006724 100003                       BPL     2$
2045 006726 004737 020710               JSR     PC,GT3RG                             ;YES, BRANCH
2046 006732 104030                       ERROR   30
2047                                     ;GO, GET RKCS, RKER
2048 006734 005000                       2$:    CLR    R0                               ;CNTRL RDY DIDN'T CLEAR AS GO WAS
2049 006736 105711                       TSTB   R1                               ;SET TO 'READ FORMAT'
2050                                     ;WAS 'CNTRL RDY' SET ON COMPLETION OF
2051 006740 100411                       BMI     3$
2052 006742 005200                       INC     R0
2053 006744 001374                       BNE     2$+2
2054                                     ;TRANSFER
2055 006746 004737 020702               JSR     PC,GT4RG                             ;YES, BRANCH
2056 006752 013737 001350 001202         MOV     DRIVAD,$REG10                        ;NO, HAVE U WAITED LONG ENOUGH?
2057 006760 104416                       BRKDA4
2058                                     ;IF NOT, LOOP BACK & WAIT
2059 006762 104045                       ERROR   45
2060                                     ;IF YES, REPORT ERROR
2061                                     ;GO, GET RKCS, ER, DS,DA
2062                                     ;GO TO 'BD4' & BREAK CONTENTS OF
2063 006764 004737 021142               3$:    JSR     PC,CHKHE                       ;$REG10 INTO DR #,CYL,SUR,SEC BITS
2064                                     ;'CNTRL RDY' DIDN'T SET ON COMPLETION
2065 006770 104046                       ERROR   46
2066                                     ;OF READ FORMAT
2067                                     ;READ FMT WAS DONE STARTING AT <DSK-ADRES>
2068                                     ;INDICATED IN EROR MESGE
2069 006772 004737 021170               4$:    JSR     PC,CHKDA                       ;CHECK IF 'ERR' OR 'HE' BIT SET, IF
2070                                     ;YES RETURN HERE.
2071 006776 104040                       ERROR   40
2072                                     ;'HE' OR 'ERR' BIT SET WHILE
2073                                     ;DOING A 'READ FORMAT'
2074 007000 004737 021224               5$:    JSR     PC,CHKWC                       ;READ FMT WAS DONE STARTING AT <DSK-ADRES>
2075                                     ;INDICATED IN EROR MESGE
2076 007004 104041                       ERROR   41
2077                                     ;CHECK IF RKDA INCREMENTED CORRECTLY
2078 007006 022712 033242               6$:    CMP     #OUTBUF+2,R2                   ;IF NOT, RETURN HERE.
2079 007012 001406                       BEQ     7$
2080 007014 012737 033242 001162         MOV     #OUTBUF+2,$REG0                       ;RKDA SHOULD HAVE INCREMENTED
2081 007022 011237 001164               MOV     R2,$REG1                             ;BY 1 SECTOR, IT DID NOT
2082 007026 104042                       ERROR   42

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F05

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2083                                     ;'READ FORMAT' OF 1 WORD
2084 007030 004737 021250    7$:    JSR    PC,CHKER    ;CHECK IF ANY BIT IN RKER SET, IF
2085                                     ;YES RETURN HERE.
2086 007034 104036          ERROR    36    ;RKER BIT SET ON DOING
2087                                  ;1 WORD READ FORMAT
2088 007036 005713    8$:    TST    @R3    ;DOES OUTBUF CONTAIN THE HEADER
2089                                  ;WORD-0
2090 007040 001407          BEQ    9$    ;YES, BRANCH
2091 007042 005037 001162       CLR    $REG0    ;GET SECTOR NO.
2092 007046 005037 001164       CLR    $REG1    ;EXPCD HEADER
2093 007052 011337 001166       MOV    @R3,$REG2 ;GET HEADER RECVD
2094 007056 104043          ERROR    43    ;CORRECT HEADER WORD-0-WAS
2095                                  ;NOT RECEIVED ON READ FORMAT
2096 007060 022711 002204    9$:    CMP    #2204,@R1 ;DOES RKCS HAVE THE 'RDFMT' BITS?
2097 007064 001406          BEQ    TST17    ;YES, BRANCH
2098 007066 012737 002204 001162       MOV    #2204,$REG0 ;GET EXPCD RKCS
2099 007074 011137 001164       MOV    @R1,$REG1 ;GET ACTUAL RKCS
2100 007100 104024          ERROR    24    ;RKCS DIDN'T CONTAIN 'RD FMT'
2101                                  ;BITS AFTER FUNCTION WAS
2102                                  ;COMPLETED
  
```

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2103
2104
2105
2106
2107    ;*****
2108    ;*TEST 17    CHECK 'READ' FUNCTION-CYLINDER 0,SECTOR 0
2109    ;*THIS IS THE FIRST TIME A PURE READ IS PERFORMED IN THIS
2110    ;*TEST SEQUENCE. THE FOLLOWING IS CHECKED
2111    ;*1) CNTRL RDY CLEARS AS GO IS SET
2112    ;*2) CNTRL RDY SETS WITHIN A CERTAIN TIME ON COMPLETION
2113    ;*OF FUNCTION
2114    ;*3) IF 'HE' OR 'ERR' BIT SET?
2115    ;*4) IF RKDA INCREMENTED CORRECTLY?
2116    ;*5) IF RKWC OVERFLOWED TO 0?
2117    ;*6) IF RKBA INCREMENTED CORRECTLY?
2118    ;*7) IF ANY RKER BIT SET?
2119    ;*8) IF THE CORRECT PSUEDO-HEADER (FIRST WORD) WAS
2120    ;*READ FROM SECTOR 0
2121    ;*9) IF THE 'READ' FUNCTION BITS ARE STILL IN RKCS
  
```

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2122 007102 000004    ;*****
2123 007104 104413    †TST17:    SCOPE    ;GO, DO CONTROL RESET
2124                                  ;THIS IS A CALL FOR THE 'CNTRL-
2125                                  ;RESET' ROUTINE. A CONTROL RESET IS
2126                                  ;ISSUED AND AFTER A CERTAIN TIME
2127                                  ;IF THE 'CNTRL RDY' DOES NOT SET
2128                                  ;AN ERROR IS REPORTED. NOTE THAT
2129                                  ;THE PC IN ERROR MESSAGE IS THE
2130                                  ;PC WHERE 'CNT.RESET' IS LOCATED.
2131                                  ;THIS IS A VERY BASIC ERR & IF IT
2132                                  ;OCCURS GO BACK TO TEST 10
2133 007106 104421          TST.SIN    ;GO CHECK IF SIN IS SET
2134                                  ;IF SET, DO DRIVE RESET TO CLR IT
2135 007110 013701 001332       MOV    RKCS,R1
2136 007114 005000          CLR    R0
2137 007116 013702 001336       MOV    RKBA,R2
2138 007122 012703 033240       MOV    @OUTBUF,R3
  
```

G05

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T17

CHECK 'READ' FUNCTION-CYLINDER 0, SECTOR 0

SEQ 0058

2139	007126	010312			MOV	R3,AR2		;SET UP ADDRS WHERE DATA WORD IS
2140								;TO BE X-FERRED
2141	007130	012777	177400	172176	MOV	#-400,AR2		;SET UP WORD COUNT
2142	007136	013777	001350	172174	MOV	DRIVAD,AR2		;SET UP DISK ADRS, SECTOR 0, CYLINDER 0
2143	007144	012711	000005		MOV	#5,AR1		;READ, GO
2144								
2145	007150	105711			1\$:	TSTB	AR1	;WAS 'CNTRL RDY' CLEARED AS GO WAS SET?
2146	007152	100003				BPL	2\$;YES, BRANCH
2147	007154	004737	020710			JSR	PC,GT3RG	;GO, GET RKCS, ER
2148	007160	104030				ERROR	30	;CNTRL RDY DID NOT CLEAR AS GO
2149								;WAS SET TO 'READ'
2150	007162	005000			2\$:	CLR	RO	
2151	007164	105711				TSTB	AR1	;WAS CNTRL RDY SET ON COMPLETION
2152								;OF TRANSFER?
2153	007166	100411				BMI	3\$;YES, BRANCH
2154	007170	005200				INC	RO	;NO, HAVE U WAITED LONG ENOUGH?
2155	007172	001374				BNE	2\$+2	;IF NOT, LOOP BACK & WAIT
2156								;IF YES, REPORT ERROR
2157	007174	004737	020702			JSR	PC,GT4RG	;GO, GET RKCS, ER, DS,DA
2158	007200	013737	001350	001202		MOV	DRIVAD,\$REG10	
2159	007206	104416				BRKDA4		;GO TO 'BD4' & BREAK CONTENTS OF
2160								;\$REG10 INTO DR #,CYL,SUR,SEC BITS
2161	007210	104045				ERROR	45	;CNTRL RDY DID NOT SET ON
2162								;COMPLETION OF READ
2163								;READ WAS DONE STARTING AT <DSK-ADRES>
2164								;INDICATED IN EROR MESGE
2165								
2166	007212	004737	021142		3\$:	JSR	PC,CHKHE	;CHECK IF 'ERR' OR 'HE' BIT IS SET
2167								;IF YES, RETURN HERE.
2168	007216	104046				ERROR	46	; 'HE' OR 'ERR' BIT SET WHILE
2169								;DOING A READ.
2170								;READ WAS DONE STARTING AT <DSK-ADRES>
2171								;INDICATED IN EROR MESGE
2172	007220	004737	021170		4\$:	JSR	PC,CHKDA	;CHECK IF RKDA INCREMENTED CORRECTLY,
2173								;IF NOT RETURN HERE.
2174	007224	104040				ERROR	40	;RKDA DID NOT INCREMENT
2175								;BY 1 (SECTOR)
2176	007226	004737	021224		5\$:	JSR	PC,CHKWC	;CHECK IF RKWC OVERFLOWED TO 0,
2177								;IF NOT RETURN HERE.
2178	007232	104041				ERROR	41	;RKWC DID NOT OVERFLOW TO 0,
2179								;AFTER X-FER ON READ
2180	007234	022712	034240		6\$:	CMP	#OUTBUF+1000,AR2	;DID RKBA INCREMENT CORRECTLY?
2181	007240	001406				BEG	7\$;YES, BRANCH
2182	007242	012737	034240	001162		MOV	#OUTBUF+1000,\$REG0	;GET EXPCTD RKBA
2183	007250	011237	001164			MOV	AR2,\$REG1	;GET ACTUAL RKBA
2184	007254	104042				ERROR	42	;RKBA DID NOT INCREMENT BY 2
2185								;AFTER 'READ' OF 1 WORD
2186	007256	004737	021250		7\$:	JSR	PC,CHKER	;CHECK IF ANY BIT IN RKER SET,
2187								;IF YES RETURN HERE.
2188	007262	104036				ERROR	36	;RKER BIT SET ON DOING 1
2189								;WORD 'READ'
2190	007264	022713	000001		8\$:	CMP	#1,AR3	;DOES OUTBUF CONTAIN THE RIGHT
2191								;DATA WORD
2192	007270	001411				BEG	9\$;YES BRANCH
2193	007272	012737	000001	001162		MOV	#1,\$REG0	;GET EXPCTD DATA WORD
2194	007300	011337	001164			MOV	(R3),\$REG1	;GET RECVD DATA WORD


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2195 007304 013737 001350 001166      MOV    DRIVAD,$REG2    ;GET DISK ADRS FROM WHICH READ WAS DONE
2196 007312 104044                      ERROR   44             ;DID NOT READ THE CORRECT
2197                                         ;DATA WORD--FROM DISK ADRES,
2198                                         ;SEC 0, CYL 0, SUR 0
2199
2200                                         ;AFTER 1 SECTOR READ RKDB CONTAINS
2201                                         ;FOR RK11C
2202                                         ;THE CHECKSUM FOR THAT SECTOR
2203                                         ;FOR RK11D
2204                                         ;THE LAST WORD TRANSFERRED TO MEMORY
2205
2206                                         ;IT SO HAPPENS THAT WITH THE SECTOR
2207                                         ;THAT WAS READ, RKDB CONTAINS THE
2208                                         ;SAME INFORMATION FOR BOTH RK11C
2209                                         ;AND RK11D
2210
2211 007314 022777 125252 172020 9$:      CMP     #125252,$RKDB  ;DOES RKDB CONTAIN THE EXPCTD WORD?
2212 007322 001407                      BEQ     10$           ;YES, BRANCH
2213 007324 012737 125252 001162      MOV     #125252,$REG0 ;GET EXPCTD RKDB
2214 007332 017737 172004 001164      MOV     $RKDB,$REG1  ;GET RECD RKDB
2215 007340 104037                      ERROR   37           ;RKDB DOES NOT CONTAIN THE
2216                                         ;EXPCTD WORD AFTER A READ OF SEC 0
2217                                         ;CYL 0
2218 007342 022711 000204              10$:    CMP     #204,$R1     ;DOES RKCS HAVE THE 'READ' BITS?
2219 007346 001406                      BEQ     11$           ;YES, BRANCH
2220 007350 012737 000204 001162      MOV     #204,$REG0   ;GET EXPCTD RKCS
2221 007356 011137 001164              MOV     $R1,$REG1    ;GET RECD RKCS
2222 007362 104024                      ERROR   24           ;RKCS DID NOT CONTAIN 'READ'
2223                                         ;FUNCTION BITS AFTER OPERATION
2224                                         ;WAS COMPLETED
2225                                         ;GO DO CONTROL RESET
2226 007364 104413 171750              11$:    CNT.RESET          ;DID CONTROL RESET CLEAR RKDB?
2227 007366 005777                      TST     $RKDB        ;YES, EXIT
2228 007372 001407                      BEQ     TST20        ;YES, EXIT
2229 007374 013737 001342 001164      MOV     RKDB,$REG1   ;GET ADRES OF RKDB
2230 007402 017737 171734 001164      MOV     $RKDB,$REG1 ;GET CONTENTS OF RKDB
2231 007410 104102                      ERROR   102         ;CONTROL RESET DIDN'T CLR RKDB

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*****
;*TEST 20      CHECK 'WRITE FORMAT' -CYLINDER 0, SECTOR 0-13
;*THIS TEST GOES ONE STEP FURTHER & PERFORMS A WRT
;*FMT ON CYLINDER 0 & CHECKS THE FOLLOWING
;*1) IF CNTRL RDY SET WITHIN A CERTAIN TIME ON COMPLETION
;*OF THE FUNCTION
;*2) IF 'HE' OR 'ERR' BIT SET?
;*3) IF THE RKDA INCREMENTS CORRECTLY?
;*4) IF THE RKDB IS CLEAR?
;*WRT FMT IS DONE ONE SECTOR AT A TIME
;*THE FIRST WORD OF EVERY SECTOR IS WRITTEN AS A
;*PSUEDO-HEADER CONSISTING OF DRIVE #, CYLINDER #, SURFACE
;*# SECTOR #. THIS WILL BE READ & CHECKED IN THE FOLLOWING TEST.
*****

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2246 007412 000004      †TST20:  SCOPE
2247 007414 013703      MOV     RKCS,R3
2248 007420 012702      MOV     #-14,R2      ;SET UP COUNT FOR 12 SECTORS
2249 007424 013704      MOV     RKDA,R4
2250 007430 013701      MOV     DRIVAD,R1    ;GET DRIVE ADDRESS

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2251	007434	010105			MOV	R1,R5			;STORE IT
2252	007436	005205			INC	R5			
2253	007440	012737	007446	001110	MOV	#1\$,SLPERR			;SET RETURN ADRES FOR LUPING
2254									;ON ERROR (SW 9)
2255	007446	104413			1\$:	CNT.RESET			;GO, DO CONTROL RESET
2256									;THIS IS A CALL FOR THE 'CNTRL-
2257									;RESET' ROUTINE. A CONTROL RESET IS
2258									;ISSUED AND AFTER A CERTAIN TIME
2259									;IF THE 'CNTRL RDY' DOES NOT SET
2260									;AN ERROR IS REPORTED. NOTE THAT
2261									;THE PC IN ERROR MESSAGE IS THE
2262									;PC WHERE 'CNT.RESET' IS LOCATED.
2263									;THIS IS A VERY BASIC ERR & IF IT
2264									;OCCURS GO BACK TO TEST 10
2265	007450	104421				TST.SIN			;GO CHECK IF SIN IS SET
2266									;IF SET, DO DRIVE RESET TO CLR IT
2267	007452	005000			CLR	RD			
2268	007454	010137	033240		MOV	R1,OUTBUF			;THIS WORD TO BE X-FERRED. FIRST
2269									;WORD OF EACH SECTOR WILL BE THE
2270									;ACTUAL DRIVE-ADRES CONSISTING OF
2271									;DRIVE NO, CYL ADRES, SURFACE
2272									;SECTOR NO.
2273	007460	012777	033240	171650	MOV	#OUTBUF,ARKBA			;ADRS FROM WHICH DATA WORD IS TO
2274									;X-FERRED
2275	007466	012777	177777	171640	MOV	#-1,ARKWC			;SET UP WORD COUNT
2276	007474	010114			MOV	R1,AR4	;ADRES	THE DRIVE, CYL 0, & CORRECT SECTOR	
2277	007476	012713	002003		MOV	#2003,AR3			;WRITE FORMAT, GO
2278									
2279	007502	105777	171624		2\$:	TSTB	ARKCS		;DID 'CNTRL RDY' SET?
2280	007506	100410				BMI	3\$;YES, BRANCH
2281	007510	005200				INC	RD		;NO, HAVE U WAITED LONG?
2282	007512	001373				BNE	2\$;IF NOT, LOOP BACK & WAIT
2283									;IF YES, REPORT ERROR
2284	007514	004737	020702		JSR	PC,GT4RG			;GO, GET RKCS, ER, DS, DA
2285	007520	010137	001202		MOV	R1,\$REG10			;GET DISK ADRES (UNIT,CYL,SUR,SEC) TO WHICH
2286									;WRITE FORMAT WAS DONE
2287	007524	104416				BRKDA4			;GO TO 'BDA4' & BREAK CONTENTS OF
2288									;\$REG10 INTO DR #,CYL,SUR,SEC BITS
2289	007526	104031				ERROR	31		; 'CNTRL RDY' DID NOT SET ON COMPLETION
2290									;OF 'WRITE FORMAT'
2291									;WRT FMT WAS DONE STARTING AT <DSK-ADRES>
2292									;INDICATED IN EROR MSGE.
2293	007530	004737	021134		3\$:	JSR	PC,CHKHE1		;CHECK IF 'ERR' OR 'HE' BIT IS SET,
2294									;IF YES RETURN HERE.
2295	007534	104032				ERROR	32		; 'HE' OR 'ERR' BIT SET WHILE DOING
2296									;WRITE FORMAT ON CYLINDER 0,
2297									;SECTOR IN ERROR IS AS SHOWN IN
2298									;DISK-ADRES BITS 0-3
2299									;WRT FMT WAS DONE STARTING AT <DSK-ADRES>
2300									;INDICATED IN EROR MSGE.
2301									
2302	007536	004737	021176		4\$:	JSR	PC,CHKDA1		;CHECK IF RKDA INCREMENTED CORRECTLY?
2303									
2304	007542	104033				ERROR	33		;RKDA DID NOT INCREMENT CORRECT
2305									;AFTER 1 WORD 'WRITE FORMAT' ON
2306									;CYLINDER 0, SECTOR IN ERROR IS 1


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2307                                     ;LESS THAN THAT SHOWN IN EXPCTD RKDA
2308 007544 005777 171572          5$:  TST      JRKDB      ;CHECK THAT RKDB DOES CONTAIN A 0
2309                                     ;AFTER WRT BECAUSE LAST WORD WRITTEN
2310                                     ;WAS SERIALLY SHIFTED OUT TO THE DISK
2311 007550 001406          BEQ      6$          ;YES, BRANCH
2312 007552 005037 001162          CLR      $REG0      ;THIS IS WHAT RKDB SHOULD CONTAIN
2313 007556 017737 171560 001164  MOV     JRKDB,$REG1 ;GET RKDB
2314 007564 104037          ERROR    37          ;RKDB SHOULD BE 0 AFTER WRT SINCE THE
2315                                     ;LAST WORD WRITTEN WAS SERIALLY SHIFTED
2316                                     ;OUT OF RKDB
2317 007566 005201          6$:  INC      R1          ;INCREMENT DRIVE ADDR TO NXT SECTOR
2318 007570 005205          INC      R5
2319 007572 122705 000014          CMPB    #14,R5      ;R U GOING TO CHECK THE LAST SECTOR?
2320 007576 001002          BNE     .+6         ;IF NOT, BRANCH
2321 007600 062705 000004          ADD     #4,R5       ;IF YES, INCREMENT R5 CORRECTLY TO 'EXPCTD RKDA'
2322                                     ;AFTER HAVING CHECKED THE LAST SECTOR
2323 007604 005202          INC      R2          ;HAVE U FORMATTED ALL 12 SECTORS?
2324 007606 001317          BNE     1$          ;IF NOT, BRANCH BACK & LOOP
2325                                     ;IF YES, EXIT
2326
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2346 007610 000004          ;*****
2347 007612 005005          ;*TEST 21      CHECK 'READ FORMAT'-CYLINDER 0, SECTOR 0-13
2348 007614 104413          ;*THIS TEST PERFORMS A RD FMT ON THE 12 SECTORS OF CYLINDER 0
2349                                     ;*THE FOLLOWING IS CHECKED
2350                                     ;*1) IF CNTRL RDY SET WITHIN A CERTAIN TIME ON COMPLETION
2351                                     ;*OF THE FUNCTION
2352                                     ;*2) IF 'HE' OR 'ERR' BIT SET?
2353                                     ;*3) IF THE RKDA INCREMENTS CORRECTLY?
2354                                     ;*4) RKBA INCREMENTED CORRECTLY BY 30 (OCTAL)
2355                                     ;*5) RKWC OVERFLOWED TO 0 FROM -14 (OCTAL)
2356                                     ;*6) CORRECT HEADER WAS RECEIVED FROM ALL 12 SECTORS.
2357                                     ;*7) RKCS STILL CONTAINS THE 'RD FMT' FUNCTION BITS.
2358                                     ;*IF THERE IS A READ ERROR IN THIS TEST OR ANY
2359                                     ;*OTHER TESTS THE USER SHOULD MAKE SURE THAT
2360                                     ;*IT IS AN IRRECOVERABLE ERROR AND NOT A TRANSIENT
2361                                     ;*ONE. THIS CAN BE DONE BY LOOPING ON THE TEST
2362                                     ;*IN QUESTION. USUALLY A TRANSIENT ERROR
2363                                     ;*DISAPPEARS ON RETRIES, WHEREAS A LOGIC ERROR DOES NOT.
2364                                     ;*****
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;*****
;*TEST 21      CHECK 'READ FORMAT'-CYLINDER 0, SECTOR 0-13
;*THIS TEST PERFORMS A RD FMT ON THE 12 SECTORS OF CYLINDER 0
;*THE FOLLOWING IS CHECKED
;*1) IF CNTRL RDY SET WITHIN A CERTAIN TIME ON COMPLETION
;*OF THE FUNCTION
;*2) IF 'HE' OR 'ERR' BIT SET?
;*3) IF THE RKDA INCREMENTS CORRECTLY?
;*4) RKBA INCREMENTED CORRECTLY BY 30 (OCTAL)
;*5) RKWC OVERFLOWED TO 0 FROM -14 (OCTAL)
;*6) CORRECT HEADER WAS RECEIVED FROM ALL 12 SECTORS.
;*7) RKCS STILL CONTAINS THE 'RD FMT' FUNCTION BITS.
;*IF THERE IS A READ ERROR IN THIS TEST OR ANY
;*OTHER TESTS THE USER SHOULD MAKE SURE THAT
;*IT IS AN IRRECOVERABLE ERROR AND NOT A TRANSIENT
;*ONE. THIS CAN BE DONE BY LOOPING ON THE TEST
;*IN QUESTION. USUALLY A TRANSIENT ERROR
;*DISAPPEARS ON RETRIES, WHEREAS A LOGIC ERROR DOES NOT.
;*****

```

```

;*****
;TST21:  SCOPE
;        CLR      R5
;        CNT.RESET
;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10
;GO CHECK IF SIN IS SET
;IS .SET, DO DRIVE RESET TO CLR IT
;SET UP COUNT FOR 12 SECTORS
2358 007616 104421          TST.SIN
2359
2360 007620 013701 001332          MOV     RKCS,R1
2361 007624 012700 177764          MOV     #-14,R0
2362 007630 013702 001340          MOV     RKDA,R2

```

K05

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SEQ 0062

2363	007634	013712	001350		MOV	DRIVAD, R2	; ADDRESS THE DRIVE
2364	007640	012704	033240		MOV	#OUTBUF, R4	
2365	007644	010477	171466		MOV	R4, RKB4	; ADRS TO WHICH X-FER DATA FROM DSK
2366	007650	012777	177764	171456	MOV	#-14, RKWC	; SET UP WORD COUNT FOR 12 HEADERS TO BREAD
2367	007656	012777	002005	171446	MOV	#2005, RKCS	; READ FORMAT, GO
2368							
2369	007664	105777	171442		1\$: TSTB	RKCS	; DID CNTRL RDY SET ON COMPLETION?
2370	007670	100411			BMI	2\$; YES, BRANCH
2371	007672	005205			INC	R5	; NO, WAIT FOR IT TO SET
2372	007674	001373			BNE	1\$; IF WAITED LONG ENOUGH REPORT
2373							; ERROR, OTHERWISE LOOP BACK & WAIT
2374	007676	004737	020702		JSR	PC, GT4RG	; GO, GET RKCS, ER, DS, DA
2375	007702	013737	001350	001202	MOV	DRIVAD, \$REG10	
2376	007710	104416			BRKDA4		; GO TO 'BD4' & BREAK CONTENTS OF
2377							; \$REG10 INTO DR#, CYL, SUR, SEC BITS
2378	007712	104045			ERROR	45	; CNTRL RDY DID NOT SET ON COMPLETION
2379							; OF READ FORMAT-OF CYLINDER 0,
2380							; SECTORS 0-13
2381							; READ FMT WAS DONE STARTING AT <DSK-ADRES>
2382							; INDICATED IN EROR MESGE
2383	007714	004737	021142		2\$: JSR	PC, CHKHE	; CHECK IF 'ERR' OR 'HE' BIT IS SET,
2384							; IF YES RETURN HERE.
2385	007720	104046			ERROR	46	; 'ERR' OR 'HE' BIT SET ON DOING
2386							; READ FMT-OF CYLINDER 0, SEC 0-13
2387							; READ FMT WAS DONE STARTING AT <DSK-ADRES>
2388							; INDICATED IN EROR MESGE
2389	007722	013705	001350		3\$: MOV	DRIVAD, R5	
2390	007726	062705	000020		ADD	#20, R5	; RKDA SHOULD HAVE INCREMENTD TO (R2)
2391							
2392	007732	004737	021176		JSR	PC, CHKDA1	; CHECK IF RKDA INCREMENTED CORRECTLY,
2393							; IF NOT, RETURN HERE.
2394	007736	104040			ERROR	40	; RKDA DID NOT INCREMENT BY 12
2395							; AFTER A 'RD FMT' OF 12 HEADERS OF
2396							; CYLINDER 0, SECTORS 0-13
2397							; RKBA SHOULD INCREMENT BY 24 BYTES
2398							; AT THE END OF X-FER
2399	007740	022777	033270	171370	4\$: CMP	#OUTBUF+30, RKB4	; DID RKBA INCREMENT CORRECTLY?
2400	007746	001407			BEQ	5\$; YES, BRANCH
2401	007750	012737	033270	001162	MOV	#OUTBUF+30, \$REG0	; GET EXPCTD RKBA
2402	007756	017737	171354	001164	MOV	RKB4, \$REG1	; GET ACTUAL RKBA
2403	007764	104042			ERROR	42	; RKBA DID NOT INCREMENT CORRECTLY
2404							; AFTER READ FORMAT OF 12 HEADERS
2405	007766	004737	021224		5\$: JSR	PC, CHKWC	; GO CHECK IF RKWC OVERFLOWED TO 0
2406							; IF NOT RETURN HERE.
2407	007772	104041			ERROR	41	; RKWC DID NOT OVERFLOW TO 0
2408							; AFTER 'RD FMT' OF 12 HEADERS
2409							; OF CYLINDER 0
2410	007774	005724			6\$: TST	(R4)+	; WAS THE CORRECT HEADER RECIEVED?
2411	007776	001413			BEQ	7\$; YES, BRANCH
2412	010000	010037	001162		MOV	R0, \$REG0	; GET SECTOR FOR WHICH THE HEADER
2413	010004	062737	000014	001162	ADD	#14, \$REG0	; COULD NOT BE READ CORRECT
2414	010012	005037	001164		CLR	\$REG1	; EXPCTD HEADER-0, FOR CYL 0
2415	010016	014437	001166		MOV	-(R4), \$REG2	; GET WRONG HEADER RECVD
2416	010022	104043			ERROR	43	; HEADER WAS NOT READ RIGHT FOR
2417							; SECTOR (AS IN ER MSGE), & CYL 0
2418	010024	005724			TST	(R4)+	; WAS THE CORRECT HEADER RECVD?


```

2587
2588
2589 010406 005777 170730
2590 010412 001407
2591 010414 013737 001342 001162
2592 010422 017737 170714 001164
2593 010430 104102

```

```

TST 2RKDB
BEQ TST23
MOV RKDB,$REG0
MOV 2RKDB,$REG1
ERROR 102

```

```

: THIS IS A VERY BASIC ERR & IF IT
: OCCURS GO BACK TO TEST 10
: DID CNTRL RESET CLEAR RKDB?
: YES, EXIT
: GET ADRES OF RKDB
: GET CONTENTS OF RKDB
: CONTROL RESET DID NOT
: CLEAR RKDB

```

```

:*****
:TEST 23 CHECK 'WRITE FORMAT' OF THE DISK

```

```

: *THIS TEST WRITE FORMATS THE ENTIRE DISK. THE FIRST
: *WORD OF EVERY SECTOR IS WRITTEN TO BE A PSEUDO-HEADER
: *CONSISTING OF THE DRIVE #, CYLINDER #, SURFACE & SECTOR #.
: *1 SECTOR IS WRITTEN AT A TIME. THE WRITING IS DONE
: *IN THIS ORDER: CYL 0-SUR 0; CYL 0-SUR 1; CYL 1-SUR 0
: *CYL 1-SUR 1; CYL 2-SUR 0; CYL 2-SUR 1----- CYL 312-SUR 1.
: *IMPORTANCE OF THIS TEST SHOULD BE REALIZED, THIS IS
: *THE FIRST TIME EACH & EVERY SECTOR ON THE DISK IS
: *ACCESSED & WRITTEN ON. THIS IS THE FIRST TIME RKDA
: *IS BEING MADE TO INCREMENT OVER THE ENTIRE DISK (FROM
: *000000 TO 014520) IF A 'SIN' OCCURS AT ANY POINT
: *A DRIVE RESET IS DONE BEFORE DOING WRT FMT FOR THE NEXT
: *SECTOR. ANY OTHER ERROR IS CLEARED THROUGH A CONTROL RESET.
: *THE FOLLOWING CHECKING IS DONE AFTER WRITING EACH
: *CYLINDER.
: *1. CNTRL RDY SETS WITHIN A CERTAIN TIME ON COMPLETION
: *OF THE FUNCTION.
: *2. IF 'SIN' OCCURRED?
: *3. IF 'HE' OR 'ERR' BIT SET?
: *4. IF RKDA INCREMENTED CORRECTLY, INCLUDING BOUNDARY
: *CONDITIONS (SECTOR COUNTER BITS OVERFLOWING INTO SURFACE,
: *SURFACE BIT OVERFLOWING INTO CYLINDER BITS) AT THE END
: *OF THIS POINTERS ARE INCREMENTED ADJUSTED, ETC.
: *8 'WRT FMT' ON THE NEXT SECTOR IS DONE.

```

```

2624 010432 000004
2625 010434 012737 000001 001206
2626 010442 012737 010472 001110
2627
2628 010450 005003
2629
2630 010452 012704 177465
2631 010456 012702 177764
2632 010462 013701 001350
2633 010466 010105
2634 010470 005205
2635 010472 104413

```

```

:*****
:†ST23: SCOPE
MOV #1,$TIMES
MOV #1,$SLPERR
CLR R3
MOV #-313,R4
MOV #-14,R2
MOV DRIVAD,R1
MOV R1,R5
INC R5
IS: CNT.RESET

```

```

: DO 1 ITERATION
: SET RETURN ADRES FOR LUPING
: ON ERROR (SW 9)
: (R3)=0, SURFACE 0 BEING WRITTEN
: (R3)-1, SURFACE 1 BEING WRITTEN
: SET UP COUNT FOR 203 CYLINDERS
: SET UP COUNT FOR 12 SECTORS
: GET DRIVE ADRES
: STORE IT
: GO, DO CONTROL RESET
: THIS IS A CALL FOR THE 'CNTRL-
: RESET' ROUTINE. A CONTROL RESET IS
: ISSUED AND AFTER A CERTAIN TIME
: IF THE 'CNTRL RDY' DOES NOT SET
: AN ERROR IS REPORTED. NOTE THAT
: THE PC IN ERROR MESSAGE IS THE
: PC WHERE 'CNT.RESET' IS LOCATED.

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2636
2637
2638
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2640
2641
2642

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2699                                     ;ON THIS CYLINDER
2700 010642 001313                       BNE     1$      ;IF NOT, LOOP BACK & FORMAT THE
2701                                     ;NEXT SECTOR
2702                                     ;YES
2703 010644 012702 177764                 MOV     #-14,R2 ;RESET THE COUNT FOR 12 SECTORS
2704 010650 042701 000037                 BIC     #37,R1  ;CLEAR THE SEC ADRES BITS
2705 010654 005703                         TST     R3      ;SURFACE 1?
2706 010656 001006                       BNE     B$      ;YES, BRANCH
2707 010660 005203                       INC     R3      ;NO, SET FLAG
2708 010662 062701 000020                 ADD     #20,R1  ;INCREMENT TO THE NXT SURFACE
2709 010666 010105                       MOV     R1,R5   ;THIS IS WHAT RKDA SHOULD
2710 010670 005205                       INC     R5      ;INCREMENT TO.
2711 010672 000677                       BR      1$      ;GO, DO NXT SURFACE
2712 010674 062701 000040                 B$: ADD   #40,R1 ;INCREMENT TO NXT CYL
2713 010700 010105                       MOV     R1,R5   ;POSITION FOR
2714 010702 005205                       INC     R5      ;EXPCD RKDA
2715 010704 005003                       CLR     R3
2716 010706 005204                       INC     R4
2717 010710 001270                       BNE     1$      ;HAVE U FORMATTED ALL 203 CYLINDERS
2718                                     ;IF NOT, LOOP BACK & FORMAT THE
2719                                     ;NEXT CYLINDER

```

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*****
; *TEST 24 CHECK 'READ FORMAT' FOR THE ENTIRE DISK
; *THIS TEST READ FORMATS THE ENTIRE DISK, WHICH WAS WRT
; *FORMATTED IN THE PREVIOUS TEST. THE FOLLOWING CHECKING
; *IS DONE
; *1. CNTRL RDY SETS WITHIN A CERTAIN TIME ON COMPLETION
; *OF FUNCTION
; *2. IF 'SIN' OCCURRED?
; *3. IF 'HE' OR 'ERR' OCCURRED?
; *4. RKDA INCREMENTED CORRECTLY.
; *5. IF THE CORRECT HEADER WAS READ.
; *6. IF RKWC OVERFLOWED CORRECTLY.
; *12 SECTORS (1 CYLINDER) ARE READ AT A TIME. IF 'SIN'
; *OCCURS A DRIVE RESET IS DONE BEFORE READING THE NEXT
; *SECTOR. READING IS DONE IN THIS ORDER CYL 0-SUR 0;
; *CYL 0-SUR 1; CYL 1-SUR 0; CYL 1-SUR 1; CYL 2-SUR 0;
; *CYL 2-SUR 1;-----CYL 312-SUR 1. IF TESTING ON SIMULATOR, ONLY
; *THE LAST CYLINDER (312), LAST SECTOR (13), SURFACE 1 IS READ.
*****

```

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2740
2741 010712 000004                         †TST24: SCOPE
2742 010714 012737 000001 001206         MOV     #1,$TIMES ;DO 1 ITERATION
2743 010722 012737 011006 001110         MOV     #1$,SLPERR ;SET RETURN ADRES FOR LUPING
2744                                     ;ON ERROR (SW 9)
2745 010730 005037 001356                 CLR     INDX1   ;INDX1=0, SURFACE 0 BEING READ
2746                                     ;INDX1=1, SURFACE 1 BEING READ
2747 010734 013701 001350                 MOV     DRIVAD,R1 ;GET DRIVE ADRES
2748 010740 010102                       MOV     R',R2
2749 010742 005737 001344                 TST     SIMUL   ;TESTING ON SIMULATOR?
2750 010746 001410                       BEQ     12$     ;NO, BRANCH
2751 010750 052701 014533                 BIS     #14533,R1 ;SET BITS FOR CYL 312, SEC 13, SUR 1
2752                                     ;ON SIMULATOR, CHECK ONLY CYL 312,
2753                                     ;SECTOR 13, SURFACE 1
2754 010754 052702 014540                 BIS     #14540,R2 ;RKDA SHOULD INCRMNT TO THIS AFTR

```


2811	011116	104001			ERROR	1			:SIN ERROR ON DOING RD FMT :TO CYL INDICATED IN \$REG3
2812									
2813									
2814	011120	004737	021134	4\$:	JSR	PC,CHKHE1			:CHECK IF 'ERR' OR 'HE' BIT IS SET, :IF YES, RETURN HERE.
2815									:HE OR ERR WHILE DOING A READ
2816	011124	104046			ERROR	46			:FORMAT. 'RKDA' IN EROR MSGE GIVES :THE CONTENTS OF RKDA AT THE TIME OF ERROR :READ FMT WAS DONE STARTING AT 'DSK-ADRES' :INDICATED IN EROR MESGE
2817									:DID RKDA INCREMENT CORRECTLY BY 12 SEC
2818									
2819									
2820									
2821	011126	020277	170206	5\$:	CMP	R2,ARKDA			
2822	011132	001410			BEG	6\$			
2823	011134	010237	001202		MOV	R2,\$REG10			:GET EXPCTD RKDA
2824	011140	104415			BRKDAO				:GO TO 'BDAD' & BREAK CONTENTS OF :\$REG10 INTO DR #,CYL,SUR,SEC BITS
2825									:GET RECVD RKDA
2826	011142	017737	170172	001202	MOV	ARKDA,\$REG10			:GO TO 'BDAD' & BREAK CONTENTS OF :\$REG10 INTO DR #,CYL,SUR,SEC BITS
2827	011150	104416			BRKDA4				:RKDA DID NOT INCREMENT BY 12 SECTOPS :AFTER RD FMT WAS DONE. ADRES :OF CYLINDER IN ERROR CAN BE OBTAINED :FROM 'EXPCTD' RDDA
2828									:SET UP COUNT FOR 12 HEADERS TO B CHKD :(ONLY 1, IF SIMULATOR)
2829	011152	104040			ERROR	40			:GET DRV-ADRES FROM WHERE RDFMT WAS DONE :GET THE CYLINDER ADRES ONLY. (HEADER) :IS THE RECVD HEADER SAME AS EXPCTD?
2830									
2831									
2832									
2833	011154	013700	001370	6\$:	MOV	EFLG1,R0			
2834									
2835	011160	010104			MOV	R1,R4			
2836	011162	042704	160037		BIC	#160037,R4			
2837	011166	020413		7\$:	CMP	R4,(R3)			
2838	011170	001412			BEG	8\$			
2839	011172	010437	001164		MOV	R4,\$REG1			:GET EXPCTD HEADER WORD
2840	011176	011337	001166		MOV	(R3),\$REG2			:GET HEADER WORD RECVD
2841	011202	010037	001162		MOV	R0,\$REG0			
2842	011206	062737	000014	001162	ADD	#14,\$REG0			:GET THE SECTOR (OCTAL NO) WHICH DID :NOT GIVE THE CORRECT HEADER :DID NOT RECIEVE THE CORRECT HEADER :WORD FROM 'SECTOR' AS INDICATED :(NOTE SECTOR # IS OCTAL)
2843									:INCREMENT POINTER TO THE NXT WORD :IN MEMORY WHERE THE RECVD HDR IS STORED :HAVE U CHECKED ALL 12 HEADERS? :IF NOT, LOOP BACK & CHK THE NXT. :YES, ALL HEADERS FOR THIS CYLINDER :CHECKED.
2844	011214	104043			ERROR	43			:CHECK IF RKWC OVERFLOWED TO 0, IF :NOT RETURN HERE.
2845									:RKWC DID NOT OVERFLOW AFTER DOING :RDFMT OF 12 SECTORS ON THE CYLINDER :NOTE THAT 'RKDA' IS THE INCREMENTED :RKDA AFTER THE RDFMT
2846									:TSTING ON SIMULATOR?
2847	011216	005723		8\$:	TST	(R3)+			:IF YES, EXIT :NO
2848									
2849	011220	005200			INC	R0			
2850	011222	001361			BNE	7\$			
2851									
2852									
2853	011224	004737	021224		JSR	PC,CHKWC			
2854									
2855	011230	104041			ERROR	41			
2856									
2857									
2858									
2859	011232	005737	001344	9\$:	TST	SIMUL			
2860	011236	001031			BNE	TST25			
2861									
2862	011240	005737	001356		TST	INDX1			:DOING SURFACE 1
2863	011244	001011			BNE	10\$:YES, BRANCH
2864	011246	005237	001356		INC	INDX1			:NO
2865	011252	062701	000020		ADD	#20,R1			:INCREMENT DRV ADRES TO THE NXT SURFACE
2866	011256	010102			MOV	R1,R2			

H06

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 DZRKKD.P11 22-SEP-76 08:47 T25 CHECK 'READ' OF THE ENTIRE DISK

SEQ 0072

2923									; IF THE 'CNTRL RDY' DOES NOT SET
2924									; AN ERROR IS REPORTED. NOTE THAT
2925									; THE PC IN ERROR MESSAGE IS THE
2926									; PC WHERE 'CNT.RESET' IS LOCATED.
2927									; THIS IS A VERY BASIC ERR & IF IT
2928									; OCCURS GO BACK TO TEST 10
2929	011400	104421							; GO CHECK SIN, IF SET DO
2930									; DRIVE RESET TO CLR IT
2931	011402	005037	001356		8\$:	CLR	INDX1		
2932	011406	010377	167724			MOV	R3,ARKBA		; ADRES TO WHICH DATA IS TO B X-FERRED
2933									; FROM THE DISK
2934	011412	012777	177777	167714		MOV	#-1,ARKWC		; SET UP WORD COUNT
2935	011420	010177	167714			MOV	R1,ARKDA		; ADRES THE DRIVE WITH CORRECT
2936									; CYLINDER & SECTOR ADRES
2937	011424	012777	000005	167700		MOV	#5,ARKCS		; READ, GO
2938									
2939	011432	105777	167674		2\$:	TSTB	ARKCS		; DID CNTRL RDY SET?
2940	011436	100411				BMI	3\$; YES, BRANCH
2941	011440	005237	001356			INC	INDX1		; NO, HAVE U WAITED LONG ENOUGH
2942	011444	001372				BNE	2\$; IF NOT, LOOP BACK & WAIT FOR IT
2943									; IF YES, REPORT ERROR
2944	011446	004737	020702			JSR	PC,GT4RG		; GO, GET RKCS, ER, DS,DA
2945	011452	010137	001202			MOV	R1,\$REG10		; GET DISK-ADRES WHERE ERROR OCCURED
2946	011456	104416				BRKDA4			; GO TO 'BD44' & BREAK CONTENTS OF
2947									; \$REG10 INTO DR #,CYL,SUR,SEC BITS
2948	011460	104045				ERROR	45		; CNTRL RDY DID NOT SET AFTER DOING
2949									; A 1 WORD READ FROM ADRES AS
2950									; INDICATED IN <DISK-ADRES>
2951									; 'RKDA' IN EROR MSGE GIVES THE
2952									; CONTENTS OF RKDA AT THE TIME OF ERROR
2953									
2954	011462	032777	001000	167636	3\$:	BIT	#1000,ARKDS		; DID 'SIN' SET?
2955	011470	001405				BEQ	4\$; NO, BRANCH
2956	011472	004737	020710			JSR	PC,GT3RG		; GO, GET RKCS, ER, DS
2957	011476	010137	001170			MOV	R1,\$REG3		; GET DISK-ADRES WHERE SIN OCCURED3
2958	011502	104001				ERROR	1		; 'SIN' ERROR ON DOING READ FROM
2959									; DISK-ADRES INDICATED IN \$REG3
2960	011504	004737	021134		4\$:	JSR	PC,CHKHE1		; CHECK IF 'ERR' OR 'HE' BIT IS SET,
2961									; IF YES, RETURN HERE.
2962	011510	104046				ERROR	46		; 'HE' OR 'ERR' ON DOING A READ OF
2963									; 1 WORD FROM ADRES AS INDICATED
2964									; IN <DISK-ADRES>
2965									; 'RKDA' IN EROR MSGE GIVES THE
2966									; CONTENTS OF RKDA AT THE TIME OF EROR
2967	011512	020113			5\$:	CMP	R1,(R3)		; WAS THE CORRECT DATA WORD RECVD?
2968	011514	001407				BEQ	6\$		
2969	011516	010137	001162			MOV	R1,\$REG0		; GET EXPCTD DATA WORD
2970	011522	011337	001164			MOV	(R3),\$REG1		; GET DATA WORD RECVD
2971	011526	010137	001166			MOV	R1,\$REG2		; GET DISK-ADRES
2972	011532	104044				ERROR	44		; DID NOT RECIEVE THE CORRECT
2973									; DATA WORD FROM DISK ON DOING
2974									; 1 WORD READ FROM 'DISK-ADRES'
2975									; AS INDICATED BY 'EXPCTD' DATA WORD
2976									; NOTE THAT IN A PREVIOUS TEST THE
2977									; FIRST WORD OF EACH SECTOR IS UNIQUELY
2978									; WRITTEN WITH A WORD GIVING THE

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3091	012044	012777	033240	167264	MOV	#OUTBUF, ARKBA	; READ ONE HEADER INTO THIS
3092	012052	012777	177777	167254	MOV	#-1, ARKWC	; BUS ADRES
3093	012060	012710	002005		MOV	#2005, AR0	; GO READ FORMAT
3094	012064	104414			CNT.RDY		; WAIT FOR CNTRL RDY
3095	012066	021337	033240		CMP	(R3), OUTBUF	; WAS THE CORRECT READER READ (FROM
3096	012072	001410			BEG	11\$; CYLINDER TO WHICH SEEK WAS DONE BEFORE)
3097	012074	005037	001162		CLR	\$REG0	; STORE SEC # FROME WHERE HDR WAS RD (0)
3098	012100	011337	001164		MOV	(R3), \$REG1	; GET EXPCTD HEADER
3099	012104	013737	033240	001166	MOV	OUTBUF, \$REG2	; GET HDR RECVD
3100	012112	104043			ERROR	43	; WRONG HDR WAS RECVD FROM CYLINDER (ADRES
3101							; IN ER MSGE). NOTE THAT A PURE SEEK WAS
3102							; DONE TO THIS CYL BEFORE READING HDR
3103							; USING READ FORMAT
3104	012114	005737	001356		11\$: TST	INDX1	; SEEK IN REVRSE DIRECTION?
3105	012120	001007			BNE	12\$; YES, BRANCH
3106	012122	005723			TST	(R3)+	; NO, INCREMENT PTR TO NXT SEEK ADRES
3107	012124	022703	001400		CMP	#SEEK2+2, R3	; DONE WITH ALL SKS IN FWD DIR?
3108	012130	001260			BNE	1\$; NO, GO & DO NXT ONE
3109	012132	005237	001356		INC	INDX1	; SET FLAG INDICATING SK IN REVRSE
3110	012136	005743			TST	-(R3)	
3111	012140	005743			12\$: TST	-(R3)	; POSITION PTR TO NXT SK IN REV
3112	012142	022703	001370		CMP	#SEEK0-2, R3	; DONE WITH ALL?
3113	012146	001251			BNE	1\$; IF NOT, DO NXT ONE

```

;*****
; *TEST 27 CHECK DRIVE RESET FROM LAST CYLINDER
; *THE HEADS ARE POSITIONED ON THE LAST CYLINDER (DOING
; *AN IMPLIED SEEK-READ). THEN A DRIVE RESET IS ISSUED.
; *IT'S CHECKED IF THE HEADS WERE BROUGHT BACK TO 0 BY
; *DOING A 1 WORD READ & CHECKING THAT THE CORRECT WORD
; *WAS RECEIVED. IF TESTING ON SIMULATOR THIS TEST IS SKIPPED.
;*****

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3125	012150	000004			TST27: SCOPE		
3126	012152	012737	000005	001206	MOV	#5, \$TIMES	; DO 5 ITERATIONS
3127	012160	005737	001344		TST	SIMUL	; R U ON A SIMULATOR?
3128	012164	001124			BNE	TST30	; YES, EXIT
3129	012166	013701	001332		MOV	ARKCS, R1	
3130	012172	104413			CNT.RESET		; GO, DO CONTROL RESET
3131							; THIS IS A CALL FOR THE 'CNTRL-
3132							; RESET' ROUTINE. A CONTROL RESET IS
3133							; ISSUED AND AFTER A CERTAIN TIME
3134							; IF THE 'CNTRL RDY' DOES NOT SET
3135							; AN ERROR IS REPORTED. NOTE THAT
3136							; THE PC IN ERROR MESSAGE IS THE
3137							; PC WHERE 'CNT.RESET' IS LOCATED.
3138							; THIS IS A VERY BASIC ERR & IF IT
3139							; OCCURS GO BACK TO TEST 10
3140	012174	005000			CLR	RD	
3141	012176	012703	033240		MOV	#OUTBUF, R3	; ADRES WHERE DATA WILL BE READ INTO
3142	012202	013704	001350		MOV	DRIVAD, R4	
3143	012206	010405			MOV	R4, R5	
3144	012210	052705	014500		BIS	#14500, R5	; SET CYL ADRES=312 (OCTAL)
3145	012214	010577	167120		MOV	R5, ARKDA	; ADRES THE DRIVE, LAST CYLINDER
3146	012220	012777	177777	167106	MOV	#-1, ARKWC	; READ 1 WORD

3147	012226	010377	167104		MOV	R3,ARKBA		; INTO THIS MEMORY ADRES
3148								
3149	012232	012711	000005		MOV	#5,AR1		; READ, GO
3150								
3151	012236	005000			CLR	RO		
3152	012240	104414		1\$:	CNT.RDY			; THIS IS A CALL FOR CN.RDY ROUTINE
3153								; WHICH WAITS FOR CNTRL RDY TO SET.
3154								; A RETURN IS MADE AFTER CNTRL RDY
3155								; SETS. IF WITHIN A CERTAIN TIME
3156								; CNTRL RDY DOESN'T SET AN ERROR
3157								; MESSAGE IS GIVEN. WAITING TIME
3158								; 883 MS FOR 11/20, 175 MS FOR 11/45
3159	012242	020513		2\$:	CMP	RS,AR3		; WAS THE CORRECT WORD READ?
3160	012244	001407			BEG	3\$; YES, SEEK TO 312 WAS DONE CORRECTLY,3
3161	012246	010537	001162		MOV	RS,\$REG0		; GET EXPCTD WORD
3162	012252	011337	001164		MOV	AR3,\$REG1		; GET WORD RECVD
3163	012256	010537	001166		MOV	RS,\$REG2		; GET DSK-ADRES FROM WHERE WORD WAS READ
3164	012262	104044			ERROR	44		; DID NOT READ BACK CORRECT WORD FROM
3165								; LAST CYL, SEC 0. IF TEST 45 & 46
3166								; WERE SUCCESSFULLY DONE THIS
3167								; ERROR MEANS THAT IMPLIED SEEK
3168								; TO CYL 312 COULD NOT B DONE
3169	012264	012711	000015	3\$:	MOV	#15,AR1		; DRIVE RESET, GO
3170	012270	104414			CNT.RDY			; THIS IS A CALL FOR CN.RDY ROUTINE
3171								; WHICH WAITS FOR CNTRL RDY TO SET.
3172								; A RETURN IS MADE AFTER CNTRL RDY
3173								; SETS. IF WITHIN A CERTAIN TIME
3174								; CNTRL RDY DOESN'T SET AN ERROR
3175								; MESSAGE IS GIVEN. WAITING TIME
3176								; 883 MS FOR 11/20, 175 MS FOR 11/45
3177	012272	005000			CLR	RO		
3178	012274	032777	000100 167024	4\$:	BIT	#100,ARKDS		; DID R/W/S RDY SET?
3179	012302	001011			BNE	5\$; YES, BRANCH
3180	012304	012702	177763		MOV	#-15,R2		; IF U R ON A SLOWER MACHINE
3181	012310	005202			INC	R2		; & DO NOT NEED SUCH A LARGE MACHINE
3182	012312	001376			BNE	.-2		; TIME LOOP, CHANGE THESE 3
3183								; INSTRUCTIONS TO 'NOP' THE
3184								; LOOP TIME WILL BE REDUCED
3185								; TO 1100 MS
3186								
3187								; THE TOTAL TIME FOR THE ABOVE
3188								; LOOPS (W/O PUTTING 'NOP'S) IS
3189								; 5304 MS FOR 11/20 AND
3190								; 1061 MS FOR 11/45 WITH MOS
3191								; OR BIPOLAR MEMORY
3192	012314	005200			INC	RO		; WAITED LONG?
3193	012316	001366			BNE	4\$; IF NOT, LUP BAK & WAIT
3194								; IF YES, ERROR
3195	012320	004737	020702		JSR	PC,GT4RG		; GET RKCS,ER,DS,DA
3196	012324	104026			ERROR	26		; R/W/S RDY DID NOT SET AFTER
3197								; DOING DRIVE RESET
3198	012326	032711	140000	5\$:	BIT	#140000,AR1		; DID HE OR ERR BIT SET?
3199	012332	001403			BEG	6\$; IF NOT, BRANCH
3200								
3201	012334	004737	020702		JSR	PC,GT4RG		; GET RKCS,ER,DS,DA FOR ERROR MESSAGE
3202	012340	104022			ERROR	22		; HE OR ERR BIT SET ON DOING DRIVE

M06

MAINDEC-11-DZRKK-D
DZRKKD.P11 22-SEP-76

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T27 CHECK DRIVE RESET FROM LAST CYLINDER

SEQ 0077

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3203                                     ;RESET FROM LAST CYLINDER
3204 012342 005205 6$: INC R5 ;POSITION R5 TO EXPCTD RKDA
3205 012344 020577 166770 CMP R5,ARKDA ;DID THE CYL ADRES BITS IN RKDA GET CHANGED?
3206 012350 001406 BEQ 7$ ;NO, BRANCH
3207 012352 010537 001162 MOV R5,$REG0 ;GET EXPCTD RKDA
3208 012356 017737 166756 001164 MOV ARKDA,$REG1 ;GET RKDA RECVD
3209 012364 104054 ERROR 54 ;CYLINDER ADRES BITS IN RKDA
3210                                     ;GOT CHANGED AFTER
3211                                     ;DRIVE RESET, FROM LAST CYLINDER
3212 012366 012777 177777 166740 7$: MOV #-1,ARKWC ;READ 1 WORD
3213 012374 010377 166736 MOV R3,ARKBA ;INTO THIS ADRES
3214 012400 010477 166734 MOV R4,ARKDA ;FROM THIS DSK ADRES-CYL 0, SEC 0
3215
3216 012404 012711 000005 MOV #5,AR1 ;READ, GO
3217
3218 012410 005000 CLR R0
3219 012412 104414 8$: CNT.RDY ;THIS IS A CALL FOR CN.RDY ROUTINE
3220                                     ;WHICH WAITS FOR CNTRL RDY TO SET.
3221                                     ;A RETURN IS MADE AFTER CNTRL RDY
3222                                     ;SETS. IF WITHIN A CERTAIN TIME
3223                                     ;CNTRL RDY DOESN'T SET AN ERROR
3224                                     ;MESSAGE IS GIVEN. WAITING TIME
3225                                     ;883 MS FOR 11/20, 175 MS FOR 11/45
3226 012414 020413 9$: CMP R4,AR3 ;WAS THE CORRECT WORD READ?
3227 012416 001407 BEQ TST30 ;YES, EXIT
3228 012420 010437 001162 MOV R4,$REG0 ;GET EXPCTD WORD
3229 012424 011337 001164 MOV AR3,$REG1 ;GET WORD RECVD
3230 012430 010437 001166 MOV R4,$REG2 ;GET DISK ADRES WHERE ERROR OCCURED
3231 012434 104044 ERROR 44 ;DID NOT READ CORRECT WORD FROM
3232                                     ;CYL 0, SEC 0. IF TEST 45 & 46
3233                                     ;WERE SUCCESSFULLY DONE THIS
3234                                     ;ERROR COULD MEAN THAT DRIVE-RESET
3235                                     ;DID NOT BRING HEADS BACK TO 0.
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3246 012436 000004
3247 012440 104413
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3256 012442 104421
3257                                     TST.SIN
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*****
; *TEST 30 'WRITE' - 256 WORD BLOCK ON SECTOR 0, CYLINDER 0
; THE TEST BELOW SHOULD BE CONSIDERED AS A SET UP PHASE FOR
; THE FOLLOWING TEST. IT WRITES A BLOCK OF 256 WORDS IN
; SECTOR 0, CYLINDER 0 WITH A SPECIFIC PATTERN AND THIS WRITTEN
; BLOCK WILL BE MADE USE OF IN THE NEXT TEST TO CHECK
; OUT 'WRITE-CHECK' AND 'READ CHECK' FUNCTIONS.
*****

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TST30: SCOPE
CNT.RESET ;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR& IF IT
;OCCURS GO BACK TO TEST 10
;CHECK IF SIN IS SET, IF SET
;DO DRIVE RESET TO CLEAR IT

```

NO6

MAINDEC-11-DZRKK-D MACY11 27(1006) 04-OCT-76 16:06 PAGE 61
 DZRKKD.P11 22-SEP-76 08:47 T30 'WRITE' - 256 WORD BLOCK ON SECTOR 0, CYLINDER 0

SEQ 0078

3259	012444	013704	001332		MOV	RKCS,R4	
3260							; THE FOLLOWING CODE IS FOR SETTING
3261							; UP THE I/O BUFFER IN MEMORY (STARTING AT
3262							; OUTBUF), WITH A PARTICULAR 256 WORD PATTERN.
3263							; STARTING FROM THE FIRST WORD IN THE BUFFER
3264							; THE LO BYTE WILL BE A COUNT PATTERN
3265							; FROM 0 TO 255 (DECIMAL), WHEREAS THE
3266							; HI-BYTE WILL BE THE COMPLEMENT OF LO BYTE,
3267							; A DECREASING COUNT PATTERN FROM 255 TO 0.
3268							; I.E. THE BUFFER WILL LOOK LIKE:
3269							; OUTBUF (1 111 111 1 00 000)
3270							; OUTBUF+2 (1 111 111 0 00 000 001)
3271							; LAST WORD (0 000 000 0 11 111 111)
3272							
3273							
3274	012450	012700	033240		MOV	#OUTBUF,R0	
3275	012454	012701	177401		MOV	#177401,R1	; PATTERN GENERATING NUMBER
3276	012460	012702	177400		MOV	#-400,R2	; SET UP COUNT FOR 256 WORDS
3277	012464	012703	177400		MOV	#177400,R3	; SET UP THE FIRST PATTERN TO B WRITTEN
3278							
3279	012470	010320			MOV	R3,(R0)+	; SET UP FIRST WORD IN I/O BUFFER
3280	012472	005202			INC	R2	; INCREMENT COUNT
3281	012474	060103		1\$:	ADD	R1,R3	; SET UP NEXT WORD PATTERN
3282	012476	010320			MOV	R3,(R0)+	; WRITE IT IN NXT I/O BUFFER WORD
3283	012500	005202			INC	R2	; HAVE U WRITTEN ALL 256 WORDS
3284	012502	001374			BNE	1\$; IF NOT GO & WRITE NEXT PATTERN
3285							
3286	012504	012777	177400	166622	MOV	#-400,DRKWC	; WRITE 256 WORDS
3287	012512	012777	033240	166616	MOV	#OUTBUF,DRKBA	; STARTING FROM THIS BUS ADRES
3288	012520	013777	001350	166612	MOV	DRIVAD,DRKDA	; TO THIS DISK ADRES, CYL 0, SEC 0
3289							
3290	012526	012714	000003		MOV	#3,DR4	; WRITE, GO
3291							
3292	012532	105714			TSTB	DR4	; WAS CNTRL RDY CLEARED AS GO WAS SET?
3293	012534	100003		2\$:	BPL	3\$-2	; YES, BRANCH
3294	012536	004737	020710		JSR	PC,GT3RG	; GET RKCS, ER, DS
3295	012542	104030			ERROR	30	; CNTRL RDY DID NOT CLEAR AS GO WAS SET
3296							; TO 'WRITE'
3297							
3298	012544	005002			CLR	R2	
3299	012546	105777	166560	3\$:	TSTB	DRKCS	; DID CNTRL RDY SET?
3300	012552	100411			BMI	4\$; YES, BRANCH
3301	012554	005202			INC	R2	; WAITED LONG ENOUGH?
3302	012556	001373			BNE	3\$; IF NOT, LUP BAK & WAIT
3303							; IF YES, ERROR
3304	012560	004737	020702		JSR	PC,GT4RG	; GO, GET RKCD, ER, DS, DA
3305	012564	013737	001350	001202	MOV	DRIVAD,\$REG10	; GET THE STARING ADRES
3306	012572	104416			BRKDA4		; BREAK CONTENTS OF \$REG10 INTO
3307							; DRV #, CYL, SUR, SEC #
3308	012574	104031			ERROR	31	; CNTRL RDY DID NOT SET ON COMPLETION
3309							; OF WRITE OF 256 WORDS ON CYL 0, SEC 0
3310							; 'RKDA' IN EROR MSGE GIVES THE
3311							; CONTENTS OF RKDA AT THE TIME OF EROR
3312							; WRITE WAS DONE STARTING AT <DSK-ADRES>
3313							; INDICATED IN EROR MSGE
3314	012576	004737	021142	4\$:	JSR	PC,CHKHE	; CHECK IF 'ERR' OR 'HE' BIT IS SET,

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012602 104032

ERROR 32

: IF YES, RETURN HERE
: HE OR ERR BIT SET ON DOING WRITE OF
: 256 WORDS ON CYL 0, SEC 0
: WRITE WAS DONE STARTING AT <DSK-ADRES>
: INDICATED IN EROR MSGE
: 'RKDA' IN EROR MSGE GIVES THE
: CONTENTS OF RKDA AT THE TIME OF EROR
: DID RKBA INCREMENT CORRECTLY?

012604 020077 166526 55:
012610 001406
012612 010037 001162
012616 017737 166514 001164
012624 104035

CMP RD, RKBA
BEQ 65
MOV RD, \$REG0
MOV RKBA, \$REG1
ERROR 35

: YES, BRANCH
: GET EXPCTD RKBA
: GET RKBA RECVD
: RKBA DID NOT INCREMENT CORRECTLY
: (BY 1000 OCTAL BYTES) AFTER WRITE
: OF 400 (OCTAL) WORDS ON SEC 0, CYL 0
: CHECK IF RKWC OVERFLOWED TO 0,
: IF NOT RETURN HERE.

012626 004737 021224 65:

JSR PC,CHKWC

: RKWC DID NOT OVERFLOW, AFTER A
: WRITE OF 256 WORDS ON CYL 0, SEC 0
: CHECK IF RKDA INCREMENTED CORRECTLY,
: IF NOT RETURN HERE

012632 104034

ERROR 34

: RKDA DID NOT INCREMENT BY 1 AFTER
: A WRITE OF 256 WORDS IN CYL 0, SEC 0
: CHECK IF ANY BIT RKER IS SET
: IF YES RETURN HERE.

012634 004737 021170 75:

JSR PC,CHKDA

: RKER BIT SET ON DOING WRITE ON
: CYLINDER 0, SECTOR 0
: DOES RKCS STILL CONTAIN THE WRITE BITS?
: YES, EXIT

012640 104033

ERROR 33

: GET EXPECTED RKCS
: GET RKCS RECVD
: RKCS DID NOT CONTAIN THE 'WRITE'
: BITS AFTER THE FUNCTION WAS DONE.

012642 004737 021250 85:

JSR PC,CHKER

012646 104036

ERROR 36

012650 022714 000202 95:
012654 001406
012656 012737 000202 001162
012664 011437 001164
012670 104024

CMP #202, R4
BEQ TST31
MOV #202, \$REG0
MOV R4, \$REG1
ERROR 24

::*****

*TEST 31 CHECK THAT WRITE WAS DONE CORRECTLY
: *THIS TEST CHECKS IF THE 'WRITE' OF 256 WORDS DONE IN PREVIOUS
: *TEST IS GOOD. THE SEQUENCE OF OPERATIONS IS AS FOLLOWING:
: *1) DO A READ OF 256 WORDS FROM SECTOR 0, CYLINDER 0
: * INTO A BUFFER STARTING AT 'OUTBUF'
: *2) COMPARE & CHECK THE DATA THAT IS READ (STARTING AT 'OUTBUF')
: * WITH THE DATA THAT WAS GENERATED PREVIOUSLY
: *3) REPORT AN ERROR IF THE DATA READ BACK FROM DISK DOES
: * NOT COMPARE WITH DATA THAT WAS SUPPOSE TO HAVE BEEN WRITTEN

::*****

012672 000004
012674 104413

TST31: SCOPE
CNT.RESET

: GO, DO CONTROL RESET
: THIS IS A CALL FOR THE 'CNTRL-
: RESET' ROUTINE. A CONTROL RESET IS
: ISSUED AND AFTER A CERTAIN TIME
: IF THE 'CNTRL RDY' DOES NOT SET
: AN ERROR IS REPORTED. NOTE THAT
: THE PC IN ERROR MESSAGE IS THE
: PC WHERE 'CNT.RESET' IS LOCATED.
: THIS IS A VERY BASIC ERR& IF IT
: OCCURS GO BACK TO TEST 10

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013076 000004
013100 104413

013102 104421

013104 013701 001332
013110 013702 001334
013114 013703 001340
013120 013704 001336
013124 012737 052525 033240
013132 012712 177400
013136 013713 001350
013142 012714 033240
013146 012711 000013

013152 105711
013154 100003
013156 004737 020710
013162 104030
013164 104412

013166 104056

013170 032711 140000
013174 001403
013176 004737 020710
013202 104057

013204 032777 000002 166116
013212 001404
013214 017737 166110 001162
013222 104060

013224 005712
013226 001405
013230 011237 001162
013234 011137 001164
013240 104061

:TEST 32 CHECK 'READ CHECK' FUNCTION - CYLINDER 0, SECTOR 0
:*THIS TEST CHECKS OUT THE BASIC 'READ CHECK' LOGIC, USING THE DATA BLOCK
:*'CYLINDER, SECTOR 0) WRITTEN IN A PREVIOUS TEST. HENCE THE TEST WHICH
:*WRITES THE DATA BLOCK SHOULD BE DONE PRIOR TO THIS TEST.

↑ST32: SCOPE
CNT.RESET

:GO, DO CONTROL RESET
:THIS IS A CALL FOR THE 'CNTRL-
:RESET' ROUTINE. A CONTROL RESET IS
:ISSUED AND AFTER A CERTAIN TIME
:IF THE 'CNTRL RDY' DOES NOT SET
:AN ERROR IS REPORTED. NOTE THAT
:THE PC IN ERROR MESSAGE IS THE
:PC WHERE 'CNT.RESET' IS LOCATED.
:THIS IS A VERY BASIC ERR& IF IT
:OCCURS GO BACK TO TEST 10
:CHECK IF SIN IS SET, IF SET
:DO DRIVE RESET TO CLEAR IT

TST.SIN

MOV RKCS,R1
MOV RKWC,R2
MOV RKDA,R3
MOV RKBA,R4
MOV #52525,OUTBUF
MOV #-400,R2
MOV DRIVAD,R3
MOV #OUTBUF,R4
MOV #13,R1

:READ CHECK 256 WORDS
:STARTING FROM CYL 0, SECTOR 0
:READ CHECK, GO

1\$: TSTB R1
BPL 2\$
JSR PC,GT3RG
ERROR 30
2\$: CHKCRDY

:DID CNTRL RDY GET CLEARED AS GO WAS SET?
:YES, BRANCH
:GET RKCS, ER, DS
:CNTRL RDY DID NOT CLEAR AS GO
:GO CHECK IF CONTROL RDY IS SET
:IF SO, SKIP THE EROR MESSAGE.
:WAS SET TO 'READ CHECK'

ERROR 56
3\$: BIT #140000,R1
BEQ 4\$
JSR PC,GT3RG
ERROR 57

:CNTRL RDY DID NOT SET ON DOING
:'READ CHECK' FROM CYL 0, SEC 0
:DID 'ERR' OR 'HE' BIT SET?
:NO, BRANCH
:GO, GET RKCS, ER, DS FOR ERROR MESSAGE
:'ERR' OR 'HE' BIT SET ON DOING
:'READ CHECK' ON CYLINDER 0, SEC 0
:DID 'CSE' BIT SET IN RKER?

4\$: BIT #2,RKER
BEQ 5\$
MOV RKER,\$REGO
ERROR 60

:NO, BRANCH
:SOFT ERROR - CSE - ON DOING 'READ
:CHECK' ON CYLINDER 0, SECTOR 0
:U SHOULD HAVE GOT ERROR 102 ALSO
:DID WORD COUNT OVERFLOW TO 0?

5\$: TST R2
BEQ 6\$
MOV R2,\$REGO
MOV R1,\$REG1
ERROR 61

:YES, BRANCH
:GET RKWC
:GET RKCS
:WORD COUNT DID NOT OVERFLOW
:ON DOING 'READ CHK' ON CYL 0, SEC 0

E07

```

3483 013242 013702 001350 6S:  MOV   DRIVAD,R2      ;RKDA SHOULD INCREMENT
3484 013246 005202          INC   R2              ;TO THIS AFTER 'RD CHK' IS DONE
3485 013250 020213          CMP   R2,R3          ;DID RKDA INCREMENT CORRECTLY?
3486 013252 001405          BEQ   7S
3487 013254 010237 001162  MOV   R2,$REG0      ;GET EXPCTD RKDA
3488 013260 011337 001164  MOV   R3,$REG1      ;GET RKDA RECVD
3489 013264 104062          ERROR 62           ;RKDA DID NOT INCREMENT CORRECTLY
3490                                     ;(BY 1) ON DOING 'READ CHK' ON
3491                                     ;CYL 0, SEC 0
3492 013266 022714 033240 7S:  CMP   #OUTBUF,R4     ;DID RKBA GET CHANGED?
3493 013272 001406          BEQ   8S            ;NO, BRANCH (RKBA WON'T CHANGE, NO NPR'S)
3494 013274 012737 033240 001162  MOV   #OUTBUF,$REG0 ;GET EXPCTD RKBA
3495 013302 011437 001164  MOV   R4,$REG1      ;GET RKBA RECVD
3496 013306 104063          ERROR 63           ;RKBA CHANGED AFTER DOING 'READ CHK'
3497                                     ;ON CYLINDER 0, SECTOR 0, SHOULD
3498                                     ;NOT CHANGE, FOR, NO NPR'S.
3499 013310 022737 052525 033240 8S:  CMP   #52525,OUTBUF ;'OUTBUF' SHOULD STILL CONTAIN THE
3500                                     ;SAME WORD AS IT DID BEFORE 'RD CHK'
3501                                     ;NOTE THAT AT THE BEGINING OF THIS TEST
3502                                     ;52525 WAS WRITTEN INTO 'OUTBUF'
3503 013316 001412          BEQ   TST33        ;YES, EXIT
3504                                     ;REPORT ERROR IF 'OUTBUF' CHANGED
3505 013320 012737 033240 001162  MOV   #OUTBUF,$REG0 ;GET ADRES OF OUTBUF
3506 013326 012737 052525 001164  MOV   #52525,$REG1 ;GET EXPCTD WORD IN 'OUTBUF'
3507 013334 013737 033240 001166  MOV   OUTBUF,$REG2  ;GET WORD FOUND IN 'OUTBUF'
3508 013342 104064          ERROR 64           ;AS MENTIONED ABOVE, IF 'WRITE' OF
3509                                     ;256 WORD DATA BLOCK WAS DONE
3510                                     ;CORRECTLY BEFORE, THEN THIS ERROR
3511                                     ;COULD MEAN THAT AN NPR WAS DONE
3512                                     ;ON 'READ CHECK'.
  
```

```

*****
;*TEST 33      CHECK THE 'WRITE CHECK' FUNCTION - ON CYLINDER 0, SECTOR 0
;*THIS TEST CHECKS OUT THE BASIC 'WRITE CHECK' LOGIC, USING THE 256
;*WORD DATA BLOCK (SECTOR 0, CYLINDER 0) WRITTEN IN A PREVIOUS
;*TEST. THE BUFFER IN MEMORY, USED FOR COMPARISON OF DATA, IS THE
;*ONE STARTING AT 'OUTBUF'. HENCE THE TEST WHICH WRITES THE
;*256 WORD BLOCK ON THE DISK (AS WELL AS CREATING THE 256
;*256 WORD MEMORY BUFFER) SHOULD BE DONE BEFORE THIS TEST.
*****
  
```

```

3523 013344 000004          TST33: SCOPE
3524 013346 104413          CNT.RESET
3525                                     ;GO, DO CONTROL RESET
3526                                     ;THIS IS A CALL FOR THE 'CNTRL-
3527                                     ;RESET' ROUTINE. A CONTROL RESET IS
3528                                     ;ISSUED AND AFTER A CERTAIN TIME
3529                                     ;IF THE 'CNTRL RDY' DOES NOT SET
3530                                     ;AN ERROR IS REPORTED. NOTE THAT
3531                                     ;THE PC IN ERROR MESSAGE IS THE
3532                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
3533                                     ;THIS IS A VERY BASIC ERR& IF IT
3534 013350 104421          TST.SIN          ;OCCURS GO BACK TO TEST 10
3535                                     ;CHECK IF SIN IS SET, IF SET
3536 013352 013701 001332  MOV   RKCS,R1      ;DO DRIVE RESET TO CLEAR IT
3537 013356 012700 177400  MOV   #-400,R0
3538 013362 012702 033240  MOV   #OUTBUF,R2
  
```


3539	013366	012703	177777			MOV	#177777,R3	
3540	013372	062703	177401	1\$:		ADD	#177401,R3	
3541	013376	010322				MOV	R3,(R2)+	
3542	013400	005200				INC	R0	
3543	013402	001373				BNE	1\$	
3544	013404	012777	177400	165722		MOV	#-400,ARKWC	;WRITE CHECK 256 WORDS
3545	013412	012777	033240	165716		MOV	#OUTBUF,ARKBA	;STARTING AT THIS BUS ADRES
3546	013420	013777	001350	165712		MOV	DRIVAD,ARKDA	;WITH THIS DISK DATA BLOCK (CYL 0, SEC 0)
3547	013426	012711	000007			MOV	#7,AR1	;WRITE CHECK, GO
3548								
3549	013432	005000				CLR	R0	;GIVE SOME TIME
3550	013434	105711			2\$:	TSTB	AR1	;DID CNTRL RDY CLEAR AS GO WAS SET?
3551	013436	100003				BPL	3\$;YES BRANCH
3552	013440	004737	020710			JSR	PC,GT3RG	;GET RKCS, ER, DS
3553	013444	104030				ERROR	30	;CNTRL RDY DID NOT CLEAR AS GO WAS
3554								;SET TO DO WRITE CHECK
3555	013446	104412			3\$:	CHKCRDY		;GO CHECK IF CONTROL RDY IS SET
3556								;IF SO, SKIP THE EROR MESSAGE.
3557	013450	104065				ERROR	65	;CNTRL RDY DID NOT SET AFTER
3558								;COMPLETING WRITE CHECK ON
3559								;CYLINDER 0, SECTOR 0
3560	013452	032711	140000		4\$:	BIT	#140000,AR1	;DID HE OR ERR BIT SET
3561	013456	001403				BEQ	5\$;NO, BRANCH
3562	013460	004737	020710			JSR	PC,GT3RG	;GO GET RKCS ER DS FOR ERROR MESSAGE
3563	013464	104066				ERROR	66	;HE OR ERR BIT SET ON DOING WRITE
3564								;CHK ON CYLINDER 0, SEC 0
3565	013466	032777	000001	165634	5\$:	BIT	#1,ARKER	;DID WCE SET IN RKER?
3566	013474	001403				BEQ	6\$;NO, BRANCH
3567	013476	004737	020710			JSR	PC,GT3RG	;YES GET RKCS, ER, DS
3568	013502	104067				ERROR	67	;WCE ON WRITE CHECK OF CYL 0, SEC 0
3569								;NOTE THAT IF A PREVIOUS TEST
3570								& THEN COMPARED WITH MEMORY BUFFER
3571								;TO SEE IF IT WAS WRITTEN CORRECT WAS
3572								;DONE RIGHT BEFORE, THIS ERROR SHOULD NOT
3573								;HAPPEN UNLESS THERE IS A FAULT IN THE
3574								;COMPARING LOGIC OF 'WRT CHK'
3575	013504	005777	165624		6\$:	TST	ARKWC	;DID RKWC OVERFLOW?
3576	013510	001406				BEQ	7\$;YES, BRANCH
3577	013512	017737	165616	001162		MOV	ARKWC,\$REG0	;NO, GET RKWC
3578	013520	011137	001164			MOV	AR1,\$REG1	;GET RKCS
3579	013524	104061				ERROR	61	;RKWC DID NOT OVERFLOW AFTER
3580								;WRITE CHECK ON CYL 0, SEC 0
3581	013526	013704	001350		7\$:	MOV	DRIVAD, R4	;RKDA SHOULD INCREMENT
3582	013532	005204				INC	R4	;TO THIS AFTER WRT CHK
3583	013534	020477	165600			CMP	R4,ARKDA	;DID RKDA INCREMENT CORRECTLY?
3584	013540	001406				BEQ	8\$;YES, BRANCH
3585	013542	010437	001162			MOV	R4,\$REG0	;NO, GET EXPCTD RKDA
3586	013546	017737	165566	001164		MOV	ARKDA,\$REG1	;GET RKDA RECVD
3587	013554	104070				ERROR	70	;RKDA DID NOT INCREMENT CORRECTLY
3588								; (BY 1 SECTOR) AFTER WAT CHK ON SEC 0, CYL 0
3589	013556	022777	034240	165552	8\$:	CMP	#OUTBUF+1000,ARKBA	;DID RKBA INCREMENT CORRECTLY?
3590	013564	001407				BEQ	9\$;YES, EXIT
3591	013566	012737	034240	001162		MOV	#OUTBUF+1000,\$REG0	;GET EPCTD RKBA
3592	013574	017737	165536	001164		MOV	ARKBA,\$REG1	;GET RKBA RECVD
3593	013602	104071				ERROR	71	;RKBA DID NOT INCREMENT CORRECTLY
3594								; (BY 1000 BYTES) AFTER A WRT CHK

```

3595                                     ;OF 256 WORDS ON CYL 0, SEC 0
3596 013604 022711 000206 9$: CMP      #206, R1      ;DOES RKCS STILL CONTAIN THE WRT CHK BITS?
3597 013610 001406          BEQ      TST34        ;: YES, BRANCH
3598 013612 012737 000206 001162  MOV     #206, $REG0    ;NO, GET EXPCTD RKCS
3599 013620 011137 001164  MOV     R1, $REG1     ;GET RKCS RECVD
3600 013624 104024          ERROR   24        ;RKCS BITS CHANGED AFTER WRT CHK
3601                                     ;WAS DONE
3602                                     ;*****
3603 ;*TEST 34 CHECK THAT IBA INHIBITS INCREMENTING OF RKBA
3604 ;*THIS TEST CHECKS THAT THE BUS ADDRESS DOES NOT INCREMENT WHEN
3605 ;*THE IBA BIT IS SET. SEQUENCE OF OPERATIONS:
3606 ;*1) CLEAR OUT 256 WORD BUFFER IN MEMORY (OUTBUF)
3607 ;*2) READ FROM SECTOR 0, CYLINDER 0 THE 256 WORD BLOCK THAT WAS
3608 ;*WRITTEN IN A PREVIOUS TEST (NOTE: THAT TEST SHOULD HAVE BEEN
3609 ;*DONE BEFORE THIS). IBA BIT IS SET DURING READ BACK.
3610 ;*3) CHECK THAT RKBA DID NOT INCREMENT
3611 ;*4) CHECK THAT THE ENTIRE BLOCK WAS READ INTO THE SAME MEMORY
3612 ;*WORD (OUTBUF) & THE REST OF THE WORDS IN THAT BUFFER ARE 0
3613 ;*AS PREVIOUSLY CLEARED OUT.
3614                                     ;*****
3615 013626 000004          †TST34: SCOPE
3616 013630 104413          CNT.RESET
3617                                     ;GO, DO CONTROL RESET
3618                                     ;THIS IS A CALL FOR THE 'CNTRL-
3619                                     ;RESET' ROUTINE. A CONTROL RESET IS
3620                                     ;ISSUED AND AFTER A CERTAIN TIME
3621                                     ;IF THE 'CNTRL RDY' DOES NOT SET
3622                                     ;AN ERROR IS REPORTED. NOTE THAT
3623                                     ;THE PC IN ERROR MESSAGE IS THE
3624                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
3625                                     ;THIS IS A VERY BASIC ERR& IF IT
3626 013632 104421          TST.SIN          ;OCCURS GO BACK TO TEST 10
3627                                     ;CHECK IF SIN IS SET, IF SET
3628                                     ;DO DRIVE RESET TO CLEAR IT
3629 013634 013701 001332  MOV     RKCS, R1
3630 013640 012700 177400  MOV     #-400, R0      ;SET UP COUNT FOR 256 WORDS
3631 013644 012702 033240  MOV     #OUTBUF, R2
3632 013650 010203          MOV     R2, R3
3633 013652 005023 1$: CLR     (R3)+      ;CLEAR OUT THE 256
3634 013654 005200          INC     R0        ;WORD MEMORY BUFFER STARTING
3635 013656 001375          BNE    1$        ;AT 'OUTBUF'
3636 013660 012777 177400 165446  MOV     #-400, R1KWC   ;READ BACK 256 WORDS
3637 013666 010277 165444          MOV     R2, R1KBA     ;INTO THIS BUS ADRES (IBA WILL B SET)
3638 013672 013777 001350 165440  MOV     DRIVAD, R1KDA ;FROM THIS DSK ADRES (SEC 0, CYL 0)
3639                                     ;NOTE: SEC 0 HAS BEEN WRITTEN IN A
3640                                     ;PREVIOUS TEST WITH A UNIQUE PATTERN
3641 013700 012711 004005          MOV     #4005, R1     ;READ, GO, IBA SET
3642
3643 013704 005037 001362 2$: CLR     COUNT
3644 013710 105711          TSTB   R1          ;DID CNTRL RDY SET?
3645 013712 100412          BMI    3$        ;YES, BRANCH
3646 013714 005237 001362          INC     COUNT       ;WAITED LONG ENOUGH?
3647 013720 001373          BNE    2$        ;IF NOT, LUP BAK & WAIT
3648 013722 004737 020702          JSR    PC, GT4RG    ;GO, GET RKCS, ER, DS, DA
3649 013726 013737 001350 001202  MOV     DRIVAD, $REG10 ;GET THE STARTING ADRES
3650 013734 104416          BRKDAH            ;BREAK CONTENTS OF $REG10

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3707                                     ;SAME MEMORY LOCATION. 'WORD #'
3708                                     ;IS OCTAL & SPECIFIES THE POSITION
3709                                     ;IN THE BUFFER (FIRST WORD IS 'WORD #' 1)
3710 014076 005205                       INC      R5
3711 014100 001401                       BEQ     TST35
3712 014102 000757                       BR      7$
3713                                     ;;EXIT
3714
3715 :*****
3716 :*TEST 35 CHECK THAT RK11 INTERRUPTS WHEN IDE IS SET
3717 :*THIS TEST CHECKS IF RK11 INTERRUPTS TO ITS DESIGNATED VECTOR
3718 :*ADDRESS WHEN IDE BIT IS SET, WITH CONTROL READY SET & GO CLEAR.
3719 :* IT IS NORMALLY 220, UNLESS IT HAS BEEN CHANGED. IF IT HAS BEEN
3720 :*CHANGED RK11 WILL INTERRUPT TO 'RKVEC'. NOTE 'RKVEC' HAS
3721 :*TO BE SET UP BY THE USER.
3722 :*****
3722 014104 000004 †TST35: SCOPE
3723 014106 104413 CNT.RESET
3724
3725                                     ;GO, DO CONTROL RESET
3726                                     ;THIS IS A CALL FOR THE 'CNTRL-
3727                                     ;RESET' ROUTINE. A CONTROL RESET IS
3728                                     ;ISSUED AND AFTER A CERTAIN TIME
3729                                     ;IF THE 'CNTRL RDY' DOES NOT SET
3730                                     ;AN ERROR IS REPORTED. NOTE THAT
3731                                     ;THE PC IN ERROR MESSAGE IS THE
3732                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
3733                                     ;THIS IS A VERY BASIC ERR& IF IT
3734                                     ;OCCURS GO BACK TO TEST 10
3735 014110 104421 TST.SIN
3736                                     ;CHECK IF SIN IS SET, IF SET
3737                                     ;DO DRIVE RESET TO CLEAR IT
3738
3739 014112 012746 000340 MOV      #340,-(SP)
3740 014116 012746 014124 MOV      #64$,-(SP)
3741 014122 000002 RTI
3742
3743 64$: MOV      RKCS,R1
3744 014124 013701 001332 MOV      RKVEC,R0
3745 014130 013700 001402 MOV      #1$, (R0)+
3746 014134 012720 014170 MOV      #340,(R0)
3747 014140 012710 000340 TSTB    @R1
3748 014144 105711 BPL     .-2
3749 014146 100376 MOV      #100,@R1
3750 014150 012711 000100 WAT.INT ,5
3751 014154 104420 000005
3752
3753 014160 011137 001162 MOV      @R1,$REGO
3754 014164 104074 ERROR  74
3755
3756 1$: BR      1$
3757 014166 000400 CMP     (SP)+,(SP)+
3758 014170 022626
3759
3760 014172 022626 CMP     (SP)+,(SP)+
3761 014174 012777 014210 165200 MOV      #2$,@RKVEC
3762 014202 104420 000005 WAT.INT ,5

```

```

;GET POINTER TO RK VECTOR ADRES
;SET UP INTERRUPT VECTOR FOR RK11
;SET PSW ON INTERRUPT
;WAIT FOR CNTRL RDY TO SET
;SET IDE BIT IN RKCS
;WAIT FOR INTERRUPT, ATLEAST
;37 US FOR 11/20, 7 US FOR 11/45
;GET RKCS
;RK11 DID NOT INTERRUPT WHEN IDE
;WAS SET, WITH CNTRLE RDY SET & GO
;CLEAR
;RK11 INTERRUPTED CORRECTLY TO
;THIS. RESTORE STACK POINTER
;(FROM RK11 INTERRUPT)
;RESTORE STACK POINTER
;(FROM WAT.INT)
;IF THERE IS FAULTY POLLING OR INTERUPT
;LOGIC SECOND INTERRUPT MIGHT OCCUR
;WAIT FOR INTERRUPT, IF ANY
;DUE TO FAULTY LOGIC

```



```

3763 014206 000403 BR 3$
3764
3765 014210 022626 2$: CMP (SP)+,(SP)+ ;RESTORE STACK PTR (FROM RK11 INTRUPT)
3766 014212 022626 CMP (SP)+,(SP)+ ;RESTORE STACK PTR (FROM WAT.INT)
3767 014214 104020 ERROR 20 ;AN UNEXPECTED RK11 INTERRUPT
3768 ;OCCURED. THERE SHOULD HAVE BEEN
3769 ;ONLY 1 INTERRUPT (TO 1$ ABOVE)
3770 014216 012777 004526 165156 3$: MOV #BADINT,ARKVEC ;RESTORE VECTOR ADRES FOR
3771 ;UNEXPECTED RK11 INTERRUPT,
3772 014224 012746 000340 MOV #340,-(SP)
3773 014230 012746 014236 MOV #65$,-(SP)
3774 014234 000002 RTI
3775 014236 65$:
3776
3777
3778
3779

```

```

;*****
;*TEST 36 CHECK THAT WITH IDE SET RK11 INTERRUPTS AFTER INTIATION & COMPLETION OF
; *THIS TEST CHECKS THAT AN INTERRUPT FROM RK11 OCCURS AFTER
; *A SEEK IS INITIATED WITH 'IDE' BIT SET, AND THEN A SECOND
; *INTERRUPT OCCURS AFTER THE SEEK IS DONE. IT ALSO CHECKS THAT
; *AFTER THE FIRST INTERRUPT 'SCP' BIT IS NOT SET, WHEREAS AFTER
; *THE SECOND INTERRUPT 'SCP' IS SET.
; *THIS TEST ALSO CHECKS A PART OF THE POLLING LOGIC.
;*****

```

```

3787 014236 000004 t$T36: SCOPE
3788 014240 012737 000005 001206 MOV #5,$TIMES ;DO 5 ITERATIONS
3789 014246 104413 CNT.RESET ;GO, DO CONTROL RESET
3790 ;THIS IS A CALL FOR THE 'CNTRL-
3791 ;RESET' ROUTINE. A CONTROL RESET IS
3792 ;ISSUED AND AFTER A CERTAIN TIME
3793 ;IF THE 'CNTRL RDY' DOES NOT SET
3794 ;AN ERROR IS REPORTED. NOTE THAT
3795 ;THE PC IN ERROR MESSAGE IS THE
3796 ;PC WHERE 'CNT.RESET' IS LOCATED.
3797 ;THIS IS A VERY BASIC ERR& IF IT
3798 ;OCCURS GO BACK TO TEST 10
3799
3800
3801
3802

```

```

3803
3804 014270 013701 001402 2$: MOV RKVEC,R1
3805 014274 012721 014340 MOV #3$, (R1)+ ;SET UP VECTOR ADRES FOR RK11 INTERUPT
3806 014300 012711 000340 MOV #340, (R1) ;SET UP PSW ON INTERRUPT
3807 014304 052777 000040 165026 BIS #40,ARKDA ;ADRES CYLINDER #1
3808 014312 012710 000111 MOV #111,ARO ;SEEK, GO WITH IDE SET
3809 014316 104420 000300 WAT.INT ,300 ;WAIT FOR THE DRIVE TO
3810 ;INTERRUPT AFTER ADRES WAS RECVD
3811 ;WAITING TIME= 1.4 MS FOR 11/20
3812 ;280 US FOR 11/45
3813 ;ERROR, IF INTERUPT DID NOT OCCUR
3814 ;BY NOW
3815 014322 012777 004526 165052 MOV #BADINT,ARKVEC ;RESTORE UNEXPECTED RK11 INTERRUPT
3816 014330 011037 001162 MOV ARO,$REGO ;GET RKCS
3817 014334 104075 ERROR 75 ;INTERRUPT DID NOT OCCUR AFTER
3818 ;SEEK WAS INITIATED WITH IDE SET

```



```

3875 014516 104413          CNT.RESET          ;GO DO CONTROL RESET
3876 014520 013777 001350 164612  MOV      DRIVAD,ARKDA ;ADRES THE DRIVE
3877 014526 032777 160000 164572  BIT      #160000,ARKDS ;DID CNTRL RESET CLEAR DRIVE ID BITS?
3878 014534 001404          BEQ      B$          ;YES, BRANCH
3879 014536 017737 164564 001162  MOV      ARKDS,$REGO  ;GET RKDS
3880 014544 104050          ERROR    50          ;CONTROL RESET DIDN'T CLEAR THE
3881                                     ;DRIVE ID BITS (13-15) IN RKDS
3882
3883
3884 014546 022710 000200 8$:  CMP      #200,ARO    ;WAS SCP BIT CLEARED BY CNTRL RESET?
3885 014552 001403          BEQ      TST37       ;:YES, EXIT
3886 014554 011037 001162  MOV      ARO,$REGO   ;GET RKCS
3887 014560 104100          ERROR    100        ;CNTRL RESET DID NOT CLEAR SCP BIT
3888
3889 ;:*****
3890 ;*TEST 37 CHECK THAT WITH IDE SET RK11 INTERRUPTS WHEN READ IS DONE
3891 ;*THIS TEST CHECKS THAT WHEN A DATA TRANSFER FUNCTION IS DONE
3892 ;*WITH IDE BIT SET, RK11 INTERRUPTS WHEN THE FUNCTION IS COMPLETED
3893 ;*FUNCTION USED IN THIS TEST IS READ.
3894 ;:*****
3895 014562 000004  TST37: SCOPE
3896 014564 104413  CNT.RESET          ;GO, DO CONTROL RESET
3897                                     ;THIS IS A CALL FOR THE 'CNTRL-
3898                                     ;RESET' ROUTINE. A CONTROL RESET IS
3899                                     ;ISSUED AND AFTER A CERTAIN TIME
3900                                     ;IF THE 'CNTRL RDY' DOES NOT SET
3901                                     ;AN ERROR IS REPORTED. NOTE THAT
3902                                     ;THE PC IN ERROR MESSAGE IS THE
3903                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
3904                                     ;THIS IS A VERY BASIC ERR& IF IT
3905                                     ;OCCURS GO BACK TO TEST 10
3906 014566 104421  TST.SIN          ;CHECK IF SIN IS SET, IF SET
3907                                     ;DO DRIVE RESET TO CLEAR IT
3908
3909 014570 013700 001332  MOV      RKCS,R0
3910 014574 013702 001340  MOV      RKDA,R2
3911 014600 013704 001336  MOV      RKBA,R4
3912 014604 013701 001350  MOV      DRIVAD,R1
3913 014610 052701 000013  BIS      #13,R1      ;SET BITS FOR SEC 13
3914 014614 012777 177600 164512  MOV      #-200,ARKWC ;READ 200 (OCTAL WORDS)
3915 014622 010112  MOV      R1,AR2     ;FROM THIS DISK ADRES (CYL 0, SEC 13)
3916 014624 012714 033240  MOV      #OUTBUF,AR4 ;INTO THIS BUS ADRES
3917 014630 013705 001402  MOV      RKVEC,R5
3918 014634 012725 014672  MOV      #1$(R5)+   ;SET UP VECTOR ADRES FOR RK11 TO INTRUPT
3919 014640 012715 000340  MOV      #340,(R5) ;SET PSW ON INTERUPT
3920 014644 012710 000105  MOV      #105,ARO  ;READ, GO, IDE SET
3921 014650 104420 127710  WAT.INT ,127710   ;WAIT FOR RK11 TO INTERRUPT ON
3922                                     ;COMPLETION OF READ
3923                                     ;WAITING TIME= 337 MS FOR 11/20
3924                                     ;67 MS FOR 11/45
3925 014654 012777 004526 164520  MOV      #BADINT,ARKVEC ;RESTORE UNEXPTD INTERRUPT VECTOR ADRES
3926 014662 011037 001162  MOV      ARO,$REGO  ;GET RKCS
3927 014666 104101  ERROR    101        ;RK11 DID NOT INTERRUPT AFTER READ
3928                                     ;WAS DONE, IDE BIT SET.
3929 014670 000404          BR      1$(+10)
3930 014672 022626 1$:  CMP      (SP)+,(SP)+ ;OK, IF RK11 INTERRUPTED TO THIS

```

M07

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3931                                     ;RESTORE STACK POINTER (FROM RK11 INTERRUPT)
3932 014674 022626                       CMP      (SP)+,(SP)+ ;RESTORE STACK POINTER (FROM WAT.INT)
3933 014676 012777 004526 164476        MOV      #BADINT,ARKVEC ;RESTORE UNEXPECTED RK11 INTERRUPT
3934                                     ;VECTOR ADRES
3935 014704 004737 021250                 JSR      PC,CHKER      ;CHECK IF ANY BIT IN RKER IS SET,
3936                                     ;IF YES, RETURN HERE.
3937 014710 104036                       ERROR    36           ;RKER SET ON DOING READ FROM SEC 0,
3938                                     ;CYL 13 IN INTERRUPT MODE
3939 014712 062701 000005 4$:           ADD      #5,R1        ;RKDA SHOULD HAVE INCREMENTED TO THIS
3940 014716 020112                       CMP      R1,AR2      ;DID RKDA INCREMENT CORRECTLY?
3941 014720 001405                       BEQ      2$          ;YES BRANCH
3942 014722 010137 001162               MOV      R1,$REG0    ;GET EXPCTD RTDA
3943 014726 011237 001164               MOV      AR2,$REG1   ;GET RKDA RECVD
3944 014732 104040                       ERROR    40           ;RKDA INCREMENTED WRONG ON DOING
3945                                     ;A READ ON CYL 0, SEC 13
3946 014734 004737 021224 2$:          JSR      PC,CHKWC     ;CHECK THAT RKWC OVERFLOWED TO 0,
3947                                     ;IF NOT RETURN HERE.
3948 014740 104041                       ERROR    41           ;RKWC DIDN'T OUFLO AFTER
3949                                     ;A READ OF 200 WORDS
3950
3951 014742 3$:
3952 014742 012746 000340                 MOV      #340,-(SP)
3953 014746 012746 014754                 MOV      #64$,-(SP)
3954 014752 000002                       RTI
3955 014754 64$:
3956 014754 022714 033640                 CMP      #OUTBUF+400,AR4 ;DID RKBA INCREMENT CORRECTLY?
3957 014760 001406                       BEQ      TST40      ;;YES, EXIT
3958 014762 012737 033640 001162        MOV      #OUTBUF+400,$REG0 ;GET EXPCT RKBA
3959 014770 011437 001164               MOV      AR4,$REG1  ;GET RKBA RECVD
3960 014774 104042                       ERROR    42           ;RKBA DID NOT INCREMENT CORRECTLY
3961                                     ;AFTER A READ OF 200 WORDS
3962
3963 ;:*****
3964 ;*TEST 40 CHECK THAT RK11 INTERRUPTS AT BRS ONLY
3965 ;*THIS TEST CHECKS THAT RK11 CAN ITERRUPT AT BRS ONLY. IF IT
3966 ;*INTERRUPTS AT A LEVEL HIGHER THAN BRS AN ERROR IS INDICATED.
3967 ;*IF IT DOES NOT INTERRUPT AT BRS ORLOWER THEN ALSO AN
3968 ;*ERROR IS INDICATED. IF FOR SOME REASON THE INTERRUPT
3969 ;*LEVEL IS CHANGED FROM BRS, THEN CONTENTS OF RKPRI WILL
3970 ;*HAVE TO BE CHANGED ACCORDINGLY AND STILL TEXT WILL
3971 ;*CHECK FOR THIS BR LEVEL.
3972 ;:*****
3973 014776 000004  †TST40: SCOPE
3974 015000 104413 CNT.RESET ;GO, DO CONTROL RESET
3975                                     ;THIS IS A CALL FOR THE 'CNTRL-
3976                                     ;RESET' ROUTINE. A CONTROL RESET IS
3977                                     ;ISSUED AND AFTER A CERTAIN TIME
3978                                     ;IF THE 'CNTRL RDY' DOES NOT SET
3979                                     ;AN ERROR IS REPORTED. NOTE THAT
3980                                     ;THE PC IN ERROR MESSAGE IS THE
3981                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
3982                                     ;THIS IS A VERY BASIC ERR& IF IT
3983                                     ;OCCURS GO BACK TO TEST 10
3984 015002 104421 TST.SIN ;CHECK IF SIN IS SET, IF SET
3985                                     ;DO DRIVE RESET TO CLEAR IT
3986 015004 012737 015040 001110        MOV      #1$,$LPERR ;SET RETURN ADRES FOR LUPING
  
```



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3987                                     ;ON ERROR (SW 9)
3988 015012 013700 001332                MOV    RKCS,R0
3989 015016 013777 001350 164314        MOV    DRIVAD,DRKDA
3990 015024 012701 000007                MOV    #7,R1
3991 015030 012702 000340                MOV    #340,R2
3992 015034 013703 001400                MOV    RKPRI,R3
3993
3994
3995
3996 015040 013704 001402                1$:   MOV    RKVEC,R4
3997 015044 012724 015152                MOV    #3$(R4)+
3998 015050 012714 000340                MOV    #340,(R4)
3999 015054 010246                        MOV    R2,-(SP)
4000 015056 012746 015064                MOV    #4$,-(SP)
4001 015062 000002                        RTI
4002 015064                                4$:
4003 015064 012710 000100                MOV    #100,AR0
4004 015070 012705 177760                MOV    #-20,R5
4005 015074 005205                        INC    R5
4006 015076 001376                        BNE    -2
4007 015100 020203                        CMP    R2,R3
4008 015102 003005                        BGT    2$
4009
4010
4011 015104 010137 001162                MOV    R1,$REG0
4012 015110 011037 001164                MOV    AR0,$REG1
4013 015114 104103                        ERROR  103
4014
4015
4016 015116 005010                        2$:   CLR    AR0
4017 015120 062702 177740                ADD    #-40,R2
4018
4019 015124 005301                        DEC    R1
4020 015126 001344                        BNE    1$
4021
4022 015130 012777 004526 164244        MOV    #BADINT,DRKVEC
4023
4024 015136 012746 000340                MOV    #340,-(SP)
4025 015142 012746 015150                MOV    #64$,-(SP)
4026 015146 000002                        RTI
4027 015150                                64$:
4028 015150 000414                        BR     TST41
4029
4030 015152 022626                        3$:   CMP    (SP)+,(SP)+
4031 015154 012777 004526 164220        MOV    #BADINT,DRKVEC
4032
4033 015162 020203                        CMP    R2,R3
4034 015164 003754                        BLE    2$
4035
4036
4037 015166 010137 001162                MOV    R1,$REG0
4038 015172 011037 001164                MOV    AR0,$REG1
4039 015176 104104                        ERROR  104
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4042

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;ON ERROR (SW 9)
;PRIORITY LEVEL 7
;BR LEVEL 7 FOR PSW
;NOTE, IF RK11 INTERRUPT LEVEL IS
;CHANGED FROM 5 TO ANY OTHER LEVEL
;THEN CHANGE CONTENTS OF 'RKPRI'
; ACCORDINGLY
;SET UP ADRES FOR RK11 TO INTERUPT
;SET UP PSW ON INTERUPT
;SET PROCESSOR PRIORITY LEVEL AS
;INDICATED BY R2
;SET THE IDE BIT
;WAIT FOR THE RK11 INTERRUPT
;WAITING TIME=78 US FOR 11/20
;13 US FOR 11/45
;WAS THE CPU PRIORITY LEVEL LESS THAN
;THE RK11 LEVEL? IF YES, RK11
;SHOULD HAVE INTERRUPTED. ERROR,
;IF IT DID NOT
;GET CPU BR LEVEL
;GET RKCS
;THOUGH CPU LEVEL WAS LESS THAN
;THE RK11 LEVEL (5), RK11 DID NOT
;INTERRUPT
;CLEAR RKCS
;DECREASE THE PRIORITY LEVEL (FOR
;CPU) BY 1
;CPU WILL B AT THIS LEVEL
;LUP BAK & CHK FOR THIS BR LEVEL.
;DONE WITH CHKING FOR ALL LEVELS.
;RESTORE UNEXPECTED RK11 INTERRUPT
;VECTOR
;;EXIT, TO NXT TST
;RESTORE STACK POINTER
;RESTORE UNEXPECTED RK11 INTERRUPT
;VECTOR
;IF THIS INTERRUPT OCCURED WHEN
;CPU LEVEL WAS LESS THAN THE
;RK11 PRIORITY LEVEL (5) THEN IT IS
;OK. IF NOT SO, ERROR
;GET CPU BR LEVEL
;GET RKCS
;RK11 INTERRUPTED WHEN THE CPU
;LEVEL (AS POINTED BY R1) WAS
;HIGHER OR SAME AS THE RK11
;LEVEL (5)

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4043 015200 000746

BR 25 ;GO BACK & CHK THE NXT LEVEL

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015202 000004
015204 104413

015206 104421

015210 013701 001350
015214 052701 014533

015220 012777 177377 164106
015226 012777 033240 164102
015234 010177 164100

015240 012777 000005 164064

015246 005002
015250 105777 164056
015254 100410
015256 005202
015260 001373

015262 017737 164046 001166
015270 004737 020716
015274 104002

015276 032777 040000 164024
015304 001006
015306 004737 020716
015312 012737 040000 001166
015320 104105

015322 022777 140204 164002
015330 001403
015332 004737 020716
015336 104106

:TEST 41 SIMULATE & CHECK 'OVR' ERROR
:THIS TEST SIMULATES OVERRUN ERROR AND CHECKS IF THE OVR
:BIT IN RKER GETS SET. THEN IT IS CLEARED USING CNTRL RESET
:& CHECKED THAT IT WAS CLEARED. OVR CONDITION IS SIMULATED
:BY TRYING TO READ 401(OCTAL) WORDS FROM LAST CYLINDER(312),
:LAST SECTOR (13), SURFACE 1.

†ST41: SCOPE
CNT.RESET

:GO, DO CONTROL RESET
:THIS IS A CALL FOR THE 'CNTRL-
:RESET' ROUTINE. A CONTROL RESET IS
:ISSUED AND AFTER A CERTAIN TIME
:IF THE 'CNTRL RDY' DOES NOT SET
:AN ERROR IS REPORTED. NOTE THAT
:THE PC IN ERROR MESSAGE IS THE
:PC WHERE 'CNT.RESET' IS LOCATED.
:THIS IS A VERY BASIC ERR& IF IT
:OCCURS GO BACK TO TEST 10
:CHECK IF SIN IS SET, IF
:SET, DO DRIVE RESET TO CLR IT
:GET ADRES OF DRIVE
:SET BITS FOR LAST CYLINDER (312),
:SUR 1, LAST SECTOR (13)
:READ 401 WORDS
:INTO THIS MEMORY BUFFER
:FROM THIS DSK ADRES, LAST CYL,
:LAST SEC, SURFACE 1
:READ, GO

:DID CNTRL RDY SET?
:YES, BRANCH
:NO, WAIT FOR IT
:IF WAITED LONG, REPORT ERROR MESSAGE BECAUSE
:OVR SHOULD HAVE SET HE CAUSING
:CNTRL RDY TO SET BY NOW

:GO, GET RKCS, ER
:CNTRL RDY DID NOT SET AFTER DOING
:AN OVR READ. HE SHOULD HAVE OCCURED
:SETTING CNTRL RDY (HE BECAUSE OF
:OVR CONDITIONS)
:DID OVR BIT SET IN RKER?

:GET RKCS, ER
:THIS BIT (OVR) DID NOT SET.
:OVR ERROR BIT DID NOT SET IN RKER
:ON SIMULATING OVR CONDITIONS
:DID HE & ERR SET WHEN OVR SET IN RKER?
:YES, BRANCH
:GET RKCS, ER
:HE OR ERR BIT DID NOT SET IN RKCS WHEN
:AN OVR ERROR WAS SIMULATED

TST.SIN
MOV DRIVAD,R1
BIS #14533,R1
MOV #-401,ARKWC
MOV #OUTBUF,ARKBA
MOV R1,ARKDA
MOV #5,ARKCS
CLR R2
TSTB ARKCS
BMI 2\$
INC R2
BNE 1\$
MOV ARKWC,\$REG2
JSR PC,GT2RG
ERROR 2

BIT #40000,ARKER
BNE 3\$
JSR PC,GT2RG
MOV #40000,\$REG2
ERROR 105

CMP #140204,ARKCS
BEQ 4\$
JSR PC,GT2RG
ERROR 106


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4099                                     ;CLEAR OVER, ERR, HE BITS
4100 015340 104413 4S: CNT.RESET        ;GO, DO CONTROL RESET
4101                                     ;THIS IS A CALL FOR THE 'CNTRL-
4102                                     ;RESET' ROUTINE. A CONTROL RESET IS
4103                                     ;ISSUED AND AFTER A CERTAIN TIME
4104                                     ;IF THE 'CNTRL RDY' DOES NOT SET
4105                                     ;AN ERROR IS REPORTED. NOTE THAT
4106                                     ;THE PC IN ERROR MESSAGE IS THE
4107                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
4108                                     ;THIS IS A VERY BASIC ERR& IF IT
4109                                     ;OCCURS GO BACK TO TEST 10
4110 015342 004737 021264 JSR PC,CHKECLR ;CHECK IF 'OVR' BIT WAS CLEARED BY
4111                                     ;CON.RESET, IF NOT RETURN HERE.
4112 015346 104102 ERROR 102             ;CNTRL RESET DID NOT CLEAR OVR
4113                                     ;BIT IN RKER
4114 015350 004737 021310 5S: JSR PC,CHKCCLR ;CHECK IF 'ERR' & 'HE' BIT GOT CLEARED BY
4115                                     ;CON.RESET, IF NOT RETURN HERE.
4116 015354 104102 ERROR 102             ;CNTRL RESET DID NOT CLEAR
4117                                     ;HE OR ERR BIT IN RKCS.
4118 015356 004737 021412 6S: JSR PC,DRESET ;GO DO DRIVE RESET
4119 015362 104026 ERROR 26              ;R/W/S RDY DIDN'T SET
4120                                     ;AFTER THE ABOVE DRIVE RESET
4121
4122 ::*****
4123 ;*TEST 42 SIMULATE & CHECK PGE ERROR
4124 ;*THIS TEST SIMULATES 'PROGRAMMING ERROR' & CHECKS IF IT IS
4125 ;*DETECTED BY PGE BIT IN RKER. THEN A CNTRL RESET IS DONE &
4126 ;*IT IS CHECKED IF PGE BIT WAS CLEARED. IT IS ALSO CHECKED IF
4127 ;*THE SETTING & CLEARING OF PGE BIT SETS & CLEARS HE, ERR
4128 ;*BITS IN RKCS.
4129 ::*****
4130 015364 000004 †ST42: SCOPE
4131 015366 104413 CNT.RESET              ;GO, DO CONTROL RESET
4132                                     ;THIS IS A CALL FOR THE 'CNTRL-
4133                                     ;RESET' ROUTINE. A CONTROL RESET IS
4134                                     ;ISSUED AND AFTER A CERTAIN TIME
4135                                     ;IF THE 'CNTRL RDY' DOES NOT SET
4136                                     ;AN ERROR IS REPORTED. NOTE THAT
4137                                     ;THE PC IN ERROR MESSAGE IS THE
4138                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
4139                                     ;THIS IS A VERY BASIC ERR& IF IT
4140                                     ;OCCURS GO BACK TO TEST 10
4141 015370 104421 TST.SIN                ;GO CHECK IF SIN IS SET, IF
4142                                     ;SET DO DRIVE RESET TO CLR IT
4143 015372 013701 001330 MOV RKER,R1
4144 015376 013777 001350 163734 MOV DRIVAD,DRKDA ;ADRES THE DRIVE, CYLINDER 0
4145
4146 015404 012777 002011 163720 MOV #2011,DRKCS ;SEEK, GO WITH FMT SET
4147                                     ;THIS IS A PGE SIMULATION
4148 015412 104414 CNT.RDY                ;THIS IS A CALL FOR 'CN.RDY'
4149                                     ;ROUTINE WHICH WAITS FOR CNT
4150                                     ;RDY TO SET. IF CNTRL RDY DOES
4151                                     ;NOT SET WITHIN 883 MS/ 11-20
4152                                     ;(176 MS FOR 11-45 WITH BIPOLAR)
4153                                     ;AN ERROR IS REPORTED
4154 015414 032711 004000 BIT #4000,DR1 ;DID PGE BIT IN RKER SET?

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4155 015420 001006          BNE      1$          :YES, BRANCH
4156 015422 012737 004000 001166  MOV     #4000,$REG2 :THIS BIT IN RKER (PGE) DID NOT SET
4157 015430 004737 020716          JSR     PC,GT2RG    :GO GET RKCS, ER FOR MESSAGE
4158 015434 104105          ERROR   10$        :PGE BIT DID NOT SET IN RKER
4159                                     :ON SIMULATION OF PGE CONDITION
4160                                     :$REG2 CONTAINS THE RKER BIT (PGE)
4161                                     :THAT SHOULD HAVE SET.
4162 015436 022777 142210 163666 1$:  CMP     #142210,$RKCS :DID HE & ERR BITS SET?
4163 015444 001403          BEQ     2$          :YES, BRANCH
4164 015446 004737 020716          JSR     PC,GT2RG    :GO, GET RKCS, ER
4165 015452 104106          ERROR   10$        :HE OR ERR BIT DID NOT SET WHEN
4166                                     :PGE SET IN RKER.
4167                                     :CLEAR PGE, HE, ERR BITS
4168 015454 104413          2$:  CNT.RESET      :GO, DO CONTROL RESET
4169                                     :THIS IS A CALL FOR THE 'CNTRL-
4170                                     :RESET' ROUTINE. A CONTROL RESET IS
4171                                     :ISSUED AND AFTER A CERTAIN TIME
4172                                     :IF THE 'CNTRL RDY' DOES NOT SET
4173                                     :AN ERROR IS REPORTED. NOTE THAT
4174                                     :THE PC IN ERROR MESSAGE IS THE
4175                                     :PC WHERE 'CNT.RESET' IS LOCATED.
4176                                     :THIS IS A VERY BASIC ERR& IF IT
4177                                     :OCCURS GO BACK TO TEST 10
4178 015456 004737 021264          JSR     PC,CHKECLR  :CHECK IF 'PGE' BIT GOT CLEARED BY
4179                                     :CONTROL RESET, IF NOT RETURN HERE.
4180 015462 104102          ERROR   102        :CNTRL RESET DID NOT CLEAR
4181                                     :PGE BIT IN RKER
4182 015464 004737 021310 3$:  JSR     PC,CHKCCLR  :CHECK IF 'ERR' BIT GOT CLEARED BY
4183                                     :CON.RESET, IF NOT RETURN HERE.
4184 015470 104102          ERROR   102        :RKCS BITS HE OR ERR DID NOT
4185                                     :GET CLEARED BY CNTRL RESET
4186
4187 ::*****
4188 :*TEST 43          SIMULATE & CHECK NXM ERROR
4189 :*THIS TEST SIMULATES A NON-EXISTENT MEMORY ERROR (NXM) AND
4190 :*CHECKS IF IT IS DETECTED BY NXM BIT OR RKER.LOCATION 760000
4191 :*IS REFERENCED & IT HAPPENS TO BE A NON EXISTENT LOCATION
4192 :*(FOR DIAGNOSTIC PURPOSES LIKE THIS). IT IS ALSO CHECKED
4193 :*IF HE & ERR BITS ALSO SET AND ALL 3 BITS CAN BE CLEARED
4194 :* BY CONTROL RESET.
4195 ::*****
4196 015472 000004          †ST43: SCOPE
4197 015474 104413          CNT.RESET      :GO, DO CONTROL RESET
4198                                     :THIS IS A CALL FOR THE 'CNTRL-
4199                                     :RESET' ROUTINE. A CONTROL RESET IS
4200                                     :ISSUED AND AFTER A CERTAIN TIME
4201                                     :IF THE 'CNTRL RDY' DOES NOT SET
4202                                     :AN ERROR IS REPORTED. NOTE THAT
4203                                     :THE PC IN ERROR MESSAGE IS THE
4204                                     :PC WHERE 'CNT.RESET' IS LOCATED.
4205                                     :THIS IS A VERY BASIC ERR& IF IT
4206                                     :OCCURS GO BACK TO TEST 10
4207 015476 104421          TST.SIN      :GO CHECK IF SIN IS SET
4208                                     :IF SET DO DRIVE RESET TO CLR IT
4209 015500 005002          CLR     R2
4210 015502 013700 001332          MOV     RKCS,R0

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E08

MAINDEC-11-DZRKK-D MACY11 27(1006) 04-OCT-76 16:06 PAGE 78
DZRKKD.P11 22-SEP-76 08:47 T43 SIMULATE & CHECK NXM ERROR

SEQ 0095

4211	015506	012777	177777	163620		MOV	#-1, DRKWC	;WRITE CHECK 1 WORD
4212	015514	012777	160000	163614		MOV	#160000, DRKBA	;AT THIS BUS ADRES
4213	015522	013777	001350	163610		MOV	DRIVAD, DRKDA	;WITH THIS DISK ADRES (CYL 0, SEC 0)
4214	015530	012710	000067			MOV	#67, DRD	;WRT CHK, GO, MEX BITS SET
4215	015534	105777	163572		1\$:	TSTB	DRKCS	;DID CNTRL RDY SET AS A RESULT OF HE?
4216	015540	100410				BMI	2\$;YES, BRANCH
4217	015542	005202				INC	R2	;WAITED LONG ENOUGH?
4218	015544	001373				BNE	1\$;IF NOT LUP BAK & WAIT
4219	015546	004737	020716			JSR	PC, GT2RG	;GET RKCS, ER
4220	015552	017737	163556	001166		MOV	DRKWC, \$REG2	;GET RKWC
4221	015560	104113				ERROR	113	;CNTRL RDY DID NOT SET ON DOING
4222								;A WRT CHK WITH A NXM LOCATION.
4223								;THIS HE SHOULD HAVE SET THE
4224								;CNTRL RDY BIT IN RKCS
4225	015562	032777	002000	163540	2\$:	BIT	#2000, DRKER	;DID NXM BIT IN RKER SET?
4226	015570	001006				BNE	3\$;YES, BRANCH
4227	015572	004737	020716			JSR	PC, GT2RG	;GO GET RKCS, RKER
4228	015576	012737	002000	001166		MOV	#2000, \$REG2	;THIS BIT (NXM) DID NOT SET IN RKER
4229	015604	104105				ERROR	105	;NXM BIT DID NOT SET IN RKER ON
4230								;SIMULATING NXM CONDITION.
4231	015606	022710	140266		3\$:	CMP	#140266, DRD	;DID HE & ERR BIT SET?
4232	015612	001403				BEQ	4\$;YES, BRANCH
4233	015614	004737	020716			JSR	PC, GT2RG	;GO, GET RKCS, RKER
4234	015620	104106				ERROR	106	;HE OR ERR BIT DID NOT SET WHEN
4235								;NXM ERROR WAS SIMULATED
4236								;CLEAR NXM, HE, ERR BITS
4237	015622	104413			4\$:	CNT.RESET		;GO, DO CONTROL RESET
4238								;THIS IS A CALL FOR THE 'CNTRL-
4239								;RESET' ROUTINE. A CONTROL RESET IS
4240								;ISSUED AND AFTER A CERTAIN TIME
4241								;IF THE 'CNTRL RDY' DOES NOT SET
4242								;AN ERROR IS REPORTED. NOTE THAT
4243								;THE PC IN ERROR MESSAGE IS THE
4244								;PC WHERE 'CNT.RESET' IS LOCATED.
4245								;THIS IS A VERY BASIC ERRS IF IT
4246								;OCCURS GO BACK TO TEST 10
4247	015624	004737	021264			JSR	PC, CHKECLR	;CHECK IF 'NXM' BIT GOT C;LEARED BY
4248						ERROR	102	;CON.RESET, IF NOT RETURN HERE.
4249	015630	104102						;CNTRL RESET DID NOT CLEAR
4250								;NXM BIT IN RKER
4251	015632	004737	021310		5\$:	JSR	PC, CHKCLR	;CHECK IF 'HE' & 'ERR' BITS GOT CLEARED
4252								;BY CON.RESET, IF NOT RETURN HERE.
4253	015636	104102				ERROR	102	;CNTRL RESET DID NOT CLEAR
4254								;HE OR ERR BIT IN RKCS.
4255	015640	004737	021344		6\$:	JSR	PC, TSTRWS	;GO CHECK IF R/W/S RDY IS SET &
4256								;WAIT FOR IT. SKIP ERROR IF IT IS SET
4257	015644	104016				ERROR	16	;R/W/S RDY IS NOT SET
4258								
4259								
4260								
4261								
4262								
4263								
4264								
4265	015646	000004				TST44:	SCOPE	
4266	015650	104413					CNT.RESET	;GO, DO CONTROL RESET

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*****
;TEST 44 SIMULATE & CHECK NXD ERROR
;THIS TEST SIMULATES NON-EXISTENT DISK ERROR & CHECKS IF
;IT IS DETECTED BY NXD BIT OF RKER. IF ALL EIGHT ARE PRESENT
;THEN THIS TEST IS ABORTED FOR SIMULATION CANNOT BE DONE.
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4323                                     :PC WHERE 'CNT.RESET' IS LOCATED.
4324                                     :THIS IS A VERY BASIC ERR& IF IT
4325                                     :OCCURS GO BACK TO TEST 10
4326 015762 004737 021264                JSR    PC,CHKECLR      :CHECK IF 'NXD' BIT WAS CLEARED BY
4327                                     :CON.RESET. IF NOT, RETURN HERE.
4328 015766 104102                        ERROR  102          :CNTRL RESET DID NOT CLEAR
4329                                     :NXD BIT IN RKER
4330 015770 004737 021310                5$:  JSR    PC,CHKCCLR    :CHECK IF 'HE' & 'ERR' BITS WERE CLEARED
4331                                     :BY CON.RESET. IF NOT RETURN HERE.
4332 015774 104102                        ERROR  102          :CNTRL RESET DID NOT CLEAR
4333                                     :HE OR ERR BIT IN RKCS
4334 015776 004737 021344                JSR    PC,TSTRWS     :GO CHECK & WAIT FOR R/W/S RDY
4335                                     :TO SET. IF SET SKIP ERROR
4336 016002 104016                        ERROR  16           :R/W/S SHOULD BE SET, IT'S
4337                                     :NOT
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:*****
:*TEST 45      SIMULATE & CHECK NXC ERROR
:              *THIS TEST SIMULATES THE NON-EXISTENT CYLINDER ERROR & CHECKS
:              *IF IT IS DETECTED BY THE NXC BIT OF RKER, HE & ERR BITS
:              *OF RKCS. IT IS CHECKED IF THEY CAN BE CLEARED BY CONTROL
:              *RESET
:*****

```

```

4346 016004 000004                †ST45: SCOPE
4347 016006 013700 001332          MOV    RKCS,RD
4348 016012 012737 177773 001362 2$:  MOV    #-5,COUNT      :ALLOW 'ERROR 133' ONLY 5 TIMES
4349 016020 013702 001350          MOV    DRIVAD,R2     :GET ADRES OF DRIVE
4350 016024 052702 014540          BIS    #14540,R2    :SET BITS FOR CYL 313
4351 016030 012737 016036 001110  MOV    #3$,SLPERR    :SET RETURN ADRES FOR
4352                                     :LUPING ON EROR (SW9)
4353 016036 104413                3$:  CNT.RESET      :GO, DO CONTROL RESET
4354                                     :THIS IS A CALL FOR THE 'CNTRL-
4355                                     :RESET' ROUTINE. A CONTROL RESET IS
4356                                     :ISSUED AND AFTER A CERTAIN TIME
4357                                     :IF THE 'CNTRL RDY' DOES NOT SET
4358                                     :AN ERROR IS REPORTED. NOTE THAT
4359                                     :THE PC IN ERROR MESSAGE IS THE
4360                                     :PC WHERE 'CNT.RESET' IS LOCATED.
4361                                     :THIS IS A VERY BASIC ERR& IF IT
4362                                     :OCCURS GO BACK TO TEST 10
4363 016040 004737 021344                JSR    PC,TSTRWS     :GO CHECK & WAIT FOR R/W/S RDY
4364                                     :TO SET. IF SET SKIP ERROR BELOW
4365 016044 104016                        ERROR  16           :R/W/S RDY IS NOT SET
4366 016046 104421                        TST.SIN          :CHECK IF SIN IS SET, IF SET
4367                                     :DO DRIVE RESET TO CLR IT
4368 016050 010277 163264                MOV    R2,DRKDA     :ADRES DRIVE, NXC CYLINDER
4369 016054 012710 000011                MOV    #11,DRD      :SEEK, GO TO NXC CYL
4370 016060 104412                        CHKCRDY          :GO CHECK IF CONTROL RDY IS SET
4371                                     :IF SO, SKIP THE EROR MESSAGE.
4372 016062 104021                        ERROR  21          :SEEK WAS TRIED TO A NON EXISTENT
4373                                     :CYLINDER, NXC SHOULD HAVE OCCURED
4374                                     :SETTING CNTRL RDY. BUT CNTRL RDY
4375                                     :DID NOT SET.
4376 016064 032777 000100 163236 9$:  BIT    #100,DRKER   :DID NXC SET?
4377 016072 001020                        BNE    4$          :YES, BRANCH
4378 016074 004737 020716                JSR    PC,GT2RG     :GO GET RKCS, ER

```

H08

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SEQ 0098

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4379 016100 017737 163234 001166      MOV   DRKDA,$REG2      ;GET RKDA
4380 016106 104110                      ERROR  110             ;NXC DID NOT SET WHEN SEEK
4381                                     ;WAS TRIED TO CYLINDER AS INDICATED
4382                                     ;IN RKDA
4383 016110 004737 021344      JSR   PC,TSTRWS       ;CHECK & WAIT FOR R/W/S RDY,
4384                                     ;IF SET SKIP ERROR
4385 016114 104016      ERROR  16             ;R/W/S SHOULD BE SET
4386 016116 104413      CNT.RESET             ;GO DO CONTROL RESET
4387 016120 004737 021412      JSR   PC,DRESET       ;GO DO DRIVE RESET
4388 016124 104026      ERROR  26             ;NXC DID NOT SET AND DRIVE MAY
4389                                     ;HAVE TRIED TO DO A SEEK, AFTER
4390                                     ;WHICH R/W/S RDY DID NOT SET
4391 016126 005237 001362      INC   COUNT           ;ALLOW ONLY 5 MESSAGES FOR
4392 016132 001405      BEQ   5$              ;ERROR 133
4393 016134 062702 000040      4$:  ADD   #40,R2      ;ADRES THE NXT CYL(IN NON-EXISTENT ZONE)
4394 016140 032702 017740      BIT   #17740,R2      ;CHKD FOR ALL NXC'S?
4395 016144 001334      BNE   3$              ;IF NOT, LUP BAK & CHK THE NXT NXC
4396                                     ;
4397 016146 032710 140000      5$:  BIT   #140000,DR0 ;DID HE & ERR BIT SET WHEN NXC BIT SET?
4398 016152 001003      BNE   6$              ;YES, BRANCH
4399 016154 004737 020716      JSR   PC,GT2RG        ;GET RKCS, ER
4400 016160 104106      ERROR  106           ;HE OR ERR BIT DID NOT SET IN RKCS
4401                                     ;WHEN NXC ERROR WAS SIMULATED
4402                                     ;CLEAR HE, ERR, NXC BITS
4403 016162 104413      6$:  CNT.RESET        ;GO, DO CONTROL RESET
4404                                     ;THIS IS A CALL FOR THE 'CNTRL-
4405                                     ;RESET' ROUTINE. A CONTROL RESET IS
4406                                     ;ISSUED AND AFTER A CERTAIN TIME
4407                                     ;IF THE 'CNTRL RDY' DOES NOT SET
4408                                     ;AN ERROR IS REPORTED. NOTE THAT
4409                                     ;THE PC IN ERROR MESSAGE IS THE
4410                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
4411                                     ;THIS IS A VERY BASIC ERR& IF IT
4412                                     ;OCCURS GO BACK TO TEST 10
4413 016164 004737 021264      JSR   PC,CHKECLR      ;CHECK IF 'NXC' BIT WAS CLEARED BY
4414                                     ;CON.RESET. IF NOT, RETURN HERE.
4415 016170 104102      ERROR  102           ;CNTRL RESET DID NOT CLEAR
4416                                     ;NXC BIT IN RKER.
4417 016172 032710 140000      7$:  BIT   #140000,DR0 ;DID HE & ERR BITS GET CLEARED?
4418 016176 001405      BEQ   TST46           ;:YES, EXIT
4419 016200 010037 001162      MOV   RO,$REG0        ;GET ADRES OF RKCS
4420 016204 011037 001164      MOV   DR0,$REG1       ;GET RKCS CONTENTS
4421 016210 104102      ERROR  102           ;CNTRL RESET DID NOT CLEAR
4422                                     ;HE OR ERR BIT IN RKCS

```

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*****
;TEST 46      SIMULATE & CHECK NXS ERROR
;THIS TEST SIMULATES NON-EXISTENT SECTOR ERROR & CHECKS THAT
;IT IS DETECTED BY NXS BIT OF RKER. IT IS CHECKED THAT
;WHEN NXS SETS HE & ERR OF RKER ALSO SETS, AND ALL THREE
;CAN BE CLEARED BY CONTROL RESET.
*****
TST46:  SCOPE
        CNT.RESET      ;GO, DO CONTROL RESET
                          ;THIS IS A CALL FOR THE 'CNTRL-
                          ;RESET' ROUTINE. A CONTROL RESET IS

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4431 016212 000004
4432 016214 104413
4433
4434

```



```

4491 016356 004737 021310 4$: JSR PC,CHKCLR ;CHECL IF 'HE' & 'ERR' BITS WERE CLEARED
4492 ;BY CON.RESET. IF NOT, RETURN HERE.
4493 016362 104102 ERROR 102 ;RKCS BITS ERR OR HE WERE NOT
4494 ;CLEARED BY CNTRL RESET
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4501
4502
4503
4504
4505 016364 000004
4506 016366 013700 001332
4507 016372 104413
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4509
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4517 016374 104421 TST.SIN
4518
4519 016376 012701 033240 MOV #OUTBUF,R1
4520 016402 012702 177400 MOV #-400,R2
4521 016406 012703 177777 MOV #177777,R3
4522
4523
4524 016412 062703 177401 1$: ADD #177401,R3
4525 016416 010321 MOV R3,(R1)+ ;LAST WORD 000377
4526 016420 005202 INC R2 ;HAVE U GENERATED ALL 256 WORDS?
4527 016422 001373 BNE 1$ ;IF NOT, LUP BAK & GENERATE NXT
4528
4529 016424 012737 170007 033256 MOV #170007,OUTBUF+16 ;WCE WILL B SIMULATED BY DROPPING A
4530 ;BIT IN THE EIGHTH WORD WHICH IS
4531 ;SUPPOSED TO B 174007
4532 016432 012777 177400 162674 MOV #-400,ARKWC ;WRT CHK 400 WORDS
4533 016440 012777 033240 162670 MOV #OUTBUF,ARKBA ;STARTING AT THIS BUS ADRES
4534 016446 013777 001350 162664 MOV DRIVAD,ARKDA ;WITH THIS DISK ADRES, SEC 0, CYL 0
4535 016454 012710 000007 MOV #7,ARO ;WRT CHK, GO
4536
4537 016460 104412 CHKCRDY ;GO CHECK IF CONTROL RDY IS SET
4538 ;IF SO, SKIP THE EROR MESSAGE.
4539 016462 104065 ERROR 65 ;CNTRL RDY DID NOT SET
4540 ;AFTER WRT CHK
4541 016464 032777 000001 162636 3$: BIT #1,ARKER ;DID WCE BIT SET?
4542 016472 001006 BNE 4$
4543 016474 004737 020716 JSR PC,GT2RG ;GO GET RKCS, RKER
4544 016500 012737 000001 001166 MOV #1,$REG2 ;THIS BIT (WCE) DID NOT SET
4545 016506 104105 ERROR 105 ;WCE DID NOT SET ON SIMULATING
4546 ;WCE CONDITIONS

```

```

*****
;TEST 47 SIMULATE & CHECK WCE
;THIS TEST SIMULATES A WRITE CHECK ERROR AND CHECKS THAT IT
;IS DETECTED BY WCE BIT OF RKER. FOR COMPARISON IT USES
;THE 256 WORDS DATA BLOCK WRITTEN ON SECTOR 0, CYLINDER 0
;IN A PREVIOUS TEST. THIS BLOCK IS COMPARED WITH THE 256 WORDS
;MEMORY BUFFER STARTING AT 'OUTBUF'. WCE IS SIMULATED BY
;DROPPING A BIT FROM ONE OF THE WORDS IN THE MEMORY BUFFER.
*****

```

```

;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR& IF IT
;OCCURS GO BACK TO TEST 10
;CHECK IF SIN IS SET, IF
;SET DO DRV-RESET TO CLR IT
;THIS CODE SETS UP A MEMORY
;BUFFER OF 256 WORDS STARTING
;AT OUTBUF
;FIRST WORD 177400
;SECOND 177001
;LAST WORD 000377
;HAVE U GENERATED ALL 256 WORDS?
;IF NOT, LUP BAK & GENERATE NXT
;WCE WILL B SIMULATED BY DROPPING A
;BIT IN THE EIGHTH WORD WHICH IS
;SUPPOSED TO B 174007
;WRT CHK 400 WORDS
;STARTING AT THIS BUS ADRES
;WITH THIS DISK ADRES, SEC 0, CYL 0
;WRT CHK, GO
;GO CHECK IF CONTROL RDY IS SET
;IF SO, SKIP THE EROR MESSAGE.
;CNTRL RDY DID NOT SET
;AFTER WRT CHK
;DID WCE BIT SET?
;GO GET RKCS, RKER
;THIS BIT (WCE) DID NOT SET
;WCE DID NOT SET ON SIMULATING
;WCE CONDITIONS

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K08

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DZRKKD.P11 22-SEP-76 08:47 T47 SIMULATE & CHECK WCE

SEQ 0101

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4547 016510 022710 100206 4$: CMP #100206,ARO ;IS RKCS CORRECT?
4548 016514 001403 BEQ 5$ ;YES, BRANCH
4549 016516 004737 020716 JSR PC,GT2RG ;GO, GET RKCS, RKER
4550 016522 104106 ERROR 106 ;HE OR ERR BIT DID NOT SET WHEN
4551 ;WCE WAS SIMULATED
4552 016524 104413 5$: CNT.RESET ;CNTRL RESET
4553 016526 004737 021264 JSR PC,CHKECLR ;WAS 'WCE' BIT CLEARED?
4554 ;IF NOT, RETURN HERE.
4555 016532 104102 ERROR 102 ;CNTRL RESET DID NOT CLEAR
4556 ;WCE BIT IN RKER
4557 016534 004737 021310 6$: JSR PC,CHKCCLR ;CHECK IF 'ERR' BIT WAS CLEARED. IF
4558 ;NOT RETURN HERE.
4559 016540 104102 ERROR 102 ;CNTRL RESET DID NOT CLEAR
4560 ;RKCS
4561
4562 ;:*****
4563 ;*TEST 50 CHECK THAT SSE STOPS ALL CONTROL ACTION ON SOFT ERROR
4564 ;*THIS TEST CHECKS THAT WHEN 'STOP ON SOFT ERROR' BIT IS SET IN
4565 ;*RKCS AND A SOFT ERROR IS ENCOUNTERED ALL CONTROL ACTION WILL
4566 ;*STOP AT THE END OF THE CURRENT SECTOR IF IDE BIT IS CLEAR.
4567 ;*SOFT ERROR IS SIMULATED BY A WCE AS IN THE PREVIOUS
4568 ;*TEST. THE PREVIOUS TEST & THE TEST WHICH WRITES DATA
4569 ;*BLOCK ON CYLINDER 0, SECTOR 0, SHOULD BE DONE PRIOR
4570 ;*TO THIS TEST. A TWO SECTOR 'WRT CHK' WILL BE DONE,
4571 ;*CONTROL ACTION SHOULD STOP AFTER THE FIRST SECTOR DURING
4572 ;*WHICH A SOFT ERROR IS SIMULATED.
4573 ;:*****
4574 016542 000004 †T50: SCOPE
4575 016544 104413 CNT.RESET ;GO, DO CONTROL RESET
4576 ;THIS IS A CALL FOR THE 'CNTRL-
4577 ;RESET' ROUTINE. A CONTROL RESET IS
4578 ;ISSUED AND AFTER A CERTAIN TIME
4579 ;IF THE 'CNTRL RDY' DOES NOT SET
4580 ;AN ERROR IS REPORTED. NOTE THAT
4581 ;THE PC IN ERROR MESSAGE IS THE
4582 ;PC WHERE 'CNT.RESET' IS LOCATED.
4583 ;THIS IS A VERY BASIC ERR& IF IT
4584 ;OCCURS GO BACK TO TEST 10
4585 016546 104421 TST.SIN ;CHECK IF SIN IS SET, IF
4586 ;SET DO DRIVE RESET TO CLR IT
4587 016550 013700 001332 MOV RKCS,RO
4588 016554 012737 170007 033256 MOV #170007,OUTBUF+16 ;WCE IS SIMULATED BY DROPPING A BIT
4589 ;IN THE EIGHTH WORD (WHICH IS ACTUALLY
4590 ;174007). NOTE THAT 256 WORD MEMORY
4591 ;BUFFER IS CREATED IN THE PREVIOUS TEST.
4592 016562 013701 001350 MOV DRIVAD,R1
4593 016566 012777 177000 162540 MOV #-1000,ARKWC ;WRT CHK 1000 (OCTAL) WORDS, 2 SECTORS
4594 016574 012777 033240 162534 MOV #OUTBUF,ARKBA ;FROM THIS BUS ADRES
4595 016602 010177 162532 MOV R1,ARKDA ;WITH THIS DISK ADRES, SEC 0, CYL 0
4596 016606 012710 000407 MOV #407,ARO ;WRT CHK, GO, SSE
4597 016612 104412 CHKCRDY ;GO CHECK IF CONTROL RDY IS SET
4598 ;IF SO, SKIP THE EROR MESSAGE.
4599 016614 104065 ERROR 65 ;CNTRL RDY DID NOT SET AFTER WRT
4600 ;CHK. A SOFT ERROR (WCE) IN
4601 ;SECTOR 0 SHOULD HAVE STOPPED
4602 ;ALL CONTROL ACTION.

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4603 016616 022777 000001 162504 2$: CMP #1, @RKER ;CHECK ONLY 'WCE' BIT SHOULD
4604 ;BE SET?
4605 016624 001407 BEQ 3$ ;YES, BRANCH
4606 016626 012737 000001 001162 MOV #1, $REG0 ;GET EXPCTD RKER
4607 016634 017737 162470 001164 MOV @RKER, $REG1 ;GET RKER RECVD
4608 016642 104107 ERROR 107 ;ONLY BIT 'WCE' OF RKER
4609 ;SHOULD BE SET (WCE WAS
4610 ;SIMULATED ABOVE). ERROR
4611 ;IF IT'S NOT
4612 016644 005201 3$: INC R1 ;CHECK THAT RKDA INCREMENTED BY
4613 016646 020177 162466 CMP R1, @RKDA ;1 SECTOR ONLY IMPLYING THAT
4614 ;CNTRL ACTION DID STOP AFTER
4615 ;SOFT ERROR IN SECTOR 0
4616 016652 001406 BEQ TST51 ;YES, EXIT
4617 016654 010137 001162 MOV R1, $REG0 ;GET EXPCTD RKDA
4618 016660 017737 162454 001164 MOV @RKDA, $REG1 ;GET RKDA RECVD
4619 016666 104070 ERROR 70 ;RKDA SHOULD HAVE INCRMNTD
4620 ;BY 1 SECTOR ONLY, IT DIDN'T.
4621 ;WCE WAS SIMULATED IN THE
4622 ;FIRST SECTOR & A WRT CHK
4623 ;OF 2 SECTORS WAS ISSUED.
4624 ;CONTROLLER SHOULD STOP AFTER
4625 ;DETECTING WCE IN THE FIRST
4626 ;SECTOR. HENCE RKDA SHOULD
4627 ;INCREMENT BY 1 SECTOR ONLY
4628
4629
4630
4631 ;*****
4632 ;*TEST 51 CHECK THAT RK11 INTERRUPTS ON SOFT ERROR WHEN SSE & IDE ARE SET
4633 ;*THIS TEST CHECKS WHEN SSE BIT IS SET WITH IDE SET AND A SOFT
4634 ;*ERROR OCCURS, THEN ALL CONTROL ACTION WILL STOP AND A BUS
4635 ;*REQUEST (INTERRUPT) WILL OCCUR AT THE END OF THE CURRENT
4636 ;*SECTOR. SOFT ERROR IS SIMULATED BY WCE AS IN PREVIOUS
4637 ;*TEST. PREREQUISITES FOR THIS TEST ARE THE SAME AS THOSE
4638 ;*FOR THE PREVIOUS TEST.
4639 ;*****
4640 016670 000004 †TST51: SCOPE
4641 016672 104413 CNT.RESET ;GO, DO CONTROL RESET
4642 ;THIS IS A CALL FOR THE 'CNTRL-
4643 ;RESET' ROUTINE. A CONTROL RESET IS
4644 ;ISSUED AND AFTER A CERTAIN TIME
4645 ;IF THE 'CNTRL RDY' DOES NOT SET
4646 ;AN ERROR IS REPORTED. NOTE THAT
4647 ;THE PC IN ERROR MESSAGE IS THE
4648 ;PC WHERE 'CNT.RESET' IS LOCATED.
4649 ;THIS IS A VERY BASIC ERR& IF IT
4650 ;OCCURS GO BACK TO TEST 10
4651 016674 104421 TST.SIN ;CHECK IF SIN IS SET, IF
4652 016676 012737 170007 033256 MOV #170007, OUTBUF+16 ;SET DO DRIVE RESET TO CLR IT
4653 ;WCE IS SIMULATED BY DROPPING A BIT
4654 ;IN THE EIGHTH WORD (WHICH IS 174007)
4655 ;NOTE THAT THE 256 WORD MEMORY
4656 ;BUFFER (STARTING AT OUTBUF) IS
4657 016704 013701 001350 MOV DRIVAD, R1 ;CREATED IN A PREVIOUS TEST.
4658 016710 012777 177000 162416 MOV #-1000, @RKWC ;WRT CHK 1000 (OCTAL) WORDS, 2 SECTORS
    
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4724 017100 010210
4725 017102 012777 177777 162224
4726 017110 013777 001350 162222
4727 017116 012777 177776 162212
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4729 017124 052710 000007
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4733 017130 104412
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4735 017132 104065
4736 017134 010205
4737 017136 062705 000020
4738 017142 042705 000100
4739 017146 011004
4740 017150 042704 177717
4741 017154 020504
4742 017156 001405
4743 017160 010537 001162
4744 017164 010437 001164
4745 017170 104112
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4750 017172 017703 162132
4751 017176 010305
4752 017200 042703 003001
4753 017204 001410
4754 017206 042705 177776
4755 017212 010537 001162
4756 017216 017737 162106 001164
4757 017224 104107
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4760 017226 062702 000020
4761 017232 005201
4762 017234 001313
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MOV R2,ARO
MOV #-1,ARKWC
MOV DRIVAD,ARKDA
MOV #177776,ARKBA
BIS #7,ARO

CHKCRDY

3$: ERROR 65
MOV R2,R5
ADD #20,R5
BIC #100,R5
MOV ARO,R4
BIC #177717,R4
CMP R5,R4
BEQ 4$
MOV R5,$REG0
MOV R4,$REG1
ERROR 112

4$: MOV ARKER,R3
MOV R3,R5
BIC #3001,R3
BEQ 5$
BIC #177776,R5
MOV R5,$REG0
MOV ARKER,$REG1
ERROR 107

5$: ADD #20,R2
INC R1
BNE 1$

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;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR& IF IT
;OCCURS GO BACK TO TEST 10
;SET MEX BITS (AS IN R2) IN RKCS
;WRT CHK 1 WORD
;THIS DISK ADRES, SEC 0, CYL 0
;THIS BUS ADRES. NOTE THIS BA
;IN CONJUCTION WITH MEX BITS OF RKCS
;WRT CHK, GO
;THERE MAY BE A NXM OR WCE BUT
;WHATEVER THE CASE RKBA SHOULD
;OVERFLOW MAKING THE MEX BITS COUNT
;GO CHECK IF CONTROL RDY IS SET
;IF SO, SKIP THE EROR MESSAGE.
;CNTRL RDY DID NOT SET AFTER WRT CHK

;MEX BITS SHOULD INCREMENT BY 1 TO THIS
;MASK OUT IDE BIT POSITION, IF SET
;GET RKCS
;MASK OUT ALL BITS EXCEPT MEX
;DID MEX BITS INCREMENT CORRECTLY?
;YES, BRANCH
;GET EXPCTD MEX BITS
;GET MEX BITS RECVD
;MEX BITS DID NOT INCREMENT AS
;'EXPCTD' WHEN RKBA OVERFLOWED.
;NOTE THAT BIT POSITION 4 & 5
;REFLECT MEX BITS 0 & 1 IN THE
;ERROR MESSAGE.
;GET RKER

;MASK WCE,DLT,NXM BIT, IF SET
;BRANCH IF REST OF RKER CLR
;MASK NON-WCE BITS
;THIS IS THE EXPCTD RKER
;GET RKER RECVD
;ERROR IN RKER. IT SHOULD
;BE AS EXPECTED IN
;ERROR MESSAGE
;INCREMENT TO NXT MEX BIT
;HAVE U CHKD THE MEX BITS 4 TIMES?
;IF NOT, LUP BACK

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;*****
;*TEST 53 TRANSFER FROM DISK TO TTY
;* THIS TEST CHECKS THE HIGH ORDER BITS OF THE ADDRESS
;* LINES. FIRST A ONE WORD (100) IS WRITTEN ON SECTOR,
;* 2, CYL 0. THEN IT IS READ BACK, BUT THE NPR IS DONE
;* NOT TO THE MEMORY, BUT THE TELETYPE BUFFER (TKS 177560)
;* AND IT CHECKED THAT THE WORD WAS RECIEVED CORRECTLY.

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017236 000004
017240 012737 000001 001206

017246 012737 017270 000004
017254 005737 177700
017260 012737 004462 000004

017266 000520
017270 022626
017272 012737 004462 000004
017300 012746 000340
017304 012746 017312
017310 000002
017312
017312 013700 001332
017316 104413

017320 012701 033240
017324 013704 001336
017330 012711 000100
017334 012777 177777 161772
017342 013702 001350
017346 052702 000002
017352 010277 161762
017356 010114
017360 012710 000003
017364 005003
017366 105710
017370 100410
017372 005203
017374 001374
017376 004737 020702
017402 010237 001202
017406 104416
017410 104031

017412 012777 177777 161714
017420 010277 161714
017424 013714 001144
017430 005077 161510

: *IF IT IS NOT, AN ERROR IS REPORTED. THIS TEST IS
: *SKIPPED ON AN 11/05.

T53: SCOPE
MOV #1, \$TIMES

MOV #55, @#4
TST @#177700
MOV #BADTMO, @#4

BR TST54
5\$: CMP (SP)+, (SP)+
MOV #BADTMO, @#4
MOV #340, -(SP)
MOV #645, -(SP)
RTI

645: MOV RKCS, RO
CNT.RESET

MOV #OUTBUF, R1
MOV RKBA, R4
MOV #100, @R1
MOV #-1, @RKWC
MOV DRIVAD, R2
BIS #2, R2
MOV R2, @RKDA
MOV R1, @R4
MOV #3, @RO
CLR R3
1\$: TSTB @RO
BMI 2\$
INC R3
BNE 1\$
JSR PC, GT4RG
MOV R2, \$REG10
BRKDA4
ERROR 31

2\$: MOV #-1, @RKWC
MOV R2, @RKDA
MOV \$TKS, @R4
CLR @TKS

: DO 1 ITERATION
: THIS CODE FINDS OUT IF THE CPU
: IS AN 11/05 OR ELSE.
: ON AN 11/05, RO (177700) CAN BE
: ADDRESSED AS A MEMORY LOCATION, BUT
: ON ANY OTHER CPU IF 177700 IS REFERENCED
: A TIME OUT WILL OCCUR.
: SET UP TIME OUT VECTOR
: REFERENCE RO
: RO WAS REFERENCED W/O TIMEOUT
: HENCE 11/05
: SKIP THIS TEST
: RESTORE STACK POINTER
: RESTORE TIMEOUT VECTOR

: GO, DO CONTROL RESET
: THIS IS A CALL FOR THE 'CNTRL-
: RESET' ROUTINE. A CONTROL RESET IS
: ISSUED AND AFTER A CERTAIN TIME
: IF THE 'CNTRL RDY' DOES NOT SET
: AN ERROR IS REPORTED. NOTE THAT
: THE PC IN ERROR MESSAGE IS THE
: PC WHERE 'CNT.RESET' IS LOCATED.
: THIS IS A VERY BASIC ERR& IF IT
: OCCURS GO BACK TO TEST 10

: WRITE THIS WORD
: WRITE 1 WORD

: ON CYL 0, SEC 2

: FROM THIS MEMORY LOC
: WRITE, GO

: GET RKCS, ER, DS
: GET THE STARTING ADRES
: BREAK IT INTO DRV #, CYL, SUR, SEC #
: CNTRL RDY DID NOT SET AFTER
: WRITE OF 1 WORD ON CYL 0, SEC 2
: READ 1 WORD
: FROM SEC 2, CYL 0
: INTO TTY STAUS REGISTER
: CLEAR TTY KEY BRD STATUS REG

```

4827
4828 017434 012710 000065          MOV      #65,DR0          ;READ, MEX BITS SET
4829 017440 005003          CLR      R3
4830 017442 105710          3$:     TSTB     DR0
4831 017444 100410          BMI     4$
4832 017446 005203          INC     R3
4833 017450 001374          BNE     3$
4834 017452 004737 020702          JSR     PC,GT4RG
4835 017456 010237 001202          MOV     R2,$REG10
4836 017462 104416          BRKDA4
4837 017464 104045          ERROR   4$
4838
4839
4840
4841
4842 017466 032737 000100 001144 4$:     BIT     #100,$TKS
4843
4844 017474 001015          BNE     TST54
4845 017476 017705 161442          MOV     @TKS,R5
4846 017502 010537 001164          MOV     R5,$REG1
4847 017506 052705 000100          BIS     #100,R5
4848 017512 010537 001162          MOV     R5,$REG0
4849 017516 011437 001166          MOV     DR4,$REG2
4850 017522 011037 001170          MOV     DR0,$REG3
4851 017526 104115          ERROR   11$
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4869 017530 000004          TST54: SCOPE
4870 017532 012737 000005 001206          MOV     #5,$TIMES          ;:DO 5 ITERATIONS
4871 017540 104421          TST.SIN          ;:CHECK IF SIN SET, IF SET DRV RESET
4872 017542 005001          CLR      R1          ;:INITIALIZE (VALUE OF RKBA)
4873 017544 012702 000002          MOV     #2,R2          ;:INITIALIZE (INCMNTD VALUE OF RKBA)
4874
4875 017550 012737 017562 001110          MOV     #1$,$LPERR        ;:SET RETURN ADRES FOR LUPING
4876
4877
4878 017556 013705 001336          MOV     RKBA,R5
4879 017562 004737 021344          1$:     JSR     PC,TSTRWS        ;:WAIT FOR R/W/S RDY
4880 017566 104016          ERROR   16          ;:R/W/S RDY IS NOT SET
4881 017570 104413          CNT.RESET
4882 017572 012777 177777 161534          MOV     #-1,DRKWC        ;:DO CONTROL RESET
                                ;:WRITE CHK 1 WORD

```

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;:*****
;:TEST 54 CHECK THAT RKBA CAN COUNT CORRECTLY
;:*THIS TEST CHECKS THAT RKBA CAN COUNT CORRECTLY. IT IS SET
;:*TO THE DESIRED INITIAL VALUE. THEN A ONE WORD WRITE CHECK
;:*IS TRIED, WITH MEX (MEMORY EXTENSION) BITS SET. IF THERE IS
;:*NO MEMORY PRESENT (FOR CERTAIN BUS ADDRESSES), THERE
;:*WILL BE AN NXM ERROR STOPPING CONTROLLER ACTION. BUT RKBA
;:*SHOULD HAVE INCREMENTED BY 1 FROM ITS INITIAL VALUE. IF IT
;:*HAS NOT, AN ERROR IS REPORTED.
;:*****

```



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4883 017600 010115      MOV      R1,RS      ;THIS BUS ADRES
4884 017602 013777 001350 161530  MOV      DRIVAD,DRKDA ;SET DISK ADRES
4885 017610 012777 000067 161514  MOV      #67,DRKCS   ;WRITE CHECK, GO, MEX BITS SET
4886 017616 104412      CHKCRDY             ;GO CHECK IF CONTROL RDY IS SET
4887                                     ;IF SO, SKIP THE EROR MESSAGE.
4888 017620 104065      ERROR      65      ;CNTRL RDY DID NOT SET AFTER
4889                                     ;WRT CHK WAS TRIED TO NXM LOC
4890                                     ;U MIGHT WANT TO USE TESTS
4891                                     ;CHECKING MEX BITS & NXM.
4892 017622 005237 001356      INC      INDX1     ;ALLOW ONLY 5 ERRORS OF ABOVE KIND
4893 017626 001417      BEQ      5$
4894
4895 017630 020215      3$:      CMP      R2,RS      ;DID RKBA INCREMENT BY 1 FROM
4896                                     ;ITS INITIAL VALUE?
4897 017632 001410      BEQ      4$      ;YES, BRANCH
4898 017634 010137 001162      MOV      R1,$REGO  ;GET EXPCTD RKBA
4899 017640 011537 001164      MOV      RS,$REG1  ;GET RKBA RECVD
4900 017644 104017      ERROR      17      ;RKBA DID NOT INCREMENT BY
4901                                     ;1 FROM ITS INITIAL VALUE.
4902                                     ;ONE WORD WRT CHK WAS TRIED
4903                                     ;TO A NXM LOCATION. THERE
4904                                     ;WILL BE AN NXM ERROR,
4905                                     ;BUT STILL RKBA SHOULD
4906                                     ;INCREMENT BY 1 FROM ITS
4907                                     ;INITIAL VALUE.
4908 017646 005237 001360      INC      INDX2     ;ALLOW ONLY 5 ERRORS OF
4909 017652 001405      BEQ      5$      ;THE ABOVE KIND
4910 017654 060201      4$:      ADD      R2,R1     ;SET NXT VALUE OF RKBA
4911 017656 010102      MOV      R1,R2
4912 017660 062702 000002      ADD      #2,R2     ;SET EXPCTD VALUE OF RKBA
4913 017664 001336      BNE      1$      ;ALL DONE?
4914
4915 017666      5$:                                     ;DUMMY EXIT POINT
4916
4917
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4923
4924 017666 000004      ;*****
4925 017670 012737 000001 001206 ;*TEST 55      CHECK FOR RK-05F
4926 017676 005737 001404      ;*THIS TEST CHECKS RK-05F TYPE DRIVES
4927 017702 001403      ;*TO INSURE THAT IF SEEKS ARE ISSUED ON ONE
4928 017704 004537 025056      ;*DRIVE, THE OTHER DRIVE BECOMES BUSY
4929 017710 104120      ;*****
4930
4931 017712      †ST55:  SCOPE
4932                                     MOV      #1,$TIMES ;DO 1 ITERATION
4933                                     TST      FFLAG     ;SEE IF RK-05F
4934                                     BEQ      1$      ;NOT F
4935                                     JSR      RS,FCHECK ;SEE IF OTHER GOES BUSY
4936                                     ERROR      120
4937
4938      1$:
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4939                                     ;*DO NOT LOOP ON THIS 'TEST'.
4940                                     ;*****
4941 017712 000004                               †T56: SCOPE
4942 017714 012737 000001 001206             MOV     #1,STIMES           ;;DO 1 ITERATION
4943 017722 005237 001352                     INC     DRVTON            ;;INCREMENT THE COUNT FOR THE NUMBER
4944                                     ;;OF DRIVES THAT ARE CHECKED
4945 017726 004737 021412                     JSR     PC,DRESET        ;;RESET THE DRIVE
4946 017732 104026                               ERROR   26                ;;R/W/S DIDN'T SET AFTER DRIVE RESET
4947 017734 023737 001412 001352 BTEOP:  CMP     DRVS,DRVTON      ;;HAVE U TESTED ALL THE DRIVES
4948                                     ;;THAT ARE PRESENT?
4949 017742 001405                               BEQ     1$                ;;IF YES, EXIT
4950 017744 062737 020000 001350             ADD     #20000,DRIVAD     ;;ADRES THE NXT POSSIBLE DRIVE
4951 017752 000137 004766                     JMP     NUDRV            ;;GO BACK AND TEST THE NEXT
4952                                     ;;DRIVE PRESENT
4953 017756 005037 001112 1$: CLR     SERTTL
4954
4955                                     ;*****
4956                                     ;*TEST 57 CHECK HARDWARE POLLING LOGIC
4957                                     ;*THIS TEST CHECKS THE HARDWARE POLL LOGIC, USING ALL THE DRIVES
4958                                     ;*PRESENT ON THE RK11. ATLEAST TWO DRIVES SHOULD BE PRESENT
4959                                     ;*TO DO A MEANINGFUL HARDWARE POLL. SEQUENCE OF OPERATIONS IS
4960                                     ;*AS FOLLOWING:
4961                                     ;*1) NUMBER OF DRIVES ON THE RK11 IS ASCERTAINED.
4962                                     ;*2) HAVING LOCKED OUT ALL INTERRUPTS (CPU PR 7), SEEK IS INITIATED
4963                                     ;*FOR ONE DRIVE AT A TIME, ONLY WHEN 'CNTRL RDY' IS SET.
4964                                     ;*3) CPU PRIORITY IS DROPPED TO 4 SO THAT RK11 CAN INTERRUPT, THE INCOMING
4965                                     ;*INTERRUPT IS PROCESSED TO CHECK IF IT WAS DUE TO 'SEEK DONE' BY
4966                                     ;*ONE OF THE DRIVES.
4967                                     ;*4) IF BY THE END OF THE SET TIME A DRIVE HAS NOT INTERRUPTED
4968                                     ;*AN ERROR MESSAGE IS GIVEN INDICATING WHICH DRIVE DID NOT
4969                                     ;*INTERRUPT AFTER SEEK WAS DONE.
4970                                     ;*****
4971                                     ;*****
4972 017762 000004                               †T57: SCOPE
4973 017764 012737 000005 001206             MOV     #5,STIMES           ;;DO 5 ITERATIONS
4974 017772 005237 001440                     INC     SIZYET           ;;FOUNR RKOSF YET?
4975 017776 001002                               BNE     25$              ;;YES
4976 020000 004737 025202                     JSR     PC,SIZEF         ;;FIND WHICH ARE RK-OSF
4977 020004 005037 001436 25$: CLR     PHYDRV          ;;NUMBER OF ACTUAL DRIVES
4978 020010 012700 001414                     MOV     #DRIVO,RO        ;;TABLE
4979 020014 005710 23$: TST     (RO)           ;;DRIVE HERE+?
4980 020016 001405                               BEQ     22$              ;;NO
4981 020020 005237 001436                     INC     PHYDRV          ;;COUNT DRIVE
4982 020024 005710                               TST     (RO)           ;;RKOSF?
4983 020026 100001                               BPL     22$              ;;NO
4984 020030 005720                               TST     (RO)+           ;;DONT COUNT F TWICE
4985 020032 005720 22$: TST     (RO)+ ;NEXT DRIVE
4986 020034 020027 001433                     CMP     RO,#DRIV7+1      ;;ALL YET
4987 020040 002765                               BLT     23$              ;;NO
4988 020042 005037 001406                     CLR     ODDEVN          ;;EVEN DRIVES FIRST IF F
4989 020046 005737 001412 T56: TST     DRVS           ;;ANY DRIVES PRESENT?
4990 020052 001002                               BNE     20$              ;;YES
4991 020054 000137 020560                     JMP     $EOP            ;;NO
4992 020060 005237 001434 20$: INC     T56FLG
4993 020064 013700 001332                     MOV     RKCS,RO
4994 020070 005037 001356                     CLR     INDX1           ;;FLAG TO INDICATE:

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020534 005237 001406
020540 022737 000002 001406
020546 001402
020550 000137 020046
020554 005037 001434

020560
020560 000004
020562 005037 001102
020566 005037 001206
020572 005237 001100
020576 042737 100000 001100
020604 005327
020606 000001
020610 003022
020612 012737
020614 000001
020616 020606
020620 104401 020665
020624 013746 001100
020630 104405
020632 104401 020662
020636 013700 000042
020642 001405
020644 000005
020646 004710
020650 000240

TSTEND: INC ODDEVN
CMP #2,ODDEVN
BEQ 21\$
JMP T56
CLR T56FLG

.SBTTL END OF PASS ROUTINE

*INCREMENT THE PASS NUMBER (\$PASS)
*INDICATE END-OF-PROGRAM AFTER 1 PASSES THRU THE PROGRAM
*TYPE "END PASS #XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)
*IF THERES A MONITOR GO TO IT
*IF THERE ISN'T JUMP TO ST4

\$EOP: SCOPE
CLR \$STNM
CLR \$TIMES
INC \$PASS
BIC #100000,\$PASS
DEC (PC)+
\$EOPCT: .WORD 1
BGT \$DOAGN
MOV (PC)+,2(PC)+
\$ENDCT: .WORD 1
\$EOPCT
TYPE \$ENDMG
MOV \$PASS,-(SP)
TYPDS
TYPE \$ENULL
\$GET42: MOV #42,R0
BEQ \$DOAGN
RESET
\$ENDAD: JSR PC,(R0)
NOP

;;WORDS ARE SET UP TO INDICATE
;;PRESENCE OF A DRIVE EG: IF
;;DRIVES 0,1,2 ARE PRESENT, IT WILL
;;LOOK LIKE
;;OUTBUF: 000000 BITS 13,14,15
;; 020000 CONTAIN THE
;; 040000 DRIVE NO.
;; 000000 REST 0'S
;;WHEN A DRIVE INTERRUPTS AFTER SEEK
;;IS DONE BIT 0 OF THE CORRESPONDING
;;INDICATOR WORD IS SET. THUS FOR THE
;;ABOVE EXAMPLE IF ALL DRIVES INTERRUPTED
;;CORRECTLY THEN IT WILL LOOK LIKE:
;; 12\$: 000001 BIT 0 SET
;; 020001 TO INDICATE
;; 040001 DR INTERRUPTED
;; 000000 REST 0'S

;;NOW ODD IF RK05F
;;SEE IF DONE
;;ALL DONE
;;TEST AGAIN

;;ZERO THE TEST NUMBER
;;ZERO THE NUMBER OF ITERATIONS
;;INCREMENT THE PASS NUMBER
;;DON'T ALLOW A NEG. NUMBER
;;LOOP?

;;YES
;;RESTORE COUNTER

;;TYPE "END PASS #"
;;SAVE \$PASS FOR TYPEOUT
;;GO TYPE--DECIMAL ASCII WITH SIGN
;;TYPE A NULL CHARACTER
;;GET MONITOR ADDRESS
;;BRANCH IF NO MONITOR
;;CLEAR THE WORLD
;;GO TO MONITOR
;;SAVE ROOM


```

5219 020652 000240          NOP          ;;FOR
5220 020654 000240          NOP          ;;PCT11
5221 020656                SDOAGN:
5222 020656 000137          JMP      @PC+      ;;RETURN
5223 020660 004404          $RTNAD: .WORD    ST4
5224 020662      377      000      $ENULL: .BYTE    -1,-1,0  ;;NULL CHARACTER STRING
5225 020665      015      042412  042116 $ENDMG: .ASCIZ  <15><12>/END PASS #/
5226 020672 050040 051501 020123
5227 020700 000043

```

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5235          .SBTTL  GT2RG:  ROUTINE FOR GETTING RKCS,RKER
5236
5237          ;SUBROUTINE FOR TRANSFERRING THE CONTENTS OF RKCS, RKER
5238          ;TO $REG0, $REG1 RESPECTIVELY BEFORE TYPING OUT AN ERROR MESSAGE.
5239          ;CALL:  JSR      PC,GT2RG

```

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5240
5241          .SBTTL  GT3RG:  ROUTINE FOR GETTING RKCS, RKER, RKDS
5242
5243          ;GT3RG
5244          ;SUBROUTINE FOR TRANSFERRING THE CONTENTS OF RKCS, RKER, RKDS
5245          ;TO $REG0, $REG1, $REG2 RESPECTIVELY BEFORE TYPING OUT AN
5246          ;ERROR MESSAGE.
5247          ;CALL:  JSR      PC,GT3RG

```

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5248
5249          .SBTTL  GT4RG:  ROUTINE FOR GETTING RKCS, RKER, RKDS, RKDA
5250
5251          ;GT4RG
5252          ;SUBROUTINE FOR TRANSFERRING CONTENTS OF RKCS, RKER, RKDS
5253          ;RKDA TO $REG0, $REG1, $REG2, $REG3 RESPECTIVELY BEFORE
5254          ;TYPING OUT AN ERROR MESSAGE.
5255          ;CALL:  JSR      PC,GT4RG

```

```

5256
5257 020702 017737 160432 001170  GT4RG:  MOV      @RKDA,$REG3      ;GET RKDA
5258 020710 017737 160412 001166  GT3RG:  MOV      @RKDS,$REG2      ;GET RKDS
5259 020716 017737 160406 001164  GT2RG:  MOV      @RKER,$REG1      ;GET RKER
5260 020724 017737 160402 001162  MOV      @RKCS,$REG0
5261 020732 000207          RTS      PC

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5267          .SBTTL  TYERM:  SPECIAL ERROR MESSAGE ROUTINE
5268
5269          ;TYERM
5270          ;THIS ROUTINE TYPES OUT 'EROR AT PC=X'
5271          ;X IS THE PC WHERE THE EXPLANATION AS TO WHAT HAPPENED IS GIVEN. THIS ROUTINE
5272          ;IS USED ONLY FOR NON-MANUAL MODE OF THE PROGRAM.
5273          ;CALL:  JSR      TYERM
5274

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5275 020734
 5276 020734 104401 020742
 5277 020740 000406
 5278
 5279 020756
 5280 020756 010346
 5281 020760 104402
 5282 020762 000207
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 5307 020764 010046
 5308 020766 012700 001172
 5309 020772 000403
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 5311 020774 010046
 5312 020776 012700 001202
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 5314 021002 032777 020000 160130
 5315 021010 001034
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 5317 021012 010146
 5318 021014 010246
 5319 021016 013701 001202
 5320
 5321 021022 042701 177760
 5322 021026 010140
 5323 021030 013701 001202
 5324 021034 006201
 5325 021036 006201
 5326 021040 006201
 5327 021042 006201
 5328 021044 010102
 5329 021046 042702 177776
 5330 021052 010240

TYERM:
 TYPE 65\$;:TYPE ASCIZ STRING
 BR 64\$;:GET OVER THE ASCIZ
 ;:65\$: .ASCIZ <15><12>/EROR,PC=
 ;:64\$:
 MOV R3,-(SP)
 TYPOC
 RTS PC

.SBTTL BDAO, BDA4: BREAK DISK ADDRESS INTO SEC, SUR, CYL, DRIVE

;BDAO, BDA4

; THIS ROUTINE BREAKS A DISK ADDRESS (BITS 0-15) INTO DRIVE #,
 ; CYLINDER #, SURFACE, SECTOR #. THE ROUTINE IS CALLED BY USING EITHER
 ; BRKDAO OR BRKDA4, BOTH BEING 'TRAP' INSTRUCTIONS WITH THEIR LOWER BYTES
 ; ENCODED TO PROVIDE INDEXING TO 'BDAO' OR 'BDA4'. BEFORE CALLING
 ; THE ROUTINE THE DISK ADDRESS WHICH IS TO BE BROKEN AS ABOVE
 ; IS DEPOSITED IN \$REG10.

; 'BRKDAO' PUTS THE
 ; DRIVE # INTO \$REG0
 ; CYLINDER # INTO \$REG1
 ; SURFACE # INTO \$REG2
 ; SECTOR # INTO \$REG3
 ; CALL: BRKDAO

BRKDA4 PUTS THE
 DRIVE # INTO \$REG4
 CYLINDER # INTO \$REG5
 SURFACE # INTO \$REG6
 SECTOR # INTO \$REG7
 BRKDA4

BDAO: MOV R0,-(SP) ;PUSH R0 ONTO THE STACK
 MOV #\$REG3+2,R0 ;SET UP POINTER
 BR BDAR

BDA4: MOV R0,-(SP) ;PUSH R0 ONTO THE STACK
 MOV #\$REG7+2,R0 ;SET UP POINTER

BDAR: BIT #20000,\$SWR ;INHIBIT TYPEOUT?
 BNE 2\$;YES, BRANCH TO EXIT POINT

MOV R1,-(SP) ;PUSH R1 ON STACK
 MOV R2,-(SP) ;PUSH R2 ON STACK
 MOV \$REG10,R1 ;GET THE ADDRESS WHICH
 ;HAS TO BE BROKEN

BIC #177760,R1 ;EXTRACT SECTOR BITS 0-3
 MOV R1,-(R0) ;MOVE SECTOR BITS TO \$REG3 OR \$REG7
 MOV \$REG10,R1 ;GET THE DSK-ADRES TO BE BROKEN
 ASR R1 ;SHIFT RIGHT 4 TIMES
 ASR R1
 ASR R1
 ASR R1

MOV R1,R2 ;STORE THIS
 BIC #177776,R2 ;EXTRACCT THE SURFACE BIT
 MOV R2,-(R0) ;MOVE SURFACE BIT TO \$REG3 OR \$REG6

MAINDEC-11-DZRKK-D MACY11 27(1006) 04-OCT-76 16:06 PAGE 98
 DZRKKD.P11 22-SEP-76 08:47 BDAO, BDA4: BREAK DISK ADDRESS INTO SEC, SUR, CYL, DRIVE

SEQ 0115

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5331 021054 006201          ASR    R1
5332 021056 010102          MOV    R1,R2          ;STORE IT
5333 021060 042702 177400  BIC    #177400,R2    ;EXTRACT THE CYLINDER BITS
5334 021064 010240          MOV    R2,-(R0)      ;MOVE CYLINDER BITS TO $REG1 OR $REG5
5335 021066 000301          SWAB   R1            ;SWAB HI-LO BYTES
5336 021070 042701 177770  BIC    #177770,R1    ;EXTRACT THE DRIVE #
5337 021074 010140          MOV    R1,-(R0)      ;MOVE DRIVE # TO $REG0 OR $REG4
5338
5339 021076 012602          MOV    (SP)+,R2      ;RESTORE R2
5340 021100 012601          MOV    (SP)+,R1      ;RESTORE R1
5341 021102 012600 2$:    MOV    (SP)+,R0      ;RESTORE R0 FROM THE STACK
5342 021104 000002          RTI                    ;RETURN FROM INTERRUPT, EXIT THIS
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5355 021106 012737 177763 021132 SHFTRT: MOV    #-15,2$      ;SET UP A COUNT OF 13
5356 021114 000241          CLC                    ;CLEAR THE C BIT
5357 021116 006066 000002 1$:    ROR    2(R6)          ;ROTATE RIGHT THE WORD TO B SHFTD
5358 021122 005237 021132          INC    2$              ;SHIFTED 13 TIMES?
5359 021126 001373          BNE    1$              ;IF NOT LUP BAK & SHIFT
5360 021130 000207          RTS    PC              ;EXIT FROM THIS SUBROUTINE
5361 021132 000000 2$:    0
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.SBTTL SHFTRT: SHIFT RIGHT ROUTINE

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;SHFTRT
;THIS ROUTINE SHIFTS A WORD TO THE RIGHT 13 TIMES. THE WORD TO BE SHIFTED
;IS PUT ON THE STACK BEFORE ENTERING THIS ROUTINE AND IT IS POPPED UP
;FROM THE STACK AFTER THE SHIFT HAS BEEN DONE.
;CALL: JSR PC,SHFTRT

```

.SBTTL CHKHE: CHECK FOR 'ERR' OR
 .SBTTL CHKHE1: CHECK FOR 'ERR' OR

```

;CHKHE
;THIS ROUTINE CHECKS IF 'HE' OR 'ERR' BITS IN RKCS ARE SET. IF ANY OF THE
;TWO BITS ARE SET, THE CONTENTS OF RKCS, ER, DS, AND DA ARE SAVED AND A
;RETURN IS MADE TO THE ERROR MESSAGE FOLLOWING THE 'JSR' CALL.
;AT THE TIME OF ENTRY 'DRIVAD' CONTAINS THE DISK ADDRESS WHICH IS TO
;BE BROKEN DOWN INTO DRIVE #, CYLINDER, SURFACE AND SECTOR #. THIS INFORMATION
;IS SAVED TO BE USED LATER FOR ERROR REPORTING. IF THE BITS ARE NOT SET,
;RETURN IS MADE TO SKIP THE ERROR MESSAGE.

```

```

;CHKHE1
;THIS ROUTINE CHECKS IF 'HE' OR 'ERR' BITS IN RKCS ARE SET. IF ANY OF THE
;TWO BITS ARE SET, THE CONTENTS OF RKCS, ER, DS, AND DA ARE SAVED AND A
;RETURN IS MADE TO THE ERROR MESSAGE FOLLOWING THE 'JSR' CALL.
;AT THE TIME OF ENTRY R1 CONTAINS THE DISK ADDRESS WHICH IS TO BE BROKEN
;DOWN INTO DRIVE #, CYLINDER, SURFACE AND SECTOR #. THIS INFORMATION IS
;SAVED TO BE USED LATER FOR ERROR REPORTING. IF THE BITS ARE NOT SET,
;RETURN IS MADE TO SKIP THE ERROR MESSAGE.

```

```

5387
5388 021134 010137 001202      CHKHE1: MOV      R1,$REG10      ;SAVE THE DISK ADRES
5389 021140 000403                      BR      CHE1
5390
5391 021142 013737 001350 001202  CHKHE:  MOV      DRIVAD,$REG10    ;SAVE THE DISK ADRES
5392 021150 032777 140000 160154  CHE1:   BIT      #140000,$RKCS    ;IS 'HE' OR 'ERR' BIT SET?
5393 021156 001467                      BEQ      CRETRN                ;NO
5394 021160 004737 020702                      JSR      PC,$GT4RG            ;GET RKCS,ER,DS, DA
5395 021164 104416                      BRKDA4   ;GO TO 'BDA4' & BREAK CONTENTS 0
5396                                     ;$REG10 INTO DR#,CYL,SUR,SEC BITS
5397 021166 000207                      RTS      PC                    ;RETURN TO THE ERROR MESSAGE
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```

.SBTTL CHKDA: CHECK IF RKDA INCREMENTED CORRECTLY

```

5409 021170 013705 001350      CHKDA:  MOV      DRIVAD,R5      ;RKDA SHOULD INCREMENT TO THIS
5410 021174 005205                      INC      R5                    ;AFTER DATA TRANSFER IS DONE
5411 021176 020577 160136      CHKDA1: CMP      R5,$RKDA      ;DID RKDA INCREMENT CORRECTLY?
5412 021202 001455                      BEQ      CRETRN                ;IF YES, BRANCH
5413                                     ;IF NOT, REPORT ERROR
5414 021204 010537 001202                      MOV      R5,$REG10            ;GET EXPCTD RKDA
5415 021210 104415                      BRKDAO   ;GO TO 'BDAO' & BREAK CONTENTS OF
5416                                     ;$REG10 INTO DR #,CYL,SUR,SEC BITS
5417 021212 017737 160122 001202  MOV      $RKDA,$REG10          ;GET ACTUAL RKDA
5418 021220 104416                      BRKDA4   ;GO TO 'BDA4' & BREAK CONTENTS OF
5419                                     ;$REG10 INTO DR #,CYL,SUR,SEC BITS
5420 021222 000207                      RTS      PC                    ;RETURN TO THE ERROR MESSAGE
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```

.SBTTL CHKWC: CHECK IF RKWC OVERFLOWED

```

5429 021224 005777 160104      CHKWC:  TST      $RKWC          ;DID WORD COUNT OVERFLOW TO 0?
5430 021230 001442                      BEQ      CRETRN                ;IF YES, BRANCH
5431                                     ;IF NOT, ERROR
5432 021232 017737 160076 001162  MOV      $RKWC,$REG0          ;GET RKWC
5433 021240 017737 160074 001164  MOV      $RKDA,$REG1          ;GET RKDA
5434 021246 000207                      RTS      PC                    ;RETURN TO THE ERROR MESSAGE
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```

.SBTTL CHKER: CHECK RKER CONTENTS

```

5438 ;CHKER
5439 ;THIS ROUTINE CHECKS IF ANY BIT IN RKER SET. IF NOT RETURN IS MADE TO SKIP
5440 ;THE ERROR MESSAGE. IF ANY BIT IS SET THE CONTENTS OF RKCS, RKER, RKDS ARE
5441 ;SAVED AND A RETURN IS MADE TO THE ERROR MESSAGE.
5442

```



```

5443 021250 005777 160054 CHKER: TST   DRKER   ;DID ANY BIT IN RKER SET?
5444 021254 001430          BEQ   CRETRN  ;NO, BRANCH
5445          004737 020710          JSR   PC,GT3RG ;YES, ERROR
5446          000207          RTS   PC       ;GO, GET RKCS, ER, DS
5447
5448          000207          RTS   PC       ;RETURN TO THE ERROR MESSAGE
5449
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```

```

;CHKECLR
;THIS ROUTINE CHECKS THAT RKER IS CLEAR. IF NOT, THE CONTENTS OF RKER
;ARE SAVED AND A RETURN IS MADE TO THE ERROR MESSAGE FOLLOWING THE "JSR"
;CALL. IF RKER IS CLEAR THE ERROR MESSAGE IS SKIPPED ON RETURN.

```

```

5456 021264 005777 160040 CHKECLR: TST   DRKER   ;ANY BIT IN RKER SET?
5457 021270 001422          BEQ   CRETRN  ;NO
5458 021272 013737 001330 001162          MOV   RKER,$REG0 ;GET ADRES OF RKER
5459 021300 017737 160024 001164          MOV   DRKER,$REG1 ;GET CONTENTS OF RKER
5460 021306 000207          RTS   PC       ;RETURN TO THE ERROR MESSAGE
5461
5462
5463

```

```

;CHKCCLR
;THIS ROUTINE CHECKS THAT RKCS IS CLEAR. IF NOT, THE CONTENTS OF RKCS ARE
;SAVED AND A RETURN IS MADE TO THE ERROR MESSAGE. IF RKCS IS CLEAR THE
;ERROR MESSAGE IS SKIPPED ON RETURN.

```

```

5467 021310 022777 000200 160014 CHKCCLR: CMP   #200,DRKCS ;IS RKCS CLEAR?
5468 021316 001407          BEQ   CRETRN  ;YES
5469 021320 013737 001332 001162          MOV   RKCS,$REG0 ;SAVE ADRES OF RKCS
5470 021326 017737 160000 001164          MOV   DRKCS,$REG1 ;SAVE THE CONTENT OF RKCS
5471 021334 000207          RTS   PC       ;RETURN TO THE ERROR MESSAGE
5472

```

```

5473 021336 062716 000002          CRETRN: ADD   #2,(SP) ;SKIP ERROR MESSAGE ON
5474 021342 000207          RTS   PC       ;RETURN
5475
5476

```

.SBTTL TSTRWS: WAIT FOR R/W/S RDY ROUTINE

```

;TSTRWS
;THIS ROUTINE WAITS FOR R/W/S RDY TO SET. WHEN IT SETS, THE RETURN PC
;IS INCREMENTED SO THAT ON RETURN (TO THE MAIN PROGRAM) THE ERROR
;MESSAGE FOLLOWING THE 'JSR' CALL IS SKIPPED. IF R/W/S RDY DOES NOT SET
;THEN A RETURN IS MADE TO THE ERROR MESSAGE (FOLLOWING THE 'JSR' CALL).
;WAITING TIME IS APPROX. 1040 MS FOR 11/20, APPROX. 208 MS FOR 11/45
;CALL: JSR   TSTRWS

```

```

5487 021344 013777 001350 157766 TSTRWS: MOV   DRIVAD,DRKDA ;ADRES THE DRIVE
5488 021352 005037 001366          CLR   TIMER     ;INITIALIZE COUNT
5489 021356 032777 000100 157742 1$: BIT   #100,DRKDS ;DID R/W/S RDY SET?
5490 021364 001007          BNE   2$       ;YES, BRANCH
5491 021366 005237 001366          INC   TIMER     ;WAIT FOR R/W/S RDY
5492 021372 001371          BNE   1$       ;ERROR IF IT'S NOT SET BY NOW
5493 021374 017737 157726 001162          MOV   DRKDS,$REG0 ;GET RKDS
5494 021402 000207          RTS   PC       ;EXIT (TO ERROR FOOLOWING 'JSR TSTRWS')
5495
5496 021404 062716 000002          2$: ADD   #2,(SP) ;ADJUST RETURN ADRES TO SKIP OVER
5497          000207          RTS   PC       ;ERROR (FOLLOWING 'JSR TSTRWS')
5498          000207          RTS   PC       ;EXIT

```

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.SBTTL DRESET: DRIVE RESET ROUTINE

```

:DRESET
:THIS ROUTINE DOES A DRIVE RESET ON THE DRIVE WHOOSE ADDRESS IS IN
:RKDA. MULTIPLE RETURN ADDRESSES FOR THIS ROUTINE ARE PROVIDED.
:IF THERE IS NO ERROR (R/W/S RDY SETS WITHIN CERTAIN TIME), THEN BEFORE
:EXITNG FROM THIS ROUTINE THE RETURN ADDRESS IS INCREMENTED BY 2, TO SKIP
:THE ERROR MESSAGE ON RETURN. IF THERE IS AN ERROR, THE 3 REGISTERS (CS,ER,DS)
:ARE STORED AND THEN A NORMAL EXIT IS MADE FROM THIS ROUTINE TO THE
:ERROR MESSAGE FOLLOWING THE CALL FOR THIS ROUTINE.
:CALL: JSR PC,DRESET
    
```

```

021412 005037 001364 157714 DRESET: CLR COUNT1 ;INITIALIZE THE COUNT
021416 013777 001350 157700 MOV DRIVAD,DRKDA ;ADRES THE DRIVE
021424 012777 000015 157700 MOV #15,DRKCS ;DRIVE RESET GO
021432 104414 CNT.RDY ;THIS IS A CALL FOR 'CN.RDY'
;ROUTINE WHICH WAITS FOR CNT
;RDY TO SET. IF CNTRL RDY DOES
;NOT SET WITHIN 883 MS/ 11-20
;(176 MS FOR 11-45 WITH BIPOLAR)
;AN ERROR IS REPORTED
;DID R/W/S RDY SET?
021434 032777 000100 157664 1$: BIT #100,DRKDS
021442 001013 BNE 2$
021444 012746 177770 MOV #-10,-(SP) ;PUSH COUNT ON SP
021450 005216 INC (SP) ;COUNT IT DOWN
021452 001376 BNE -2
021454 005726 TST (SP)+ ;POP UP SP
021456 005237 001364 INC COUNT1 ;IF NOT WAIT
021462 001364 BNE 1$ ;WAITED LONG?
021464 004737 020702 JSR PC,GT4RG
021470 000402 BR 2$+4
021472 062716 000002 2$: ADD #2,DR6
021476 000207 RTS PC
    
```

.SBTTL TSTSIN: CHECK 'SIN' ROUTINE

```

:TSTSIN
:THIS ROUTINE CHECKS IF 'SIN' IS SET, IF IT IS SET A
:DRIVE RESET IS DONE TO CLEAR 'SIN' AND INITIALIZE POSITIONER.
:CALL: TST.SIN
:IF ON DOING DRIVE RESET R/W/S RDY DOES NOT SET A MESSAGE
:ERROR PC=XXXXXX IS GIVEN.
:XXXXXX=PC IN THE MAIN PROGRAM WHERE 'TST.SIN' CALL IS LOCATED.
    
```

```

021500 013777 001350 157632 TSTSIN: MOV DRIVAD,DRKDA ;ADRES THE DRIVE
021506 032777 001000 157612 BIT #100,DRKDS ;IS SIN SET?
021514 001403 BEQ 1$
    
```



```

5555 021516 004737 021412 JSR PC,DRESET ;GO DO DRIVE RESET, SIN SET
5556 021522 000401 BR 2$ ;REPORT ERROR
5557 021524 000002 1$: RTI
5558 021526 032777 020000 157404 2$: BIT #SW13,DSWR ;INHIBIT TYPEOUT?
5559 021534 001373 BNE 1$ ;IF YES, SKIP TYPEOUT
5560 021536 104401 021544 TYPE 65$ ;:TYPE ASCIZ STRING
5561 021542 000406 BR 64$ ;:GET OVER THE ASCIZ
5562 65$: .ASCIZ /ERROR PC= /
5563 64$:
5564 021560 011646 MOV (SP),-(SP)
5565 021562 062716 177776 ADD #-2,(SP) ;GET THE PC WHERE 'TST.SIN' IS LOCATED
5566 021566 104402 TYPOC ;GO TYPE OUT PC
5567 021570 000755 BR 1$

```

.SBTTL DELAY: TIME DELAY ROUTINE

```

5571
5572
5573 ;DELAY
5574 ;THIS ROUTINE PROVIDES A VARIABLE TIME DELAY. THE CALL FOR THIS
5575 ;ROUTINE IS AN ENCODED 'TRAP' INSTRUCTION.
5576 ;CALL: DELAY ,N N IS ANY OCTAL NO. FROM 1 TO 177777
5577 ;THE DELAY PROVIDED IS 7.5N US (CONVERT N TO DECIMAL) FOR 11/20
5578 ;1.5N US FOR 11/45
5579 ;IF THE USER WANTS TO CHANGE THE DELAY TIME (EXMP: SHORTER DELAY TO
5580 ;GET A TIGHTER SCOPE LOOP) THE VARIABLE 'N' FOLLOWING 'DELAY' SHOULD
5581 ;BE CHANGED TO SUIT THE INDIVIDUAL NEED.
5582
5583 021572 017637 000000 001366 DELA.Y: MOV 2(SP),TIMER ;GET 'AMOUNT' (N) FOR WHICH
5584 021600 062716 000002 ADD #2,(SP) ;DELAY IS TO BE PROVIDED
5585 ;ADJUST STACK POINTER TO SKIP OVER 'N'
5586 021604 005337 001366 1$: DEC TIMER ;COUNT DOWN TO 0
5587 021610 001375 BR 1$
5588
5589 021612 000002 RTI ;RETURN TO MAIN PROGRAM
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```

.SBTTL WAT.INT: WAIT FOR INTERRUPT ROUTINE

```

5601
5602
5603 ;WAT.INT
5604 ;THIS ROUTINE PROVIDES A VARIABLE TIME WAIT LOOP DURING WHICH AN INTERRUPT
5605 ;FROM RK11 CAN OCCUR. THE CALL IS AN ENCODED 'TRAP' INSTRUCTION.
5606 ;CALL: WAT.INT ,N N IS ANY OCTAL NO. FROM 1 TO 177777
5607 ;WAIT LOOP TIME= APPROX. 7.5N US (CONVERT N TO DECIMAL) FOR 11/20
5608 ;APPROX. 1.5N US FOR 11/45
5609 ;UPON ENTERING THE ROUTINE THE CPU PRIORITY IS DROPPED SO THAT
5610 ;RK11 CAN INTERRUPT. NOTE THAT WHEN RK11 INTERRUPTS THIS ROUTINE
5611 ;IS EXITED WITHOUT POPPING THE STACK, THIS POPPING IS DONE AFTER GETTING
5612 ;TO RK11 INTERRUPT HANDLER.
5613 ;IF FOR ANY REASON THE WAIT LOOP TIME HAS TO BE CHANGED IT CAN BE DONE
5614 ;BY SIMPLY CHANGING THE VARIABLE 'N' FOLLOWING THE 'WAT.INT'.

```

D10

MAINDEC-11-DZRKK-D MACY11 27(1006) 04-OCT-76 16:06 PAGE 103
 DZRKKD.P11 22-SEP-76 08:47 WAT.INT: WAIT FOR INTERRUPT ROUTINE SEQ 0120

```

5611 021614 017637 000000 001366 WATINT: MOV      2(SP),TIMER      ;GET 'AMOUNT' (N) FOR WHICH
5612 021622 062716 000002                ADD      #2,(SP)      ;WAITING IS TO BE DONE
5613                                ;ADJUST STACK POINTER FOR CORRECT RETURN
5614 021626 013746 001400                MOV      RKPRI, -(SP) ;DROP CPU PRIORITY SO THAT RK11 CAN
5615 021632 012746 021640                MOV      #1$, -(SP)  ; INTERRUPT
5616 021636 000002                RTI
5617 021640 005337 001366 1$: DEC      TIMER      ;WAIT FOR RK11 TO INTERRUPT
5618 021644 001375                BNE     1$
5619                                ;IF INTERRUPT HAS NOT OCCURED BY NOW
5620                                ;RETURN AND REPORT ERROR
5621 021646 000002                RTI      ;EXIT
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```

;WATIME

```

WATIME: CLR      RO
          CLR      RI
1$:      INC      RO
          BNE     1$
          INCB    RI
          BNE     1$
          RTS     PC
  
```

.SBTTL CHKCRDY: CHECK CONTROL READY

```

;:CH.CRDY
;:THIS ROUTINE WAITS FOR THE CONTROL READY TO SET. IF THE CONTROL READY BIT
;:DOES NOT SET WITHIN A CERTAIN TIME, THEN THE CONTENTS OF RKCS, RKER, RKDS
;:AND RKDA ARE SAVED AND AN EXIT MADE TO THE ERROR MESSAGE FOLLOWING THE
;:'JSR' CALL FOR THIS ROUTINE.
;:IF CONTROL READY SETS THEN THE RETURN ADDRESS IS ADJUSTED TO SKIP THE
;:ERROR MESSAGE ON RETURN.
;:CALL:  CHKCRDY
;:      ERROR      ;RETURN HERE IF ERROR
;:      ---        ;RETURN HERE IF NO ERROR
  
```

```

CH.CRDY: CLR      TIMER
1$:      TSTB    2RKCS      ;CNTRL RDY SET?
          BMI     2$        ;YES
          INC    TIMER
          BNE     1$
          JSR    PC,GT4RG   ;NO, WAIT
          RTI                ;SAVE RKCS, ER, DS, DA
2$:      ADD     #2,(SP)    ;ADJUST RETURN ADDRESS TO
          RTI                ;SKIP ERROR MESSAGE ON RETURN
  
```

.SBTTL CON.RESET: CONTROL REST ROUTINE

```

;:CON.RESET
;:THIS ROUTINE ISSUES A CONTROL RESET AND WAITS FOR
;:THE 'CNTRL RDY' FLAG TO SET. WHEN THE FLAG SETS
;:AN EXIT IS MADE OUT OF THE ROUTINE. IF 'CNTRL-RDY'
  
```


E10

MAINDEC-11-DZRKK-D MACY11 27(1006) 04-OCT-76 16:06 PAGE 104
DZRKKD.P11 22-SEP-76 08:47

CON.RESET: CONTROL REST ROUTINE

SEQ 0121

5667 :DOES NOT SET WITHIN A CERTAIN TIME AN ERROR MESSAGE
5668 : CNT RDY DIDN'T SET
5669 : PC=XXXXXX RKCS=YYYYYY
5670 : IS GIVEN. NOTE THAT XXXXXX IS THE PC WHERE 'CNT.RESET' OR 'CNT.RDY'
5671 : IS CALLED.
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5673
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5675
5676
5677
5678
5679

:CALL: CNT.RESET

.SBTTL CNT.RDY: WAIT FOR CONTROL READY ROUTINE

5680 :CN.RDY
5681 :THIS ROUTINE WAITS FOR THE CONTROL READY BIT TO SET AND WHEN IT
5682 :SETS EXITS OUT. IF WITHIN A CERTAIN TIME CNTRL RDY DOES
5683 :NOT SET AN ERROR IS REPORTED. WAITING TIME IS 883 MS FOR 11/20
5684 :175 MS FOR 11/45 WITH BIPOLAR MEMORY.
5685 :CALL: CNT.RDY

5686 021722 012777 000001 157402 CN.RST: MOV #1, @RKCS ;ISSUE A CONTROL RESET
5687 021730 012737 177500 001170 MOV #-300, \$REG3 ;SET UP COUNT
5688 021736 000402 BR CN.RDY+4 ;SKIP OVER CN.RDY

5689 021740 005037 001170 CN.RDY: CLR \$REG3
5690 021744 105777 157362 1\$: TSTB @RKCS ;DID CNTRL-RDY SET?
5691 021750 100435 BMI 3\$;YES, EXIT
5692 021752 005237 001170 INC \$REG3 ;WAITED LONG?
5693 021756 001372 BNE 1\$;IF NOT, GO BAK & WAIT
5694 021760 032777 020000 157152 2\$: BIT #SW13, @SWR ;INHIBIT TYPEOUT?
5695 021766 001026 BNE 3\$;IF YES, SKIP TYPEOUT

5696 021770 104401 TYPE
5697 021772 001245 MSG3
5698 021774 104401 022002 TYPE 65\$;:TYPE ASCIZ STRING
5699 022000 000403 BR 64\$;:GET OVER THE ASCIZ

5700 ;:65\$: .ASCIZ <15><12>/PC=
5701 022010 64\$: MOV (SP), -(SP)
5702 022010 011646 SUB #2, (SP)
5703 022012 162716 000002 TYPOC ;GO TYPE PC IN THE MAIN PROGRAM,
5704 022016 104402 ; WHERE ERROR OCCURRED
5705 ;:TYPE ASCIZ STRING
5706 022020 104401 022026 TYPE 67\$;:GET OVER THE ASCIZ
5707 022024 000404 BR 66\$

5708 ;:67\$: .ASCIZ / RKCS=
5709 022036 66\$: MOV @RKCS, -(SP) ;GET RKCS
5710 022036 017746 157270 TYPOC ;GO TYPE IT
5711 022042 104402

5712 022044 000002 3\$: RTI ;RETURN FROM THIS
5713 ;ROUTINE TO THE MAIN
5714 ;PROGRAM
5715
5716
5717

5718 :THIS PART OF THE PROGRAM CONTAINS THE COMMON ROUTINES CALLED
5719 :FROM THE SYSMAC.SML PACKAGE
5720 :
5721

5722 .SBTTL SCOPE HANDLER ROUTINE

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5736 022046
5737 022046 104407
5738 022050 032777 040000 157062
5739 022056 001111
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5741 022060 000416
5742
5743 022062 013746 000004
5744 022066 012737 022106 000004
5745 022074 005737 177060
5746 022100 012637 000004
5747 022104 000463
5748 022106 022626
5749 022110 012637 000004
5750 022114 000423
5751 022116
5752 022116 032777 000400 157014
5753 022124 001404
5754 022126 127737 157006 001102
5755 022134 001462
5756 022136 105737 001103
5757 022142 001421
5758 022144 123737 001115 001103
5759 022152 101015
5760 022154 032777 001000 156756
5761 022162 001404
5762 022164 013737 001110 001106
5763 022172 000443
5764 022174 105037 001103
5765 022200 005037 001206
5766 022204 000415
5767 022206 032777 004000 156724
5768 022214 001011
5769 022216 005737 001100
5770 022222 001406
5771 022224 005237 001104
5772 022230 023737 001206 001104
5773 022236 002021
5774 022240 012737 000001 001104
5775 022246 013737 022316 001206
5776 022254 105237 001102
5777 022260 011637 001106
5778 022264 011637 001110

;*****
;THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
;AND LOAD THE TEST NUMBER($STNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
;AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
;THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
;SW14=1 LOOP ON TEST
;SW11=1 INHIBIT ITERATIONS
;SW09=1 LOOP ON ERROR
;SW08=1 LOOP ON TEST IN SWR<7:0>
;CALL
;* SCOPE ;;SCOPE=IOT

$SCOPE:
1$: CKSWR ;;TEST FOR CHANGE IN SOFT-SWR
BIT #BIT14,$SWR ;;LOOP ON PRESENT TEST?
BNE $OVER ;;YES IF SW14=1
;*****START OF CODE FOR THE XOR TESTER*****
$XTSTR: BR 6$ ;;IF RUNNING ON THE "XOR" TESTER CHANGE
;;THIS INSTRUCTION TO A "NOP" (NOP=240)
MOV @#ERRVEC,-(SP) ;;SAVE THE CONTENTS OF THE ERROR VECTOR
MOV #5,$@#ERRVEC ;;SET FOR TIMEOUT
TST @#177060 ;;TIME OUT ON XOR?
MOV (SP)+,@#ERRVEC ;;RESTORE THE ERROR VECTOR
BR $SVLAD ;;GO TO THE NEXT TEST
5$: CMP (SP)+,(SP)+ ;;CLEAR THE STACK AFTER A TIME OUT
MOV (SP)+,@#ERRVEC ;;RESTORE THE ERROR VECTOR
BR 7$ ;;LOOP ON THE PRESENT TEST
6$;*****END OF CODE FOR THE XOR TESTER*****
BIT #BIT08,$SWR ;;LOOP ON SPEC. TEST?
BEQ 2$ ;;BR IF NO
CMPB @SWR,$STNM ;;ON THE RIGHT TEST? SWR<7:0>
BEQ $OVER ;;BR IF YES
2$: TSTB $ERFLG ;;HAS AN ERROR OCCURRED?
BEQ 3$ ;;BR IF NO
CMPB $ERMAX,$ERFLG ;;MAX. ERRORS FOR THIS TEST OCCURRED?
BHI 3$ ;;BR IF NO
BIT #BIT09,$SWR ;;LOOP ON ERROR?
BEQ 4$ ;;BR IF NO
7$: MOV $LPERR,$LPADR ;;SET LOOP ADDRESS TO LAST SCOPE
BR $OVER
4$: CLRB $ERFLG ;;ZERO THE ERROR FLAG
CLR $TIMES ;;CLEAR THE NUMBER OF ITERATIONS TO MAKE
BR 1$ ;;ESCAPE TO THE NEXT TEST
3$: BIT #BIT11,$SWR ;;INHIBIT ITERATIONS?
BNE 1$ ;;BR IF YES
TST $PASS ;;IF FIRST PASS OF PROGRAM
BEQ 1$ ;;INHIBIT ITERATIONS
INC $ICNT ;;INCREMENT ITERATION COUNT
CMP $TIMES,$ICNT ;;CHECK THE NUMBER OF ITERATIONS MADE
BGE $OVER ;;BR IF MORE ITERATION REQUIRED
1$: MOV #1,$ICNT ;;REINITIALIZE THE ITERATION COUNTER
MOV $MXCNT,$TIMES ;;SET NUMBER OF ITERATIONS TO DO
$SVLAD: INCB $STNM ;;COUNT TEST NUMBERS
MOV (SP),$LPADR ;;SAVE SCOPE LOOP ADDRESS
MOV (SP),$LPERR ;;SAVE ERROR LOOP ADDRESS

```



```

5779 022270 005037 001210          CLR      $ESCAPE      ;; CLEAR THE ESCAPE FROM ERROR ADDRESS
5780 022274 112737 000001 001115      MOV     #1,$ERMAX    ;; ONLY ALLOW ONE(1) ERROR ON NEXT TEST
5781 022302 013777 001102 156632 $OVER:  MOV     $TSTNM,$DISPLAY ;; DISPLAY TEST NUMBER
5782 022310 013716 001106          MOV     $LPADR,(SP)  ;; FUDGE RETURN ADDRESS
5783 022314 000002          RTI                    ;; FIXES PS
5784 022316 000050 $MXCNT: 50          ;; MAX. NUMBER OF ITERATIONS
5785
5786
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5798
5799 022320 104407          $ERROR: CKSWR      ;; CHECK FOR SOFTWARE SWITCH REGISTER REQUEST
5800 022322 105237 001103      7$:      INCB     $ERFLG    ;; SET THE ERROR FLAG
5801 022326 001775          BEQ      7$          ;; DON'T LET THE FLAG GO TO ZERO
5802 022330 013777 001102 156604      MOV     $TSTNM,$DISPLAY ;DISPLAY TEST NUMBER AND ERROR FLAG
5803 022336 005237 001112          1$:      INC      $ERTTL  ;; COUNT THE NUMBER OF ERRORS
5804
5805 022342 032777 000100 156570      BIT     #BIT6,$SWR   ;; DESELECT DRIVE SW SET?
5806 022350 001404          BEQ     6$          ;; NO
5807 022352 023727 001112 000005      CMP     $ERTTL,#5   ;; MORE THAN 5 ERRORS ON THIS DRIVE?
5808 022360 101047          BHI     8$          ;; YES, DESELCT THE DRIVE
5809
5810 022362 011637 001116          6$:      MOV     (SP),$ERRPC  ;; GET ADDRESS OF ERROR INSTRUCTION
5811 022366 162737 000002 001116      SUB     #2,$ERRPC
5812 022374 117737 156516 001114      MOV     $ERRPC,$ITEMB ;; STRIP AND SAVE THE ERROR ITEM CODE
5813 022402 032777 020000 156530      BIT     #SW13,$SWR  ;; SKIP TYPEOUT IF SET
5814 022410 001004          BNE     2$          ;; SKIP TYPEOUTS
5815 022412 004737 022632          JSR     PC,$ERRTYP  ;; GO TO USER ERROR ROUTINE
5816 022416 104401 001213          TYPE   $CRLF
5817 022422 005777 156512          2$:      TST     $SWR
5818 022426 100002          BPL     3$          ;; HALT ON ERROR
5819 022430 000000          HALT
5820 022432 104407          CKSWR  ;; CHECK FOR SOFTWARE SWITCH REGIATER REQUEST
5821 022434 032777 010000 156476      3$:      BIT     #SW12,$SWR  ;; SW 12 SET?
5822 022442 001402          BEQ     .+6         ;; NO, BRANCH
5823 022444 013716 001106          MOV     $LPADR,(SP) ;; ADJUST RETURN ADRES FOR SW12
5824 022450 032777 001000 156462      BIT     #SW09,$SWR  ;; LOOP ON ERROR SWITCH SET?
5825 022456 001402          BEQ     4$          ;; BR IF NO
5826 022460 013716 001110          MOV     $LPERR,(SP) ;; FUDGE RETURN FOR LOOPING
5827 022464 005737 001210          4$:      TST     $ESCAPE    ;; CHECK FOR AN ESCAPE ADDRESS
5828 022470 001402          BEQ     5$          ;; BR IF NONE
5829 022472 013716 001210          MOV     $ESCAPE,(SP) ;; FUDGE RETURN ADDRESS FOR ESCAPE
5830 022476 000002          5$:      RTI                    ;; RETURN
5831
5832 022500 005737 001434          8$:      TST     T56FLG    ;; IF EROR WAS IN LAST TEST (POLL)
5833
5834 022504 001407          BEQ     10$         ;; DROP ALL THE DRIVES

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5835 022506 104401 001303          TYPE      MSG5
5836 022512 005037 001412          CLR        DRIVS
5837 022516 022626                CMP        (SP)+,(SP)+
5838 022520 000137 020560          JMP        $EOP
5839 022524 013746 001354          10$:      MOV        DRVPT, -(SP)          ;DROP THE DRIVE FROM THE
5840 022530 162716 000002          SUB        #2,(SP)          ;SELECTION LIST
5841 022534 013746 001350          MOV        DRIVAD, -(SP)    ;DRIVE ADDR TO STACK
5842 022540 004737 021106          JSR        PC,SHFTRT        ;RIGHT JUSTIFY
5843 022544 042716 000001          BIC        #1,(R6)          ;MAKE EVEN
5844 022550 062716 001414          ADD        #DRIVO,(SP)     ;POINTS TO TABLE FOR EVEN DRIVE
5845 022554 042776 100000 000000          BIC        #BIT15,@(R6)    ;TEST REMAINING DRIVE AS RK05E
5846 022562 062716 000002          ADD        #2,(R6)          ;POINT TO ODD
5847 022566 042736 100000          BIC        #BIT15,@(SP)+   ;TEST AS RK-05E
5848 022572 012736 010000          MOV        #BIT12,@(SP)+   ;INDICATE THIS DRIVE DROPPED
5849 022576 104401 001272          TYPE      MSG4
5850 022602 013746 001350          MOV        DRIVAD, -(R6)    ;PUSH DRIVE # ON STACK
5851 022606 004737 021106          JSR        PC,SHFTRT        ;SHIFT IT BEFORE TYPING
5852 022612 104402                TYPOC      ;TYPE OUT DRIVE #
5853 022614 104401 001315          TYPE      MSG6
5854 022620 005337 001412          DEC        DRIVS          ;DECREMENT # OF DRIVES PRESENT
5855 022624 022626                CMP        (SP)+,(SP)+     ;RESTORE STACK
5856 022626 000137 017734          JMP        BTEOP           ;GO BACK TO THE END OF PROGRAM
5857
5858
5859
5860

```

.SBTTL ERROR MESSAGE TYPEOUT ROUTINE

```

;*****
;THIS ROUTINE USES THE "ITEM CONTROL BYTE" ($ITEMB) TO DETERMINE WHICH
;ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" ($ERRTB),
;AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.

```

```

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5864
5865
5866 022632                $ERRTYP:
5867 022632 104401 001213          TYPE      $CRLF           ;; "CARRIAGE RETURN" & "LINE FEED"
5868 022636 010046          MOV        RO, -(SP)      ;; SAVE RO
5869 022640 005000          CLR        RO           ;; PICKUP THE ITEM INDEX
5870 022642 153700 001114          BISB      @#$ITEMB,RO
5871 022646 001004          BNE        1$
5872
5873 022650 013746 001116          MOV        $ERRPC, -(SP)  ;; IF ITEM NUMBER IS ZERO, JUST
5874
5875 022654 104402                TYPOC      ;; TYPE THE PC OF THE ERROR
5876 022656 000426          BR        6$           ;; SAVE $ERRPC FOR TYPEOUT
5877 022660 005300          1$:      DEC        RO    ;; ERROR ADDRESS
5878 022662 006300          ASL        RO           ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
5879 022664 006300          ASL        RO           ;; GET OUT
5880 022666 006300          ASL        RO           ;; ADJUST THE INDEX SO THAT IT WILL
5881 022670 062700 001442          ADD        # $ERRTB,RO   ;; WORK FOR THE ERROR TABLE
5882 022674 012037 022704          MOV        (RO)+,2$
5883 022700 001404          BEQ        3$
5884 022702 104401                TYPOC      ;; FORM TABLE POINTER
5885 022704 000000          2$:      .WORD      0    ;; PICKUP "ERROR MESSAGE" POINTER
5886 022706 104401 001213          TYPE      $CRLF           ;; SKIP TYPEOUT IF NO POINTER
5887 022712 012037 022722          3$:      MOV        (RO)+,4$  ;; TYPE THE "ERROR MESSAGE"
5888 022716 001404          BEQ        5$           ;; "ERROR MESSAGE" POINTER GOES HERE
5889 022720 104401                TYPOC      ;; "CARRIAGE RETURN" & "LINE FEED"
5890 022722 000000          4$:      .WORD      0    ;; PICKUP "DATA HEADER" POINTER

```

;; "DATA HEADER" POINTER GOES HERE


```

5891 022724 104401 001213
5892 022730 011000
5893 022732 001004
5894 022734 012600
5895 022736 104401 001213
5896 022742 000207
5897 022744
5898 022744 013046
5899 022746 104402
5900 022750 005710
5901 022752 001770
5902 022754 104401 022762
5903 022760 000771
5904 022762 020040 000
5905 022766

```

```

5891 TYPE $CRLF ;:"CARRIAGE RETURN" & "LINE FEED"
5892 5$: MOV (RO),RO ;: PICKUP "DATA TABLE" POINTER
5893 BNE 7$ ;: GO TYPE THE DATA
5894 6$: MOV (SP)+,RO ;: RESTORE RO
5895 TYPE $CRLF ;:"CARRIAGE RETURN" & "LINE FEED"
5896 RTS PC ;: RETURN
5897
5898 7$: MOV @ (RO)+, -(SP) ;: SAVE @ (RO)+ FOR TYPEOUT
5899 TYPOC ;: GO TYPE--OCTAL ASCII(ALL DIGITS)
5900 TST (RO) ;: IS THERE ANOTHER NUMBER?
5901 BEQ 6$ ;: BR IF NO
5902 TYPE 8$ ;: TYPE TWO(2) SPACES
5903 BR 7$ ;: LOOP
5904 8$: .ASCIZ / / ;: TWO(2) SPACES
5905 .EVEN

```

.SBTTL TYPE ROUTINE

```

;:*****
;:ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
;:THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
;:NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
;:NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
;:NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
;:
;:CALL:
;:1) USING A TRAP INSTRUCTION
;: TYPE ,MESADR ;:MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
;:OR
;: TYPE
;: MESADR
;:

```

```

5924 022766 105737 001157
5925 022772 100002
5926 022774 000000
5927 022776 000407
5928 023000 010046
5929 023002 017600 000002
5930 023006 112046
5931 023010 001005
5932 023012 005726
5933 023014 012600
5934 023016 062716 000002
5935 023022 000002
5936 023024 122716 000011
5937 023030 001430
5938 023032 122716 000200
5939 023036 001006
5940 023040 005726
5941 023042 104401
5942 023044 001213
5943 023046 105037 023202
5944 023052 000755
5945 023054 004737 023136
5946 023060 123726 001156

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5924 $TYPE: TSTB $TPFLG ;: IS THERE A TERMINAL?
5925 BPL 1$ ;: BR IF YES
5926 HALT ;: HALT HERE IF NO TERMINAL
5927 BR 3$ ;: LEAVE
5928 1$: MOV RO, -(SP) ;: SAVE RO
5929 MOV @2(SP), RO ;: GET ADDRESS OF ASCIZ STRING
5930 2$: MOVB (RO)+, -(SP) ;: PUSH CHARACTER TO BE TYPED ONTO STACK
5931 BNE 4$ ;: BR IF IT ISN'T THE TERMINATOR
5932 TST (SP)+ ;: IF TERMINATOR POP IT OFF THE STACK
5933 60$: MOV (SP)+, RO ;: RESTORE RO
5934 3$: ADD #2, (SP) ;: ADJUST RETURN PC
5935 RTI ;: RETURN
5936 4$: CMPB #HT, (SP) ;: BRANCH IF <HT>
5937 BEQ 8$
5938 CMPB #CRLF, (SP) ;: BRANCH IF NOT <CRLF>
5939 BNE 5$
5940 TST (SP)+ ;: POP <CR><LF> EQUIV
5941 TYPE ;: TYPE A CR AND LF
5942 $CRLF
5943 CLRB $CHARCNT ;: CLEAR CHARACTER COUNT
5944 BR 2$ ;: GET NEXT CHARACTER
5945 5$: JSR PC, $TYPEC ;: GO TYPE THIS CHARACTER
5946 6$: CMPB $FILLC, (SP)+ ;: IS IT TIME FOR FILLER CHARS.?

```

```

5947 023064 001350          BNE      2$          ;; IF NO GO GET NEXT CHAR.
5948 023066 013746 001154    MOV      $NULL,-(SP) ;; GET # OF FILLER CHARS. NEEDED
5949                                     ;; AND THE NULL CHAR.
5950 023072 105366 000001    7$:     DECB     1(SP)  ;; DOES A NULL NEED TO BE TYPED?
5951 023076 002770          BLT      6$          ;; BR IF NO--GO POP THE NULL OFF OF STACK
5952 023100 004737 023136    JSR     PC,$TYPEC  ;; GO TYPE A NULL
5953 023104 105337 023202    DECB     $CHARCNT  ;; DO NOT COUNT AS A COUNT
5954 023110 000770          BR       7$          ;; LOOP
5955
5956                                     ;HORIZONTAL TAB PROCESSOR
5957
5958 023112 112716 000040    8$:     MOV     #' (SP)  ;; REPLACE TAB WITH SPACE
5959 023116 004737 023136    9$:     JSR     PC,$TYPEC ;; TYPE A SPACE
5960 023122 132737 000007 023202    BITB    #',$CHARCNT  ;; BRANCH IF NOT AT
5961 023130 001372          BNE     9$          ;; TAB STOP
5962 023132 005726          TST     (SP)+       ;; POP SPACE OFF STACK
5963 023134 000724          BR      2$          ;; GET NEXT CHARACTER
5964 023136 105777 156006    $TYPEC: TST     @STPS    ;; WAIT UNTIL PRINTER IS READY
5965 023142 100375          BPL     $TYPEC
5966 023144 116677 000002 156000    MOV     2(SP),@STPB  ;; LOAD CHAR TO BE TYPED INTO DATA REG.
5967 023152 122766 000015 000002    CMPB   #CR,2(SP)    ;; IS CHARACTER A CARRIAGE RETURN?
5968 023160 001003          BNE     1$          ;; BRANCH IF NO
5969 023162 105037 023202    CLRB   $CHARCNT    ;; YES--CLEAR CHARACTER COUNT
5970 023166 000406          BR      $TYPEX     ;; EXIT
5971 023170 122766 000012 000002    1$:     CMPB   #LF,2(SP) ;; IS CHARACTER A LINE FEED?
5972 023176 001402          BEQ     $TYPEX     ;; BRANCH IF YES
5973 023200 105227          INCB   (PC)+       ;; COUNT THE CHARACTER
5974 023202 000000    $CHARCNT: .WORD   0  ;; CHARACTER COUNT STORAGE
5975 023204 000207    $TYPEX: RTS      PC
5976
5977
5978                                     .SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
5979
5980                                     ;*****
5981                                     ;*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
5982                                     ;*SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
5983                                     ;*NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
5984                                     ;*BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
5985                                     ;*REPLACED WITH SPACES.
5986                                     ;*CALL:
5987                                     ;*   MOV     NUM,-(SP)  ;; PUT THE BINARY NUMBER ON THE STACK
5988                                     ;*   TYPDS  ;; GO TO THE ROUTINE
5989
5990 023206                                     $TYPDS:
5991 023206 010046          MOV     R0,-(SP)    ;; PUSH R0 ON STACK
5992 023210 010146          MOV     R1,-(SP)    ;; PUSH R1 ON STACK
5993 023212 010246          MOV     R2,-(SP)    ;; PUSH R2 ON STACK
5994 023214 010346          MOV     R3,-(SP)    ;; PUSH R3 ON STACK
5995 023216 010546          MOV     R5,-(SP)    ;; PUSH R5 ON STACK
5996 023220 012746 020200    MOV     #20200,-(SP) ;; SET BLANK SWITCH AND SIGN
5997 023224 016605 000020    MOV     20(SP),R5   ;; GET THE INPUT NUMBER
5998 023230 100004          BPL     1$          ;; BR IF INPUT IS POS.
5999 023232 005405          NEG     R5          ;; MAKE THE BINARY NUMBER POS.
6000 023234 112766 000055 000001    1$:     MOV     #'-,1(SP) ;; MAKE THE ASCII NUMBER NEG.
6001 023242 005000          CLR     R0          ;; ZERO THE CONSTANTS INDEX
6002 023244 012703 023422    MOV     #$DBLK,R3   ;; SETUP THE OUTPUT POINTER

```


L10

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6071 023432 017646 000000
6072 023436 116637 000001 023655
6073 023444 112637 023657
6074 023450 062716 000002
6075 023454 000406
6076 023456 112737 000001 023655
6077 023464 112737 000006 023657
6078 023472 112737 000005 023654
6079 023500 010346
6080 023502 010446
6081 023504 010546
6082 023506 113704 023657
6083 023512 005404
6084 023514 062704 000006
6085 023520 110437 023656
6086 023524 113704 023655
6087 023530 016605 000012
6088 023534 005003
6089 023536 006105 1$:
6090 023540 000404 BR
6091 023542 006105 2$:
6092 023544 006105 ROL
6093 023546 006105 ROL
6094 023550 010503 MOV
6095 023552 006103 3$:
6096 023554 105337 023656 DEC
6097 023560 100016 BPL
6098 023562 042703 177770 BIC
6099 023566 001002 BNE
6100 023570 005704 TST
6101 023572 001403 BEQ
6102 023574 005204 4$:
6103 023576 052703 000060 BIS
6104 023602 052703 000040 5$:
6105 023606 110337 023652 MOV
6106 023612 104401 023652 TYPE
6107 023616 105337 023654 7$:
6108 023622 003347 BGT
6109 023624 002402 BLT
6110 023626 005204 INC
6111 023630 000744 BR
6112 023632 012605 6$:
6113 023634 012604 MOV
6114 023636 012603 MOV

```

```

;*
;*$STYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
;*$STYPOS OR $STYPOC
;*$CALL:
;*      MOV      NUM,-(SP)      ;;NUMBER TO BE TYPED
;*      TYPON                      ;;CALL FOR TYPEOUT
;*
;*$STYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
;*$CALL:
;*      MOV      NUM,-(SP)      ;;NUMBER TO BE TYPED
;*      TYPOC                      ;;CALL FOR TYPEOUT
;*
$STYPOS: MOV      2(SP),-(SP)      ;;PICKUP THE MODE
        MOV      1(SP),$OFILL      ;;LOAD ZERO FILL SWITCH
        MOV      (SP)+,$SOMODE+1    ;;NUMBER OF DIGITS TO TYPE
        ADD      #2,(SP)            ;;ADJUST RETURN ADDRESS
        BR      $STYPON
$STYPOC: MOV      #1,$OFILL        ;;SET THE ZERO FILL SWITCH
        MOV      #6,$SOMODE+1      ;;SET FOR SIX(6) DIGITS
$STYPON: MOV      #5,$SOCNT        ;;SET THE ITERATION COUNT
        MOV      R3,-(SP)          ;;SAVE R3
        MOV      R4,-(SP)          ;;SAVE R4
        MOV      R5,-(SP)          ;;SAVE R5
        MOV      $SOMODE+1,R4      ;;GET THE NUMBER OF DIGITS TO TYPE
        NEG      R4
        ADD      #6,R4              ;;SUBTRACT IT FOR MAX. ALLOWED
        MOV      R4,$SOMODE        ;;SAVE IT FOR USE
        MOV      $OFILL,R4         ;;GET THE ZERO FILL SWITCH
        MOV      12(SP),R5        ;;PICKUP THE INPUT NUMBER
        CLR      R3                ;;CLEAR THE OUTPUT WORD
        ROL     R5                 ;;ROTATE MSB INTO "C"
        BR      3$                ;;GO DO MSB
        ROL     R5                 ;;FORM THIS DIGIT
        ROL     R5
        ROL     R5
        MOV     R5,R3
        ROL     R3                 ;;GET LSB OF THIS DIGIT
        DECB   $SOMODE            ;;TYPE THIS DIGIT?
        BPL    7$                 ;;BR IF NO
        BIC    #177770,R3        ;;GET RID OF JUNK
        BNE    4$                 ;;TEST FOR 0
        TST   R4                  ;;SUPPRESS THIS 0?
        BEQ   5$                 ;;BR IF YES
        INC   R4                  ;;DON'T SUPPRESS ANYMORE 0'S
        BIS   #'0,R3             ;;MAKE THIS DIGIT ASCII
        BIS   #' ,R3             ;;MAKE ASCII IF NOT ALREADY
        MOV   R3,$S              ;;SAVE FOR TYPING
        TYPE  $S                 ;;GO TYPE THIS DIGIT
        DECB  $SOCNT             ;;COUNT BY 1
        BGT  2$                  ;;BR IF MORE TO DO
        BLT  6$                  ;;BR IF DONE
        INC  R4                  ;;INSURE LAST DIGIT ISN'T A BLANK
        BR  2$                   ;;GO DO THE LAST DIGIT
        MOV  (SP)+,R5            ;;RESTORE R5
        MOV  (SP)+,R4            ;;RESTORE R4
        MOV  (SP)+,R3            ;;RESTORE R3

```


M10

MAINDEC-11-DZRKK-D MACY11 27(1006) 04-OCT-76 16:06 PAGE 112
 DZRKKD.P11 22-SEP-76 08:47 BINARY TO OCTAL (ASCII) AND TYPE

SEQ 0129

```

6115 023640 016666 000002 000004      MOV      2(SP),4(SP)      ;;SET THE STACK FOR RETURNING
6116 023646 012616                MOV      (SP)+,(SP)
6117 023650 000002                RTI
6118 023652      000                8$:      .BYTE      0      ;;RETURN
6119 023653      000                .BYTE      0      ;;STORAGE FOR ASCII DIGIT
6120 023654      000                $OCNT:    .BYTE      0      ;;TERMINATOR FOR TYPE ROUTINE
6121 023655      000                $OFILL:   .BYTE      0      ;;OCTAL DIGIT COUNTER
6122 023656 000000                $OMODE:   .WORD      0      ;;ZERO FILL SWITCH
6123                                     ;;NUMBER OF DIGITS TO TYPE
6124                                     .SBTTL  TTY INPUT ROUTINE
6125                                     ;;*****
6126                                     .ENABL  LSB
6127                                     ;;*****
6128                                     *SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
6129                                     *ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
6130                                     *SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP CALL
6131                                     *WHEN OPERATING IN TTY FLAG MODE.
6132                                     $CKSWR:  CMP      #SWREG,SWR      ;; IS THE SOFT-SWR SELECTED?
6133                                     BNE      15$                    ;; BRANCH IF NO
6134 023660 022737 000176 001140      TSTB     @STKS                    ;; CHAR THERE?
6135 023666 001074                BPL      15$                    ;; IF NO, DON'T WAIT AROUND
6136 023670 105777 155250      MOV      @STKB,-(SP)              ;; SAVE THE CHAR
6137 023674 100071                BIC      #17,(SP)                 ;; STRIP-OFF THE ASCII
6138 023676 117746 155244      CMP      #7,(SP)+                 ;; IS IT A CONTROL G?
6139 023702 042716 177600      BNE      15$                    ;; NO, RETURN TO USER
6140 023706 022726 000007      CMP      $AUTOB,#1                ;; ARE WE RUNNING IN AUTO-MODE?
6141 023712 001062                BEQ      15$                    ;; BRANCH IF YES
6142 023714 123727 001134 000001      $GTSWR: TYPE      , $CNTLG          ;; ECHO THE CONTROL-G (↑G)
6143 023722 001456                TYPE      , $MSWR                ;; TYPE CURRENT CONTENTS
6144                                     MOV      SWREG,-(SP)              ;; SAVE SWREG FOR TYPEOUT
6145 023724 104401 024545      TYPOC   TYPE      , $MNEW          ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
6146 023730 104401 024552      19$:    CLR      -(SP)                ;; PROMPT FOR NEW SWR
6147 023734 013746 000176      CLR      -(SP)                ;; CLEAR COUNTER
6148 023740 104402                TSTB     @STKS                    ;; THE NEW SWR
6149 023742 104401 024563      7$:    BPL      7$                    ;; CHAR THERE?
6150 023746 005046                BPL      7$                    ;; IF NOT TRY AGAIN
6151 023750 005046                MOV      @STKB,-(SP)              ;; PICK UP CHAR
6152 023752 105777 155166      BIC      #17,(SP)                ;; MAKE IT 7-BIT ASCII
6153 023756 100375                ;;
6154                                     ;;
6155 023760 117746 155162      9$:    CMP      (SP),#25              ;; IS IT A CONTROL-U?
6156 023764 042716 177600      BNE      10$                    ;; BRANCH IF NOT
6157                                     TYPE      , $CNTLU                ;; YES, ECHO CONTROL-U (↑U)
6158                                     ADD      #6,SP                    ;; IGNORE PREVIOUS INPUT
6159                                     BR       19$                      ;; LET'S TRY IT AGAIN
6160 023770 021627 000025      20$:   ADD      #6,SP
6161 023774 001005                BR       19$
6162 023776 104401 024540      10$:   CMP      (SP),#15              ;; IS IT A <CR>?
6163 024002 062706 000006      BNE      16$                    ;; BRANCH IF NO
6164 024006 000757                TST      4(SP)                   ;; YES, IS IT THE FIRST CHAR?
6165                                     BEQ      11$                      ;; BRANCH IF YES
6166
6167 024010 021627 000015
6168 024014 001022
6169 024016 005766 000004
6170 024022 001403
    
```

```

6171 024024 016677 000002 155106      MOV      2(SP),@SWR      ;;SAVE NEW SWR
6172 024032 062706 000006      ADD      #6,SP          ;;CLEAR UP STACK
6173 024036 104401 001213      TYPE    $CRLF          ;;ECHO <CR> AND <LF>
6174 024042 123727 001135 000001      CMPB    $INTAG,#1      ;;RE-ENABLE TTY KBD INTERRUPTS?
6175 024050 001003      BNE     15$            ;;BRANCH IF NOT
6176 024052 012777 000100 155064      MOV      #100,@STKS    ;;RE-ENABLE TTY KBD INTERRUPTS
6177 024060 000002      RTI     ;              ;;RETURN
6178 024062 004737 023136      JSR     PC,$TYPEC      ;;ECHO CHAR
6179 024066 021627 000060      CMP     (SP),#60       ;;CHAR < 0?
6180 024072 002420      BLT    18$            ;;BRANCH IF YES
6181 024074 021627 000067      CMP     (SP),#67       ;;CHAR > 7?
6182 024100 003015      BGT    18$            ;;BRANCH IF YES
6183 024102 042726 000060      BIC     #60,(SP)+      ;;STRIP-OFF ASCII
6184 024106 005766 000002      TST     2(SP)          ;;IS THIS THE FIRST CHAR
6185 024112 001403      BEQ    17$            ;;BRANCH IF YES
6186 024114 006316      ASL    (SP)           ;;NO, SHIFT PRESENT
6187 024116 006316      ASL    (SP)           ;;CHAR OVER TO MAKE
6188 024120 006316      ASL    (SP)           ;;ROOM FOR NEW ONE.
6189 024122 005266 000002      INC     2(SP)          ;;KEEP COUNT OF CHAR
6190 024126 056616 177776      BIS     -2(SP),(SP)    ;;SET IN NEW CHAR
6191 024132 000707      BR     7$             ;;GET THE NEXT ONE
6192 024134 104401 001212      TYPE    $QUES          ;;TYPE ?<CR><LF>
6193 024140 000720      BR     20$           ;;SIMULATE CONTROL-U
6194 .DSABL  LSB
6195
6196
6197
6198
6199
6200
6201
6202
6203
6204
6205 024142 011646      $RDCHR: MOV      (SP),-(SP)    ;;PUSH DOWN THE PC
6206 024144 016666 000004 000002      MOV      4(SP),2(SP)  ;;SAVE THE PS
6207 024152 105777 154766      1$:     TSTB    @STKS    ;;WAIT FOR
6208 024156 100375      BPL     1$            ;;A CHARACTER
6209 024160 117766 154762 000004      MOVB    @STKB,4(SP)   ;;READ THE TTY
6210 024166 042766 177600 000004      BIC     #1C<177>,4(SP) ;;GET RID OF JUNK IF ANY
6211 024174 026627 000004 000023      CMP     4(SP),#23    ;;IS IT A CONTROL-S?
6212 024202 001013      BNE     3$            ;;BRANCH IF NO
6213 024204 105777 154734      2$:     TSTB    @STKS    ;;WAIT FOR A CHARACTER
6214 024210 100375      BPL     2$            ;;LOOP UNTIL ITS THERE
6215 024212 117746 154730      MOVB    @STKB,-(SP)   ;;GET CHARACTER
6216 024216 042716 177600      BIC     #1C177,(SP)  ;;MAKE IT 7-BIT ASCII
6217 024222 022627 000021      CMP     (SP)+,#21    ;;IS IT A CONTROL-Q?
6218 024226 001366      BNE     2$            ;;IF NOT DISCARD IT
6219 024230 000750      BR     1$            ;;YES, RESUME
6220 024232 026627 000004 000140      3$:     CMP     4(SP),#140 ;;IS IT UPPER CASE?
6221 024240 002407      BLT    4$            ;;BRANCH IF YES
6222 024242 026627 000004 000175      CMP     4(SP),#175   ;;IS IT A SPECIAL CHAR?
6223 024250 003003      BGT    4$            ;;BRANCH IF YES
6224 024252 042766 000040 000004      BIC     #40,4(SP)    ;;MAKE IT UPPER CASE
6225 024260 000002      RTI     ;              ;;GO BACK TO USER
6226

```

THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
*CALL:
* RDCHR INPUT A SINGLE CHARACTER FROM THE TTY
* RETURN HERE CHARACTER IS ON THE STACK
* WITH PARITY BIT STRIPPED OFF

```

6227          ;*THIS ROUTINE WILL INPUT A STRING FROM THE TTY
6228          ;*CALL:
6229          ;*      RDLIN          ;* INPUT A STRING FROM THE TTY
6230          ;*      RETURN HERE    ;* ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
6231          ;*                    ;* TERMINATOR WILL BE A BYTE OF ALL 0'S
6232
6233 024262 010346 SRDLIN: MOV      R3, -(SP)          ;* SAVE R3
6234 024264 005046          CLR      -(SP)          ;* CLEAR THE RUBOUT KEY
6235 024266 012703 024516 1$:  MOV      #STTYIN,R3      ;* GET ADDRESS
6236 024272 022703 024540 2$:  CMP      #STTYIN+22,R3    ;* BUFFER FULL?
6237          101456          BLOS     4$              ;* BR IF YES
6238 024300 104410          RDCHR          ;* GO READ ONE CHARACTER FROM THE TTY
6239 024302 112613          MOVB    (SP)+,(R3)      ;* GET CHARACTER
6240 024304 122713 000177 10$: CMPB    #177,(R3)      ;* IS IT A RUBOUT
6241 024310 001022          BNE     5$              ;* BR IF NO
6242 024312 005716          TST     (SP)          ;* IS THIS THE FIRST RUBOUT?
6243 024314 001007          BNE     6$              ;* BR IF NO
6244 024316 112737 000134 024514 MOVB    #' \,9$        ;* TYPE A BACK SLASH
6245 024324 104401 024514          TYPE   ,9$
6246 024330 012716 177777          MOV     #-1,(SP)      ;* SET THE RUBOUT KEY
6247 024334 005303 6$:  DEC     R3              ;* BACKUP BY ONE
6248 024336 020327 024516          CMP     R3,#STTYIN    ;* STACK EMPTY?
6249 024342 103434          BLO     4$              ;* BR IF YES
6250 024344 111337 024514          MOVB    (R3),9$      ;* SETUP TO TYPEOUT THE DELETED CHAR.
6251 024350 104401 024514          TYPE   ,9$
6252 024354 000746          BR      2$              ;* GO TYPE
6253 024356 005716 5$:  TST     (SP)          ;* GO READ ANOTHER CHAR.
6254 024360 001406          BEQ     7$              ;* RUBOUT KEY SET?
6255 024362 112737 000134 024514 MOVB    #' \,9$        ;* BR IF NO
6256 024370 104401 024514          TYPE   ,9$          ;* TYPE A BACK SLASH
6257 024374 005016          CLR     (SP)          ;* CLEAR THE RUBOUT KEY
6258 024376 122713 000025 7$:  CMPB    #25,(R3)      ;* IS CHARACTER A CTRL U?
6259 024402 001003          BNE     8$              ;* BR IF NO
6260 024404 104401 024540          TYPE   ,SCNTLU      ;* TYPE A CONTROL "U"
6261 024410 000726          BR      1$              ;* GO START OVER
6262 024412 122713 000022 8$:  CMPB    #22,(R3)      ;* IS CHARACTER A "↑R"?
6263 024416 001011          BNE     3$              ;* BRANCH IF NO
6264 024420 105013          CLRB   (R3)          ;* CLEAR THE CHARACTER
6265 024422 104401 001213          TYPE   ,SCLF        ;* TYPE A "CR" & "LF"
6266 024426 104401 024516          TYPE   ,STTYIN      ;* TYPE THE INPUT STRING
6267 024432 000717          BR      2$              ;* GO PICKUP ANOTHER CHARACTER
6268 024434 104401 001212 4$:  TYPE   ,QUES        ;* TYPE A '?'
6269 024440 000712          BR      1$              ;* CLEAR THE BUFFER AND LOOP
6270 024442 111337 024514 3$:  MOVB    (R3),9$      ;* ECHO THE CHARACTER
6271 024446 104401 024514          TYPE   ,9$
6272 024452 122723 000015          CMPB    #15,(R3)+    ;* CHECK FOR RETURN
6273 024456 001305          BNE     2$              ;* LOOP IF NOT RETURN
6274 024460 105063 177777          CLRB   -1(R3)        ;* CLEAR RETURN (THE 15)
6275 024464 104401 001214          TYPE   ,SLF         ;* TYPE A LINE FEED
6276 024470 005726          TST     (SP)+        ;* CLEAN RUBOUT KEY FROM THE STACK
6277 024472 012603          MOV     (SP)+,R3      ;* RESTORE R3
6278 024474 011646          MOV     (SP),-(SP)    ;* ADJUST THE STACK AND PUT ADDRESS OF THE
6279 024476 016666 000004 000002 MOV     4(SP),2(SP)    ;* FIRST ASCII CHARACTER ON IT
6280 024504 012766 024516 000004 MOV     #STTYIN,4(SP)
6281 024512 000002          RTI
6282 024514 000          9$:  .BYTE  0          ;* RETURN
          ;* STORAGE FOR ASCII CHAR. TO TYPE
    
```


6283 024515 000
6284 024516 000022
6285 024540 052536 005015 000
6286 024545 136 006507 000012
6287 024552 005015 053523 020122
6288 024560 020075 000
6289 024563 040 047040 053505
6290 024570 036440 000040

.BYTE 0 ;: TERMINATOR
\$TTYIN: .BLKB 22 ;: RESERVE 22 BYTES FOR TTY INPUT
\$CNTLU: .ASCIZ /!U/<15><12> ;: CONTROL "U"
\$CNTLG: .ASCIZ /!G/<15><12> ;: CONTROL "G"
\$MSWR: .ASCIZ <15><12>/SWR = /

\$MNEW: .ASCIZ / NEW = /
;CONTROL U, RUBOUT CAPABILITY
.SBTTL TRAP DECODER

;;*****
;*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
;*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
;*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
;*GO TO THAT ROUTINE.

6300 024574 010046
6301 024576 016600 000002
6302 024602 005740
6303 024604 111000
6304 024606 006300
6305 024610 016000 024630
6306 024614 000200

\$TRAP: MOV RO, -(SP) ;: SAVE RO
MOV 2(SP), RO ;: GET TRAP ADDRESS
TST -(RO) ;: BACKUP BY 2
MOVB (RO), RO ;: GET RIGHT BYTE OF TRAP
ASL RO ;: POSITION FOR INDEXING
MOV \$TRPAD(RO), RO ;: INDEX TO TABLE
RTS RO ;: GO TO ROUTINE

;;THIS IS USE TO HANDLE THE "GETPRI" MACRO

6311 024616 011646
6312 024620 016666 000004 000002
6313 024626 000002

\$TRAP2: MOV (SP), -(SP) ;: MOVE THE PC DOWN
MOV 4(SP), 2(SP) ;: MOVE THE PSW DOWN
RTI ;: RESTORE THE PSW

.SBTTL TRAP TABLE

;;THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
;;BY THE "TRAP" INSTRUCTION.

6321
6322 024630 024616
6323 024632 022766
6324 024634 023456
6325 024636 023432
6326 024640 023472
6327 024642 023206
6328
6329 024644 023730
6330
6331 024646 023660
6332 024650 024142
6333 024652 024262
6334
6335 024654 021666
6336
6337 024656 021722
6338

: ROUTINE
:-----
\$TRPAD: .WORD \$TRAP2
\$TYPE ;: CALL=TYPE TRAP+1(104401) TTY TYPEOUT ROUTINE
\$TYPOC ;: CALL=TYPOC TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS)
\$TYPOS ;: CALL=TYPOS TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS)
\$TYPON ;: CALL=TYPON TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL)
\$TYPDS ;: CALL=TYPDS TRAP+5(104405) TYPE DECIMAL NUMBER (WITH SIGN)

\$GTSWR ;: CALL=GTSWR TRAP+6(104406) GET SOFT-SWR SETTING

\$CKSWR ;: CALL=CKSWR TRAP+7(104407) TEST FOR CHANGE IN SOFT-SWR
\$RDCHR ;: CALL=RDCHR TRAP+10(104410) TTY TYPEIN CHARACTER ROUTINE
\$RDLIN ;: CALL=RDLIN TRAP+11(104411) TTY TYPEIN STRING ROUTINE

CH.CRDY ;: CALL=CHKCRDY TRAP+12(104412) CHECK CONTROL READY
CN.RST ;: CALL=CN.RESET TRAP+13(104413) CONTROL RESET ROUTINE


```

6339 024660 021740 CN.RDY ;;CALL=CNT.RDY TRAP+14(104414) WAIT FOR CNTRL RDY TO SET
6340
6341 024662 020764 BDAO ;;CALL=BRKDAC TRAP+15(104415) BREAK RKDA INTO DR #,CYL,SUR,SEC BITS
6342
6343 024664 020774 BDA4 ;;CALL=BRKDA4 TRAP+16(104416) BREAK RKDA INTO DR #,CYL,SUR,SEC BITS
6344
6345 024666 021572 DELA.Y ;;CALL=DELAY TRAP+17(104417) TIME DELAY ROUTINE
6346
6347 024670 021614 WATINT ;;CALL=WAT.INT TRAP+20(104420) WAIT FOR RK11 INTERRUPT ROUTINE
6348
6349 024672 021500 TSTSIN ;;CALL=TST.SIN TRAP+21(104421) TEST SIN ROUTINE
6350
6351
6352
6353
6354
6355
    
```

.SBTTL POWER DOWN AND UP ROUTINES

```

6356 024674 012737 025040 000024 $PWRDN: MOV #SILLUP,@PWRVEC ;;SET FOR FAST UP
6357 024702 012737 000340 000026 MOV #340,@PWRVEC+2 ;;PRIO:7
6358 024710 010046 MOV RO,-(SP) ;;PUSH RO ON STACK
6359 024712 010146 MOV R1,-(SP) ;;PUSH R1 ON STACK
6360 024714 010246 MOV R2,-(SP) ;;PUSH R2 ON STACK
6361 024716 010346 MOV R3,-(SP) ;;PUSH R3 ON STACK
6362 024720 010446 MOV R4,-(SP) ;;PUSH R4 ON STACK
6363 024722 010546 MOV R5,-(SP) ;;PUSH R5 ON STACK
6364 024724 017746 154210 MOV @SWR,-(SP) ;;PUSH @SWR ON STACK
6365 024730 010637 025044 MOV SP,$SAVR6 ;;SAVE SP
6366 024734 012737 024746 000024 MOV $PWRUP,@PWRVEC ;;SET UP VECTOR
6367 024742 000000 HALT
6368 024744 000776 BR .-2 ;;HANG UP
6369
6370
6371
6372 024746 012737 025040 000024 $PWRUP: MOV #SILLUP,@PWRVEC ;;SET FOR FAST DOWN
6373 024754 013706 025044 MOV $SAVR6,SP ;;GET SP
6374 024760 005037 025044 CLR $SAVR6 ;;WAIT LOOP FOR THE TTY
6375 024764 005237 025044 1$: INC $SAVR6 ;;WAIT FOR THE INC
6376 024770 001375 BNE 1$ ;;OF WORD
6377 024772 012677 154142 MOV (SP)+,@SWR ;;POP STACK INTO @SWR
6378 024776 012605 MOV (SP)+,R5 ;;POP STACK INTO R5
6379 025000 012604 MOV (SP)+,R4 ;;POP STACK INTO R4
6380 025002 012603 MOV (SP)+,R3 ;;POP STACK INTO R3
6381 025004 012602 MOV (SP)+,R2 ;;POP STACK INTO R2
6382 025006 012601 MOV (SP)+,R1 ;;POP STACK INTO R1
6383 025010 012600 MOV (SP)+,R0 ;;POP STACK INTO R0
6384 025012 012737 024674 000024 MOV $PWRDN,@PWRVEC ;;SET UP THE POWER DOWN VECTOR
6385 025020 012737 000340 000026 MOV #340,@PWRVEC+2 ;;PRIO:7
6386 025026 104401 TYPE ;;REPORT THE POWER FAILURE
6387 025030 025046 $PWRMG: .WORD $POWER ;;POWER FAIL MESSAGE POINTER
6388 025032 012716 MOV (PC)+,(SP) ;;RESTART AT PFSTR
6389 025034 004630 $PWRAD: .WORD PFSTR ;;RESTART ADDRESS
6390 025036 000002 RTI
6391 025040 000000 $SILLUP: HALT ;;THE POWER UP SEQUENCE WAS STARTED
6392 025042 000776 BR .-2 ;;BEFORE THE POWER DOWN WAS COMPLETE
6393 025044 000000 $SAVR6: 0 ;;PUT THE SP HERE
6394 025046 005015 047520 042527 $POWER: .ASCIZ <15><12>"POWER"
    
```

```

6395 025054 000122
6396
6397
6398 025056 004737 021412 FCHECK: JSR PC,DRESET ;RESETB DRIVE
6399 025062 104026 ERROR 26
6400 025064 104413 CNT.RESET
6401 025066 013737 001350 025200 MOV DRIVAD,DRHOLD ;SAVE DRIVE ADDR
6402 025074 032737 020000 001350 BIT #20000,DRIVAD ;SEE IF ODD
6403 025102 001404 BEQ 1$
6404 025104 042737 020000 001350 BIC #20000,DRIVAD ;MAKE EVEN
6405 025112 000403 BR 2$
6406 025114 052737 020000 001350 1$: BIS #20000,DRIVAD ;MAKE ODD
6407 025122 013777 001350 154210 2$: MOV DRIVAD,DRKDA ;DRIVE ADDR
6408 025130 012777 000011 154174 MOV #11,DRKCS ;DRIVE SEEK
6409 025136 104414 CNT.RDY
6410 025140 013777 025200 154172 MOV DRHOLD,DRKDA ;OTHER DRIVE
6411 025146 104414 CNT.RDY
6412 025150 032777 000100 154150 BIT #100,DRKDS ;HEADS IN MOTION?
6413 025156 001001 BNE 3$ ;NO SO RK-05J
6414 025160 005725 TST (R5)+ ;YES RK-05F
6415 025162 013737 025200 001350 3$: MOV DRHOLD,DRIVAD ;RESTORE ADDR
6416 025170 004737 021412 JSR PC,DRESET ;WAIT FOR RESET
6417 025174 104026 ERROR 26
6418 025176 000205 RTS R5
6419 025200 000000 DRHOLD: 0
6420 025202 005037 001350 SIZEF: CLR DRIVAD ;START AT DRO
6421 025206 012700 001414 MOV #DRIVO,RO ;TABLE OF AVAIL DRIVES
6422 025212 005710 4$: TST (RO) ;THIS DRIVE HERE?
6423 025214 001413 BEQ 2$ ;NO
6424 025216 005760 000002 TST 2(RO) ;COMPLEMENT HERE?
6425 025222 001410 BEQ 2$ ;NO
6426 025224 004537 025056 JSR R5,FCHECK ;SEE IF F MODEL
6427 025230 000405 BR 2$ ;J MODEL
6428 025232 052710 100000 BIS #100000,(RO) ;SET SIGN FOR F
6429 025236 052760 100000 000002 BIS #100000,2(RO) ;BOTH DRIVES
6430 025244 005720 2$: TST (RO)+
6431 025246 005720 TST (RO)+ ;NEXT PAIR OF DRIVES
6432 025250 062737 040000 001350 ADD #40000,DRIVAD ;NEXT ACTUL ADDR
6433 025256 022700 001433 CMP #DRIV7+1,RO ;CHECKED ALL?
6434 025262 003353 BGT 4$ ;NOT YET
6435 025264 000207 RTS PC
6436
6437 ;ERROR MESSAGES
6438
6439 .SBTTL ERROR MESSAGES
6440
6441 025266 045522 041527 042440 EM11: .ASCIZ /RKWC EROR/
6442 025274 047522 000122
6443
6444
6445 025300 044523 020116 051511 EM12: .ASCIZ /SIN IS SET/
6446 025306 051440 052105 000
6447
6448 025313 122 041113 020101 EM13: .ASCIZ /RKBA EROR/
6449 025320 051105 051117 000
6450

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6451	025325	122	042113	020101	EM16:	.ASCIZ	/RKDA WRONG AFTER 'SSE' /
6452	025332	051127	047117	020107			
6453	025340	043101	042524	020122			
6454	025346	051447	042523	000047			
6455							
6456	025354	045522	051504	042440	EM21:	.ASCIZ	/RKDS EROR /
6457	025362	047522	000122				
6458							
6459	025366	050104	020114	042523	EM30:	.ASCIZ	/DPL SET /
6460	025374	000124					
6461							
6462	025376	051104	020125	042523	EM31:	.ASCIZ	/DRU SET /
6463	025404	000124					
6464							
6465	025406	045522	032460	041040	EM32:	.ASCIZ	/RKOS BIT NOT SET /
6466	025414	052111	047040	052117			
6467	025422	051440	052105	000			
6468							
6469	025427	104	054522	041040	EM33:	.ASCIZ	/DRY BIT NOT SET /
6470	025434	052111	047040	052117			
6471	025442	051440	052105	000			
6472							
6473	025447	123	045517	042040	EM34:	.ASCIZ	/SOK DIDN'T SET /
6474	025454	042111	023516	020124			
6475	025462	042523	000124				
6476							
6477	025465	042523	026503	047103	EM35:	.ASCIZ	/SEC-CNTR DIDN'T COUNT TO 0 /
6478	025474	051124	042040	042111			
6479	025502	023516	020124	047503			
6480	025510	047125	020124	047524			
6481	025516	030040	000				
6482							
6483	025521	123	041505	041455	EM36:	.ASCIZ	/SEC-CNTR DIDN'T INCRMNT /
6484	025526	052116	020122	044504			
6485	025534	047104	052047	044440			
6486	025542	041516	046522	052116			
6487	025550	000					
6488							
6489	025551	123	041505	041455	EM37:	.ASCIZ	/SEC-COUNTR INCRMENTED WRONG /
6490	025556	052517	052116	020122			
6491	025564	047111	051103	042515			
6492	025572	052116	042105	053440			
6493	025600	047522	043516	000			
6494							
6495	025605	104	042111	023516	EM40:	.ASCIZ	/DIDN'T GET SC=SA FOR THIS SECTR /
6496	025612	020124	042507	020124			
6497	025620	041523	051475	020101			
6498	025626	047506	020122	044124			
6499	025634	051511	051440	041505			
6500	025642	051124	000				
6501							
6502	025645	105	047522	026522	EM41:	.ASCIZ	"EROR-R/W/S RDY SHOULD BE SET"
6503	025652	027522	027527	020123			
6504	025660	042122	020131	044123			
6505	025666	052517	042114	041040			
6506	025674	020105	042523	000124			

6507						
6508	025702	047125	054105	042520	EM43:	.ASCIZ /UNEXPECTED RK11 INTERRUPT/
6509	025710	052103	042105	051040		
6510	025716	030513	020061	047111		
6511	025724	042524	051122	050125		
6512	025732	000124				
6513						
6514	025734	047103	051124	020114	EM44:	.ASCIZ /CNTRL RDY DIDN'T SET AFTER SEEK OR DR RESET/
6515	025742	042122	020131	044504		
6516	025750	047104	052047	051440		
6517	025756	052105	040440	052106		
6518	025764	051105	051440	042505		
6519	025772	020113	051117	042040		
6520	026000	020122	042522	042523		
6521	026006	000124				
6522						
6523	026010	051105	020122	051117	EM45:	.ASCIZ /ERR OR HE BIT SET ON SEEK OR DR RESET/
6524	026016	044040	020105	044502		
6525	026024	020124	042523	020124		
6526	026032	047117	051440	042505		
6527	026040	020113	051117	042040		
6528	026046	020122	042522	042523		
6529	026054	000124				
6530						
6531	026056	045522	051105	041040	EM46:	.ASCIZ /RKER BIT, ON SEEK OR DR RESET/
6532	026064	052111	020054	047117		
6533	026072	051440	042505	020113		
6534	026100	051117	042040	020122		
6535	026106	042522	042523	000124		
6536						
6537	026114	045522	051503	041440	EM47:	.ASCIZ /RKCS CHNGD AFTR FUNCTION WAS DONE/
6538	026122	047110	042107	040440		
6539	026130	052106	020122	052506		
6540	026136	041516	044524	047117		
6541	026144	053440	051501	042040		
6542	026152	047117	000105			
6543						
6544	026156	027522	027527	020123	EM50:	.ASCIZ "R/W/S RDY DIDN'T CLEAR"
6545	026164	042122	020131	044504		
6546	026172	047104	052047	041440		
6547	026200	042514	051101	000		
6548						
6549	026205	122	053457	051457	EM51:	.ASCIZ "R/W/S RDY DIDN'T SET AFTR SEEK OR DR RESET"
6550	026212	051040	054504	042040		
6551	026220	042111	023516	020124		
6552	026226	042523	020124	043101		
6553	026234	051124	051440	042505		
6554	026242	020113	051117	042040		
6555	026250	020122	042522	042523		
6556	026256	000124				
6557						
6558	026260	045522	040504	041440	EM52:	.ASCIZ /RKDA CHNGD AFTR SEEK/
6559	026266	047110	042107	040440		
6560	026274	052106	020122	042523		
6561	026302	045505	000			
6562						

H11

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SEQ 0137

6563	026305	103	052116	046122	EM53:	.ASCIZ /CNTRL RDY DIDN'T CLR AS GO WAS SET/
6564	026312	051040	054504	042040		
6565	026320	042111	023516	020124		
6566	026326	046103	020122	051501		
6567	026334	043440	020117	040527		
6568	026342	020123	042523	000124		
6569						
6570	026350	047103	051124	020114	EM54:	.ASCIZ "CNTRL RDY DIDN'T SET ON WRT/FMT STARTING FROM <DSK-ADRES>"
6571	026356	042122	020131	044504		
6572	026364	047104	052047	051440		
6573	026372	052105	047440	020116		
6574	026400	051127	027524	046506		
6575	026406	020124	052123	051101		
6576	026414	044524	043516	043040		
6577	026422	047522	020115	042074		
6578	026430	045523	040455	051104		
6579	026436	051505	000076			
6580						
6581	026442	042510	047440	020122	EM55:	.ASCIZ "HE OR ERR ON WRT/FMT STARTING FROM <DSK-ADRES>"
6582	026450	051105	020122	047117		
6583	026456	053440	052122	043057		
6584	026464	052115	051440	040524		
6585	026472	052122	047111	020107		
6586	026500	051106	046517	036040		
6587	026506	051504	026513	042101		
6588	026514	042522	037123	000		
6589						
6590	026521	122	042113	020101	EM56:	.ASCIZ /RKDA INCRMNTD WRONG ON WRT-FMT/
6591	026526	047111	051103	047115		
6592	026534	042124	053440	047522		
6593	026542	043516	047440	020116		
6594	026550	051127	026524	046506		
6595	026556	000124				
6596						
6597	026560	045522	041527	042040	EM57:	.ASCIZ /RKWC DIDN'T OVRFLO ON WRT FMT/
6598	026566	042111	023516	020124		
6599	026574	053117	043122	047514		
6600	026602	047440	020116	051127		
6601	026610	020124	046506	000124		
6602						
6603	026616	045522	040502	044440	EM60:	.ASCIZ /RKBA INCRMNTD WRONG ON WRT FMT/
6604	026624	041516	046522	052116		
6605	026632	020104	051127	047117		
6606	026640	020107	047117	053440		
6607	026646	052122	043040	052115		
6608	026654	000				
6609						
6610	026655	122	042513	020122	EM61:	.ASCIZ /RKER SET, ON WRT OR RD OR FMT/
6611	026662	042523	026124	047117		
6612	026670	053440	052122	047440		
6613	026676	020122	042122	047440		
6614	026704	020122	046506	000124		
6615						
6616	026712	045522	041104	042440	EM62:	.ASCIZ /RKDB EROR/
6617	026720	047522	000122			
6618						

6619	026724	045522	040504	044440	EM63:	.ASCIZ /RKDA INCRMNTD WRONG ON RD OR RD FMT/
6620	026732	041516	046522	052116		
6621	026740	020104	051127	047117		
6622	026746	020107	047117	051040		
6623	026754	020104	051117	051040		
6624	026762	020104	046506	000124		
6625						
6626	026770	045522	041527	042040	EM64:	.ASCIZ /RKWC DIDN'T OVRFLO ON RD OR RD FMT/
6627	026776	042111	023516	020124		
6628	027004	053117	043122	047514		
6629	027012	047440	020116	042122		
6630	027020	047440	020122	042122		
6631	027026	043040	052115	000		
6632						
6633	027033	122	041113	020101	EM65:	.ASCIZ /RKBA INCRMNTD WRONG ON RD OR RD FMT/
6634	027040	047111	051103	047115		
6635	027046	042124	053440	047522		
6636	027054	043516	047440	020116		
6637	027062	042122	047440	020122		
6638	027070	042122	043040	052115		
6639	027076	000				
6640						
6641	027077	111	041516	051117	EM66:	.ASCIZ /INCORRECT HEADER FROM 'SECTOR'/'
6642	027104	042522	052103	044040		
6643	027112	040505	042504	020122		
6644	027120	051106	046517	023440		
6645	027126	042523	052103	051117		
6646	027134	000047				
6647						
6648	027136	040504	040524	042440	EM67:	.ASCIZ /DATA ERROR/
6649	027144	051122	051117	000		
6650						
6651	027151	103	052116	046122	EM70:	.ASCIZ "CNTRL RDY DIDN'T SET ON RD/FMT STARTING FROM <DSK-ADRES>"
6652	027156	051040	054504	042040		
6653	027164	042111	023516	020124		
6654	027172	042523	020124	047117		
6655	027200	051040	027504	046506		
6656	027206	020124	052123	051101		
6657	027214	044524	043516	043040		
6658	027222	047522	020115	042074		
6659	027230	045523	040455	051104		
6660	027236	051505	000076			
6661						
6662	027242	042510	047440	020122	EM71:	.ASCIZ "HE OR ERR ON RD/FMT STARTING FROM <DSK-ADRES>"
6663	027250	051105	020122	047117		
6664	027256	051040	027504	046506		
6665	027264	020124	052123	051101		
6666	027272	044524	043516	043040		
6667	027300	047522	020115	042074		
6668	027306	045523	040455	051104		
6669	027314	051505	000076			
6670						
6671	027320	051127	047117	020107	EM72:	.ASCIZ /WRONG DRIVE ID IN RKDS AFTER SEEK/
6672	027326	051104	053111	020105		
6673	027334	042111	044440	020116		
6674	027342	045522	051504	040440		

6675	027350	052106	051105	051440	
6676	027356	042505	000113		
6677					
6678	027362	051110	053504	042522	EM73: .ASCIZ /HRDWRE POLL-DRV ID BITS(13-15) SHLD BE CLR/
6679	027370	050040	046117	026514	
6680	027376	051104	020126	042111	
6681	027404	041040	052111	024123	
6682	027412	031461	030455	024465	
6683	027420	051440	046110	041104	
6684	027426	020105	046103	000122	
6685					
6686	027434	051110	053504	042522	EM74: .ASCIZ /HRDWRE POLL-INTRUPTING DRV # NOT PRSNT/
6687	027442	050040	046117	026514	
6688	027450	047111	051124	050125	
6689	027456	044524	043516	042040	
6690	027464	044522	020126	020043	
6691	027472	047516	020124	051120	
6692	027500	047123	000124		
6693					
6694	027504	051104	053111	021440	EM75: .ASCIZ /DRV # DIDN'T INTRUPT AFTER HRDWRE POLL/
6695	027512	042040	042111	023516	
6696	027520	020124	047111	051124	
6697	027526	050125	020124	043101	
6698	027534	042524	020122	051110	
6699	027542	053504	042522	050040	
6700	027550	046117	000114		
6701					
6702	027554	041523	020120	044504	EM76: .ASCIZ /SCP DIDN'T SET AFTER SEEK WAS DONE/
6703	027562	047104	052047	051440	
6704	027570	052105	040440	052106	
6705	027576	051105	051440	042505	
6706	027604	020113	040527	020123	
6707	027612	047504	042516	000	
6708					
6709	027617	122	042113	020101	EM77: .ASCIZ /RKDA CHANGD AFTER DRV RESET/
6710	027624	044103	047101	042107	
6711	027632	040440	052106	051105	
6712	027640	042040	044522	020126	
6713	027646	042522	042523	000124	
6714					
6715	027654	040504	040524	042440	EM100: .ASCIZ /DATA EROR AT WORD#/
6716	027662	047522	020122	052101	
6717	027670	053440	051117	021504	
6718	027676	000			
6719					
6720	027677	103	052116	046122	EM101: .ASCIZ /CNTRL RDY DIDN'T SET AFTER RD CHK/
6721	027704	051040	054504	042040	
6722	027712	042111	023516	020124	
6723	027720	042523	020124	043101	
6724	027726	042524	020122	042122	
6725	027734	041440	045510	000	
6726					
6727	027741	105	051122	047440	EM102: .ASCIZ /ERR OR HE ON RD CHK/
6728	027746	020122	042510	047440	
6729	027754	020116	042122	041440	
6730	027762	045510	000		

6731					
6732	027765	103	042523	047440	EM103: .ASCIZ /CSE ON RD CHK/
6733	027772	020116	042122	041440	
6734	030000	045510	000		
6735					
6736	030003	122	053513	020103	EM104: .ASCIZ /RKWC DIDN'T OVERFLO ON RD CHK OR WRT CHK/
6737	030010	044504	047104	052047	
6738	030016	047440	042526	043122	
6739	030024	047514	047440	020116	
6740	030032	042122	041440	045510	
6741	030040	047440	020122	051127	
6742	030046	020124	044103	000113	
6743					
6744	030054	045522	040504	044440	EM105: .ASCIZ /RKDA INCRMNTD WRONG ON RD CHK/
6745	030062	041516	046522	052116	
6746	030070	020104	051127	047117	
6747	030076	020107	047117	051040	
6748	030104	020104	044103	000113	
6749					
6750	030112	045522	040502	041440	EM106: .ASCIZ /RKBA CHANGD AFTER RD CHK/
6751	030120	040510	043516	020104	
6752	030126	043101	042524	020122	
6753	030134	042122	041440	045510	
6754	030142	000			
6755					
6756	030143	115	046505	051117	EM107: .ASCIZ /MEMORY WORD CHANGED AFTER RD CHK/
6757	030150	020131	047527	042122	
6758	030156	041440	040510	043516	
6759	030164	042105	040440	052106	
6760	030172	051105	051040	020104	
6761	030200	044103	000113		
6762					
6763	030204	047103	051124	020114	EM110: .ASCIZ /CNTRL RDY DIDN'T SET AFTER WRT CHK/
6764	030212	042122	020131	044504	
6765	030220	047104	052047	051440	
6766	030226	052105	040440	052106	
6767	030234	051105	053440	052122	
6768	030242	041440	045510	000	
6769					
6770	030247	110	020105	051117	EM111: .ASCIZ /HE OR ERR ON WRT CHK/
6771	030254	042440	051122	047440	
6772	030262	020116	051127	020124	
6773	030270	044103	000113		
6774					
6775	030274	051127	052111	020105	EM112: .ASCIZ /WRITE CHECK EROR/
6776	030302	044103	041505	020113	
6777	030310	051105	051117	000	
6778					
6779	030315	122	042113	020101	EM113: .ASCIZ /RKDA INCRMNTD WRONG ON WRT CHK/
6780	030322	047111	051103	047115	
6781	030330	042124	053440	047522	
6782	030336	043516	047440	020116	
6783	030344	051127	020124	044103	
6784	030352	000113			
6785					
6786	030354	045522	040502	044440	EM114: .ASCIZ /RKBA INCRMNTD WRONG ON WRT CHK/

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SEQ 0141

6787	030362	041516	046522	052116	
6788	030370	020104	051127	047117	
6789	030376	020107	047117	053440	
6790	030404	052122	041440	045510	
6791	030412	000			
6792					
6793	030413	122	041113	020101	EM115: .ASCIZ /RKBA INCRMNTD, WITH IBA SET/
6794	030420	047111	051103	047115	
6795	030426	042124	020054	044527	
6796	030434	044124	044440	040502	
6797	030442	051440	052105	000	
6798					
6799	030447	127	047522	043516	EM116: .ASCIZ /WRONG MEMORY LOCATION CHANGED WITH IBA SET/
6800	030454	046440	046505	051117	
6801	030462	020131	047514	040503	
6802	030470	044524	047117	041440	
6803	030476	040510	043516	042105	
6804	030504	053440	052111	020110	
6805	030512	041111	020101	042523	
6806	030520	000124			
6807					
6808	030522	045522	030461	042040	EM117: .ASCIZ /RK11 DIDN'T INTRUPT WHEN IDE WAS SET/
6809	030530	042111	023516	020124	
6810	030536	047111	051124	050125	
6811	030544	020124	044127	047105	
6812	030552	044440	042504	053440	
6813	030560	051501	051440	052105	
6814	030566	000			
6815					
6816	030567	122	030513	020061	EM120: .ASCIZ /RK11 DIDN'T INTRUPT AFTER SK WAS INITIATED/
6817	030574	044504	047104	052047	
6818	030602	044440	052116	052522	
6819	030610	052120	040440	052106	
6820	030616	051105	051440	020113	
6821	030624	040527	020123	047111	
6822	030632	052111	040511	042524	
6823	030640	000104			
6824					
6825	030642	041523	020120	042523	EM121: .ASCIZ /SCP SET BEFORE SEEK COMPLETED/
6826	030650	020124	042502	047506	
6827	030656	042522	051440	042505	
6828	030664	020113	047503	050115	
6829	030672	042514	042524	000104	
6830					
6831	030700	045522	030461	042040	EM122: .ASCIZ /RK11 DIDN'T INTRUPT AFTER SK COMPLETED/
6832	030706	042111	023516	020124	
6833	030714	047111	051124	050125	
6834	030722	020124	043101	042524	
6835	030730	020122	045523	041440	
6836	030736	046517	046120	052105	
6837	030744	042105	000		
6838					
6839	030747	103	052116	046122	EM123: .ASCIZ /CNTRL RESET DIDN'T CLEAR 'SCP'/
6840	030754	051040	051505	052105	
6841	030762	042040	042111	023516	
6842	030770	020124	046103	040505	

6843	030776	020122	051447	050103	
6844	031004	000047			
6845					
6846	031006	045522	030461	042040	EM124: .ASCIZ /RK11 DIDN'T INTRUPT AFTER RD DONE/
6847	031014	042111	023516	020124	
6848	031022	047111	051124	050125	
6849	031030	020124	043101	042524	
6850	031036	020122	042122	042040	
6851	031044	047117	000105		
6852					
6853	031050	047103	051124	020114	EM125: .ASCIZ /CNTRL RESET DIDN'T CLR REGISTR/
6854	031056	042522	042523	020124	
6855	031064	044504	047104	052047	
6856	031072	041440	051114	051040	
6857	031100	043505	051511	051124	
6858	031106	000			
6859					
6860	031107	122	030513	020061	EM126: .ASCIZ /RK11 DIDN'T INTRUPT AT CPU LEVEL/
6861	031114	044504	047104	052047	
6862	031122	044440	052116	052522	
6863	031130	052120	040440	020124	
6864	031136	050103	020125	042514	
6865	031144	042526	000114		
6866					
6867	031150	045522	030461	044440	EM127: .ASCIZ /RK11 INTRUPTED AT WRONG CPU LEVEL/
6868	031156	052116	052522	052120	
6869	031164	042105	040440	020124	
6870	031172	051127	047117	020107	
6871	031200	050103	020125	042514	
6872	031206	042526	000114		
6873					
6874	031212	042447	051122	041040	EM130: .ASCIZ /'ERR BIT' DIDN'T SET IN RKER/
6875	031220	052111	020047	044504	
6876	031226	047104	052047	051440	
6877	031234	052105	044440	020116	
6878	031242	045522	051105	000	
6879					
6880	031247	110	020105	051117	EM131: .ASCIZ /HE OR ERR DIDN'T SET/
6881	031254	042440	051122	042040	
6882	031262	042111	023516	020124	
6883	031270	042523	000124		
6884					
6885	031274	045522	051105	042440	EM132: .ASCIZ /RKER EROR/
6886	031302	047522	000122		
6887					
6888	031306	054116	020103	044502	EM133: .ASCIZ /NXC BIT DIDN'T SET/
6889	031314	020124	044504	047104	
6890	031322	052047	051440	052105	
6891	031330	000			
6892					
6893	031331	122	030513	020061	EM134: .ASCIZ /RK11 DIDN'T INTRUPT ON SOFT EROR/
6894	031336	044504	047104	052047	
6895	031344	044440	052116	052522	
6896	031352	052120	047440	020116	
6897	031360	047523	052106	042440	
6898	031366	047522	000122		

6899						
6900	031372	042515	020130	044502	EM135:	.ASCIZ /MEX BITS INCRMNTD WRONG-RKCS/
6901	031400	051524	044440	041516		
6902	031406	046522	052116	020104		
6903	031414	051127	047117	026507		
6904	031422	045522	051503	000		
6905						
6906	031427	127	051520	047040	EM137:	.ASCIZ /WPS NOT CLEAR/
6907	031434	052117	041440	042514		
6908	031442	051101	000			
6909						
6910	031445	104	052101	020101	EM140:	.ASCIZ /DATA EROR ON TRANSFER FROM DISK TO TTY/
6911	031452	051105	051117	047440		
6912	031460	020116	051124	047101		
6913	031466	043123	051105	043040		
6914	031474	047522	020115	044504		
6915	031502	045523	052040	020117		
6916	031510	052124	000131			
6917						
6918	031514	042047	044522	020126	EM141:	.ASCIZ /'DRIV #' PRESENT, BUT NOT INDICATED/
6919	031522	023443	050040	042522		
6920	031530	042523	052116	020054		
6921	031536	052502	020124	047516		
6922	031544	020124	047111	044504		
6923	031552	040503	042524	000104		
6924	031560	047040	020117	052502	EM142:	.ASCIZ / NO BUSY ON OTHER HALF OF RK-05F/
6925	031566	054523	047440	020116		
6926	031574	052117	042510	020122		
6927	031602	040510	043114	047440		
6928	031610	020106	045522	030055		
6929	031616	043065	000			
6930						
6931						
6932						
6933						
6934						
6935	031622					.EVEN
6936						.SBTTL ERROR DATA POINTERS
6937						
6938						
6939	031622	001116	001162	000000	DT1:	.WORD \$ERRPC,\$REG0,0
6940						
6941	031630	001116	001162	001164	DT2:	.WORD \$ERRPC,\$REG0,\$REG1,0
6942	031636	000000				
6943						
6944	031640	001116	001162	001164	DT20:	.WORD \$ERRPC,\$REG0,\$REG1,\$REG2,\$REG3,0
6945	031646	001166	001170	000000		
6946						
6947	031654	001116	000000		DT21:	.WORD \$ERRPC,0
6948						
6949	031660	001116	001162	001164	DT26:	.WORD \$ERRPC,\$REG0,\$REG1,\$REG2,0
6950	031666	001166	000000			
6951						
6952	031672	001116	001162	001164	DT54:	.WORD \$ERRPC,\$REG0,\$REG1,\$REG2,\$REG3,\$REG4,\$REG5,\$REG6,\$REG7,0
6953	031700	001166	001170	001172		
6954	031706	001174	001176	001200		

7123
7124
7125 033240 000400
7126
7127
7128
7129 000001

;DATA BUFFER

OUTBUF: .BLKW 256.

: THIS 256 WORD BUFFER IS FOR
: DATA TRANSFERS FROM AND
: TO THE DISK.

.END

BADINT	004526	CR	= 000015	DT20	031640	EM40	025605	PFSTRT	004630
BADTMO	004462	CRETRN	021336	DT21	031654	EM41	025645	PHYDRV	001436
BDAR	021002	CRLF	= 000200	DT26	031660	EM43	025702	PIRG	= 177772
BDAO	020764	DDISP	= 177570	DT54	031672	EM44	025734	PIRQVE	= 000240
BDA4	020774	DDPCH	001410	EFLG1	001370	EM45	026010	PRO	= 000000
BIT0	= 000001	DELAY	= 104417	EMTVEC	= 000030	EM46	026056	PR1	= 000040
BIT00	= 000001	DELA.Y	021572	EM100	027654	EM47	026114	PR2	= 000100
BIT01	= 000002	DH100	032651	EM101	027677	EM50	026156	PR3	= 000140
BIT02	= 000004	DH103	032706	EM102	027741	EM51	026205	PR4	= 000200
BIT03	= 000010	DH104	032722	EM103	027765	EM52	026260	PR5	= 000240
BIT04	= 000020	DH107	032746	EM104	030003	EM53	026305	PR6	= 000300
BIT05	= 000040	DH117	033004	EM105	030054	EM54	026350	PR7	= 000340
BIT06	= 000100	DH126	033020	EM106	030112	EM55	026442	PS	= 177776
BIT07	= 000200	DH130	033046	EM107	030143	EM56	026521	PSW	= 177776
BIT08	= 000400	DH131	033105	EM11	025266	EM57	026560	PWRVEC	= 000024
BIT09	= 001000	DH133	033133	EM110	030204	EM60	026616	RDCHR	= 104410
BIT1	= 000002	DH14	032011	EM111	030247	EM61	026655	RDLIN	= 104411
BIT10	= 002000	DH140	033171	EM112	030274	EM62	026712	RESVEC	= 000010
BIT11	= 004000	DH2	031716	EM113	030315	EM63	026724	RKBP	001336
BIT12	= 010000	DH21	032046	EM114	030354	EM64	026770	RKCS	001332
BIT13	= 020000	DH30	032053	EM115	030413	EM65	027033	RKDA	001340
BIT14	= 040000	DH34	032111	EM116	030447	EM66	027077	RKDB	001342
BIT15	= 100000	DH35	032127	EM117	030522	EM67	027136	RKDS	001326
BIT2	= 000004	DH36	032147	EM12	025300	EM70	027151	RKER	001330
BIT3	= 000010	DH4	031745	EM120	030567	EM71	027242	RKPRI	001400
BIT4	= 000020	DH40	032177	EM121	030642	EM72	027320	RKVEC	001402
BIT5	= 000040	DH44	032225	EM122	030700	EM73	027362	RKWC	001334
BIT6	= 000100	DH5	031773	EM123	030747	EM74	027434	R6	=%000006
BIT7	= 000200	DH54	032272	EM124	031006	EM75	027504	R7	=%000007
BIT8	= 000400	DH56	032401	EM125	031050	EM76	027554	SEEK0	001372
BIT9	= 001000	DH64	032506	EM126	031107	EM77	027617	SEEK1	001374
BPTVEC	= 000014	DH66	032532	EM127	031150	ERRVEC	= 000004	SEEK2	001376
BRKDAO	= 104415	DH67	032570	EM13	025313	FCHECK	025056	SHFTRT	021106
BRKDA4	= 104416	DH74	032630	EM130	031212	FFLAG	001404	SIMUL	001344
BTEOP	017734	DISPLA	001142	EM131	031247	FTITLE	001346	SIZEF	025202
CHE1	021150	DISPRE	000174	EM132	031274	GTSWR	= 104406	SIZYET	001440
CHKCCL	021310	DRESET	021412	EM133	031306	GT2RG	020716	STACK	= 001100
CHKCRD	= 104412	DRHOLD	025200	EM134	031331	GT3RG	020710	START	002636
CHKDA	021170	DRIVAD	001350	EM135	031372	GT4RG	020702	START1	003262
CHKDA1	021176	DRIVS	001412	EM137	031427	HT	= 000011	STKLMT	= 177774
CHKECL	021264	DRIVO	001414	EM140	031445	INDX1	001356	ST2	003664
CHKER	021250	DRIV1	001416	EM141	031514	INDX2	001360	ST3	004210
CHKHE	021142	DRIV2	001420	EM142	031560	IOTVEC	= 000020	ST4	004404
CHKHE1	021134	DRIV3	001422	EM16	025325	LF	= 000012	SWR	001140
CHKWC	021224	DRIV4	001424	EM21	025354	MSG1	001216	SWREG	000176
CH.CRD	021666	DRIV5	001426	EM30	025366	MSG2	001236	SWO	= 000001
CKSWR	= 104407	DRIV6	001430	EM31	025376	MSG3	001245	SWO0	= 000001
CNT.RD	= 104414	DRIV7	001432	EM32	025406	MSG4	001272	SWO1	= 000002
CNT.RE	= 104413	DRVON	001352	EM33	025427	MSG5	001303	SWO2	= 000004
CN.RDY	021740	DRVPTR	001354	EM34	025447	MSG6	001315	SWO3	= 000010
CN.RST	021722	DSWR	= 177570	EM35	025466	NUDRV	004766	SWO4	= 000020
COUNT	001362	DT1	031622	EM36	025521	ODDEVN	001406	SWO5	= 000040
COUNT1	001364	DT2	031630	EM37	025551	OUTBUF	033240	SWO6	= 000100

SW07 = 000200	TST21 007610	TYPOC = 104402	\$GDADR 001120	\$REG6 001176
SW08 = 000400	TST22 010062	TYPON = 104404	\$GDDAT 001124	\$REG7 001200
SW09 = 001000	TST23 010432	TYPOS = 104403	\$GET42 020636	\$RTNAD 020660
SW1 = 000002	TST24 010712	T56 020046	\$GTSWR 023730	\$SAVR6 025044
SW10 = 002000	TST25 011322	T56FLG 001434	\$HD = 000000	\$SCOPE 022046
SW11 = 004000	TST26 011630	WATIME 021650	\$ICNT 001104	\$SETUP= 000117
SW12 = 010000	TST27 012150	WATINT 021614	\$ILLUP 025040	\$STUP = 177777
SW13 = 020000	TST28 012516	WAT.IN= 104420	\$INTAG 001135	\$SVLAD 022254
SW14 = 040000	TST29 012436	\$AUTOB 001134	\$ITEMB 001114	\$SVPC = 000204
SW15 = 100000	TST30 012672	\$BDADR 001122	\$LF 001214	\$SWR = 165400
SW2 = 000004	TST31 013076	\$BDDAT 001126	\$LPADR 001106	\$SWRMK= 000000
SW3 = 000010	TST32 013344	\$SCHARC 023202	\$LPERR 001110	\$TIMES 001206
SW4 = 000020	TST33 013626	\$CKSWR 023660	\$MNEW 024563	\$TKB 001146
SW5 = 000040	TST34 014104	\$CMTAG 001100	\$MSWR 024552	\$TKS 001144
SW6 = 000100	TST35 014236	\$CM1 = 000012	\$MXCNT 022316	\$TN = 000060
SW7 = 000200	TST36 014562	\$CM2 = 000024	\$NULL 001154	\$TPB 001152
SW8 = 000400	TST37 014562	\$CM3 = 000012	\$NWTST= 000001	\$TPFLG 001157
SW9 = 001000	TST4 005276	\$CNTLG 024545	\$OCNT 023654	\$TPS 001150
TBITVE= 000014	TST40 014776	\$CNTLU 024540	\$OMODE 023656	\$TRAP 024574
TIMER 001366	TST41 015202	\$CRLF 001213	\$OVER 022302	\$TRAP2 024616
TKVEC = 000060	TST42 015364	\$DBLK 023422	\$PASS 001100	\$TRP = 000022
TPVEC = 000064	TST43 015472	\$DOAGN 020656	\$POWER 025046	\$TRPAD 024630
TRAPVE= 000034	TST44 015646	\$DTBL 023412	\$PWAD 025034	\$STNM 001102
TRTVEC= 000014	TST45 016004	\$ENDAD 020646	\$PWADN 024674	\$TTYIN 024516
TSTEND 020534	TST46 016212	\$ENDCT 020614	\$PWARMG 025030	\$TYPDS 023206
TSTRWS 021344	TST47 016364	\$ENDMG 020665	\$PWUP 024746	\$TYPE 022766
TSTSIN 021500	TST5 005364	\$ENULL 020662	\$QUES 001212	\$TYPEC 023136
TST.SI= 104421	TST50 016542	\$EOP 020560	\$RDCHR 024142	\$TYPEX 023204
TST1 004634	TST51 016670	\$EOPCT 020606	\$ROLIN 024262	\$TYPC 023456
TST10 005670	TST52 017042	\$ERFLG 001103	\$RDSZ = 000022	\$TYPON 023472
TST11 005756	TST53 017236	\$ERMAX 001115	\$REGAD 001160	\$TYPOS 023432
TST12 006032	TST54 017530	\$ERROR 022320	\$REGO 001162	\$XTSTR 022060
TST13 006160	TST55 017666	\$ERRPC 001116	\$REG1 001164	\$SET4= 000000
TST14 006324	TST56 017712	\$ERRTB 001442	\$REG10 001202	\$OFILL 023655
TST15 006416	TST57 017762	\$ERRTY 022632	\$REG11 001204	. = 034240
TST16 006654	TST6 005412	\$ERTTL 001112	\$REG2 001166	
TST17 007102	TST7 005450	\$ESCAP 001210	\$REG3 001170	
TST2 004766	TYERM 020734	\$FILLC 001156	\$REG4 001172	
TST20 007412	TYPDS = 104405	\$FILLS 001155	\$REG5 001174	
	TYPE = 104401			

. ABS. 034240 000

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

DZRKKD, DZRKKD/LI:ME/NL:MC:MD:CND/SOL/NSQ+DZRKKD.P11
RUN-TIME: 65 62 1 SECONDS
RUN-TIME RATIO: 505/130=3.8
CORE USED: 24K (47 PAGES)

