

DH11

DH11 MODEM
MD-11-DZDHK-B

EP-DZDHK-B-DL-A

FEB 1976

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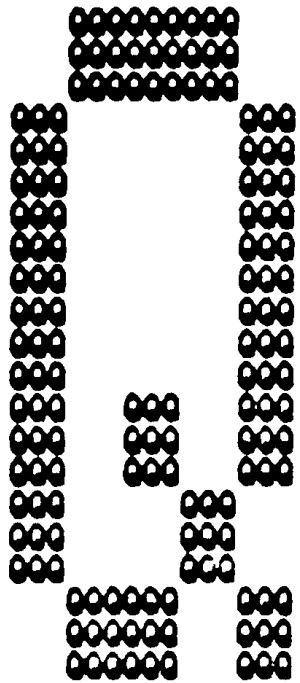
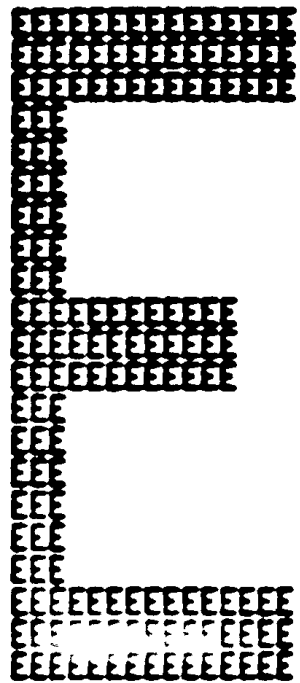
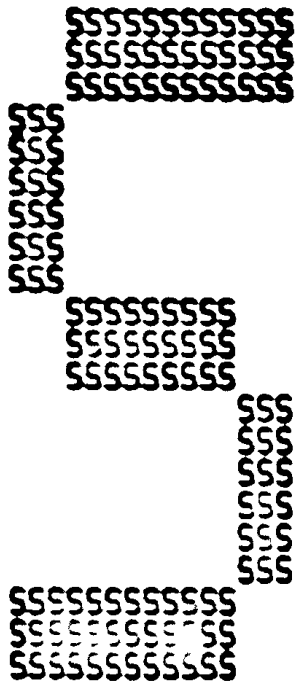
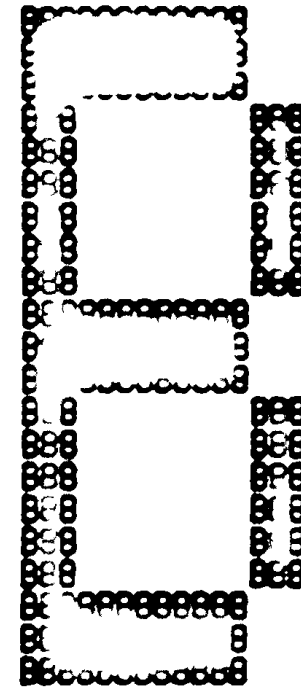
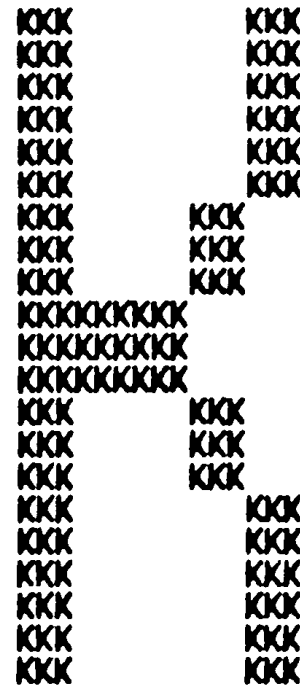
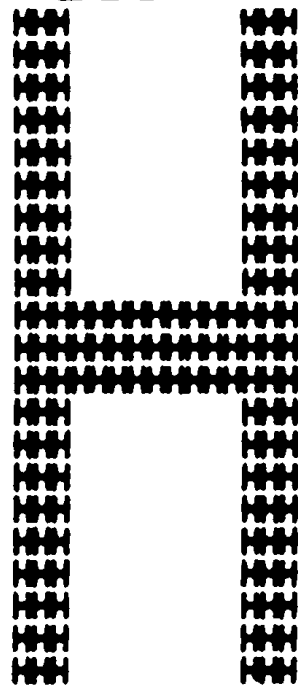
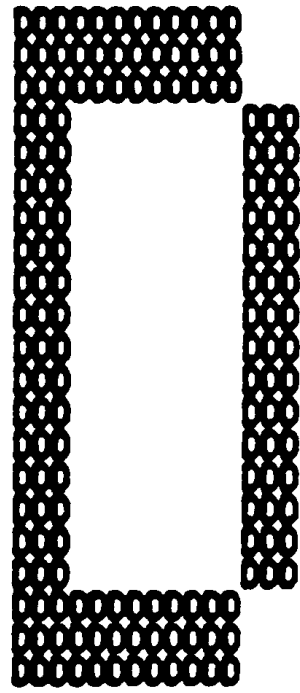
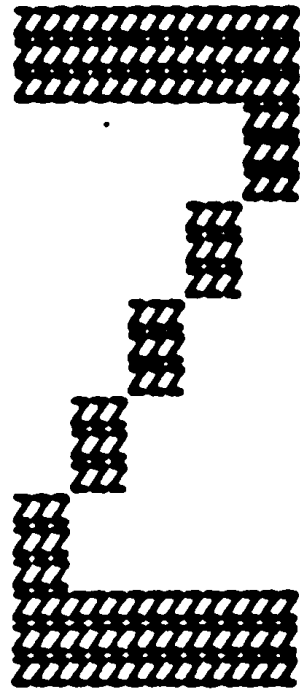
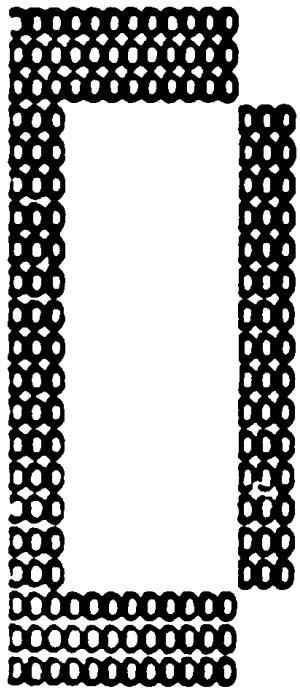
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Made In U.S.A.

DZDHKB
SEQ

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91	92	93	94	95	96	97	98	99	100

B01



LPTSPL Version 101(2107) Running on MTA140
START User WELINGHAM A11 21 Job DZDMKB Seq. 3586 Date 17-Jan-76 16:50:02 Monitor RV225D KI10 SYS#514 *START*
Request created: 17-Jan-76 16:30:54
File: WMTG:DZDMKB.SEQ(055)(404,3722) Created: 09-Jan-76 16:08:56 Printed: 17-Jan-76 16:50:04
QUEUE Switches: /FILE:ASCII /COPIES:1 /SPACING:1 /LIMIT:214 /FORMS:NORMAL

IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DZDNK-B-D
PRODUCT NAME: MODEM CONTROL
MULTIPLEXER DIAGNOSTIC
DATE : 21 FEBRUARY 1976
MAINTAINER: DIAGNOSTIC GROUP
AUTHOR: G. BAISLEY
E. CROWLEY (MODIFIED PROGRAM TO ALLOW
LINE SELECTION FOR TEST GROUP D)

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1.0 ABSTRACT

THIS PROGRAM IS A TEST OF THE MODEM CONTROL MULTIPLEXER USED WITH THE DH11-AD OPTION
THE PROGRAM IS DIVIDED INTO FUNCTIONAL TEST GROUPS AS FOLLOWS:

- GROUP 0: ALL LINE SCANNER AND LINE MULTIPLEXER FUNCTIONS ARE TESTED USING THE H861 TEST CONNECTOR
- GROUP 1: A SINGLE LINE IS TESTED USING THE MODEM CABLE AND A H315 TEST CONNECTOR
- GROUP 2: CONNECT-DISCONNECT TEST FOR 103A MODEMS
- GROUP 3: CONNECT-DISCONNECT TEST FOR 202C MODEMS

2.0 REQUIREMENTS

2.1 EQUIPMENT

PDP-11 COMPUTER WITH AT LEAST 4K OF MEMORY
ASR-33 TELETYPE OR EQUIVALENT
MODEM CONTROL MODULES M7807 & M7808

2.1.1 FOR 16 LINE SCANNER TEST

4 CABLES TO CONNECT TO TEST CONNECTOR
H861 TEST CONNECTOR

2.1.2 FOR SINGLE LINE CABLE TEST

4 CABLES TO CONNECT TO THE DISTRIBUTION PANEL
H315 TEST CONNECTOR

2.1.3 FOR ON LINE TESTS

4 CABLES TO CONNECT TO THE DISTRIBUTION PANEL
2 BELL 103A MODEMS (FOR 103A TEST)
2 BELL 202C MODEMS (FOR 202C TEST)

2.2 MEMORY

THE PROGRAM UTILIZES LOCATIONS 0000-17500

3.0 LOADING PROCEDURE

THE STANDARD PROCEDURE FOR LOADING BINARY TAPES IS TO BE USED.

4.0 STARTING PROCEDURE

4.1 STARTING ADDRESS

THE STARTING ADDRESS FOR ALL TESTS IS 000200.

RESTART ADDRESS FOR ALL TESTS IS 000200

4.2 OPERATOR AND/OR PROGRAM ACTION

4.2.1 INITIAL PROGRAM START

NOTE

IF PROGRAM IS BEING RUN WITH THE "XOR" MODULE TESTER
LOCATION 1030(8) MUST BE MODIFIED TO CONTAIN A 240(8)
"NOP" TO ACTIVATE THAT CODE AFFECTING THE "XOR" TESTER.

4.2.1.1 LOAD ADDRESS 000200
SET SW00 = 1
PRESS START

4.2.1.2 PROGRAM WILL TYPE
"DM11-MODEM CONTROL DIAGNOSTIC "(ONCE ONLY)

4.2.1.3 PROGRAM WILL TYPE (WITH SW00 = 1)
VECTOR ADDRESS-" AND WILL WAIT FOR AN INPUT
FROM THE TELETYPE KEYBOARD.

4.2.1.4 TYPE A THREE DIGIT NUMBER (OCTAL) WHICH IS THE
ADDRESS THAT THE MODEM CONTROL WILL INTERRUPT TO, FOLLOWED BY
<RETURN>. IF AN INCORRECT ADDRESS IS TYPED, THE PROGRAM WILL
TYPE "*" AND THEN REPEAT 4.2.1.3.

NOTE: IF THE ADDRESS ENTERED IS ACCEPTIBLE TO THE PROGRAM,
BUT IS NOT THE INTERRUPT VECTOR ADDRESS OF THE MODEM CONTROL
UNDER TEST, A HALT WILL OCCUR AT THAT ADDRESS+2, WHEN
THE MODEM CONTROL INTERRUPTS.

TO RECOVER, PERFORM 4.2.2.1.

4.2.1.5 THE PROGRAM WILL TYPE "CONTROL REGISTER ADDRESS-" AND WAIT FOR
AN INPUT FROM THE TELETYPE KEYBOARD.

4.2.1.6 TYPE A 6 DIGIT (OCTAL NUMBER) WHICH IS THE ADDRESS OF THE
MODEM CONTROL'S CONTROL REGISTER FOLLOWED BY <RETURN>.
IF AN INCORRECT ADDRESS IS TYPED, THE PROGRAM WILL
TYPE "*" AND THEN REPEAT 4.2.1.6.

NOTE: IF THE ADDRESS ENTERED IS ACCEPTIBLE TO THE PROGRAM
BUT IS A NON-EXISTANT REGISTER, A BUS ERROR TRAP WILL
OCCUR WHEN THE PROGRAM ADDRESSES THE REGISTER, AND THE
PROGRAM WILL HALT AT LOCATION 6.

TO RECOVER, PERFORM 4.2.2.1.

4.2.1.7 THE PROGRAM WILL TYPE "LINE SELECTION PARAMETER-" AND WAIT FOR
INPUT FROM THE TTY KEYBOARD.

4.2.1.8 TYPE AN OCTAL NUMBER TO SPECIFY THE LINES TO BE TESTED USING
THE FOLLOWING ENCODING SCHEME:

BIT00 = 1 TEST LINE 00

F01

SEQ 0004

BIT01 = 1 TEST LINE 01
BIT02 = 0 DO NOT TEST LINE 2

"
BIT15 = 1 TEST LINE 15

EG: TYPING 377(8) SELECTS LINES 00 THRU 07
 TYPING 177777(8) SELECTS ALL 16 LINES

IF THE NO. TYPED IS NOT ACCEPTABLE, THE PROGRAM TYPES A "?"
AND ASKS FOR THE LINE SELECT PARAMETER AGAIN.

4.2.1.9 THE PROGRAM WILL TYPE
"TEST-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD.

4.2.1.10 TYPE A THREE DIGIT OCTAL NUMBER CORRESPONDING TO THE
NUMBER OF THE TEST TO BE RUN FOLLOWED BY <RETURN>.
IF AN INCORRECT TEST NUMBER IS TYPED THE PROGRAM WILL
TYPE "-" AND THEN REPEAT 4.2.1.7
THE AVAILABLE TESTS TOGETHER WITH THE NUMBER TO BE TYPED
ARE GIVEN BELOW.

TEST GROUP 0:
OFF LINE TESTS USING HB61 TEST CONNECTOR-FIRST TEST=0
TEST GROUP 1:
OFF LINE TESTS USING DC11 TEST CONNECTOR AND MODEM CABLE-FIRST TEST=100
TEST GROUP 2:
CONNECT/DISCONNECT TEST FOR BELL 103A MODEMS-FIRST TEST=200
TEST GROUP 3:
CONNECT/DISCONNECT TEST FOR BELL 202C MODEMS-FIRST TEST=300

4.2.1.9 THE PROGRAM WILL ENTER THE SELECTED TEST GROUP.

4.2.2 PROGRAM RESTART

4.2.2.1 WITH SW00=1

LOAD ADDRESS 200
SET SW00=1 BEFORE PRESSING START.
PRESS START

PROGRAM WILL PERFORM AS DESCRIBED IN 4.2.1.3 TO 4.2.1.10.

4.2.2.2 WITH SW00=0

LOAD ADDRESS 200
PRESS START

PROGRAM WILL PERFORM AS DESCRIBED IN 4.2.1.7 TO 4.2.1.10

5.0 OPERATING PROCEDURE

5.1 TEST GROUP 0 16 LINE SCANNER TEST

5.1.1 TEST INITIALIZATION

NONE REQUIRED, PROGRAM TYPES "16 LINE SCANNER TEST"
AND BEGINS TEST EXECUTION.

5.1.2 OPERATIONAL SWITCH SETTINGS

SW15=1, HALT ON ERROR
SW14=1, LOOP ON CURRENT TEST
SW13=1, SUPPRESS ERROR TYPEOUT
SW11=1, SUPPRESS ITERATIONS
SW10=1, ESCAPE TO NEXT TEST ON ERROR
SW09=1, FREEZE DATA

5.1.3 PROGRAM AND/OR OPERATOR ACTION

5.1.3.1 WITH ALL SWITCHES DOWN, THE PROGRAM WILL RUN
ALL TESTS IN THE SELECTED GROUP, SEQUENTIALLY. EACH TEST IS REPEATED
A FIXED NUMBER OF TIMES (SEE LISTING FOR DETAILS),
EXCEPT FOR TO WHICH IS EXECUTED ONCE ONLY AFTER START OF TEST. WHEN ALL
TESTS HAVE BEEN COMPLETED, THE PROGRAM WILL ISSUE A "RESET", RING THE TELETYPE
BELL, AND RESTART AT THE FIRST TEST OF THE SELECTED GROUP.

IF AN ERROR OCCURS, THE PROGRAM WILL TYPE AN APPROPRIATE
ERROR MESSAGE AND CONTINUE TESTING.

5.1.3.2 WITH SW15=1, PROGRAM ACTION WILL BE AS IN 5.1.3.1 EXCEPT THAT
A HALT WILL OCCUR AFTER ERROR TYPEOUT.

5.1.3.3 WITH SW13=1, PROGRAM ACTION WILL BE AS IN 5.1.3.1 EXCEPT THAT
NO ERROR TYPEOUT WILL OCCUR. THE PC OF THE TEST THAT FAILED
WILL BE DISPLAYED IN THE COMPUTER DATA LIGHTS.

5.1.3.4 THIS PROGRAM WILL NO LONGER TRACE TRAP WITH THIS RELEASE

5.1.3.5 WITH SW10=1, PROGRAM ACTION WILL BE AS IN 5.1.3.1 EXCEPT THAT
AFTER AN ERROR HAS OCCURED, THE PROGRAM WILL IMMEDIATELY
START THE NEXT TEST IN SEQUENCE.

5.2 TEST GROUP 1 SINGLE LINE CABLE TEST

5.2.1 TEST INITIALIZATION

THE PROGRAM WILL TYPE "SINGLE LINE CABLE TEST
LINE NUM. R-" AND WILL WAIT FOR AN INPUT FROM
THE TELETYPE KEYBOARD.

TYPE A 2 DIGIT OCTAL NUMBER BETWEEN 0 AND 17, CORRESPONDING
TO THE NUMBER OF THE LINE TO BE TESTED, FOLLOWED BY
<RETURN>. THE PROGRAM WILL THEN BEGIN TEST EXECUTION.
IF THE TELETYPE INPUT IS INCORRECT, THE PROGRAM
WILL TYPE "P" AND REPEAT THE MESSAGE.

5.2.2 OPERATIONAL SWITCH SETTINGS

SAME AS 5.1.2

5.2.3 PROGRAM AND/OR OPERATOR ACTION

SAME AS 5.1.3

5.3 TEST GROUP 2 BELL 103A MODEM CONNECT-DISCONNECT TEST

5.3.1 TEST INITIALIZATION

THE PROGRAM WILL TYPE "103A CONNECT-DISCONNECT TEST
ORIGINATE LINE-" AND WAIT FOR AN INPUT FROM THE TELETYPE
KEYBOARD.

TYPE THE NUMBER OF THE LINE THAT WILL ORIGINATE THE
CALL (0-17 OCTAL) FOLLOWED BY RETURN.

THE PROGRAM WILL TYPE "ANSWER LINE-" AND WILL WAIT
FOR AN INPUT FROM THE TELETYPE KEYBOARD.

TYPE THE NUMBER OF THE LINE THAT WILL ANSWER THE CALL
(0-17 OCTAL) FOLLOWED BY <RETURN>.

THE PROGRAM WILL TYPE "DIAL ANSWERING DATA SET"
AND WILL WAIT FOR THE ORIGINATE AND ANSWERING MODEMS
TO GENERATE INTERRUPTS.

5.3.2 OPERATOR ACTION TO MAKE TELEPHONE CONNECTION

AFTER THE MESSAGE "DIAL ANSWERING DATA SET" IS TYPED
THE OPERATOR HAS APPROXIMATELY 5 MINUTES TO ESTABLISH
A CONNECTION BETWEEN THE 2 DATA SETS.

5.3.2.1 PLACE ANSWERING DATA SET IN "AUTO ANSWER" MODE

- 5.3.2.2 PLACE ORIGINATING DATA SET IN "TALK" MODE
- 5.3.2.3 DIAL DIAL ANSWERING DATA SET FROM ORIGINATING DATA SET
- 5.3.2.4 LISTEN FOR TONE IN HANDSET OF ORIGINATING DATA SET.
WHEN TONE IS HEARD, PRESS "DATA" BUTTON ON ORIGINATING DATA SET.
"DATA" LIGHT SHOULD ILLUMINATE
- 5.3.2.5 "DATA" LIGHT ON ANSWERING DATA SET SHOULD BE LIT.
- 5.3.2.6 THE PROGRAM WILL NOW WAIT FOR INTERRUPTS FROM THE MODEM CONTROL.
- 5.3.2.7 IF THE CONNECTION HAS BEEN PROPERLY ESTABLISHED, THE PROGRAM WILL TYPE "SET SW01=1 TO DISCONNECT".
WHEN SW01 IS SET TO 1, THE PROGRAM WILL BEGIN THE DISCONNECT SEQUENCE.
- 5.3.2.8 WHEN THE DISCONNECT SEQUENCE HAS BEEN COMPLETED THE PROGRAM WILL TYPE "103A TEST COMPLETE", AND WILL REQUEST THE OPERATOR TO SELECT NEW LINES.
- 5.3.3 PROGRAM ACTION IN CASE OF ERROR
- 5.3.3.1 RING ON INCORRECT LINE
IF THE PROGRAM DETECTS A RING SIGNAL ON AN INCORRECT LINE, OR IF ANY OTHER TRANSITION BESIDES RING IS DETECTED BEFORE RING, THE PROGRAM WILL TYPE A FATAL ERROR MESSAGE AND REQUEST THE OPERATOR TO RESELECT LINES AND REDIAL.
- 5.3.3.2 OTHER ERRORS
IF ANY ERRORS OCCUR AFTER THE FIRST RING HAS BEEN DETECTED, THE PROGRAM WILL TYPE AN APPROPRIATE ERROR MESSAGE AND CONTINUE TESTING TO COMPLETION.
THE ONLY EXCEPTION TO THIS IS IF AN INTERRUPT OCCURS ON A LINE NOT SELECTED, IN WHICH CASE A FATAL ERROR WILL BE REPORTED, AND THE PROGRAM WILL PROCEED AS DESCRIBED IN 5.3.3.1

5.3.4 OPERATION SWITCH SETTINGS

SW15=1, HALT ON ERROR
SW13=1, SUPPRESS ERROR TYPEOUT
SW01=1, START DISCONNECT SEQUENCE

5.3.5 DATA SET MODE SWITCHING

AFTER THE PROGRAM HAS TYPED THE MESSAGE DESCRIBED IN 5.3.2.7, BUT BEFORE SW01 IS SET, THE OPERATOR MAY SWITCH EITHER DATA SET FROM THE MODE THAT IT IS IN TO ANOTHER MODE. ALL TRANSITIONS DETECTED AT THIS TIME WILL BE REPORTED.

NOTE: THE ORIGINATE DATA SET MUST BE RETURNED TO "TALK" MODE AND THE ANSWERING DATA SET TO "AUTO ANSWER" BEFORE DISCONNECT IS STARTED TO PREVENT ERRORS FROM BEING DETECTED THAT ARE CAUSED BY THE FACT THAT THE MODEM IS IN THE INCORRECT STATE.

5.4 TEST GROUP 3 BELL 202C MODEM CONNECT-DISCONNECT TEST

5.4.1 TEST INITIALIZATION

SAME AS 5.3.1 EXCEPT PROGRAM WILL TYPE "202C CONNECT DISCONNECT TEST".

5.4.2 OPERATOR ACTION TO MAKE TELEPHONE CONNECTION

SAME AS 5.3.2 EXCEPT AT END OF TEST, PROGRAM WILL TYPE "202C TEST COMPLETE".

5.4.3 PROGRAM ACTION IN CASE OF ERRORS

SAME AS 5.3.3

5.4.4 OPERATIONAL SWITCH SETTINGS

SAME AS 5.3.4

5.4.5 DATA SET MODE SWITCHING

SAME AS 5.3.5

5.5 TEST RESELECTION

TO ESCAPE FROM THE TEST IN PROGRESS, AND SELECT A NEW TEST, TYPE (CONTROL C).

THE PROGRAM WILL STOP EXECUTION OF THE TEST IN PROGRESS AND THEN TYPE "TEST-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD.

PROCEED AS DESCRIBED IN 4.2.1.8

5.5 ADDRESS CHANGE

TO CHANGE THE VECTOR AND REGISTER ADDRESS OF THE MODEM CONTROL UNDER TEST, TYPE (CONTROL V). THE PROGRAM WILL STOP EXECUTION OF THE TEST IN PROGRESS AND PROCEED AS DESCRIBED IN SECTION 4.2.1, EXCEPT THAT "MODEM CONTROL DIAGNOSTIC" WILL NOT BE TYPED.

5.6 LINE NUMBER CHANGE

TO CHANGE THE LINE NUMBER(S) UNDER TEST, TYPE (CONTROL L). THE PROGRAM WILL SUSPEND THE TEST IN PROGRESS AND RETURN TO THE INITIALIZATION STAGE OF THE SELECTED TEST.

WHEN THE LINE NUMBER(S) HAS BEEN CHANGED, THE PROGRAM WILL RESTART THE SELECTED TEST USING THE NEW LINE NUMBER(S).

5.7 POWER FAILURE

IF A POWER FAIL TRAP OCCURS DURING TEST EXECUTION THE PROGRAM WILL SAVE THE GENERAL REGISTERS OF THE PROCESSOR AND HALT.

WHEN POWER UP OCCURS, THE PROGRAM WILL TYPE "POWER FAILURE-CURRENT TEST WILL BE RESTARTED".

THE PROGRAM WILL THEN RESUME TEST EXECUTION.

NOTE: IF A TEST IS NOT IN PROGRESS, I.E., IF THE PROGRAM IS WAITING FOR AN INPUT FROM THE TELETYPE KEYBOARD, THE ERROR MESSAGE WILL BE "POWER FAILUP". THE PROGRAM WILL THEN REQUEST THE OPERATOR TO SELECT A TEST.

6.0 ERRORS

6.1 NORMAL OPERATION

IF AN ERROR OCCURS WITH ALL SWITCHES DOWN, THE PROGRAM WILL TYPE AN APPROPRIATE ERROR MESSAGE AND THEN RESUME TESTING.

THERE ARE SEVERAL ERROR MESSAGE FORMATS, AND THE PARTICULAR MESSAGE TYPED DEPENDS UPON THE TEST IN PROGRESS.

6.1.1 ERROR MESSAGES

6.1.1.1 UNIQUE ERROR

ONLY PC OF FAILING TEST IS OUTPUT TO TELEPRINTER

AN EXAMPLE OF THIS TYPE OF ERROR IS:

1. AN INTERRUPT OCCURED AT THE WRONG PRIORITY
2. A REGISTER BIT WAS NOT CLEARED BY RESET

6.1.1.2 TRANSITION DETECTION ERROR

THIS ERROR WILL OCCUR IN ONE OF THE ON-LINE TESTS IF AN EXPECTED INTERRUPT DOES NOT OCCUR, OR IF AN UNEXPECTED INTERRUPT DOES OCCUR, ON THE LINES UNDER TEST.

FORMAT FOR ERROR TYPEOUT IS

```
XXXXXX TRANSITION ERROR
EXP  REC  LINE
AA   BB   CC
```

WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE
 AA=EXPECTED INTERRUPT FLAGS (CORRESPONDS TO 4 MSB OF CONTROL REGISTER)
 BB=RECEIVED INTERRUPT FLAGS (AS ABOVE)
 CC=LINE ON WHICH ERROR OCCURED

6.1.1.3 SINGLE LINE STATUS ERROR

THIS ERROR WILL OCCUR IN ANY TEST, OFF LINE OR ON-LINE WHEN THE EXPECTED AND RECEIVED LINE STATUS ARE NOT THE SAME.

FORMAT FOR SINGLE LINE STATUS ERROR IS

```
XXXX LINE ERROR
EXP  REC  LINE
AAA  BBB  CC
```

WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE
 AAA=EXPECTED LINE STATUS AT TIME OF ERROR
 BBB=RECEIVED LINE STATUS AT TIME OF ERROR
 CC=LINE ON WHICH ERROR OCCURED

6.1.1.4 FATAL TRANSITION ERROR

THIS ERROR WILL OCCUR IN AN ON-LINE TEST IF AN INTERRUPT OCCURS ON A LINE NOT SELECTED FOR TESTING.

FORMAT FOR FATAL ERROR TYPEOUT IS

```
XXXXXX FATAL ERROR
CSTAT LSTAT
AAAAA BBB
```

WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE
AAAAA=RECEIVED CONTROL STATUS ON LINE THAT INTERRUPTED
BBB=RECEIVED LINE STATUS ON LINE THAT INTERRUPTED

6.1.1.4 CONTROL STATUS ERROR

THIS ERROR WILL OCCUR IN A TEST THAT PRIMARILY INVOLVES THE LINE SCANNER

FORMAT FOR CONTROL STATUS ERROR IS

```
XXXXXX STATUS ERROR
EXP REC
AAAAA BBBB
```

WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE
AAAAA=EXPECTED CONTROL STATUS AT TIME OF ERROR
BBBB=RECEIVED(ACTUAL) CONTROL STATUS AT TIME OF ERROR

6.1.1.5 LINE STATUS ERROR

THIS ERROR WILL OCCUR IN THOSE OFF LINE TESTS THAT SET ONE LINE TO A PARTICULAR STATE, AND THEN CHECK ALL OTHER LINES

FORMAT FOR LINE STATUS ERROR IS

```
XXXX LINE ERROR
EXP REC LINE SEL
AAA DDD CC DD
```

WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE
AAA=EXPECTED LINE STATUS AT TIME OF ERROR
DDD=RECEIVED LINE STATUS AT TIME OF ERROR
CC=LINE ON WHICH ERROR OCCURED
DD=THE LINE ON WHICH THE PROGRAM WAS OPERATING

6.1.2 REPEATED ERRORS

IF THE SAME ERROR OCCURS REPEATEDLY IN A GIVEN TEST ONLY THE DATA RELATING TO THAT ERROR WILL BE TYPED
IF THE ERROR OCCURS IN THE SAME TEST ON THE SAME PASS

6.2 SCOPE LOOPS

NOTE: SCOPE LOOPING APPLIES ONLY TO TEST GROUPS 0 AND 1

6.2.1 AFTER ERROR HALT

TO LOOP ON A GIVEN TEST AFTER AN ERROR HALT,
SET SW15=0 TO RUN WITHOUT STOPPING
SET SW14=1 TO LOOP ON CURRENT TEST
SET SW13=1 TO SUPPRESS ERROR TYPEOUT
SET SW10=0 (IF IT IS 1)
SET SW09=1 TO LOOP ON SAME DATA (IF REQUIRED)

PRESS CONTINUE

THE PROGRAM WILL LOOP ON THE SAME TEST.

6.2.2 FROM PROGRAM START

6.2.2.1 PROCEED AS DESCRIBED IN 4.2.1.1 TO 4.2.1.4

6.2.2.2 WHEN THE PROGRAM TYPES "TEST-", SET SW14=1 TO LOOP ON THE TEST THAT WILL BE SELECTED.

6.2.2.3 TYPE IN THE NUMBER OF THE TEST THAT IS TO BE LOOPE
ON (SEE LISTING FOR TEST NUMBER REFERENCE DESIGNATIONS)

6.2.2.4 THE PROGRAM WILL LOOP ON THE SELECTED TEST UNTIL SW14=0.

6.2.3 AFTER <CONTROL>

SAME AS 6.2.2.2 TO 6.2.2.4

7.0 RESTRICTIONS

7.1 STARTING

7.1.1 FOR 16 LINE SCANNER TEST

H861 TEST CONNECTOR MUST BE INSTALLED.

7.1.2 FOR SINGLE LINE CABLE TEST

H315 TEST CONNECTOR MUST BE INSTALLED ON MODEM CABLE

7.1.3 FOR ON LINE TESTS

NONE

7.2 OPERATING

NONE.

7.3 WHEN ON ACT-11 OR "XOR"
PROGRAM WILL DEFAULT TO 16 LINE SCANNER TEST
H861 TEST CONNECTOR MUST BE INSTALLED.

7.4 DEFAULT PARAMETERS (INCLUDING ACT-11 & "XOR")

VECTORS

DHMVEC: 300 (AUTOMATICALLY GENERATED
DHMLVL: 302 BY PROGRAM WHEN UNDER ACT-11 OR "XOR")
ADDRESSES

DHMCSR: 170500
DHMLSR: 170502

NOTE: SW00 (RESELECT ADDRESSES AND VECTORS BECOMES
INOPERATIVE UNDER ACT-11 OR "XOR".

8.0 EXECUTION TIME**8.1 16 LINE SCANNER TEST**

THE TIME FOR 2 PASSES OF THE 16 LINE SCANNER TEST IS APPROXIMATELY 1.5 MINUTES.

8.2 SINGLE LINE CABLE TEST

THE TIME FOR 12 PASSES OF THE SINGLE LINE CABLE TEST IS APPROXIMATELY 1 MINUTE.

8.3 103A MODEM CONNECT-DISCONNECT TEST

APPROXIMATELY 30 SECONDS WILL ELAPSE BETWEEN THE TIME THAT THE ANSWERING DATA SET FIRST DETECTS A RING SIGNAL TO THE TIME THAT THE PROGRAM TYPES "SET SWD1=1 TO DISCONNECT".

APPROXIMATELY 30 SECONDS WILL ELAPSE BETWEEN THE TIME THAT THE PROGRAM TYPES THE ABOVE MESSAGE UNTIL THE TIME THAT THE PROGRAM TYPES "103A TEST COMPLETE".

8.4 202C MODEM CONNECT-DISCONNECT TEST

APPROXIMATELY 1.5 MINUTES WILL ELAPSE BETWEEN THE TIME THAT THE ANSWERING DATA SET DETECTS THE FIRST RING SIGNAL TO THE TIME THAT THE PROGRAM TYPES "SET SWD1=1 TO DISCONNECT".

APPROXIMATELY 30 SECONDS WILL ELAPSE BETWEEN THE TIME THAT THE PROGRAM TYPES THE ABOVE MESSAGE UNTIL THE PROGRAM TYPES "202C TEST COMPLETE".

9. PROGRAM DESCRIPTION

THIS PROGRAM CONSISTS OF A SERIES OF TEST GROUPS LINKED BY A SET OF COMMON SERVICE ROUTINES AND A KEYBOARD MONITOR.

WHEN INITIALLY LOADED AND STARTED ... SWD0 MUST BE SET =1, THE PROGRAM WILL BEGIN A DIALOG WITH THE OPERATOR TO INPUT THE PARAMETERS REQUIRED BY THE PROGRAM.

WHEN ALL INFORMATION HAS BEEN INPUTTED, THE PROGRAM WILL REQUEST THE OPERATOR TO SELECT A TEST BY TYPING THE NUMBER OF THE TEST TO BE RUN. WHEN A CORRECT TEST NUMBER IS RECEIVED, THE PROGRAM WILL BEGIN EXECUTION OF THE SELECTED TEST.

AT ANY TIME DURING TEST EXECUTION, THE OPERATOR MAY CHANGE A TEST PARAMETER BY ENTERING THE APPROPRIATE COMMAND VIA THE TELETYPE KEYBOARD.

9. CONT'D

IF AN OFF LINE TEST HAS BEEN SELECTED, THAT TEST WILL BE REPEATED UNTIL THE OPERATOR INTERVENES.

IF AN ON LINE TEST HAS BEEN SELECTED, THE OPERATOR IS REQUIRED TO TAKE ACTION EACH TIME THE TEST IS COMPLETED.

AT THE END OF EVERY OFF LINE TEST PASS, THE PROGRAM WILL RING THE TELETYPE BELL.

AT THE END OF AN ON LINE TEST, A TEST COMPLETE MESSAGE WILL BE TYPED.

10. LISTING

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41
42
43
44
45
46
47
48
49
50

.TITLE DZDMM-B
.ENABLE ABS,AMA
:MODEM CONTROL DIAGNOSTIC
:COPYRIGHT 1971, 1972, 1976, DIGITAL EQUIPMENT CORP., MAYNARD, MASS. 01754
:THIS PROGRAM CONTAINS TESTS OF THE MODEM CONTROL IN
:THE OFF LINE MODE OF OPERATION ONLY

;SWITCH REGISTER OPTIONS

:SW15=1, HALT ON ERROR
:SW14=1, LOOP ON CURRENT TEST
:SW13=1, SUPPRESS ERROR TYPEOUT
:SW12=1, SUPPRESS TRACE TRAPPING (THIS IS INOPERATIVE IN THIS RELEASE)
:SW11=1, SUPPRESS ITERATIONS
:SW10=1, ESCAPE TO NEXT TEST ON ERROR
:SW09=1, FREEZE DATA
:SW01=1, START DISCONNECT SEQUENCE
:SW00=1, RESELECT VECTOR AND CONTROL REGISTER ADDRESS
;AFTER PROGRAM RESTART

;STARTING ADDRESS FOR ALL TESTS IS 000200
;RESTART ADDRESS=000200

;TESTS AVAILABLE

:TEST GROUP 0-
:OFF LINE TESTS USING H861 TEST CONNECTOR-FIRST TEST=0
:TEST GROUP 1-
:OFF LINE TESTS USING DC11 TEST CONNECTOR AND MODEM CABLE-FIRST TEST=100
:TEST GROUP 2-
:CONNECT/DISCONNECT TEST FOR BELL 103A MODEMS-FIRST TEST=200
:TEST GROUP 3-
:CONNECT/DISCONNECT TEST FOR BELL 202C MODEMS-FIRST TEST=300

;SYMBOL DEFINITIONS

100000 SW15=100000
040000 SW14=40000
020000 SW13=20000
010000 SW12=10000
004000 SW11=4000
002000 SW10=2000
001000 SW09=1000
000400 SW08=400
000100 SW06=100

.NLIST MC,MD,CND
.LIST ME

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;REGISTER DEFINITIONS

000000	R0=X0	: GENERAL REGISTER
000001	R1=X1	: GENERAL REGISTER
000002	R2=X2	: GENERAL REGISTER
000003	R3=X3	: GENERAL REGISTER
000004	R4=X4	: GENERAL REGISTER
000005	R5=X5	: GENERAL REGISTER
000006	SP=X6	: PROCESSOR STACK POINTER
000007	PC=X7	: PROGRAM COUNTER

;LOCATION EQUIVALENCIES

177570	SWR=177570	: CONSOLE SWITCH REGISTER
177776	PS=177776	: PROCESSOR STATUS WORD
020422	STACK=ENDC00+200	: START OF PROCESSOR STACK
015026	RADIX=DIVIS	: CONVERSION FACTOR FOR DECIMAL OUTPUT
015022	BINWRD=DIVIDL	: WORD TO BE CONVERTED TO OCTAL ASCII
015024	DIGIT=DIVIDH	: ASCII OCTAL DIGIT

;CONTROL STATUS REGISTER BIT FUNCTIONS

000020	BUSY=20	: LINE SCANNER RUNNING
000040	SCNENA=40	: LINE SCANNER ENABLE
000100	INTENA=100	: INTERRUPT ENABLE
000200	DONE=200	: SCANNER DONE
000400	STEP=400	: CAUSES LINE COUNTER TO BE INCREMENTED BY 1 COUNT
001000	MAINT=1000	: FORCES IS TO INPUT OF SCRATCH PAD MEMORY
002000	CLRMUX=2000	: CLEAR MULTIPLEXER FUNCTION FLIPFLOPS
004000	CLRSCN=4000	: CLEARS SCANNER SCRATCHPAD MEMORY
010000	SECRXF=10000	: SECONDARY RECEIVE TRANSITION WAS DETECTED BY SCANNER
020000	CSF=20000	: CLEAR TO SEND TRANSITION WAS DETECTED BY SCANNER
040000	COF=40000	: CARRIER TRANSITION WAS DETECTED BY SCANNER
100000	RINGF=100000	: RING SIGNAL WAS DETECTED BY SCANNER

;LINE REGISTER BIT FUNCTIONS

000001	LINENA=1	: =1, RECOGNIZE TRANSITIONS ON THIS LINE
000002	TRMRDY=2	: =1, SEND TERMINAL READY TO MODEM
000004	RS=4	: =1, SEND REQUEST TO SEND TO MODEM
000010	SECTX=10	: =1, SEND SECONDARY TRANSMIT TO MODEM
000020	SECRX=20	: =1, SECONDARY RECEIVE TURNED ON BY MODEM
000040	CS=40	: =1, CLEAR TO SEND TURNED ON BY MODEM
000100	CO=100	: =1, CARRIER TURNED ON BY MODEM
000200	RING=200	: =1, RING TURNED ON BY MODEM

;SOFTWARE TRANSITION FLAGS

000004	XCO=4	: CARRIER TRANSITION WAS DETECTED
000002	XCS=2	: CLEAR TO SEND TRANSITION WAS DETECTED
000001	XSCRX=1	: SECONDARY RECEIVE TRANSITION WAS DETECTED

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; INSTRUCTION DEFINITIONS

005746	PUSH1SP=5746	; DECREMENT PROCESSOR STACK 1 WORD
005726	POP1SP=5726	; INCREMENT PROCESSOR STACK 1 WORD
010046	PUSHRO=10046	; SAVE RO ON STACK
012600	POPPO=12600	; RESTORE RO FROM STACK
024646	PUSH2SP=24646	; DECREMENT STACK TWICE
022626	POP2SP=22626	; INCREMENT STACK TWICE

; EMT DEFINITION TABLE

104000	ERRORC=EMT+X	; CONTROL STATUS ERROR SERVICE
104001	ERRJAL=EMT+X	; LINE STATUS ERROR SERVICE
104002	SCOPE=EMT+X	; SCOPE LOOP AND ITERATION SERVICE
104003	SCOPEF=EMT+X	; DATA FREEZE SERVICE
104004	TYPE=EMT+X	; TELETYPE OUTPUT
104005	SAVOSP=EMT+X	; SAVE RO-R5, PC+2 OF CALL
104006	OCTASC=EMT+X	; CONVERT DATA TO ASCII AND TYPE
104007	RESOS=EMT+X	; RESTORE RO-R5
104010	CONVERT=EMT+X	; ASCII CONVERSION ROUTINE
104011	EXTRACT=EMT+X	; DIGIT EXTRACTION ROUTINE
104012	ERROR=EMT+X	; TYPE PC OF FAILING TESTS ONLY
104013	INSTRG=EMT+X	; INPUT OCTAL DATA STRING
104014	ERRORT=EMT+X	; TRANSITION ERROR
104015	ERRORS=EMT+X	; ON LINE STATUS ERROR
104016	ERRORN=EMT+X	; FATAL TRANSITION
104017	GETLNS=EMT+X	; INPUT LINE NUMBERS
104020	SETUP=EMT+X	; SET UP FOR ON LINE TEST
104021	CKRING=EMT+X	; CHECK FOR RING ON CONVERT LINE
104022	WAITRN=EMT+X	; WAIT FOR TRANSITIONS
104023	CKTRAN=EMT+X	; CHECK TRANSITIONS
104024	WAITS=EMT+X	; DELAY FOR TRANSIENTS

(1)	000146	000000	HP
(1)	000150	000152	+T
(1)	000152	000000	+T
(1)	000154	000156	+T
(1)	000156	000000	+T
(1)	000160	000162	+T
(1)	000162	000000	+T
(1)	000164	000166	+T
(1)	000166	000000	+T
(1)	000170	000172	+T
(1)	000172	000000	+T
(1)	000174	000176	+T
(1)	000176	000000	+T
(1)	000200	000202	+T
(1)	000202	000000	+T
(1)	000204	000206	+T
(1)	000206	000000	+T
(1)	000210	000212	+T
(1)	000212	000000	+T
(1)	000214	000216	+T
(1)	000216	000 00	+T
(1)	000220	000222	+T
(1)	000222	000 00	+T
(1)	000224	000226	+T
(1)	000226	000000	+T
(1)	000230	000232	+T
(1)	000232	000000	+T
(1)	000234	000236	+T
(1)	000236	000000	+T
(1)	000240	000242	+T
(1)	000242	000000	+T
(1)	000244	000246	+T
(1)	000246	000000	+T
(1)	000250	000252	+T
(1)	000252	000000	+T
(1)	000254	000256	+T
(1)	000256	000000	+T
(1)	000260	000262	+T
(1)	000262	000000	+T
(1)	000264	000266	+T
(1)	000266	000000	+T
(1)	000270	000272	+T
(1)	000272	000000	+T
(1)	000274	000276	+T
(1)	000276	000000	+T
(1)	000300	000302	+T
(1)	000302	000000	+T
(1)	000304	000306	+T
(1)	000306	000000	+T
(1)	000310	000312	+T
(1)	000312	000000	+T
(1)	000314	000316	+T
(1)	000316	000000	+T
(1)	000320	000322	+T

(1)	000476	000000	+
(1)	000500	000502	+
(1)	000502	000000	+
(1)	000504	000506	+
(1)	000506	000000	+
(1)	000510	000512	+
(1)	000512	000300	+
(1)	000514	000516	+
(1)	000516	000300	+
(1)	000520	000522	+
(1)	000522	000300	+
(1)	000524	000526	+
(1)	000526	000300	+
(1)	000530	000532	+
(1)	000532	000000	+
(1)	000534	000536	+
(1)	000536	000000	+
(1)	000540	000542	+
(1)	000542	000000	+
(1)	000544	000546	+
(1)	000546	000000	+
(1)	000550	000552	+
(1)	000552	000000	+
(1)	000554	000556	+
(1)	000556	000000	+
(1)	000558	000560	+
(1)	000560	000000	+
(1)	000562	000564	+
(1)	000564	000000	+
(1)	000566	000568	+
(1)	000568	000000	+
(1)	000570	000572	+
(1)	000572	000000	+
(1)	000574	000576	+
(1)	000576	000000	+
(1)	000600	000602	+
(1)	000602	000000	+
(1)	000604	000606	+
(1)	000606	000000	+
(1)	000610	000612	+
(1)	000612	000000	+
(1)	000614	000616	+
(1)	000616	000000	+
(1)	000620	000622	+
(1)	000622	000000	+
(1)	000624	000626	+
(1)	000626	000000	+
(1)	000630	000632	+
(1)	000632	000000	+
(1)	000634	000636	+
(1)	000636	000000	+
(1)	000640	000642	+
(1)	000642	000000	+
(1)	000644	000646	+
(1)	000646	000000	+
(1)	000650	000652	+

(1)	000652	000000	HP	T
(1)	000654	000656	.	+2
(1)	000656	000000	HP	T
(1)	000660	000662	.	+2
(1)	000662	000000	HP	T
(1)	000664	000666	.	+2
(1)	000666	000000	HP	T
(1)	000670	000672	.	+2
(1)	000672	000000	HP	T
(1)	000674	000676	.	+2
(1)	000676	000000	HP	T
(1)	000700	000702	.	+2
(1)	000702	000000	HP	T
(1)	000704	000706	.	+2
(1)	000706	000000	HP	T
(1)	000710	000712	.	+2
(1)	000712	000000	HP	T
(1)	000714	000716	.	+2
(1)	000716	000000	HP	T
(1)	000720	000722	.	+2
(1)	000722	000000	HP	T
(1)	000724	000726	.	+2
(1)	000726	000000	HP	T
(1)	000730	000732	.	+2
(1)	000732	000000	HP	T
(1)	000734	000736	.	+2
(1)	000736	000000	HP	T
(1)	000740	000742	.	+2
(1)	000742	000000	HP	T
(1)	000744	000746	.	+2
(1)	000746	000000	HP	T
(1)	000750	000752	.	+2
(1)	000752	000000	HP	T
(1)	000754	000756	.	+2
(1)	000756	000000	HP	T
(1)	000760	000762	.	+2
(1)	000762	000000	HP	T
(1)	000764	000766	.	+2
(1)	000766	000000	HP	T
(1)	000770	000772	.	+2
(1)	000772	000000	HP	T
(1)	000774	000776	.	+2
(1)	000776	000000	HP	T

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161 000024 000024      . =24
162 000026 015542      PFAIL          ;POWER FAIL HANDLER
163 000030 000340      340          ;SERVICE AT LEVEL 7
164 000032 013720      EMTSRV       ;EMT DISPATCH SERVICE
165          000340      340          ;SERVICE AT LEVEL 7
166
167 000046 013702      . =46
168          LOGICAL          ;ACT11?
169
170 000060 000060      . =60
171 000062 001562      KBDINT       ;KEYBOARD MONITOR
172          000340      340          ;SERVICE AT LEVEL 7
173 000200 000137 001000 . =200
174          JMP      START    ;GO TO START OF PROGRAM
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001000 001000 012737 015542 000024 .#1000 START: MOV #PFail,24 ;SET UP POWER FAIL
001006 005037 001560 CLR TIPFLG ;INTERRUPT SERVICE VECTOR
001012 012777 000100 015142 MOV #INTENA,JKCSR ;CLEAR TEST IN PROGRESS FLAG
001020 012706 020422 MOV #STACK,SP ;ENABLE TELETYPE INTERRUPTS
001024 005037 001070 CLR XFLAG ;XOR = NO ;SET UP STACK POINTER
;*****
;REPLACE THE FOLLOWING BRANCH WITH A "NOP" (240) TO ACTIVATE "XOR" CODE
;*****
001030 000423 BR START0 ;SKIP XOR STUFF
001032 013746 000004 MOV 4,-(SP) ;SAVE 4
001036 012737 001072 000004 MOV #XORSVC,4 ;SET UP SVC ROUTINE
001044 00737 177060 TST 177060 ;GOT AN XOR TESTER OUT THERE ?
001050 012637 000004 MOV (SP)+,4 ;YES
001054 005137 001070 COM XFLAG ;XOR = YES
001060 004737 015672 JSR PC,XOR ;AUTO VECTOR
001064 003137 001100 JMP START0 ;RESTORE TRAPCATCHER
001070 007000 XFLAG: 0 ;XOR FLAG
001072 001626 XORSVC: POP2SP
001074 012637 000004 MOV (SP)+,4 ;RESTORE 4
001100 005737 016256 START0: TST TIPFLG ;TYPED TITLE?
001104 001005 BNE .+14 ;YES
001106 104004 TYPE ;TYPE "MODEM CONTROL DIAGNOSTIC"
001110 017046 HTITLE
001112 012737 000001 016256 MOV #1,TIPFLG ;SET TITLE TYPED FLAG
001120 005737 001070 TST XFLAG ;X OR ?
001124 100412 BMI VECSTR ;RESTORE TRAPCATCHER
001128 005737 000042 TST 42 ;ACT 11?
001132 001403 BEQ START1 ;NO
001136 004737 015672 JSR PC,XOR ;YES AUTO VECTOR
001140 000404 BR VECSTR ;GET VECTOR AND REGISTER ADDRESS
001142 032737 000001 177570 START1: BIT #1,SWR ;IF SW BIT 0=1, ON PROGRAM RESTART
001150 001506 BEQ STARTN ;INPUT VECTOR AND REGISTER ADDRESSES
001152 012706 020422 VECSTR: MOV #STACK,SP ;SET UP PROCESSOR STACK POINTER
001156 012737 000300 013620 MOV #300,DATA1 ;ADDRESS OF FIRST FLOATING VECTOR
001164 012737 000302 013622 MOV #302,DATA2 ;ADDRESS OF STATUS WORD
001172 013777 013622 012420 VECSTA: MOV DATA2,DATA1 ;MOVE ADDRESS OF STATUS WORD TO VECTOR
001200 005077 012416 CLR DATA2 ;CLEAR STATUS WORD
; (FOR HALT ON ILLEGAL INTERRUPT)
001204 062737 000004 013620 ADD #4,DATA1 ;NEXT VECTOR
001212 062737 000004 013622 ADD #4,DATA2 ;NEXT STATUS WORD
001220 023727 013620 001000 CMP DATA1,#1000 ;IS TABLE CLEARED
001226 001361 BNE VECSTA ;IF NOT, CONTINUE
001230 005737 001070 TST XFLAG ;XOR ?
001234 100522 BMI TSTGO ;YES
001236 005737 000042 TST 42 ;ACT 11 ?
001242 001117 BNE TSTGO ;YES
001244 104013 INSTRG ;GET VECTOR ADDRESS
001246 017132 MVECTOR ;MESSAGE "VECTOR ADDRESS-"
001250 000300 300 ;LOWER LIMIT FOR ADDRESS
001252 000774 774 ;UPPER LIMIT FOR ADDRESS
001254 016152 DMIVC ;STORAGE FOR ADDRESS

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280	001256	032737	000003	016152		BIT	#3, DHMVEC	:TEST 2 LSB OF ADDRESS
281	001254	001403				BEQ	VECST1	:IF 0, CONTINUE
282	001256	024646				PUSH2SP		
283	001270	000137	015406			JMP	INSTER	:INCORRECT ADDRESS, TRY AGAIN
284	001274	013737	016152	016154	VECST1:	MOV	DHMVEC, DHMLVL	:GENERATE ADDRESS OF
285	001302	062737	000002	016154		ADD	#2, DHMLVL	:INTERRUPT STATUS WORD
286	001310	104013				INSTRG		:GET ADDRESS OF CONTROL REGISTER
287	001312	017154				MREGAO		:MESSAGE "REGISTER ADDRESS-"
288	001314	170500				17C500		:LOWER LIMIT FOR ADDRESS
289	001316	170670				170670		:UPPER LIMIT FOR ADDRESS
290	001320	016156				DHMCSR		:STORAGE FOR ADDRESS
291	001322	032737	000007	016156		BIT	#7, DHMCSR	:IF 3 LSB ARE NOT 0
292	001330	001403				BEQ	REGST1	
293	001332	024646				PUSH2SP		
294	001334	000137	015406			JMP	INSTER	:INCORRECT ADDRESS, TRY AGAIN
295	001340	013737	016156	016160	REGST1:	MOV	DHMCSR, DHMLSR	:SET UP ADDRESS OF LINE STATUS REGISTER
296	001346	062737	000002	016160		ADD	#2, DHMLSR	
297	001354	104013				INSTRG		:GET LINE SELECT PARAMETER
298	001356	017210				MLINSL		
299	001362	000000				0		
300	001363	177777				177777		
301	001364	016260				LINSEL		

303										
304	001366	012706	020422		STARTN:	MOV	#STACK, SP			:SET UP PROCESSOR STACK
305	001372	104013				INSTRG				:GET TEST NUMBER
306	001374	017242				MTEST				:MESSAGE "TEST"
307	001376	000000				0				:LOWER LIMIT FOR TEST NUMBER
308	001400	000777				777				:UPPER LIMIT FOR TEST NUMBER
309	001432	016200				TSTNO				:STORAGE FOR TEST NUMBER
310	001404	013705	016200			MOV	TSTNO, R5			:GET TEST NUMBER
311	001410	042705	177077			BIC	#177077, R5			:EXTRACT TEST GROUP NUMBER
314	001414	006205				ASR	R5			
(1)	001416	000000				ASR	R5			
(1)	001418	000000				ASR	R5			
(1)	001420	000000				ASR	R5			
(1)	001422	000000				ASR	R5			
315	001426	016537	017734	016234		MOV	GRO(R5), TSTMAX			:GET HIGHEST TEST IN GROUP
316	001424	016537	017714	016232		MOV	TSTLST(R5), TSTPNT			:GET POINTER TO TEST TABLE
317	001442	005737	016232			TST	TSTPNT			:IF 0, INVALID TEST GROUP
318	001446	001003				BNE	STRTOA			
319	001450	024646				PUSH2SP				
320	001452	000137	015406			JMP	INSTER			:TRY AGAIN
321	001456	042737	177700	016200	STRTOA:	BIC	#177700, TSTNO			:GET NUMBER OF FIRST TEST TO BE EXECUTED IN SELECTED GROUP
322										:IS NUMBER TOO LARGE
323	001464	023737	016200	016234		CMP	TSTNO, TSTMAX			
324	001472	003403				BLE	TSTGO			
325	001474	024646				PUSH2SP				
326	001476	000137	015406			JMP	INSTER			:TRY AGAIN
327	001502	012716	000340		TSTGO:	MOV	#340, (SP)			:SET UP PRIORITY LEVEL
328	001506	005746				PUSH1SP				
329	001510	000005				RESET				
330	001512	052777	000100	014442		BIS	#INTENA, @TKCSR			:ENABLE TELETYPE INTERRUPTS
331	001520	012737	001720	001722		MOV	#DMYRTI, KRET			:SET UP DUMMY KEYBOARD RETURN
332	001526	005037	016236			CLR	LINFLG			:CLEAR LINE SELECTED FLAG
333	001532	005037	016174			CLR	TRACON			:CLEAR TRACE TRAP FLAG
334	001536	005037	016176			CLR	PASCNT			:CLEAR PASS COUNT
335	001542	104004				TYPE				
336	001544	017256				MCRLF				
337	001546	012737	000001	001560		MOV	#1, TIPFLG			:SET TEST IN PROGRESS FLAG
338	001554	000137	014102			JMP	TSTENT			:START TESTING
339	001560	000000			TIPFLG:	0				

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; TELETYPE KEYBOARD INTERRUPT SERVICE ROUTINE

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001562 005037 001560          KBDINT: CLR      TIPFLG      ; CLEAR TEST IN PROGRESS FLAG
001566 005037 001724          CLR      SINTFL      ; CLEAR SOFTWARE INTERRUPT FLAG
001572 022777 000203 014364      CMP      #203, @TKDBR ; IF <CONTROL C> WAS TYPED
001600 001007          BNE      KBDIN1      ; TYPE "IC" AND
001602 104004          TYPE                     ; SELECT NEW TEST
001604 017506          MCONTC
001606 022626          POP2SP
001610 005077 014342          CLR      @DMCSR
001614 000137 001366          JMP      STARTN
001620 022777 000226 014336      KBDIN1: CMP      #226, @TKDBR ; IF <CONTROL V> WAS TYPED
001626 001007          BNE      KBDIN2      ; TYPE "IV" AND GET NEW
001630 104004          TYPE                     ; VECTOR AND REGISTER ADDRESS
001632 017511          MCONTV
001634 022626          POP2SP
001636 005077 014314          CLR      @DMCSR
001642 000137 001152          JMP      VECSTR
001646 022777 000214 014310      KBDIN2: CMP      #214, @TKDBR ; IF <CONTROL L> WAS TYPED
001654 001013          BNE      KBDIN3      ; TYPE "IL" AND GET NEW
001656 104004          TYPE                     ; LINE NUMBERS, UNLESS
001660 017514          MCONTL                    ; TEST GROUP 0 WAS IN PROGRESS
001662 022737 001720 001722      CMP      @DMYRTI, KRET ; IF <CONTROL L> WAS TYPED IN TEST
001670 001413          BEQ      DMYRTI        ; GROUP 0, IGNORE
001672 022526          POP2SP
001674 005077 014256          CLR      @DMCSR
001700 000177 000016          JMP      @KRET
001704 012737 000001 001724      KBDIN3: MOV      #1, SINTFL ; SET SOFTWARE INTERRUPT FLAG
001712 012737 000001 001560      MOV      #1, TIPFLG ; SET TEST IN PROGRESS FLAG
001720 005002      DMYRTI: RTI
001722          .EVEN
001724          KRET: 0
          SINTFL: 0
  
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553 ;INITIALIZATION CHECK - PERFORMED ONLY AT PROGRAM START
554 ;VERIFY THAT CONTROL STATUS REGISTER AND LINE STATUS
555 ;REGISTER WERE CLEARED BY INITIALIZE
556
557 TO: ;REFERENCE DESIGNATION
558 ;TYPE "16 LINE SCANNER TEST"
559
560 001726 104004
561 001726 017013
562 001730 017013
563 001732 005777 014220 INIT1: TST 20HMCSR ;TEST CONTROL STATUS REGISTER
564 001736 001401 BEQ .+4
565 001740 104012 ERROR ;CONTROL STATUS NOT CLEARED, ERROR
566 001742 005777 014212 TST 20HMLSR ;TEST LINE STATUS REGISTER
567 001746 001401 BEQ .+4
568 001750 104012 ERROR ;LINE STATUS NOT CLEARED, ERROR
569 001752 104002 SCOPE ;CHECK FOR LOOP
570
571 ;VERIFY THAT "INTERRUPT ENABLE" CAN BE
572 ;SET AND CLEARED.
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579 001754 T1: ;REFERENCE DESIGNATION
580 001754 012777 000100 014174 CSTR1: MOV #INTENA,20HMCSR ;SET INTERRUPT ENABLE
581 001762 032777 000100 014166 BIT #INTENA,20HMCSR ;WAS INTERRUPT ENABLE SET
582 001770 001001 BNE .+4
583 001772 104012 ERROR ;NO, ERROR
584 001774 042777 000100 014154 BIC #INTENA,20HMCSR ;CLEAR INTERRUPT ENABLE
585 002002 032777 000100 014146 BIT #INTENA,20HMCSR ;WAS INTERRUPT ENABLE CLEARED
586 002010 001401 BEQ .+4
587 002012 104012 ERROR ;NO, ERROR
588 002014 104002 SCOPE ;CHECK FOR ITERATIONS, LOOP
589
590 ;VERIFY THAT "DONE" CAN BE SET AND CLEARED
591
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606 002016 T2: ;REFERENCE DESIGNATION
607 002016 012777 000200 014132 CSTR2: MOV #DONE,20HMCSR ;SET DONE
608 002024 032777 000200 014124 BIT #DONE,20HMCSR ;WAS DONE SET
609 002032 001001 BNE .+4
610 002034 104012 ERROR ;NO, ERROR
611 002036 042777 000200 014112 BIC #DONE,20HMCSR ;CLEAR DONE
612 002044 032777 000200 014104 BIT #DONE,20HMCSR ;WAS DONE CLEARED
613 002052 001401 BEQ .+4
614 002054 104012 ERROR ;NO, ERROR
615 002056 104002 SCOPE ;CHECK FOR ITERATIONS, LOOP
616
617 ;VERIFY "MAINTENANCE MODE" CAN BE SET AND CLEARED
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620 002122 012777 000040 014026 T4:
621 002122 032777 000040 014026 CSTR4: MOV #SCNENA,JOHMCSR ;REFERENCE DESIGNATION
622 002136 001001 BNE .+4 ;SET SCAN ENABLE
623 ;WAS SCAN ENABLE SET
624
625 002140 104012 ERROR ;NO, ERROR
626 002142 042777 000040 014006 BIC #SCNENA,JOHMCSR ;CLEAR SCAN ENABLE
627 002150 032777 000040 014000 BIT #SCNENA,JOHMCSR ;WAS SCAN ENABLE CLEARED
628 002156 001401 BEQ .+4
629
630 002160 104012 ERROR ;NO, ERROR
631 002162 104002 SCOPE ;CHECK FOR ITERATIONS, LOOP
632
633 ;VERIFY THAT "BUSY" IS SET WHEN "SCAN ENABLE" IS SET
634 ;VERIFY THAT "BUSY" IS CLEARED WHEN "SCAN ENABLE" IS CLEARED
635
636 002164 012777 000040 013764 T5:
637 002164 032777 000020 013756 CSTR5: MOV #SCNENA,JOHMCSR ;REFERENCE DESIGNATION
638 002172 001001 BNE .+4 ;SET SCAN ENABLE
639 002200 104012 ERROR ;IS BUSY BIT SET
640 002202 104012 ERROR ;BUSY NOT SET, ERROR
641 002204 042777 000040 013744 BIC #SCNENA,JOHMCSR ;CLEAR SCAN ENABLE
642 002212 032777 000020 013736 BIT #BUSY,JOHMCSR ;IS BUSY BIT CLEARED
643 002220 001401 BEQ .+4
644 002222 104012 ERROR ;BUSY NOT CLEARED, ERROR
645 002224 104002 SCOPE ;CHECK FOR LOOP, ITERATIONS
646
647 ;VERIFY THAT SETTING "DONE" DOES NOT CAUSE AN
648 ;INTERRUPT IF "INTERRUPT ENABLE" IS CLEARED.
649
650 002226 052737 000340 177776 T6:
651 002234 005077 013716 INT1: BIS #340,PS ;REFERENCE DESIGNATION
652 002240 012777 002274 013704 CLR JOHMCSR ;LOCK OUT INTERRUPTS
653 002246 013777 177776 013700 MOV #INT1A,JOHMVEC ;CLEAR CONTROL REGISTER
654 002254 052777 000200 013674 MOV PS,JOHMLVL ;SET UP INTERRUPT SERVICE ADDRESS
655 002262 042737 000340 177776 BIS #DONE,JOHMCSR ;SET UP INTERRUPT PRIORITY
656 002270 002240 BIC #340,PS ;SET DONE
657 002272 000402 NOP ;ALLOW INTERRUPTS
658 002274 022626 BR INT1B ;DELAY FOR INTERRUPT
659 002276 104012 INT1A: POP2SP ;NO INTERRUPT, CONTINUE
660 002300 104002 INT1B: ERROR ;RESTORE STACK, INTERRUPT
;OCCURED, ERROR
;CHECK FOR LOOP, ITERATIONS
  
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662
663 ;VERIFY THAT NO INTERRUPT OCCURS WITH "INTERRUPT ENABLE"
664 ;SET AND "DONE" CLEARED.
665
666 002302 T7: ;REFERENCE DESIGNATION
667 002302 052737 000340 177776 INT2: BIS #340,PS ;LOCK OUT INTERRUPTS
668 002310 005077 013642 CLR #0HMCSR ;CLEAR CONTROL REGISTER
669 002314 012777 002350 013630 MOV #INT2A,#0HMVEC ;SET UP INTERRUPT SERVICE ADDRESS
670 002322 013777 177776 013624 MOV PS,#0HMLVL ;SET UP INTERRUPT SERVICE LEVEL
671 002330 052777 000100 013620 BIS #INTENA,#0HMCSR ;SET INTERRUPT ENABLE
672 002336 042737 000340 177776 BIC #340,PS ;ALLOW INTERRUPTS
673 002344 000240 NOP ;DELAY FOR INTERRUPTS
674 002346 000402 BR INT2B ;NO INTERRUPT, CONTINUE
675 002350 022626 INT2A: POP2SP ;RESTORE STACK
676 002352 104012 ERROR ;INTERRUPT OCCURED, ERROR
677 002354 104002 INT2B: SCOPE ;CHECK FOR ITERATIONS, LOOP
678
679 ;VERIFY THAT SETTING "DONE" CAUSES AN INTERRUPT
680 ;WITH "INTERRUPT ENABLE" SET
681
682 002356 T10: ;REFERENCE DESIGNATION
683 002356 052737 000340 177776 INT3: BIS #340,PS ;LOCK OUT INTERRUPTS
684 002364 005077 013566 CLR #0HMCSR ;CLEAR CONTROL REGISTER
685 002370 012777 002434 013554 MOV #INT3A,#0HMVEC ;SET UP INTERRUPT SERVICE ADDRESS
686 002376 012777 000100 013552 MOV #INTENA,#0HMCSR ;SET "INTERRUPT ENABLE"
687 002404 013777 177776 013542 MOV PS,#0HMLVL ;SET "INTERRUPT LEVEL"
688 002412 042737 000340 177776 BIC #340,PS ;ALLOW INTERRUPTS
689 002420 052777 000200 013530 BIS #DONE,#0HMCSR ;SET "DONE"
690 002426 000240 NOP ;DELAY FOR INTERRUPT
691 002430 104012 ERROR ;INTERRUPT OCCURED, ERROR
692 002432 000401 BR INT3B ;CONTINUE
693 002434 022626 INT3A: POP2SP ;INTERRUPT OCCURED, RESTOR STACK
694 002436 104002 INT3B: SCOPE ;CHECK FOR ITERATION, LOOP
695
713
(2) ;VERIFY THAT NO INTERRUPT OCCURS WITH
(2) ;"INTERRUPT ENABLE" SET AND "DONE" SET AT PRIORITY 7.
(2)
(3)
(2) 002440 T11: ;REFERENCE DESIGNATION
(2) 002440 005077 013512 INT4: CLR #0HMCSR ;CLEAR CONTROL REGISTER
(2) 002444 042737 000340 177776 BIC #340,PS ;SET PROCESSOR PRIORITY
(2) 002452 052737 000340 177776 BIS #340,PS ;TO LEVEL 7.
(2) 002460 012777 002514 013464 MOV #INT4A,#0HMVEC ;SET UP INTERRUPT SERVICE ADDRESS
(2) 002466 013777 177776 013460 MOV PS,#0HMLVL ;SET UP INTERRUPT SERVICE LEVEL
(2) 002474 012777 000100 013454 MOV #INTENA,#0HMCSR ;SET INTERRUPT ENABLE
(2) 002502 052777 000200 013446 BIS #DONE,#0HMCSR ;GENERATE INTERRUPT
(2) 002510 000240 NOP ;DELAY FOR INTERRUPT
(2) 002512 000402 BR INT4B ;NO INTERRUPT, CONTINUE
(2) 002514 022626 INT4A: POP2SP ;RESTORE STACK
(2) 002516 104012 ERROR ;INTERRUPT OCCURED, ERROR
(2) 002520 104002 INT4B: SCOPE ;CHECK FOR ITERATION, LOOP
    
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;VERIFY THAT AN INTERRUPT OCCURS WITH "INTERRUPT
;ENABLE" SET AND "DONE" SET AT PRIORITY 0.

T15:

INT10:

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CLR      20HMCSR
BIC      #340,PS
MOV      #INT10A,20HMVEC
CLR      20HMLVL
BIS      #0,PS
MOV      #INTENA,20HMCSR
BIS      #DONE,20HMCSR
NOP
ERROR
BR       INT10B
INT10A:  POP2SP
INT10B:  SCOPE
    
```

```

;REFERENCE DESIGNATION
;CLEAR CONTROL REGISTER
;ALLOW INTERRUPTS
;SET UP INTERRUPT SERVICE ADDRESS
;SET UP INTERRUPT SERVICE PRIORITY
;SET PROCESSOR PRIORITY TO LEVEL 0.
;SET INTERRUPT ENABLE
;GENERATE INTERRUPT
;WAIT FOR INTERRUPT
;NO INTERRUPT, ERROR
;CONTINUE
;INTERRUPT OCCURED, RESTORE STACK
;CHECK FOR INTERATIONS, LOOP.
    
```

;VERIFY THAT AN INTERRUPT OCCURS WITH "INTERRUPT
;ENABLE" SET AND "DONE" SET AT PRIORITY 1.

T16:

INT11:

```

CLR      20HMCSR
BIC      #340,PS
MOV      #INT11A,20HMVEC
CLR      20HMLVL
BIS      #40,PS
MOV      #INTENA,20HMCSR
BIS      #DONE,20HMCSR
NOP
ERROR
BR       INT11B
INT11A:  POP2SP
INT11B:  SCOPE
    
```

```

;REFERENCE DESIGNATION
;CLEAR CONTROL REGISTER
;ALLOW INTERRUPTS
;SET UP INTERRUPT SERVICE ADDRESS
;SET UP INTERRUPT SERVICE PRIORITY
;SET PROCESSOR PRIORITY TO LEVEL 1.
;SET INTERRUPT ENABLE
;GENERATE INTERRUPT
;WAIT FOR INTERRUPT
;NO INTERRUPT, ERROR
;CONTINUE
;INTERRUPT OCCURED, RESTORE STACK
;CHECK FOR INTERATIONS, LOOP.
    
```

;VERIFY THAT AN INTERRUPT OCCURS WITH "INTERRUPT
;ENABLE" SET AND "DONE" SET AT PRIORITY 2.

T17:

INT12:

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CLR      20HMCSR
BIC      #340,PS
MOV      #INT12A,20HMVEC
CLR      20HMLVL
BIS      #100,PS
MOV      #INTENA,20HMCSR
BIS      #DONE,20HMCSR
NOP
ERROR
BR       INT12B
INT12A:  POP2SP
INT12B:  SCOPE
    
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;REFERENCE DESIGNATION
;CLEAR CONTROL REGISTER
;ALLOW INTERRUPTS
;SET UP INTERRUPT SERVICE ADDRESS
;SET UP INTERRUPT SERVICE PRIORITY
;SET PROCESSOR PRIORITY TO LEVEL 2.
;SET INTERRUPT ENABLE
;GENERATE INTERRUPT
;WAIT FOR INTERRUPT
;NO INTERRUPT, ERROR
;CONTINUE
;INTERRUPT OCCURED, RESTORE STACK
;CHECK FOR INTERATIONS, LOOP.
    
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002750      005077      013202
002750      042737      000340      177776
002754      012777      003024      013162
002762      052737      000000      177776
002770      012777      000100      013146
002774      052737      000200      013140
003002      012777      000401
003006      052737      104012
003026      022626
003026      104002
    
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003030      005077      013122
003030      042737      000340      177776
003034      012777      003104      013102
003042      052737      000040      177776
003054      012777      000100      013066
003062      052737      000200      013060
003070      000240
003076      104012
003100      000401
003102      022626
003104      104002
003106
    
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003110      005077      013042
003110      042737      000340      177776
003114      012777      003164      013022
003122      052737      000100      177776
003130      012777      000100      013006
003134      052737      000200      013000
003142      000240
003150      104012
003156      000401
003164      022626
003166      104002
    
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K03

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SEQ 0035

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(2) 003170
(2) 003170 005077 012762 T20:
(2) 003174 042737 000340 177776 INT13: CLR 20HMCSR ;REFERENCE DESIGNATION
(2) 003202 012777 003244 012742 BIC #340,PS ;CLEAR CONTROL REGISTER
(2) 003210 005077 012740 CLR 20HMLV,20HMVEC ;ALLOW INTERRUPTS
(2) 003214 052737 000140 177776 BIS #140,PS ;SET UP INTERRUPT SERVICE ADDRESS
(2) 003222 012777 000100 012726 MOV #INT13A,20HMCSR ;SET UP INTERRUPT SERVICE PRIORITY
(2) 003230 052777 000200 012720 BIS #DONE,20HMCSR ;SET PROCESSOR PRIORITY TO LEVEL 3.
(2) 003236 000240 NOP ;SET INTERRUPT ENABLE
(2) 003240 104012 ERROR ;GENERATE INTERRUPT
(2) 003242 000401 BR INT13B ;WAIT FOR INTERRUPT
(2) 003244 022626 INT13A: POP2SP ;NO INTERRUPT, ERROR
(2) 003246 104002 INT13B: SCOPE ;CONTINUE
;INTERRUPT OCCURED, RESTORE STACK
;CHECK FOR ITERATIONS, LOOP.

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737 003250
738 003250 005077 012702
739 003254 042737 000340 177776
740 003262 012737 000001 016262
741 003270 005005
742 003272 012700 000020
743 003276 033737 016262 016260
744 003304 001407
745 003306 010577 012644
746 003312 017704 012640
747 003316 020504
748 003320 001401
749 003322 104000
750 003324 104003
751 003326 003276
752 003330 005205
753 003332 006337 016262
754 003336 005300
755 003340 001356
756 003342 104002
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761 003344
762 003344 042737 000340 177776
763 003352 005077 012600
764 003356 005005
765 003360 012737 000001 016262
766 003366 012701 177777
767 003372 012700 000020
768 003376 012777 000017 012552
769 003404 033737 016262 016260
770 003412 001410
771 003414 052777 000400 012534
772 003422 017704 012530
773 003425 020504
774 003427 001401
775 003430 104000
776 003434 104003
777 003436 003344
778 003440 005205
779 003442 006337 016262
780 003446 005201
781 003450 010177 012502
782 003454 005300
783 003456 001352
784 003460 104002

;VERIFY THAT ALL LINE NUMBERS CAN BE WRITTEN INTO AND
;READ BACK FROM LINE COUNTER

T21:
LINT1: CLR 20HMCSR
      BIC #340,PS
      MOV #1,SELMSK
      CLR R5
      MOV #16,R0
LINT1A: BIT SELMSK,LINSEL
      BEQ LINT1B
      MOV R5,20HMCSR
      MOV 20HMCSR,R4
      CMP R5,R4
      BEQ LINT1B
      ERRORC
LINT1B: SCOPEF
      LINT1A
      INC R5
      ASL SELMSK
      DEC R0
      BNE LINT1A
      SCOPE

;REFERENCE DESIGNATION
;CLEAR CONTROL STATUS REGISTER
;ENABLE INTERRUPTS
;INIT LINE SELECT MASK
;CLEAR EXPECTED LINE NUMBER
;SET UP TO TEST 16 LINE NUMBERS
;THIS LINE SELECTED ??
;BR IF NOT
;SET LINE NUMBER
;READ BACK LINE NUMBER
;ARE EXPECTED AND RECEIVED
;LINE NUMBERS THE SAME
;LINE NUMBERS DIFFERENT, ERROR
;CHECK FOR DATA FREEZE
;RETURN FOR DATA FREEZE
;UPDATE LINE COUNT
;SELECT NEXT LINE TO TEST
;UPDATE LINE NUMBER
;CONTINUE
;CHECK FOR ITERATION, LOOP

;USING "STEP" MODE, VERIFY THAT THE
;LINE COUNTER CAN BE STEPPED THRU ALL STATES.

T22:
LINT2: BIC #340,PS
      CLR 20HMCSR
      CLR R5
      MOV #1,SELMSK
      MOV #-1,R1
      MOV #16,R0
      MOV #17,20HMCSR
LINT2A: BIT SELMSK,LINSEL
      BEQ LINT2B
      BIS #STEP,20HMCSR
      MOV 20HMCSR,R4
      CMP R5,R4
      BEQ LINT2B
      ERRORC
LINT2B: SCOPEF
      LINT2
      INC R5
      ASL SELMSK
      INC R1
      MOV R1,20HMCSR
      DEC R0
      BNE LINT2A
      SCOPE

;REFERENCE DESIGNATION
;ENABLE INTERRUPTS
;CLEAR CONTROL STATUS REGISTER
;CLEAR EXPECTED LINE COUNT
;SET UP SELECT MASK
;INIT LINE COUNTER
;SET UP TO TEST 16 VALUES
;FIRST VALUE =0
;THIS LINE SELECTED ??
;BR IF NOT
;STEP LINE COUNTER
;READ LINE COUNTER
;COMPARE EXPECTED AND
;RECEIVED LINE NUMBERS
;LINE COUNTER ERROR
;CHECK FOR DATA FREEZE

;UPDATE EXPECTED LINE NUMBER
;SHIFT SELECT MASK
;GEN NEW LINE NO.
;SET NEW LINE NO. IN CSR

;CHECK FOR ITERATIONS, LOOP

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 003462 012777 002000 012466
 003470 042737 000340 177776
 003476 012700 000020
 003502 052777 001017 012446
 00 510 052777 000400 012440
 00 516 005300
 00 20 001373
 003522 012700 000020
 003526 012705 070000
 003532 012777 000017 012416
 003540 052777 000400 012410
 003546 017704 012404
 003552 020504
 003 54 001403
 003 56 104000
 003560 104003
 003562 003462
 003564 005205
 003 56 005300
 003570 001363
 003572 012777 004000 012356
 003600 032777 000020 012350
 003606 001374
 003610 012700 000020
 003614 005005
 003616 012777 000017 012332
 003624 052777 000400 012324
 003632 017704 012320
 003636 020504
 003640 001403
 003642 104000
 003644 104003
 003646 003572
 003650 005205
 003652 005300
 003654 001363
 003656 104002

T23:
 MENT1: MOV #CLRMUX, @DHMCSR
 BIC #340, PS
 MOV #16., R0
 BIS #MAINT+17, @DHMCSR
 MEM11A: BIS #STEP, @DHMCSR
 DEC R0
 BNE MENT1A
 MOV #16., R0
 MOV #70000, R5
 MOV #17, @DHMCSR
 MENT1B: BIS #STEP, @DHMCSR
 MOV @DHMCSR, R4
 CMP R5, R4
 BEQ MENT1C
 ERRORC
 SCOPEF
 MENT1
 MENT1C: INC R5
 DEC R0
 BNE MENT1B
 MENT1D: MOV #CLRSCN, @DHMCSR
 BIT #BUSY, @DHMCSR
 BNE .-6
 MOV #16., R0
 CLR R5
 MOV #17, @DHMCSR
 MENT1E: BIS #STEP, @DHMCSR
 MOV @DHMCSR, R4
 CMP R5, R4
 BEQ MENT1F
 ERRORC
 SCOPEFF
 MENT1D
 MENT1F: INC R5
 DEC R0
 BNE MENT1E
 SCOPE

;WRITE 1'S INTO ALL SCANNER MEMORY LOCATIONS.
 ;VERIFY THAT ALL LOCATIONS HAVE BEEN WRITTEN
 ;TO 1'S.
 ;VERIFY THAT "CLEAR SCAN" CLEARS ALL SCANNER
 ;MEMORY LOCATIONS.

;REFERENCE DESIGNATION
 ;CLEAR CONTROL STATUS REGISTER
 ;ENABLE INTERRUPTS
 ;SET UP TO TEST 16 LOCATIONS
 ;SET MAINTENANCE MODE
 ;SET LINE COUNTER THRU ALL
 ;STATES, WRITING 1'S INTO
 ;ALL MEMORY WORDS
 ;SET UP TO TEST 16 WORDS
 ;SET UP EXPECTED STATUS REGISTER
 ;START WITH LINE 0
 ;ACCESS SCANNER MEMORY
 ;READ DATA
 ;COMPARE EXPECTED AND RECEIVED
 ;DATA
 ;CONTROL STATUS OR MEMORY ERROR
 ;CHECK FOR DATA FREEZE
 ;UPDATE EXPECTED STATUS
 ;UPDATE LINE COUNT
 ;CONTINUE
 ;SET "CLEAR SCAN"
 ;WAIT FOR "CLEAR CYCLES"
 ;SET UP TO TEST 16 MEMORY
 ;LOCATIONS
 ;FIRST TO BE TESTED=0
 ;ACCESS SCANNER MEMORY
 ;READ DATA
 ;COMPARE EXPECTED AND RECEIVED
 ;DATA
 ;CONTROL STATUS OF MEMORY ERROR
 ;CHECK FOR DATA FREEZE
 ;UPDATE EXPECTED DATA
 ;UPDATE LINE COUNT
 ;CONTINUE
 ;CHECK FOR ITERATIONS, LOOP

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832
833 ;WRITE 1'S INTO SELECTED SCANNER MEMORY LOCATION.
834 ;VERIFY THAT ONLY SELECTED LOCATION WAS WRITTEN INTO.
835
836 003660 T24: ;REFERENCE DESIGNATION
837 003660 005077 012272 MENT2: CLR @OHMCSR ;CLEAR CONTROL STATUS REGISTER
838 003664 042737 000340 177776 BIC #340,PS ;ENABLE INTERRUPTS
839 003672 012700 000020 MOV #16,R0 ;SET UP TO TEST 16 ADDRESSES
840 003676 012702 000017 MOV #17,R2 ;FIRST ADDRESS TO BE TESTED=0
841 003702 012777 004000 012246 MENT2A: MOV @CLRSCN,@OHMCSR ;CLEAR SCANNER MEMORY
842 003710 032777 000020 012240 BIT #BUSY,@OHMCSR ;WAIT FOR CLEAR CYCLE
843 003716 001374 BNE #-6
844 003720 012777 001000 012230 MOV #MAINT,@OHMCSR ;SET "MAINTENANCE MODE"
845 003726 050277 012224 BIS R2,@OHMCSR ;SET LINE COUNTER TO TEST ADDRESS-1
846 003732 052777 000400 012216 BIS #STEP,@OHMCSR ;WRITE 1'S INTO TEST ADDRESS
847 003740 042777 001000 012210 BIC #MAINT,@OHMCSR ;CLEAR "MAINTENANCE MODE"
848 003746 012703 000020 MOV #16,R3 ;SET UP TO TEST ALL 16
849 003752 012777 000017 012176 MOV #17,@OHMCSR ;SCANNER MEMORY LOCATIONS
850 003760 005202 INC R2
851 003762 000001 CLR R1
852 003764 052777 000400 012164 MENT2B: BIS #STEP,@OHMCSR ;ACCESS SCANNER MEMORY
853 003772 117704 012160 MOVB @OHMCSR,R4 ;READ CONTENTS OF MEMORY
854 003776 010105 MOV R1,R5 ;SET UP EXPECTED CONTENTS
855 004000 120402 CMPB R4,R2 ;OF SCANNER MEMORY
856 004002 001002 BNE MENT2C
857 004004 052705 070000 BIS #70000,R5
858 004010 020405 MENT2C: CMP R4,R5 ;COMPARE EXPECTED AND RECEIVED
859 004012 001403 BEQ MENT2D ;VALUES
860 004014 104000 ERRORC ;SCANNER MEMORY ERROR
861 004016 104003 SCOPEF ;CHECK FOR DATA FREEZE
862 004020 003702 MENT2A
863 004022 005201 MENT2D: INC R1
864 004024 005303 DEC R3 ;TEST NEXT SCANNED LOCATION
865 004026 001356 BNE MENT2B
866 004030 005300 DEC R0 ;UPDATE LINE COUNT
867 004032 001323 BNE MENT2A
868 004034 104002 SCOPE ;CHECK FOR ITERATION, LOOP
    
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870
871 ;WITH ALL SCANNER MEMORY LOCATIONS SET TO 1'S,
872 ;WRITE 0'S INTO SELECTED LOCATION
873 ;VERIFY THAT ONLY SELECTED LOCATION WAS CLEARED.
874
875 004036 T25: ;REFERENCE DESIGNATION
876 005077 012114 MENT3: CLR 20HCSR ;CLEAR CONTROL STATUS REGISTER
877 042737 000340 177776 BIC 8340,PS ;ENABLE INTERRUPTS
878 004042 012700 000020 MOV 816,R0 ;SET UP TO TEST 16 ADDRESSES
879 004050 012702 000017 MOV 817,R2 ;FIRST ADDRESS TO BE TESTED=0
880 004054 012703 000020 MENT3A: MOV 816,R3 ;WRITE 1'S INTO ALL SCANNER
881 004060 012777 001017 012064 MENT3B: MOV #MAINT+17,20HCSR ;MEMORY LOCATIONS
882 004072 052777 000400 012056 MENT3B: BIS #STEP,20HCSR
883 004100 005303 DEC R3
884 004102 001373 BNE MENT3B
885 004104 010277 012046 MOV R2,20HCSR ;SET LINE COUNTER TO TEST ADDRESS-1
886 004110 052777 000400 012040 BIS #STEP,20HCSF ;WRITE 0'S INTO TEST ADDRESS
887 004116 012703 000020 MOV 816,R3 ;SET UP TO TEST ALL 16
888 004122 012777 000017 012026 MOV 817,20HCSR ;SCANNER MEMORY LOCATIONS
889 004130 000202 INC R2
890 004134 052777 000400 012014 MENT3C: CLR R1
891 004140 117704 012010 MENT3C: BIS #STEP,20HCSR ;ACCESS SCANNER MEMORY
892 004146 010105 MOV 20HCSR,R4 ;READ CONTENTS OF MEMORY
893 004150 120402 MOV R1,R5 ;SET UP EXPECTED CONTENTS
894 004152 001002 CMPB R4,R2 ;OF SCANNER MEMORY
895 004154 052705 070000 BNE MENT3D
896 004160 020405 MENT3D: BIS #70000,R5 ;COMPARE EXPECTED AND
897 004162 001403 MENT3D: CMP R4,R5 ;RECEIVED VALUES
898 004164 104000 BEQ MENT3E ;SCANNER MEMORY ERROR
899 004166 104003 ;CHECK FOR DATA FREEZE
900 004168 104000 SCOPEF
901 004170 004750 MENT3A: MENT3A
902 004172 000001 MENT3E: INC R1
903 004174 000303 DEC R3
904 004176 001356 BNE MENT3C ;TEST NEXT SCANNER LOCATION
905 004200 005300 DEC R0 ;UPDATE ADDRESS COUNT
906 004202 001326 BNE MENT3A
907 004204 104002 SCOPE ;CHECK FOR ITERATION, LOOP

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909          ;VERIFY THAT LINE ENABLE FUNCTION FLIP-FLOP CAN
(1)          ;BE SET AND CLEARED FOR SELECTED LINE
(1)
(2) 004206          T26:          ;REFERENCE DESIGNATION
(1) 004206 005077 011744          MUX1:  CLR  20HCSR          ;CLEAR CONTROL STATUS REGISTER
(1) 004212 042737 000340 177776      BIC  8340,PS          ;ENABLE INTERRUPTS
(1) 004220 012700 000020          MOV  016,R0          ;SET UP TO TEST 16 FUNCTION FLIP-FLOP
(1) 004234 012737 000001 016262      MOV  01,SELMSK      ;INIT LINE SELECT MASK
(1) 004234 005001          CLR  R1          ;START AT LINE 0
(1) 004234 012777 002000 011714      MUX1A: MOV  @CLRMUX,20HCSR
(1) 004242 012702 000020          MOV  016,R2
(1) 004246 033737 016262 016260      BIT  SELMSK,LINSEL  ;IS THIS LINE SELECTED FOR TEST ?
(1) 004254 001463          BEQ  MUX1F          ;BR IF NOT
(1) 004256 010177 011674          MOV  R1,20HCSR      ;SELECT LINE TO BE TESTED
(1) 004270 012777 000001 011670      MOV  @LINENA,20HLSR ;SET LINE ENABLE FUNCTION FLIP-FLOP
(1) 004270 012737 000001 016264      MOV  01,SLMSK      ;INIT ANOTHER SELECT MASK
(1) 004276 005077 011654          CLR  20HCSR
(1) 004302 005005          MUX1B: CLR  R5
(1) 004304 033737 016264 016260      BIT  SLMSK,LINSEL  ;SELECTED ??
(1) 004312 001417          BEQ  MUX1D          ;BR IF NOT
(1) 004314 017704 011640          MOV  20HLSR,R4      ;READ LINE STATUS REGISTER
(1) 004320 117703 011632          MOVB 20HCSR,R3      ;READ CONTROL STATUS REGISTER
(1) 004324 042703 177760          BIC  0177760,R3     ;CLEAR UNWANTED BITS
(1) 004330 020103          CMP  R1,R3          ;IF LINE NUMBER=SELECTED LINE NUMBER,
(1) 004332 001002          BNE  MUX1C          ;EXCEPT LINE ENABLE FUNCTION FLIP FLOP
(1) 004334 012705 000001          MOV  @LINENA,R5
(1)
(1) 004340 020504          MUX1C: CMP  R5,R4          ;TO BE SET
(1) 004342 001403          BEQ  MUX1D          ;COMPARE EXPECTED AND RECEIVED
(1) 004344 104001          ERRORL          ;RESULTS
(1) 004346 104003          SCOPEF          ;LINE STATUS ERROR
(1) 004350 004352          MUX1D
(1) 004352 052777 000400 011576      MUX1D: BIS  #STEP,20HCSR  ;EXAMINE NEXT LINE
(1) 004360 006337 016264          ASL  SLMSK          ;SHIFT MASK
(1) 004364 005302          DEC  R2
(1) 004366 001345          BNE  MUX1B
(1) 004370 005005          CLR  R5
(1) 004372 010177 011560          MUX1E: MOV  R1,20HCSR
(1) 004376 010103          MOV  R1,R3          ;SET LINE COUNTER TO SELECTED LINE
(1) 004400 005077 011554          CLR  20HLSR        ;CLEAR LINE ENABLE FLIP FLOP
(1) 004404 105227 000000          INCB  #0          ;DELAY FOR CABLE
(1) 004410 001375          BNE  .-4          ;DITTO
(1) 004412 017704 011542          MOV  20HLSR,R4      ;READ LINE STATUS REGISTER
(1) 004416 005704          TST  R4          ;WAS LINE ENABLE FUNCTION FLIP FLOP
(1) 004420 001401          BEQ  MUX1F          ;CLEARED
(1) 004422 104001          ERRORL          ;NO LINE STATUS ERROR
(1) 004424 104003          SCOPEF          ;CHECK FOR LOOP ON SAME DATA
(1) 004426 004234          MUX1F: MUX1A
(1) 004430 006337 016262          ASL  SELMSK        ;SHIFT SELECT MASK
(1) 004434 005201          INC  R1          ;SELECT NEXT LINE
(1) 004436 005300          DEC  R0          ;DECREMENT LINE COUNT
(1) 004440 001275          BNE  MUX1A        ;CONTINU IF NOT DONE
(1) 004442 104002          SCOPE          ;CHECK FOR ITERATIONS, LOOP
    
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911          ;VERIFY THAT TERMINAL READY FUNCTION FLIP-FLOP CAN
(1)          ;BE SET AND CLEARED FOR SELECTED LINE
(1)
(2) 004444          T27:          ;REFERENCE DESIGNATION
(1) 004444 005077 011506          MUX2:  CLR      @DHMCSR          ;CLEAR CONTROL STATUS REGISTER
(1) 004450 042737 000340 177776          BIC      @340,PS          ;ENABLE INTERRUPTS
(1) 004456 012700 000020          MOV      @16,R0          ;SET UP TO TEST 16 FUNCTION FLIP-FLOP
(1) 004462 012737 000001 016262          MOV      @1,SELMSK          ;INIT LINE SELECT MASK
(1) 004470 005001          CLR      R1          ;START AT LINE 0
(1) 004472 012777 002000 011456          MUX2A: MOV      @CLRMUX,@DHMCSR
(1) 004500 012702 000020          MOV      @16,R2
(1) 004504 033737 016262 016260          BIT      SELMSK,LINSEL          ;IS THIS LINE SELECTED FOR TEST ?
(1) 004512 001463          BEQ      MUX2F          ;BR IF NOT
(1) 004514 010177 011436          MOV      R1,@DHMCSR          ;SELECT LINE TO BE TESTED
(1) 004520 012777 000002 011432          MOV      @TRMRDY,@DHMLSR          ;SET TERMINAL READY FUNCTION FLIP-FLOP
(1) 004526 012737 000001 016264          MOV      @1,SLMSK          ;INIT ANOTHER SELECT MASK
(1) 004534 005077 011416          CLR      @DHMCSR
(1) 004540 005005          MUX2B: CLR      R5
(1) 004542 033737 016264 016260          BIT      SLMSK,LINSEL          ;SELECTED ??
(1) 004550 001417          BEQ      MUX2D          ;BR IF NOT
(1) 004552 017704 011402          MOV      @DHMLSR,R4          ;READ LINE STATUS REGISTER
(1) 004556 117703 011374          MOVB    @DHMCSR,R3          ;READ CONTROL STATUS REGISTER
(1) 004562 042703 177760          BIC      @177760,R3          ;CLEAR UNWANTED BITS
(1) 004566 020103          CMP      R1,R3          ;IF LINE NUMBER=SELECTED LINE NUMBER,
(1) 004570 001002          BNE      MUX2C          ;EXCEPT TERMINAL READY FUNCTION FLIP FLOP
(1) 004572 012705 000002          MOV      @TRMRDY,R5          ;TO BE SET
(1) 004576 020504          MUX2C: CMP      R5,R4          ;COMPARE EXPECTED AND RECEIVED
(1) 004600 001403          BEQ      MUX2D          ;RESULTS
(1) 004602 104001          ERRORL          ;LINE STATUS ERROR
(1) 004604 104003          SCOPEF
(1) 004606 004610          MUX2D
(1) 004610 052777 000400 011340          MUX2D: BIS      @STEP,@DHMCSR          ;EXAMINE NEXT LINE
(1) 004616 006337 016264          ASL      SLMSK          ;SHIFT MASK
(1) 004622 005302          DEC      R2
(1) 004624 001345          BNE      MUX2B
(1) 004626 005005          CLR      R5
(1) 004630 010177 011322          MUX2E: MOV      R1,@DHMCSR
(1) 004634 010103          MOV      R1,R3
(1) 004636 005077 011316          CLR      @DHMLSR          ;SET LINE COUNTER TO SELECTED LINE
(1) 004642 105227 000000          INCB    @0          ;CLEAR TERMINAL READY FLIP FLOP
(1) 004646 001375          BNE      -4          ;DELAY FOR CABLE
(1) 004650 017704 011304          MOV      @DHMLSR,R4          ;DITTO
(1) 004654 005704          TST     R4          ;READ LINE STATUS REGISTER
(1) 004656 001401          BEQ      MUX2F          ;WAS TERMINAL READY FUNCTION FLIP FLOP
(1) 004660 104001          ERRORL          ;CLEARED
(1) 004662 104003          SCOPEF          ;NO, LINE STATUS ERROR
(1) 004664 004472          MUX2A          ;CHECK FOR LOOP ON SAME DATA
(1) 004666 006337 016262          ASL      SELMSK          ;SHIFT SELECT MASK
(1) 004672 005201          INC     R1          ;SELECT NEXT LINE
(1) 004674 005300          DEC     R0          ;DECREMENT LINE COUNT
(1) 004676 001275          BNE     MUX2A          ;CONTINUE IF NOT DONE
(1) 004700 104002          SCOPE          ;CHECK FOR ITERATIONS, LOOP
    
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913          ;VERIFY THAT REQUEST TO SEND FUNCTION FLIP-FLOP CAN
(1)          ;BE SET AND CLEARED FOR SELECTED LINE
(1)
(2) 004702          T30:          ;REFERENCE DESIGNATION
(1) 004702 005077 011250 MUX3: CLR 20HMCSR ;CLEAR CONTROL STATUS REGISTER
(1) 004706 042737 000340 177776 BIC #340,PS ;ENABLE INTERRUPTS
(1) 004714 012700 000020 MOV #16,R0 ;SET UP TO TEST 16 FUNCTION FLIP-FLOP
(1) 004720 012737 000001 016262 MOV #1,SELMSK ;INIT LINE SELECT MASK
(1) 004726 005001 CLR R1 ;START AT LINE 0
(1) 004730 012777 002000 011220 MUX3A: MOV #CLRMUX,20HMCSR
(1) 004736 012702 000020 MOV #16,R2
(1) 004742 033737 016262 016260 BIT SELMSK,LINSEL ;IS THIS LINE SELECTED FOR TEST ?
(1) 004750 001463 BEQ MUX3F ;BR IF NOT
(1) 004752 010177 011200 MOV R1,20HMCSR ;SELECT LINE TO BE TESTED
(1) 004756 012777 000004 011174 MOV #RS,20HMLSR ;SET REQUEST TO SEND FUNCTION FLIP-FLOP
(1) 004764 012737 000001 016264 MOV #1,SLMSK ;INIT ANOTHER SELECT MASK
(1) 004772 005077 011160 CLR 20HMCSR
(1) 004776 005005 MUX3B: CLR RS
(1) 005000 033737 016264 016260 BIT SLMSK,LINSEL ;SELECTED ??
(1) 005006 001417 BEQ MUX3D ;BR IF NOT
(1) 005010 017704 011144 MOV 20HMLSR,R4 ;READ LINE STATUS REGISTER
(1) 005014 117703 011136 MOVB 20HMCSR,R3 ;READ CONTROL STATUS REGISTER
(1) 005020 042703 177760 BIC #177760,R3 ;CLEAR UNWANTED BITS
(1) 005024 020103 CMP R1,R3 ;IF LINE NUMBER=SELECTED LINE NUMBER,
(1) 005026 001002 BNE MUX3C ;EXCEPT REQUEST TO SEND FUNCTION FLIP FLOP
(1) 005030 012705 000004 MOV #RS,RS ;TO BE SET
(1) 005034 020504 MUX3C: CMP RS,R4 ;COMPARE EXPECTED AND RECEIVED
(1) 005036 001403 BNE MUX3D ;RESULTS
(1) 005040 104001 ERRORL ;LINE STATUS ERROR
(1) 005042 104003 SCOPEF
(1) 005044 005046 MUX3D
(1) 005046 052777 000400 011102 MUX3D: BIS #STEP,20HMCSR ;EXAMINE NEXT LINE
(1) 005054 006337 016264 ASL SLMSK ;SHIFT MASK
(1) 005060 005302 DEC R2
(1) 005062 001345 BVE MUX3B
(1) 005064 005005 CLR RS
(1) 005066 010177 011064 MUX3E: MOV R1,20HMCSR
(1) 005072 010103 MOV R1,R3 ;SET LINE COUNTER TO SELECTED LINE
(1) 005074 005077 011060 CLR 20HMLSR ;CLEAR REQUEST TO SEND FLIP FLOP
(1) 005100 105227 000000 INCB #0 ;DELAY FOR CABLE
(1) 005104 001375 BVE ;DITTO
(1) 005106 017704 011046 MOV 20HMLSR,R4 ;READ LINE STATUS REGISTER
(1) 005112 005704 TST R4 ;WAS REQUEST TO SEND FUNCTION FLIP FLOP
(1) 005114 001401 BEQ MUX3F ;Cleared
(1) 005116 104001 ERRORL ;NO, LINE STATUS ERROR
(1) 005120 104003 SCOPEF ;CHECK FOR LOOP ON SAME DATA
(1) 005122 004730 MUX3A
(1) 005124 006337 016262 ASL SELMSK ;SHIFT SELECT MASK
(1) 005130 005201 INC R1 ;SELECT NEXT LINE
(1) 005132 005300 DEC R0 ;DECREMENT LINE COUNT
(1) 005134 001275 BNE MUX3A ;CONTINUE IF NOT DONE
(1) 005136 104002 SCOPE ;CHECK FOR ITERATIONS, LOOP

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915
(1)
(1)
(2) 005140
(1) 005140 005077 011012
(1) 005144 042737 000340 177776
(1) 005152 012700 000020
(1) 005156 012737 000001 016262
(1) 005164 005001
(1) 005166 012777 002000 010762 MUX4A:
(1) 005174 012702 000020
(1) 005200 033737 016262 016260
(1) 005206 001463
(1) 005210 010177 010742
(1) 005214 012777 000010 010736
(1) 005220 012737 000001 016264
(1) 005230 005077 010722
(1) 005234 005005 MUX4B:
(1) 005238 033737 016264 016260
(1) 005244 001417
(1) 005246 017704 010706
(1) 005250 117703 010700
(1) 005254 042703 177760
(1) 005258 00103
(1) 005266 005077 010722
(1) 005272 005004 MUX4C:
(1) 005274 005033
(1) 005276 104001
(1) 005300 104003
(1) 005302 005304
(1) 005304 005777 000400 010644 MUX4D:
(1) 005312 006337 016264
(1) 005316 005302
(1) 005320 001345
(1) 005322 005005
(1) 005324 010177 010626 MUX4E:
(1) 005330 010103
(1) 005332 005077 010622
(1) 005336 105227 000000
(1) 005342 001375
(1) 005344 017704 010610
(1) 005350 005704
(1) 005352 001401
(1) 005354 104001
(1) 005356 104003 MUX4F:
(1) 005360 005166
(1) 005362 006337 016262
(1) 005366 005301
(1) 005370 005300
(1) 005372 001275
(1) 005374 104002
    
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:VERIFY THAT SECONDARY TRANSMIT FUNCTION FLIP-FLOP CAN
 :BE SET AND CLEARED FOR SELECTED LINE

:REFERENCE DESIGNATION
 :CLEAR CONTROL STATUS REGISTER
 :ENABLE INTERRUPTS
 :SET UP TO TEST 16 FUNCTION FLIP-FLOP
 :INIT LINE SELECT MASK
 :START AT LINE 0

:IS THIS LINE SELECTED FOR TEST ?
 :OR IF NOT
 :SELECT LINE TO BE TESTED
 :SET SECONDARY TRANSMIT FUNCTION FLIP-FLOP
 :INIT ANOTHER SELECT MASK

:SELECTED ??
 :OR IF NOT
 :READ LINE STATUS REGISTER
 :READ CONTROL STATUS REGISTER
 :CLEAR UNWANTED BITS
 :IF LINE NUMBER=SELECTED LINE NUMBER,
 :EXCEPT SECONDARY TRANSMIT FUNCTION FLIP FLOP

:TO BE SET
 :COMPARE EXPECTED AND RECEIVED
 :RESULTS
 :LINE STATUS ERROR

:EXAMINE NEXT LINE
 :SHIFT MASK

:SET LINE COUNTER TO SELECTED LINE
 :CLEAR SECONDARY TRANSMIT FLIP FLOP
 :DELAY FOR CABLE
 :DITTO
 :READ LINE STATUS REGISTER
 :WAS SECONDARY TRANSMIT FUNCTION FLIP FLOP
 :CLEARED
 :NO, LINE STATUS ERROR
 :CHECK FOR LOOP ON SAME DATA

:SHIFT SELECT MASK
 :SELECT NEXT LINE
 :DECREMENT LINE COUNT
 :CONTINUE IF NOT DONE
 :CHECK FOR ITERATIONS, LOOP

917									
(1)									:VERIFY THAT CLEAR TO SEND AND CARRIER ARE SET IF "LINE ENABLE"
(1)									:AND TERMINAL ARE SET FOR SELECTED LINE.
(1)									
(2)	005376								:REFERENCE DESIGNATION
(1)	005376	005077	010554		T32:	CLR	20HMCSR		:CLEAR CONTROL REGISTER
(1)	005402	042737	000340	177776	MUXS:	BIC	#340,PS		:ENABLE INTERRUPTS
(1)	005410	012700	000020			MOV	#16.,R0		:SET UP TO TEST 16 LINES
(1)	005414	005301				CLR	R1		:START AT LINE 0
(1)	005416	012737	000001	016262		MOV	#1,SELMSK		:INIT LINE SELECT MASK
(1)	005424	012702	000020		MUXSA:	MOV	#16.,R2		:16 LINES
(1)	005430	033737	016262	016260		BIT	SELMSK,LINSEL		:THIS LINE SELECTED FOR TEST ?
(1)	005436	001454				BEQ	MUXSF		:IF NOT
(1)	005440	010177	010512			MOV	R1,20HMCSR		:SELECT A LINE
(1)	005444	012777	000003	010506		MOV	#LINENA+TRMRDY,20HMLSR		:SET LINE ENABLE +TRMRDY
(1)	005452	005077	010500			CLR	20HMCSR		:CLEAR CONTROL REGISTER
(1)	005456	005305			MUXSB:	CLR	R5		:CLEAR EXPECTED RESULT
(1)	005460	017704	010474			MOV	20HMLSR,R4		:READ LINE STATUS
(1)	005464	117703	010466			MOVB	20HMLSR,R3		:READ LINE NUMBER
(1)	005470	042703	177760			BIC	#177760,R3		:CLEAR UNEXPECTED BITS
(1)	005474	020103				CMP	R1,R3		:IF RECEIVED LINE=SELECTED LINE
(1)	005476	001002				BNE	MUXSC		:EXPECT LINE ENABLE AND
(1)	005500	012705	000143			MOV	#LINENA+TRMRDY+CO+CS,R5		
(1)									:CLEAR TO SEND AND CARRIER ARE SET
(1)	005504	020405			MUXSC:	CMP	R4,R5		:CARRIER EXPECTED AND
(1)	005506	001403				BEQ	MUXSD		:RECEIVED RESULTS
(1)	005510	104001				ERRORL			:LINE STATUS ERROR
(1)	005512	104003				SCOPEF			
(1)	005514	005516				MUXSD			
(1)	005516	052777	000400	010432	MUXSD:	BIS	#STEP,20HMCSR		:UPDATE LINE COUNTER
(1)	005524	005302				DEC	R2		:CONTINUE IF ALL CHECKS
(1)	005526	001353				BNE	MUXSB		:ARE NOT DONE FOR THIS LINE
(1)	005530	012705	000001			MOV	#LINENA,R5		:EXPECT LINE ENABLE
(1)	005534	010103			MUXSE:	MOV	R1,R3		:ON SELECTED LINE
(1)	005536	010177	010414			MOV	R1,20HMCSR		:SELECT LINE
(1)	005542	042777	000002	010410		BIC	#TRMRDY,20HMLSR		:CLEAR TERMINAL
(1)	005550	105227	000000			INCB	#0		:DELAY FOR CABLE
(1)	005554	001375				BNE	#-4		:DITTO
(1)	005556	017704	010376			MOV	20HMLSR,R4		:READ LINE STATUS REGISTER
(1)	005562	020504				CMP	R5,R4		:ONLY LINE ENABLE SHOULD BE
(1)	005564	001401				BEQ	MUXSF		:SET ON THIS LINE
(1)	005566	104001				ERRORL			:LINE STATUS ERROR
(1)	005570	104003			MUXSF:	SCOPEF			:CHECK FOR LOOP ON SAME DATA
(1)	005572	005424				MUXSA			
(1)	005574	005201				INC	R1		:UPDATE LINE NUMBER
(1)	005576	005077	010356			CLR	20HMLSR		:CLEAR LINE STATUS REGISTER
(1)	005602	006337	016262			ASL	SELMSK		:SHIFT MARK TO TEST NEXT LINE
(1)	005606	005300				DEC	R0		:CONTINUE IF ALL LINES NOT
(1)	005610	001305				BNE	MUXSA		:TESTED
(1)	005612	104002				SCOPE			:CHECK FOR ITERATIONS, LOOP

919

```

(1) ;VERIFY THAT RING IS SET IF "LINE ENABLE"
(1) ;AND REQUEST TO SEND ARE SET FOR SELECTED LINE.
(1)
(2) 005614 T33: ;REFERENCE DESIGNATION
(1) 00 514 005077 010336 MUX6: CLR 20HMCSR ;CLEAR CONTROL REGISTER
(1) 00 20 042737 000340 177776 BIC #340,PS ;ENABLE INTERRUPTS
(1) 005626 012700 000020 MOV #16.,R0 ;SET UP TO TEST 16 LINES
(1) 005632 005001 CLR R1 ;START AT LINE 0
(1) 005634 012737 000001 016262 MUX6A: MOV #1,SELMSK ;INIT LINE SELECT MASK
(1) 005642 012702 000020 MOV #16.,R2 ;16 LINES
(1) 005646 033737 016262 016260 BIT SELMSK,LINSEL ;THIS LINE SELECTED FOR TEST ?
(1) 005654 001454 BEQ MUX6F ;BR IF NOT
(1) 005656 010177 010274 MOV R1,20HMCSR ;SELECT A LINE
(1) 00 52 012777 000005 010270 MOV #LINENA+RS,20HMLSR ;SET LINE ENABLE +RS
(1) 00 70 005077 010262 CLR 20HMCSR ;CLEAR CONTROL REGISTER
(1) 005674 005005 MUX6B: CLR R5 ;CLEAR EXPECTED RESULT
(1) 005676 017704 010256 MOV 20HMLSR,R4 ;READ LINE STATUS
(1) 005702 117703 010250 MOVB 20HMCSR,R3 ;READ LINE NUMBER
(1) 005706 042703 177760 BIC #177760,R3 ;CLEAR UNWANTED BITS
(1) 005712 020103 CMP R1,R3 ;IF RECEIVED LINE=SELECTED LINE
(1) 005714 001002 BNE MUX6C ;EXPECT LINE ENABLE AND
(1) 005716 012705 000205 MOV #LINENA+RS+RING,RS ;RING IS SET
(1) MUX6C: CMP R4,R5 ;COMPARE EXPECTED AND
(1) 005724 001403 BEQ MUX6D ;RECEIVED RESULTS
(1) 005726 104001 ERRORL ;LINE STATUS ERROR
(1) 005730 104003 SCOPEF
(1) 005732 005734 MUX6D
(1) 005734 052777 000400 010214 MUX6D: BIS #STEP,20HMCSR ;UPDATE LINE COUNTER
(1) 005742 005302 DEC R2 ;CONTINUE IF ALL CHECKS
(1) 005744 001353 BNE MUX6B ;ARE NOT DONE FOR THIS LINE
(1) 005746 012705 000001 MOV #LINENA,RS ;EXPECT LINE ENABLE
(1) 005752 010103 MUX6E: MOV R1,R3 ;ON SELECTED LINE
(1) 005754 010177 010176 MOV R1,20HMCSR ;SELECT LINE
(1) 005760 042777 000004 010172 BIC #RS,20HMLSR ;CLEAR REQUEST TO SEND
(1) 005766 105227 000000 INCB #0 ;DELAY FOR CABLE
(1) 005772 001375 BNE .-4 ;DITTO
(1) 005774 017704 010160 MOV 20HMLSR,R4 ;READ LINE STATUS REGISTER
(1) 006000 020504 CMP R5,R4 ;ONLY LINE ENABLE SHOULD BE
(1) 005002 001401 BEQ MUX6F ;SET ON THIS LINE
(1) 006004 104001 ERRORL ;LINE STATUS ERROR
(1) 006006 104003 MUX6F: SCOPEF ;CHECK FOR LOOP ON SAME DATA
(1) 006010 005642 MUX6A
(1) 006012 005201 INC R1 ;UPDATE LINE NUMBER
(1) 006014 005077 010140 CLR 20HMLSR ;CLEAR LINE STATUS REGISTER
(1) 006020 006337 016262 ASL SELMSK ;SHIFT MARK TO TEST NEXT LINE
(1) 006024 005300 DEC R0 ;CONTINUE IF ALL LINES NOT
(1) 006026 001305 BNE MUX6A ;TESTED
(1) 006030 104002 SCOPE ;CHECK FOR ITERATIONS, LOOP

```

```

921
(1)
(1)
(1)
(2) 006032
(1) 006032 005077 010120
(1) 006036 042737 000340 177776
(1) 006044 012700 000020
(1) 006050 005001
(1) 006052 012737 000001 016262
(1) 006060 012702 000020
(1) 006054 033737 016262 016260
(1) 006072 001454
(1) 006074 010177 010056
(1) 006100 012777 000011 010052
(1) 006106 005077 010044
(1) 006112 005005
(1) 006114 017704 010040
(1) 006120 117703 010032
(1) 006124 042703 177760
(1) 006130 020103
(1) 006132 001032
(1) 006134 012705 000031
(1)
(1) 006140 020405
(1) 006142 001403
(1) 006144 104001
(1) 006146 104003
(1) 006150 006152
(1) 006152 052777 000400 007776
(1) 006160 005302
(1) 006162 001353
(1) 006164 012705 000001
(1) 006170 010103
(1) 006172 010177 007760
(1) 006176 042777 000010 007754
(1) 006204 105227 000000
(1) 006210 001375
(1) 006212 017704 007742
(1) 006216 020504
(1) 006220 001401
(1) 006222 104001
(1) 006224 104003
(1) 006226 006060
(1) 006230 005201
(1) 006232 005077 007722
(1) 006236 006337 016262
(1) 006242 005300
(1) 006244 001305
(1) 006246 104002

;VERIFY THAT SECONDARY RECEIVE IS SET IF "LINE ENABLE"
;AND SECONDARY TRANSMIT ARE SET FOR SELECTED LINE.

T34:
MUX7: CLR @DHMCSR
      BIC #340,PS
      MOV #16.,R0
      CLR R1
      MOV #1,SELMSK
MUX7A: MOV #16.,R2
      BIT SELMSK,LINSEL
      BEQ MUX7F
      MOV R1,@DHMCSR
      MOV #LINENA+SECTX,@DHMLSR
      CLR @DHMCSR
MUX7B: CLR R5
      MOV @DHMLSR,R4
      MOVB @DHMCSR,R3
      BIC #177760,R3
      CMP R1,R3
      BNE MUX7C
      MOV #LINENA+SECTX+SECRX,R5
MUX7C: CMP R4,R5
      BEQ MUX7D
      ERRORL SCOPEF
      MUX7D
MUX7D: BIS #STEP,@DHMCSR
      DEC R2
      BNE MUX7B
      MOV #LINENA,R5
MUX7E: MOV R1,R3
      MOV R1,@DHMCSR
      BIC #SECTX,@DHMLSR
      INCB #0
      BNE .-4
      MOV @DHMLSR,R4
      CMP R5,R4
      BEQ MUX7F
      ERRORL SCOPEF
MUX7F: MUX7A
      INC R1
      CLR @DHMLSR
      ASL SELMSK
      DEC R0
      BNE MUX7A
      SCOPE

;REFERENCE DESIGNATION
;CLEAR CONTROL REGISTER
;ENABLE INTERRUPTS
;SET UP TO TEST 16 LINES
;START AT LINE 0
;INIT LINE SELECT MASK
;16 LINES
;THIS LINE SELECTED FOR TEST ?
;BR IF NOT
;SELECT A LINE
;SET LINE ENABLE +SECTX
;CLEAR CONTROL REGISTER
;CLEAR EXPECTED RESULT
;READ LINE STATUS
;READ LINE NUMBER
;CLEAR UNWANTED BITS
;IF RECEIVED LINE=SELECTED LINE
;EXPECT LINE ENABLE AND
;SECONDARY RECEIVE IS SET
;COMPARE EXPECTED AND
;RECEIVED RESULTS
;LINE STATUS ERROR
;UPDATE LINE COUNTER
;CONTINUE IF ALL CHECKS
;ARE NOT DONE FOR THIS LINE
;EXPECT LINE ENABLE
;ON SELECTED LINE
;SELECT LINE
;CLEAR SECONDARY TRANSMIT
;DELAY FOR CABLE
;DITTO
;READ LINE STATUS REGISTER
;ONLY LINE ENABLE SHOULD BE
;SET ON THIS LINE
;LINE STATUS ERROR
;CHECK FOR LOOP ON SAME DATA
;UPDATE LINE NUMBER
;CLEAR LINE STATUS REGISTER
;SHIFT MARK TO TEST NEXT LINE
;CONTINUE IF ALL LINES NOT
;TESTED
;CHECK FOR ITERATIONS, LOOP

```



```

923
924
925
926
927 006250
928 006250 005077 007702
929 006254 042737 000340 177776
930 006262 012700 000020
931 006266 012777 000017 007664
932 006274 052777 000400 007654
933 006302 005300
934 006304 001370
935 006306 012737 000001 016262
936 006314 005003
937 006316 012700 000020
938 006322 012777 000000 007626
939 006330 033737 016262 016260
940 006336 001425
941 006340 012777 007612
942 006344 017704 007610
943 006350 000005
944 006354 016704
945 006358 001403
946 006356 104001
947 006350 104003
948 006352 006022
949 006354 000005
950 006366 052777 000001 007564
951 006374 017704 007560
952 006400 020504
953 006402 001403
954 006404 104001
955 006406 104003
956 006410 006022
957 006412 000003
958 006414 005077 007540
959 006420 005337 016262
960 006424 005300
961 006426 001340
962 006430 104002
    
```

```

;VERIFY THAT "CLEAR MULTIPLXER" CLEARS ALL MULTIPLEXER
;FUNCTION FLIP-FLOPS
    
```

```

T35:
MUX8: CLR @DHMCSR
      BIC #340, P5
      MOV #16, R0
MUX8A: MOV #17, @DHMLSR
      BIS #STEP, @DHMCSR
      DEC R0
      BNE MUX8A
      MOV #1, SELMSK
      CLR R3
      MOV #16, R0
MUX8B: MOV #CLRMUX, @DHMCSR
MUX8C: BIT SELMSK, LINSEL
      BEQ MUX8E
      MOV R3, @DHMCSR
      MOV @DHMLSR, R4
      CLR R5
      TST R4
      BEQ MUX8D
      ERRORL
      SCOPEF
      MUX8B
MUX8D: INC R5
      BIS #LINENA, @DHMLSR
      MOV @DHMLSR, R4
      CMP R5, R4
      BEQ MUX8E
      ERRORL
      SCOPEF
      MUX8B
MUX8E: INC R3
      CLR @DHMLSR
      ASL SELMSK
      DEC R0
      BNE MUX8C
      SCOPE
    
```

```

;REFERENCE DESIGNATION
;CLEAR CONTROL REGISTER
;ENABLE INTERRUPTS
;SET UP TO TEST 16 LINES
;WRITE 15 INTO ALL MULTIPLEXER
;FUNCTION FLIPFLOPS

;INIT SELECT MASK
;SET UP FOR 16 LINES

;CLEAR MULTIPLEXER
;SELECTED ??
;BR IF NOT
;SELECT LINE
;READ LINE STATUS REGISTER
;EXPECT 05
;WAS LINE STATUS REGISTER CLEARED

;LINE STATUS ERROR
;CHECK FOR LOOP ON SAME DATA

;EXPECT LINE ENABLE
;SET LINE ENABLE ON SELECTED LINE
;READ LINE STATUS REGISTER
;IS ANYTHING BUT LINE ENABLE SET

;LINE STATUS ERROR
;CHECK FOR LOOP ON SAME DATA

;UPDATE LINE NUMBER
;CLEAR CURRENT LINE
;SHIFT SELECT MASK
;CONTINUE IF ALL LINES NOT
;TESTED
;CHECK FOR ITERATIONS, LOOP
    
```

```

864
865
866
867
868
869
970 006432
971 006432 012777 002000 007516 T36:
972 006440 005077 007512 SCNT1: MOV #CLRMUX,20HMSCR
973 006444 042737 000340 177776 CLR 20HMSCR
974 006452 012700 000020 BIC #340,PS
975 006456 012777 001017 007472 MOV #16,R0
976 006464 012737 000001 016262 MOV #MAINT+17,20HMSCR
977 006472 052777 000400 007456 SCNT1A: MOV #1,SELMSK
978 006500 012777 000001 007452 BIS #STEP,20HMSCR
979 006506 005300 DEC #LINEA,20HMSLR
980 006510 001370 BNE SCNT1A
981 006512 012701 177777 MOV #-1,R1
982 006516 012705 070340 MOV #70340,R5
983 006522 012777 006632 007422 SCNT1C: MOV #SCNT1C,20HMEC
984 006530 013777 177776 007416 MOV PS,20HMLVL
985 006536 012700 000020 MOV #16,R0
986 006542 012777 000117 007406 SCNT1B: MOV #INTENA+17,20HMSCR
987 006550 033737 016262 016260 BIT SELMSK,LINSEL
988 006556 001435 BEQ SCNT1D
989 006560 052737 000340 177776 BIS #340,PS
990 006566 052777 000040 007362 BIS #SCNENA,20HMSCR
991 006574 042737 000340 177776 BIC #340,PS
992 006602 105777 007360 TSTB 20HMSCR
993 006606 100375 BPL .-4
994
995 006610 052737 000340 177776 BIS #340,PS
996 006616 017704 007334 MOV 20HMSCR,R4
997 006622 104000 ERRORC
998 006624 104003 SCOPEF
999 006626 006432 SCNT1
1000 006630 000410 BR SCNT1D
1001 006632 022626 SCNT1C: POP2SP
1002 006634 017704 007316 MOV 20HMSCR,R4
1003 006640 020504 CMP R5,R4
1004 006642 001403 BEQ SCNT1D
1005 006644 104000 ERRORC
1006 006646 104003 SCOPEF
1007 006650 006432 SCNT1
1008 006652 042777 000257 007276 SCNT1D: BIC #SCNENA+DONE+17,20HMSCR
1009 006660 005201 INC R1
1010 006662 150177 007270 BISB R1,20HMSCR
1011 006666 006337 016262 ASL SELMSK
1012 006672 005205 INC R5
1013 006674 005300 DEC R0
1014 006676 001324 BNE SCNT1B
1015 006700 104002 SCOPE

```

```

;WRITE 1'S INTO ALL SCANNER MEMORY LOCATIONS
;SET "LINE ENABLE" FOR ALL LINES
;VERIFY THAT AN INTERRUPT OCCURS FOR EACH LINE

```

```

;REFERENCE DESIGNATION
;CLEAR ALL MULTIPLEXER FLIPFLOPS
;CLEAR CONTROL REGISTER
;ENABLE INTERRUPTS
;SET UP TO WRITE 1'S INTO
;ALL SCANNER MEMORY LOCATION
;INIT SELECT MASK
;WRITE A LOCATION
;LET "LINE ENABLE"

;INIT LINE NO. GEN.
;EXPECT "DONE"+"SCNENA"+"COF"+"CSF"+"SECRXF"
;SET UP LOCAL INTERRUPT SERVICE
;SERVICE AT LEVEL 7

;SET INTERRUPT ENABLE
;SELECTED ??
;BR IF NOT
;LOCK OUT INTERRUPTS
;START SCANNER
;ENABLE INTERRUPTS
;WAIT FOR DONE
;PROGRAM WILL HANG HERE
;IF DONE NEVER SETS
;INTERRUPT DID NOT OCCUR
;ERROR
;CONTROL STATUS ERROR
;CHECK FOR LOOP ON SAME DATA

;INTERRUPT OCCURED, REPOSITION STACK
;READ CONTROL STATUS
;ARE EXPECTED AND RECEIVED
;REGISTERS THE SAME
;NO LINE STATUS ERROR
;CHECK FOR LOOP WITH CURRENT DATA

;CLEAR SCAN ENABLE AND DONE
;GEN NXT LINE NO.
;SET LINE NO. BITS
;SHIFT SELECT MASK
;UPDATE EXPECTED RESULT
;CONTINUE IF NOT DONE

;CHECK FOR ITERATIONS, LOOP

```

```

1017
1018
1019
1020
1021
1022 006702
1023 006702 012700 000020
1024 006706 012777 002000 007242
1025 006714 005077 007236
1026 006720 042737 000340 177776
1027 006726 012737 000001 016262
1028 006734 012777 000017 007216
1029 006742 052777 000400 007206
1030 006750 005300
1031 006752 001370
1032 006754 012777 004000 007174
1033 006762 032777 000020 007166
1034 006770 001374
1035 006772 012700 000020
1036 006776 012701 177777
1037 007002 012705 170340
1038 007006 012777 007112 007136
1039 007014 013777 177776 007132
1040 007022 012777 000117 007126
1041 007030 033737 016262 016260
1042 007036 001435
1043 007040 052737 000340 177776
1044 007046 0 2777 000040 007102
1045 007054 042737 000340 177776
1046 007062 105777 007070
1047 007066 100375
1048
1049 007070 052737 000340 177776
1050 007076 017704 007054
1051 007102 104000
1052 007104 104003
1053 007106 006702
1054 007110 000410
1055 007112 022626
1056 007114 017704 007036
1057 007120 020504
1058 007122 001403
1059 007124 104000
1060 007126 104003
1061 007130 006702
1062 007132 042777 000257 007016
1063 007140 006337 016262
1064 007144 005201
1065 007146 150177 007004
1066 007152 005205
1067 007154 005300
1068 007156 001324
1069 007160 104002

;WRITE 1'S INTO ALL MULTIPLEXER FUNCTION FLIP-FLOPS
;CLEAR SCANNER MEMORY
;VERIFY THAT AN INTERRUPT OCCURS FOR EACH LINE

T37:
SCNT2: MOV #16, R0
MOV #CLRMUX, @DHWCSR
CLR @DHWCSR
BIC #340, PS
MOV #1, SELMSK
SCNT2A: MOV #17, @DHWLSR
BIS #STEP, @DHWCSR
RO
DEC
PNE SCNT2A
MOV #CLRSCN, @DHWCSR
BIT #BUSY, @DHWCSR
BNE
MOV #16, R0
MOV #-1, R1
MOV #170340, R5
MOV #SCNT2C, @DHWVEC
MOV PS, @DHWMLVL
SCNT2B: MOV #INTENA+17, @DHWCSR
BIT SELMSK, LINSEL
REQ SCNT2D
BIS #340, PS
BIS #SCNENA, @DHWCSR
BIC #340, PS
TSTB @DHWCSR
BPL
BIS #340, PS
MOV @DHWCSR, R4
ERRORC
SCOPEF
SCNT2
BR SCNT2D
SCNT2C: POP2SP
MOV @DHWCSR, R4
CMP R5, R1
BEQ SCNT2D
ERRORC
SCOPEF
SCNT2
SCNT2D: BIC #SCNENA+DONE+17, @DHWCSR
ASL SELMSK
INC R1
BISB R1, @DHWCSR
INC R5
DEC RO
BNE SCNT2B
SCOPE

;REFERENCE DESIGNATION
;WRITE 1S INTO ALL
;CLEAR MULTIPLEXER
;MULTIPLEXER FUNCTION
;ENABLE TELETYPE INTERRUPTS
;INIT LINE SELECT MASK
;FLIPFLOPS

;CLEAR SCANNER MEMORY
;WAIT FOR CLEAR CYCLE TO COMPLETE

;SET UP TO TEST 16 LINES
;INIT LINE NO. GENERATOR
;FIRST EXPECTED RESULT
;SET UP LOCAL INTERRUPT RETURN

;SET INTERRUPT ENABLE
;IS THIS LINE SELECTED ??
;BR IF NOT
;LOCK OUT INTERRUPTS
;START SCANNER
;ENABLE INTERRUPTS
;WAIT FOR DONE
;PROGRAM WILL HANG HERE
;IF DONE NEVER SETS
;LOCK OUT INTERRUPTS
;READ CONTROL STATUS
;INTERRUPT DID NOT OCCUR
;CHECK FOR LOOP ON CURRENT DATA

;CONTINUE
;INTERRUPT OCCURED, RESTORE STACK
;READ CONTROL STATUS REGISTER
;COMPARE TO EXPECTED RESULT

;CONTROL STATUS ERROR
;CHECK FOR LOOP ON CURRENT DATA

;CLEAR SCAN ENABLE AND DONE AND LINE NO.
;SHIFT SELECT BIT
;GEN NEW LINE NO.
;SET IT IN CSR
;UPDATE EXPECTED RESULT
;CONTINUE IF ALL
;LINES NOT TESTED
;CHECK FOR ITERATIONS, LOOP
    
```

1071
1078
1079
1080
1081
1082
1083
1084
1085
1094
1095
1096
1097
1098
1099
1100
1101
1102
1103
1104
1105

007162
007162 012737 007202 001722
007170 042737 000340 177776
007176 104004
007200 017261
007202 104013
007204 017314
007206 000000
007210 000017
007212 016240
007214 104004
007216 017256

T100:
STRLIN: MOV #STRLNA,KRET
BIC #340,PS
TYPE
MLINE
STRLNA: INSTRG
MLINEI
0
17
LINE
TYPE
MCRLF

;SINGLE LINE CABLE TEST
;FOR USE WITH MODEM CABLE AND DC11 TEST CONNECTOR

;NOTE: MODEM CONTROL MULTIPLEXER INPUTS SHOULD BE CONNECTED
;TO DISTRIBUTION PANEL VIA DM11-DC

;REFERENCE DESIGNATION
;SET UP FOR NEW LINE SELECTION
;ENABLE INTERRUPTS
;TYPE "SINGLE LINE CABLE TEST"

;GET LINE NUMBER

```

1107
1108
(1)
(1)
(2) 007220
(1) 007220 005077 006732
(1) 007224 042737 000340 177776
(1) 007232 013701 016240
(1) 007236 012777 002000 006712
(1) 007244 012702 000020
(1) 007250 033737 016262 016260
(1) 007256 001463
(1) 007260 010177 006672
(1) 007264 012777 000001 006666
(1) 007272 012737 000001 016264
(1) 007300 005077 006652
(1) 007304 005005
(1) 007306 033737 016264 016260
(1) 007314 001417
(1) 007316 017704 006636
(1) 007322 117703 006630
(1) 007326 042703 177760
(1) 007332 020103
(1) 007334 001002
(1) 007336 012705 000001
(1)
(1) 007342 020504
(1) 007344 001403
(1) 007346 104001
(1) 007350 104003
(1) 007352 007354
(1) 007354 052777 000400 006574
(1) 007362 006337 016264
(1) 007366 005302
(1) 007370 001345
(1) 007372 005005
(1) 007374 010177 006556
(1) 007400 010103
(1) 007402 005077 006552
(1) 007406 105227 000000
(1) 007412 001375
(1) 007414 017704 006540
(1) 007420 005704
(1) 007422 001401
(1) 007424 104001
(1) 007426 104002

;VERIFY THAT LINE ENABLE FUNCTION FLIP-FLOP CAN
;BE SET AND CLEARED FOR SELECTED LINE

T101:
MUX11: CLR @DHMCSR
        BIC #340,PS
        MOV LINE,R1
MUX11A: MOV #CLRMUX,@DHMCSR
        MOV #16,R2
        BIT SELMSK,LINSEL
        BEQ MUX11F
        MOV R1,@DHMCSR
        MOV #LINENA,@DHMLSR
        MOV #1,SLMSK
        CLR @DHMCSR
MUX11B: CLR R5
        BIT SLMSK,LINSEL
        BEQ MUX11D
        MOV @DHMLSR,R4
        MOVB @DHMCSR,R3
        BIC #177760,R3
        CMP R1,R3
        BNE MUX11C
        MOV #LINENA,R5
MUX11C: CMP R5,R4
        BEQ MUX11D
        ERRORL
        SCOPEF
        MUX11D
MUX11D: BIS #STEP,@DHMCSR
        ASL SLMSK
        DEC R2
        BNE MUX11B
        CLR R5
MUX11E: MOV R1,@DHMCSR
        MOV R1,R3
        CLR @DHMLSR
        INCB #0
        BNE -4
        MOV @DHMLSR,R4
        TST R4
        BEQ MUX11F
        ERRORL
MUX11F: SCOPE

;REFERENCE DESIGNATION
;CLEAR CONTROL STATUS REGISTER
;ENABLE INTERRUPTS

;IS THIS LINE SELECTED FOR TEST ?
;BR IF NOT
;SELECT LINE TO BE TESTED
;SET LINE ENABLE FUNCTION FLIP-FLOP
;INIT ANOTHER SELECT MASK

;SELECTED ??
;BR IF NOT
;READ LINE STATUS REGISTER
;READ CONTROL STATUS REGISTER
;CLEAR UNWANTED BITS
;IF LINE NUMBER=SELECTED LINE NUMBER,
;EXCEPT LINE ENABLE FUNCTION FLIP FLOP

;TO BE SET
;COMPARE EXPECTED AND RECEIVED
;RESULTS
;LINE STATUS ERROR

;EXAMINE NEXT LINE
;SHIFT MASK

;SET LINE COUNTER TO SELECTED LINE
;CLEAR LINE ENABLE FLIP FLOP
;DELAY FOR CABLE
;DITTO
;READ LINE STATUS REGISTER
;WAS LINE ENABLE FUNCTION FLIP FLOP
;CLEARED
;NO, LINE STATUS ERROR
;CHECK FOR ITERATIONS, LOOP
    
```

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1110
(1)                                     :VERIFY THAT TERMINAL READY FUNCTION FLIP-FLOP CAN
(1)                                     :BE SET AND CLEARED FOR SELECTED LINE
(2)
(1) 007430 T102:                                     :REFERENCE DESIGNATION
(1) 007430 MUX12: CLR 20HMCSR                       :CLEAR CONTROL STATUS REGISTER
(1) 007434 042737 000340 177776 BIC 2340,PS                       :ENABLE INTERRUPTS
(1) 007442 013701 016240 MOV LINE,R1
(1) 007446 012777 002000 006502 MUX12A: MOV 20CLRMUX,20HMCSR
(1) 007454 012702 000020 MOV 216,R2
(1) 007460 033737 016262 016260 BIT SELMSK,LINSEL           :IS THIS LINE SELECTED FOR TEST ?
(1) 007466 001463 BEQ MUX12F                       :BR IF NOT
(1) 007470 010177 006462 MOV R1,20HMCSR           :SELECT LINE TO BE TESTED
(1) 007474 012777 000002 006456 MOV 20TRMROY,20HMLSR       :SET TERMINAL READY FUNCTION FLIP-FLOP
(1) 007478 012737 000001 016264 MOV 21,SLMSK           :INIT ANOTHER SELECT MASK
(1) 007482 005077 006442 CLR 20HMCSR
(1) 007486 005005 MUX12B: CLR RS
(1) 007490 033737 016264 016260 BIT SLMSK,LINSEL           :SELECTED ??
(1) 007494 001417 BEQ MUX12D                       :BR IF NOT
(1) 007498 017704 006426 MOV 20HMLSR,R4           :READ LINE STATUS REGISTER
(1) 007502 117703 006420 MOV2 20HMCSR,R3           :READ CONTROL STATUS REGISTER
(1) 007506 042703 177760 BIC 2177760,R3           :CLEAR UNWANTED BITS
(1) 007510 020103 CMP R1,R3
(1) 007514 001002 BNE MUX12C
(1) 007518 012705 000002 MOV 20TRMROY,R5           :IF LINE NUMBER=SELECTED LINE NUMBER,
:EXCEPT TERMINAL READY FUNCTION FLIP' FLOP
(1)
(1) 007552 020504 MUX12C: CMP RS,R4
(1) 007554 001403 BEQ MUX12D
(1) 007556 104001 ERRORL
(1) 007560 104003 SCOPEF
(1) 007562 007564 MUX12D
(1) 007564 052777 000400 006364 MUX12D: BIS 20STEP,20HMCSR           :EXAMINE NEXT LINE
(1) 007572 006337 016264 ASL SLMSK
(1) 007576 005302 DEC R2
(1) 007600 001345 BNE MUX12B
(1) 007602 005005 CLR RS
(1) 007604 010177 006346 MUX12E: MOV R1,20HMCSR
(1) 007610 010103 MOV R1,R3
(1) 007612 005077 006342 CLR 20HMLSR
(1) 007616 105227 000000 INCB 20
(1) 007622 001375 BNE 20-4
(1) 007624 017704 006330 MOV 20HMLSR,R4
(1) 007630 005704 TST R4
(1) 007632 001401 BEQ MUX12F
(1) 007634 104001 ERRORL
(1) 007636 104002 MUX12F: SCOPE
:SET LINE COUNTER TO SELECTED LINE
:CLEAR TERMINAL READY FLIP FLOP
:DELAY FOR CABLE
:BITTO
:READ LINE STATUS REGISTER
:WAS TERMINAL READY FUNCTION FLIP FLOP
:CLEARED
:NO, LINE STATUS ERROR
:CHECK FOR ITERATIONS, LOOP
    
```

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1112                                     ;VERIFY THAT REQUEST TO SEND FUNCTION FLIP-FLOP CAN
(1)                                     ;BE SET AND CLEARED FOR SELECTED LINE
(1)
(2) 007640 T103: REFERENCE DESIGNATION
(1) 007640 005077 006312 MUX13: CLR 20HMCSR ;CLEAR CONTROL STATUS REGISTER
(1) 007644 042737 000340 177776 BIC 0340,PS ;ENABLE INTERRUPTS
(1) 007652 013701 016240 MOV LINE,R1
(1) 007656 012777 002000 006272 MUX13A: MOV 0CLRMUX,20HMCSR
(1) 007664 012702 000020 MOV 016,R2
(1) 007670 033737 016262 016260 BIT SELMSK,LINSEL ;IS THIS LINE SELECTED FOR TEST ?
(1) 007676 001463 BEQ MUX13F ;OR IF NOT
(1) 007700 010177 006252 MOV R1,20HMCSR ;SELECT LINE TO BE TESTED
(1) 007704 012777 000004 006246 MOV 0RS,20HMLSR ;SET REQUEST TO SEND FUNCTION FLIP-FLOP
(1) 007712 012737 000001 016264 MOV 01,SLMSK ;INIT ANOTHER SELECT MASK
(1) 007720 005077 006232 CLR 20HMCSR
(1) 007724 005305 MUX13B: CLR RS
(1) 007726 033737 016264 016260 BIT SLMSK,LINSEL ;SELECTED ??
(1) 007734 001417 BEQ MUX130 ;OR IF NOT
(1) 007736 017704 006216 MOV 20HMLSR,R4 ;READ LINE STATUS REGISTER
(1) 007742 117703 006210 MOV 20HMCSR,R3 ;READ CONTROL STATUS REGISTER
(1) 007746 042703 177760 BIC 0177760,R3 ;CLEAR UN-TESTED BITS
(1) 007752 020103 CMP R1,R3 ;IF LINE NUMBER=SELECTED LINE NUMBER
(1) 007754 001002 BNE MUX13C ;EXCEPT REQUEST TO SEND FUNCTION FLIP FLOP
(1) 007756 012705 000004 MOV 0RS,RS ;TO BE SET
(1) 007762 020504 MUX13C: CMP RS,R4 ;COMPARE EXPECTED AND RECEIVED
(1) 007764 001403 BEQ MUX130 ;RESULTS
(1) 007766 104001 ERRORL ;LINE STATUS ERROR
(1) 007770 104003 SCOPEF
(1) 007772 007774 MUX13D
(1) 007774 052777 000400 006154 MUX13D: BIS 0STEP,20HMCSR ;EXAMINE NEXT LINE
(1) 010002 006337 016264 ASL SLMSK ;SHIFT MASK
(1) 010006 005302 DEC R2
(1) 010010 001345 ENE MUX13B
(1) 010012 005005 CLR RS
(1) 010014 010177 006136 MUX13E: MOV R1,20HMCSR
(1) 010020 010103 MOV R1,R3 ;SET LINE COUNTER TO SELECTED LINE
(1) 010022 005077 006132 CLR 20HMLSR ;CLEAR REQUEST TO SEND FLIP FLOP
(1) 010026 105227 000000 INCB 00 ;DELAY FOR CABLE
(1) 010032 001375 BNE 0-4 ;DITTO
(1) 010034 017704 006120 MOV 20HMLSR,R4 ;READ LINE STATUS REGISTER
(1) 010040 005704 TST R4 ;WAS REQUEST TO SEND FUNCTION FLIP FLOP
(1) 010042 001401 BEQ MUX13F ;CLEARED
(1) 010044 104001 ERRORL ;NO, LINE STATUS ERROR
(1) 010046 104002 MUX13F: SCOPE ;CHECK FOR ITERATIONS, LOOP
    
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1114 ;VERIFY THAT SECONDARY TRANSMIT FUNCTION FLIP-FLOP CAN
(1) ;BE SET AND CLEARED FOR SELECTED LINE
(1)
(2) 010050 T104: ;REFERENCE DESIGNATION
(1) 010050 005077 006102 MUX14: CLR 20HMCSR ;CLEAR CONTROL STATUS REGISTER
(1) 010054 042737 000340 177776 BIC #340,PS ;ENABLE INTERRUPTS
(1) 010062 013701 016240 MOV LINE,R1
(1) 010066 012777 002500 006062 MUX14A: MOV #CLRAUX,20HMCSR
(1) 010074 012702 000020 MOV #16,R2
(1) 010100 033737 016262 016260 BIT SELMSK,LINSEL ;IS THIS LINE SELECTED FOR TEST ?
(1) 010106 001463 BEQ MUX14F ;BR IF NOT
(1) 010110 010177 006042 MOV R1,20HMCSR ;SELECT LINE TO BE TESTED
(1) 010114 012777 000010 006036 MOV #SECTX,20HMLSR ;SET SECONDARY TRANSMIT FUNCTION FLIP-FLOP
(1) 010122 012737 000001 016264 MOV #1,SLMSK ;INIT ANOTHER SELECT MASK
(1) 010130 005077 006022 CLR 20HMCSR
(1) 010134 005005 MUX14B: CLR R5
(1) 010136 033737 016264 016260 BIT SLMSK,LINSEL ;SELECTED ??
(1) 010144 001417 BEQ MUX14D ;BR IF NOT
(1) 010146 017704 006006 MOV 20HMLSR,R4 ;READ LINE STATUS REGISTER
(1) 010152 117703 006000 MOVB 20HMCSR,R3 ;READ CONTROL STATUS REGISTER
(1) 010156 042703 177760 BIC #177760,R3 ;CLEAR UNWANTED BITS
(1) 010162 020103 CMP R1,R3 ;IF LINE NUMBER=SELECTED LINE NUMBER
(1) 010164 001002 BNE MUX14C ;EXCEPT SECONDARY TRANSMIT FUNCTION FLIP FLOP
(1) 010166 012705 000010 MOV #SECTX,R5
(1)
(1) 010172 020504 MUX14C: CMP R5,R4 ;TO BE SET
(1) 010174 001403 BEQ MUX14D ;COMPARE EXPECTED AND RECEIVED
(1) 010176 104001 ERRORL ;RESULTS
(1) 010200 104003 SCOPEF ;LINE STATUS ERROR
(1) 010202 010204 MUX14D
(1) 010204 052777 000400 005744 MUX14D: BIS #STEP,20HMCSR ;EXAMINE NEXT LINE
(1) 010212 006337 016264 ASL SLMSK ;SHIFT MASK
(1) 010216 005302 DEC R2
(1) 010220 001345 BNE MUX14B
(1) 010222 005005 CLR R5
(1) 010224 010177 005726 MUX14E: MOV R1,20HMCSR
(1) 010230 010103 MOV R1,R3 ;SET LINE COUNTER TO SELECTED LINE
(1) 010232 005077 005722 CLR 20HMLSR ;CLEAR SECONDARY TRANSMIT FLIP FLOP
(1) 010236 105227 000000 INCB #0 ;DELAY FOR CABLE
(1) 010242 001375 BNE .-4 ;DITTO
(1) 010244 017704 005710 MOV 20HMLSR,R4 ;READ LINE STATUS REGISTER
(1) 010250 005704 TST R4 ;WAS SECONDARY TRANSMIT FUNCTION FLIP FLOP
(1) 010252 001401 BEQ MUX14F ;CLEARED
(1) 010254 104001 ERRORL ;NO LINE STATUS ERROR
(1) 010256 104002 MUX14F: SCOPE ;CHECK FOR ITERATIONS, LOOP

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1116

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(1)                                     ;VERIFY THAT CLEAR TO SEND AND CARRIER ARE SET IF "LINE ENABLE"
(1)                                     ;AND TERMINAL ARE SET FOR SELECTED LINE.
(1)
(1)
(2) 010260                               T105:                               ;REFERENCE DESIGNATION
(1) 010260 005077 005672                MUX15: CLR 20HMC SR                ;CLEAR CONTROL REGISTER
(1) 010264 042737 000340 177776        BIC #340,PS                       ;ENABLE INTERRUPTS
(1) 010272 013701 016240                MOV LINE,R1
(1) 010276 012702 000020                MUX15A: MOV #16,R2                ;16 LINES
(1) 010302 033737 016262 016260        BIT SELMSK,LINSEL                 ;THIS LINE SELECTED FOR TEST ?
(1) 010310 001454                        BEQ MUX15F                         ;BR IF NOT
(1) 010312 010177 005640                MOV R1,20HMC SR                   ;SELECT A LINE
(1) 010316 012777 000003 005634        MOV #LINE NA+TRMRDY,20HMLSR       ;SET LINE ENABLE +TRMRDY
(1) 010324 005077 005626                CLR 20HMC SR                       ;CLEAR CONTROL REGISTER
(1) 010330 000005                        MUX15B: CLR RS                     ;CLEAR EXPECTED RESULT
(1) 010332 017704 005622                MOV 20HMLSR,R4                    ;READ LINE STATUS
(1) 010336 117703 005614                MOVB 20HMC SR,R3                   ;READ LINE NUMBER
(1) 010342 042703 177760                BIC #177760,R3                    ;CLEAR UNWANTED BITS
(1) 010346 020103                        CMP R1,R3                          ;IF RECEIVED LINE=SELECTED LINE
(1) 010350 001002                        BNE MUX15C                          ;EXPECT LINE ENABLE AND
(1) 010352 012705 000143                MOV #LINE NA+TRMRDY+CO+CS,RS      ;CLEAR TO SEND AND CARRIER ARE SET
(1)                                     MUX15C: CMP R4,RS                  ;COMPARE EXPECTED AND
(1) 010356 020405                        BEQ MUX15D                          ;RECEIVED RESULTS
(1) 010360 001403                        ERRORL                               ;LINE STATUS ERROR
(1) 010362 104001                        SCOPEF
(1) 010364 104003                        MUX15D
(1) 010366 010370                        MUX15D: BIS #STEP,20HMC SR         ;UPDATE LINE COUNTER
(1) 010370 052777 000400 005560        DEC R2                             ;CONTINUE IF ALL CHECKS
(1) 010376 005302                        BNE MUX15B                          ;ARE NOT DONE FOR THIS LINE
(1) 010400 001353                        MOV #LINE NA,RS                     ;EXPECT LINE ENABLE
(1) 010402 012705 000001                MUX15E: MOV R1,R3                  ;ON SELECTED LINE
(1) 010406 010103                        MOV R1,20HMC SR                     ;SELECT LINE
(1) 010410 010177 005542                BIC #TRMRDY,20HMLSR                ;CLEAR TERMINAL
(1) 010414 042777 000002 005536        INCB #0                             ;DELAY FOR CABLE
(1) 010422 105227 000000                BNE #4                              ;DITTO
(1) 010426 001375                        MOV 20HMLSR,R4                       ;READ LINE STATUS REGISTER
(1) 010430 017704 005524                CMP RS,R4                           ;ONLY LINE ENABLE SHOULD BE
(1) 010434 020504                        BEQ MUX15F                          ;SET ON THIS LINE
(1) 010436 001401                        ERRORL                               ;LINE STATUS ERROR
(1) 010440 104001                        MUX15F: SCOPE                       ;CHECK FOR ITERATIONS, LOOP
(1) 010442 104002

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1120

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(1)                                     ;VERIFY THAT SECONDARY RECEIVE IS SET IF "LINE ENABLE"
(1)                                     ;AND SECONDARY TRANSMIT ARE SET FOR SELECTED LINE.
(1)
(2) 010630                               T107: REFERENCE DESIGNATION
(1) 010630 005077 005322 MUX17: CLR 20HMCSR ;CLEAR CONTROL REGISTER
(1) 010634 042737 000340 177776 BIC 2340,PS ;ENABLE INTERRUPTS
(1) 010642 013701 016240 MOV LINE,R1
(1) 010646 012702 000020 MUX17A: MOV 216,R2 ;16 LINES
(1) 010652 033737 016262 016260 BIT SELMSK,LINSEL ;THIS LINE SELECTED FOR TEST ?
(1) 010660 001454 BEQ MUX17F ;BR IF NOT
(1) 010662 010177 005270 MOV R1,20HMCSR ;SELECT A LINE
(1) 010666 012777 000011 005264 MOV 2LINENA+SECTX,20HMLSR ;SET LINE ENABLE +SECTX
(1) 010674 005077 005256 CLR 20HMCSR ;CLEAR CONTROL REGISTER
(1) 010700 005005 MUX17B: CLR RS ;CLEAR EXPECTED RESULT
(1) 010702 017704 005252 MOV 20HMLSR,R4 ;READ LINE STATUS
(1) 010706 117703 005244 MOV2 20HMCSR,R3 ;READ LINE NUMBER
(1) 010712 042703 177760 BIC 2177760,R3 ;CLEAR UNWANTED BITS
(1) 010716 020103 CMP R1,R3 ;IF RECEIVED LINE=SELECTED LINE
(1) 010720 001002 BNE MUX17C ;EXPECT LINE ENABLE AND
(1) 010722 012705 000031 MOV 2LINENA+SECTX+SECRX,RS ;SECONDARY RECEIVE IS SET
(1) 010726 020405 MUX17C: CMP R4,RS ;COMPARE EXPECTED AND
(1) 010730 001403 BEQ MUX17D ;RECEIVED RESULTS
(1) 010732 104001 ERRORL ;LINE STATUS ERROR
(1) 010734 104003 SCOPEF
(1) 010736 010740 MUX17D
(1) 010740 052777 000400 005210 MUX17D: BIS 2STEP,20HMCSR ;UPDATE LINE COUNTER
(1) 010746 005302 DEC R2 ;CONTINUE IF ALL CHECKS
(1) 010750 001353 BNE MUX17B ;ARE NOT DONE FOR THIS LINE
(1) 010752 012705 000001 MOV 2LINENA,RS ;EXPECT LINE ENABLE
(1) 010756 010103 MUX17E: MOV R1,R3 ;ON SELECTED LINE
(1) 010760 010177 005172 MOV R1,20HMCSR ;SELECT LINE
(1) 010764 042777 000010 005166 BIC 2SECTX,20HMLSR ;CLEAR SECONDARY TRANSMIT
(1) 010772 105227 000000 INCB #0 ;DELAY FOR CABLE
(1) 010776 001375 BNE -4 ;DITTO
(1) 011000 017704 005154 MOV 20HMLSR,R4 ;READ LINE STATUS REGISTER
(1) 011004 020504 CMP RS,R4 ;ONLY LINE ENABLE SHOULD BE
(1) 011006 001401 BEQ MUX17F ;SET ON THIS LINE
(1) 011010 104001 ERRORL ;LINE STATUS ERROR
(1) 011012 104002 MUX17F: SCOPE ;CHECK FOR ITERATIONS, LOOP

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1122
1123
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1125
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1128
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1133
1134
1143 011014
1144 011014 000005
1145 011016 012777 000100 005136
1146 011024 012737 000340 177776
1147 011032 104004
1148 011034 016510
1149 011036 012737 011054 012576
1150 011044 012737 011052 001722
1151 011052 104017
1152
1153 011054 104020
1154
1155 011056 011066
1156 011060 011062
1157 011062 104012
1158 011064 000772
1159
1160
1161
1162
1163
1164
1165 011066 104021
1166
1167
1168
1169 011070 011106
1170
1171 011072 011076
1172
1173 011074 011102
1174
1175 011076 104014
1176 011100 000207
1177 011102 104014
1178 011104 000762

;MODEM CONTROL ON LINE TEST USING 103A TYPE MODEMS
;ANSWER STATION TO BE OPERATED IN AUTO-ANSWER MODE
;THIS TEST VERIFIES THE CONNECT AND DISCONNECT SEQUENCES
;USING THE MODEM CONTROL TO CONTROL 103A TYPE MODEMS

;NOTE: IF THE DM11-AA IS NOT CONNECTED TO THE
;DISTRIBUTION PANEL, AN M974 DM11 MAINTENANCE JUMPER
;SHOULD BE INSTALLED IN SLOT B1 OR B3 OF THE DISTRIBUTION
;PANNEL TO PREVENT A POSSIBLE LONG SPACE
;DISCONNECT FROM HANGING UP THE MODEM

T200:
ST103A: RESET ;REFERENCE DESIGNATION
MOV #INTENA, JTKCSR ;INITIALIZE INTERFACE
MOV #340, PS ;DISABLE ALL INTERRUPTS
TYPE "103A MODEM CONNECT-
MT103T ;DISCONNECT TEST"
MOV #T103A, FATRET ;SET UP FOR FATAL ERROR
MOV #ST1038, KRET ;SET UP FOR LINE CHANGE
ST1038: GETLNS ;INPUT ORIGINATE AND
;AND ANSWER LINE NUMBERS
T103A: SETUP ;SET UP TO RECEIVE INTERRUPTS
;WAIT FOR RING
T103B ;GO HERE IF RING OK
T103A1: ERROR ;GO HERE IF NO RING
BR ST1038 ;NO RING WITHIN 5 MINUTES
;SELECT NEW LINES AND REDIAL

;CHECK FOR RING INTERRUPT ON SELECTED ANSWER LINE
;IF AN INCORRECT TRANSITION OCCURS, THE PROGRAM
;WILL TYPE AN ERROR MESSAGE, AND THE OPERATOR
;WILL BE REQUESTED TO RESELECT LINES AND REDIAL

T1038: CKRING ;CHECK FOR RING INTERRUPT
;ONLY ON ANSWER LINE
;AND NO TRANSITIONS ON
;ORIGINATE LINE
T103C ;GO HERE IF TRANSITIONS
;ARE CORRECT
T10381 ;GO HERE IF INCORRECT
;TRANSITION ON ANSWER LINE
T10382 ;GO HERE IF INCORRECT TRANSITION
;ON ORIGINATE LINE
T10381: ERRORT ;TRANSITION ERROR ON ANSWER LINE
RTS PC ;CONTINUE CHECKING
T10382: ERRORT ;TRANSITION ERROR ON ORIGINATE LINE
BR ST1038 ;RESELECT LINES AND REDIAL

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1180									
1181									:SET TERMINAL READY ON SELECTED ANSWER LINE
1182									:WAIT FOR TRANSITIONS TO OCCUR ON SELECTED LINES
1183									
1184	011106	013777	016244	005042	T103C:	MOV	LINANS,	ZOHMCSR	:SET LINE COUNTER TO
1185									:ANSWER LINE NUMBER
1186	011114	052777	000002	005036		BIS	#TRMRDY,	ZOHMLSR	:SET TERMINAL READY ON
1187									:SELECTED ANSWER LINE
1188	011122	104022				WAITRN			:WAIT FOR TRANSITIONS TO OCCUR
1189									
1190									:CHECK FOR CORRECT STATUS AND TRANSITIONS ON
1191									:SELECTED ORIGINATE AND ANSWER LINES
1192									
1193	011124	104023				CKTRAN			:CHECK TRANSITIONS AND
1194									:STATUS ON SELECTED
1195									:ANSWER AND ORIGINATE LINES
1196	011126	000143				CO+CS+LINENA+TRMRDY			:EXPECT CARRIER, CLEAR TO SEND,
1197									:LINE ENABLE AND TERMINAL
1198									:READY STATUS BITS SET ON
1199									:ANSWER LINE
1200	011130	000143				CO+CS+LINENA+TRMRDY			:EXPECT CARRIER, CLEAR TO SEND,
1201									:LINE ENABLE AND TERMINAL
1202									:READY STATUS BITS ON
1203									:ORIGINATE LINE
1204	011132	100006				RINGF+XCO+XCS			:EXPECT CARRIER, CLEAR TO SEND
1205									:AND POSSIBLE RING TRANSITIONS
1206									:ON ANSWER LINE
1207	011134	000006				XCO+XCS			:EXPECT CARRIER AND CLEAR
1208									:TO SEND TRANSITIONS ON
1209									:ORIGINATE LINE
1210	011136	011150				T103D1			:GO HERE ON ANSWER LINE STATUS ERROR
1211									
1212	011140	011154				T103D2			:GO HERE ON ORIGINATE LINE STATUS ERROR
1213	011142	011160				T103D3			:GO HERE ON ANSWER LINE TRANSITION ERROR
1214	011144	011164				T103D4			:GO HERE ON ORIGINATE LINE TRANSITION ERROR
1215	011146	011170				T103E			:GO TO NEXT TEST IF NO ERRORS
1216	011150	104015				T103D1: ERRORS			:ANSWER LINE STATUS ERROR
1217	011152	000207				RTS	PC		:CONTINUE CHECKING
1218	011154	104015				T103D2: E. JAS			:ORIGINATE LINE STATUS ERROR
1219	011156	000207				RTS	PC		:CONTINUE CHECKING
1220	011160	104014				T103D3: ERRORT			:ANSWER LINE TRANSITION ERROR
1221	011162	000207				RTS	PC		:CONTINUE CHECKING
1222	011164	104014				T103D4: ERRORT			:ORIGINATE LINE TRANSITION ERROR
1223	011166	000207				RTS	PC		:CONTINUE CHECKING

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1225
1226
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1231
1232
1233
1234
1235 011170 104004          T103E: TYPE          ;TYPE "SET SW01=1 TO
1236 011172 016753          MDISC          ;TEST DISCONNECT"
1237 011174 012737 000340 177776  MOV          #340,PS  ;LOCK OUT INTERRUPTS
1238 011202 012777 012620 004742  MOV          #TRNTYP,@DHMVEC ;SET UP TO DETECT TRANSITIONS
1239
1240 011210 012737 011230 016272  MOV          #T103ES,RNGRET ;BEFORE DISCONNECT SEQUENCE STARTS
1241
1242 011216 012777 000140 004732  MOV          #SCNENA+INTENA,@DHMCSR ;SET SCAN ENABLE AND INTERRUPT ENABLE
1243 011224 005037 177776          CLR          PS      ;ALLOW INTERRUPTS
1244 011230 032737 000002 177570  T103ES: BIT          #2,SWR  ;WAIT FOR SW00=1, BEFORE
1245
1246 011236 001774          BEQ          T103ES  ;STARTING DISCONNECT SEQUENCE
1247 011240 012737 000340 177776  MOV          #340,PS  ;START DISCONNECT SEQUENCE
1248 011246 005077 004704          CLR          @DHMCSR ;CLEAR CONTROL REGISTER
1249 011252 013777 016242 004676  MOV          LINORG,@DHMCSR ;SET LINE COUNTER TO SELECTED ORIGINATE LINE
1250 011260 042777 000002 004672  BIC          #TRMADY,@DHMLSR ;SET TERMINAL READY ON SELECTED LINE
1251 011266 104022          WAITRN        ;WAIT FOR TRANSITIONS TO OCCUR

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1253									
1254									
1255									
1256									
1257	011270	104023		CKTRAN					
1258									
1259									
1260	011272	000003		LINENA+TRMRDY					
1261									
1262									
1263	011274	000001		LINENA					
1264									
1265	011276	000006		XCO+XCS					
1266									
1267									
1268	011300	000006		XCO+XCS					
1269									
1270									
1271	011302	011314		T103E1					
1272									
1273	011304	011320		T103E2					
1274	011306	011324		T103E3					
1275	011310	011330		T103E4					
1276	011312	011334		T103EN					
1277	011314	104015		T103E1: ERRORS					
1278	011316	000207		RTS	PC				
1279	011320	104015		T103E2: ERRORS					
1280	011322	000207		RTS	PC				
1281	011324	104014		T103E3: ERRORT					
1282	011326	000207		RTS	PC				
1283	011330	104014		T103E4: ERRORT					
1284	011332	000207		RTS	PC				
1285									
1286	011334			T201:					
1287	011334	104004		T103EN: TYPE					
1288	011336	016675		MT103A					
1289	011340	005037	016200	CLR	TSTNO				
1290	011344	000137	011052	JMP	ST103B				
1291									

;CHECK FOR CORRECT STATUS AND TRANSITIONS ON SELECTED
 ;ORIGINATE AND ANSWER LINES

;CHECK TRANSITIONS AND
 ;STATUS ON SELECTED
 ;ANSWER AND ORIGINATE LINES
 ;EXPECT LINE ENABLE AND
 ;TERMINAL READY STATUS BITS
 ;SET ON ANSWER LINE
 ;EXPECT LINE ENABLE STATUS BIT
 ;SET ON ORIGINATE LINE
 ;EXPECT CARRIER AND CLEAR
 ;TO SEND TRANSITIONS ON
 ;ANSWER LINE
 ;EXPECT CARRIER AND CLEAR
 ;TO SEND TRANSITIONS ON
 ;ORIGINATE LINE
 ;GO HERE ON ANSWER LINE STATUS ERROR
 ;GO HERE ON ORIGINATE LINE STATUS ERROR
 ;GO HERE ON ANSWER LINE TRANSITION ERROR
 ;GO HERE ON ORIGINATE LINE TRANSITION ERROR
 ;GO TO NEXT TEST IF NO ERRORS
 ;ANSWER LINE STATUS ERROR
 ;CONTINUE CHECKING
 ;ORIGINATE LINE STATUS ERROR
 ;CONTINUE CHECKING
 ;ANSWER LINE TRANSITION ERROR
 ;CONTINUE CHECKING
 ;ORIGINATE LINE TRANSITION ERROR
 ;CONTINUE CHECKING
 ;REFERENCE DESIGNATION
 ;TYPE "103A TEST COMPLETE"
 ;CLEAR TEST NUMBER FOR LOOPING
 ;SELECT NEW LINE NUMBERS AND
 ;RESTART TEST

```

1293
1294
1295
1296
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1298
1299
1300
1301
1310 011350
1311 011350 000005
1312 011352 012777 000100 004602
1313 011360 012737 000340 177776
1314 011366 104004
1315 011370 016561
1316 011372 012737 011410 012576
1317 011400 012737 011406 001722
1318 011406 104017
1319
1320 011410 104020
1321
1322 011412 011422
1323 011414 011416
1324 011416 104012
1325 011420 000772
1326
1327
1328
1329
1330
1331
1332 011422 104021
1333
1334
1335
1336 011424 011442
1337
1338 011426 011432
1339
1340 011430 011436
1341
1342 011432 104014
1343 011434 000207
1344 011436 104014
1345 011440 000762

;MODEM CONTROL ON LINE TEST USING 202C TYPE MODEMS
;ANSWER STATION TO BE OPERATED IN AUTO-ANSWER MODE
;THIS TEST VERIFIES THE CONNECT AND DISCONNECT SEQUENCES
;USING THE MODEM CONTROL TO CONTROL 202C TYPE MODEMS

;ALSO TESTED ARE LINE TURN-AROUND AND
;SECONDARY TRANSMIT-SECONDARY RECEIVE

T300:
ST202A: RESET
MOV #INTENA,ATKCSR
MOV #340,PS
TYPE
MT202T
MOV #T202A,FATRET
MOV #ST202B,KRET
ST202B: GETLNS
T202A: SETUP
T202B
T202A1: ERROR
BR ST202B
T202B: CKRING
T202C
T202B1
T202B2
T202B1: ERROR
RTS PC
T202B2: ERROR
BR ST202B

;REFERENCE DESIGNATION
;INITIALIZE INTERFACE
;DISABLE ALL INTERRUPTS
;TYPE "202C MODEM CONNECT-
;DISCONNECT TEST"
;SET UP FOR FATAL ERROR
;SET UP FOR LINE CHANGE
;INPUT ORIGINATE AND
;ANSWER LINE NUMBERS
;SET UP TO RECEIVE INTERRUPTS
;WAIT FOR RING
;GO HERE IF RING OK
;GO HERE IF NO RING
;NO RING WITHIN 5 MINUTES
;SELECT NEW LINES AND REDIAL

;CHECK FOR RING INTERRUPT ON SELECTED ANSWER LINE
;IF AN INCORRECT TRANSITION OCCURS, THE PROGRAM
;WILL TYPE AN ERROR MESSAGE, AND THE OPERATOR
;WILL BE REQUESTED TO RESELECT LINES AND REDIAL

;CHECK FOR RING INTERRUPT
;ONLY ON ANSWER LINE
;AND NO TRANSITIONS ON
;ORIGINATE LINE
;GO HERE IF TRANSITIONS
;ARE CORRECT
;GO HERE IF INCORRECT
;TRANSITION ON ANSWER LINE
;GO HERE IF INCORRECT
;TRANSITION ON ORIGINATE LINE
;ANSWER LINE TRANSITION ERROR
;CONTINUE CHECKING
;ORIGINATE LINE TRANSITION ERROR
;RESELECT LINES AND REDIAL

```


M05

1347						:SET TERMINAL READY ON SELECTED ANSWER LINE	
1348						:SET REQUEST TO SEND ON SELECTED ORIGINATE LINE	
1349						:WAIT FOR TRANSITIONS TO OCCUR ON SELECTED LINES	
1350							
1351							
1352	011442	013777	016244	004506	T2020:	MOV LINANS, J0HMCSR	:SET LINE COUNTER TO ANSWER LINE
1353	011450	052777	000002	004502		BIS TRMRDY, J0HMLSR	:SET TERMINAL READY ON ANSWER LINE
1354	011456	013777	016242	004472	T2020:	MOV LINORG, J0HMCSR	:SET LINE COUNTER TO ORIGINATE LINE
1355	011464	052777	000004	004466		BIS RS, J0HMLSR	:SET REQUEST TO SEND ON ORIGINATE LINE
1356	011472	104022				WAITRN	:WAIT FOR TRANSITIONS TO OCCUR
1357							
1358						:CHECK FOR CORRECT STATUS AND TRANSITIONS ON	
1359						:SELECTED ORIGINATE AND ANSWER LINES	
1360							
1361	011474	104023				CKTRAN	:CHECK TRANSITIONS AND STATUS
1362							:ON SELECTED ANSWER AND
1363							:ORIGINATE LINES
1364	011476	000103				CO+LINENA+TRMRDY	:EXPECT CARRIER, LINE ENABLE
1365							:AND TERMINAL READY STATUS
1366							:BITS SET ON ANSWER LINE
1367	011500	000147				RS+CO+CS+LINENA+TRMRDY	:EXPECT REQUEST TO SEND, CLEAR
1368							:TO SEND, CARRIER, LINE ENABLE
1369							:AND TERMINAL READY STATUS BITS
1370							:SET ON ORIGINATE LINE
1371	011502	100004				RINGF+XCO	:EXPECT CARRIER AND POSSIBLE
1372							:RING TRANSITIONS ON
1373							:ANSWER LINE
1374	011504	000006				XCO+XCS	:EXPECT CARRIER AND CLEAR
1375							:TO SEND TRANSITIONS ON
1376							:ORIGINATE LINE
1377	011506	011520				T20201	:GO HERE ON ANSWER LINE STATUS ERROR
1378	011510	011524				T20202	:GO HERE ON ORIGINATE LINE STATUS ERROR
1379	011512	011530				T20203	:GO HERE ON ANSWER LINE STATUS ERROR
1380	011514	011534				T20204	:GO HERE ON ORIGINATE LINE TRANSITION ERROR
1381	011516	011540				T2020E	:GO TO NEXT TEST IF NO ERRORS
1382	011520	104015				T20201: ERRORS	:ANSWER LINE STATUS ERROR
1383	011522	000207				RTS PC	:CONTINUE CHECKING
1384	011524	104015				T20202: ERRORS	:ORIGINATE LINE STATUS ERROR
1385	011526	000207				RTS PC	:CONTINUE CHECKING
1386	011530	104014				T20203: ERROR	:ANSWER LINE TRANSITION ERROR
1387	011532	000207				RTS PC	:CONTINUE CHECKING
1388	011534	104014				T20204: ERROR	:ORIGINATE LINE TRANSITION ERROR
1389	011536	000207				RTS PC	:CONTINUE CHECKING

N05

1391									
1392									
1393									
1394									
1395	011540	013777	016244	004410	T202E:	MOV	LINANS,JOHMLSR		
1396	011546	052777	000010	004404		BIS	#SECTX,JOHMLSR		
1397	011554	104022				WAITRN			
1398									
1399									
1400									
1401									
1402	011556	104023				CKTRAN			
1403									
1404									
1405	011560	000133				SECTX+CO+LINENA+TRMRDY+SECRX			
1406									
1407									
1408									
1409	011562	000167				SECRX+RS+CO+CS+LINENA+TRMRDY			
1410									
1411									
1412									
1413									
1414	011564	000001				XSCRX			
1415									
1416	011566	000001				XSCRX			
1417									
1418	011570	011602				T202E1			
1419	011572	011606				T202E2			
1420	011574	011612				T202E3			
1421	011576	011616				T202E4			
1422	011600	011622				T202F			
1423	011602	104015			T202E1:	ERRORS			
1424	011604	000207				RTS	PC		
1425	011606	104015			T202E2:	ERRORS			
1426	011610	000207				RTS	PC		
1427	011612	104014			T202E3:	ERRORT			
1428	011614	000207				RTS	PC		
1429	011616	104014			T202E4:	ERRORT			
1430	011620	000207				RTS	PC		

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;SET SECONDARY TRANSMIT ON ANSWER LINE
;WAIT FOR TRANSITIONS TO OCCUR ON SELECTED LINES
;SET LINE COUNTER TO ANSWER LINE
;SET SECONDARY RECEIVE ON ANSWER LINE
;WAIT FOR TRANSITIONS TO OCCUR
;CHECK FOR CORRECT STATUS AND TRANSITIONS ON
;SELECTED ORIGINATE AND ANSWER LINES
;CHECK TRANSITIONS AND STATUS
;ON SELECTED ANSWER AND
;ORIGINATE LINES
;EXPECT SECONDARY TRANSMIT
;SECONDARY RECEIVE, CARRIER
;LINE ENABLE AND TERMINAL READY
;STATUS BITS SET ON ANSWER LINE
;EXPECT SECONDARY RECEIVE
;REQUEST TO SEND, CLEAR TO SEND
;CARRIER, LINE ENABLE AND
;TERMINAL READY STATUS BITS
;SET ON ORIGINATE LINE
;EXPECT SECONDARY RECEIVE
;TRANSITION ON ANSWER LINE
;EXPECT SECONDARY RECEIVE
;TRANSITION ON ORIGINATE LINE
;GO HERE ON ANSWER LINE STATUS ERROR
;GO HERE ON ORIGINATE LINE STATUS ERROR
;GO HERE ON ANSWER LINE TRANSITION ERROR
;GO HERE ON ORIGINATE LINE TRANSITION ERROR
;GO TO NEXT TEST IF NO ERRORS
;ANSWER LINE STATUS ERROR
;CONTINUE CHECKING
;ORIGINATE LINE STATUS ERROR
;CONTINUE CHECKING
;ANSWER LINE TRANSITION ERROR
;CONTINUE CHECKING
;ORIGINATE LINE TRANSITION ERROR
;CONTINUE CHECKING
  
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173
    :DPOP REQUEST TO SEND ON ORIGINATE LINE
    :DPOP SECONDARY TRANSMIT ON ANSWER LINE
    :WAIT FOR TRANSITIONS TO OCCUR ON SELECTED LINES

011622 013777 016242 004326 T202F: MOV LINORG,204MCSR :SET LINE COUNTER TO ORIGINATE LINE
011630 042777 000004 004322 BIC BRS,204MLSR :DROP REQUEST TO SEND
011636 013777 016244 004312 MOV LINANS,204MCSR :SET LINE COUNTER TO ANSWER LINE
011644 042777 000010 004306 BIC #SECTX,204MLSR :DROP SECONDARY RECEIVE
011652 104022 WAITRN :WAIT FOR TRANSITIONS TO OCCUR

    :CHECK FOR CORRECT STATUS AND TRANSITIONS ON
    :SELECTED ORIGINATE AND ANSWER LINES

011654 104023 CKTRAN :CHECK TRANSITIONS AND STATUS
    :ON SELECTED ANSWER AND
    :ORIGINATE LINES
011656 000003 LINENA+TRMRDY :EXPECT LINE ENABLE AND
    :TERMINAL READY STATUS BITS
    :SET ON ANSWER LINE
011660 000003 LINENA+TRMRDY :EXPECT LINE ENABLE AND
    :TERMINAL READY STATUS BITS
    :SET ON ORIGINATE LINE
011662 000005 XCO+XSCRX :EXPECT CARRIER AND SECONDARY
    :RECEIVE TRANSITIONS ON
    :ANSWER LINE
011664 000007 XCO+XCS+XSCRX :EXPECT CARRIER, CLEAR TO SEND
    :AND SECONDARY RECEIVE
    :TRANSITIONS ON ORIGINATE LINE
011666 011700 T202F2 :GO HERE ON ANSWER LINE STATUS ERROR
011670 011704 T202F3 :GO HERE ON ORIGINATE LINE STATUS ERROR
011672 011710 T202F4 :GO HERE ON ANSWER LINE TRANSITION ERROR
011674 011714 T202F5 :GO HERE ON ORIGINATE LINE TRANSITION ERROR
011676 011720 T202G :GO TO NEXT TEST IF NO ERRORS
011700 104015 T202F2: ERRORS :ANSWER LINE STATUS ERROR
011702 000207 RTS PC :CONTINUE CHECKING
011704 104015 T202F3: ERRORS :ORIGINATE LINE STATUS ERROR
011706 000207 RTS PC :CONTINUE CHECKING
011710 104014 T202F4: ERRORT :ANSWER LINE TRANSITION ERROR
011712 000207 RTS PC :CONTINUE CHECKING
011714 104014 T202F5: ERRORT :ORIGINATE LINE TRANSITION ERROR
011716 000207 RTS PC :CONTINUE CHECKING
    
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1475								
1476								
1477								
1478								
1479								
1480	011720	013777	016244	004230	T202G:	MOV	LINANS, ZOHMCSR	;SET LINE COUNTER TO ANSWER LINE
1481	011726	052777	000004	004224		BIS	ORS, ZOHMLSR	;SET REQUEST TO SEND
1482	011734	104022				WAITRN		;WAIT FOR TRANSITIONS TO OCCUR
1483								
1484								
1485								
1486								
1487	011736	104023				CKTRAN		;CHECK TRANSITIONS AND STATUS
1488								;ON SELECTED ANSWER AND
1489								;ORIGINATE LINES
1490	011740	000147				RS+CO+CS+LINENA+TRMRDY		;EXPECT LINE ENABLE, TERMINAL
1491								;READY, REQUEST TO SEND, CLEAR
1492								;TO SEND, AND CARRIER
1493								;STATUS BITS SET ON ANSWER LINE
1494	011742	000103				CO+LINENA+TRMRDY		;EXPECT LINE ENABLE, TERMINAL
1495								;READY AND CARRIER STATUS
1496								;BITS SET ON ORIGINATE LINE
1497	011744	000006				XCO+XCS		;EXPECT CARRIER AND CLEAR
1498								;TO SEND TRANSITIONS ON
1499								;ANSWER LINE
1500	011746	000004				XCO		;EXPECT CARRIER TRANSITION
1501								;ON ORIGINATE LINE
1502	011750	011762				T202G1		;GO HERE ON ANSWER LINE STATUS ERROR
1503	011752	011766				T202G2		;GO HERE ON ORIGINATE LINE STATUS ERROR
1504	011754	011772				T202G3		;GO HERE ON ANSWER LINE TRANSITION ERROR
1505	011756	011776				T202G4		;GO HERE ON ORIGINATE LINE TRANSITION ERROR
1506	011760	012002				T202H		;GO TO NEXT TEST IF NO ERRORS
1507	011762	104015				T202G1: ERRORS		;ANSWER LINE STATUS ERROR
1508	011764	000207				RTS	PC	;CONTINUE TESTING
1509	011766	104015				T202G2: ERRORS		;ORIGINATE LINE STATUS ERROR
1510	011770	000207				RTS	PC	;CONTINUE TESTING
1511	011772	104014				T202G3: ERROR		;ANSWER LINE TRANSITION ERROR
1512	011774	000207				RTS	PC	;CONTINUE TESTING
1513	011776	104014				T202G4: ERROR		;ORIGINATE LINE TRANSITION ERROR
1514	012000	000207				RTS	PC	;CONTINUE TESTING

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1516
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1519
1520 012002 013777 016242 004146 T202H: MOV LINORG,20HMLSR ;SET LINE COUNTER TO ORIGINATE LINE
1521 012010 052777 000010 004142 BIS #SECTX,20HMLSR ;SET SECONDARY TRANSMIT
1522 012016 104022 WAITRN ;WAIT FOR TRANSITIONS TO OCCUR
1523
1524
1525 ;CHECK FOR CORRECT STATUS AND TRANSITIONS ON
1526 ;SELECTED ORIGINATE AND ANSWER LINES
1527 012020 104023 CKTRAN ;CHECK TRANSITIONS AND STATUS
1528 ;ON SELECTED ANSWER AND
1529 ;ORIGINATE LINES
1530 012022 000167 RS+CS+CO+LINENA+TRMROY+SECRX ;EXPECT LINE ENABLE, TERMINAL
1531 ;READY, REQUEST TO SEND, CLEAR
1532 ;TO SEND, CARRIER AND SECONDARY
1533 ;RECEIVE STATUS BITS SET
1534 ;ON ANSWER LINE
1535 012024 000133 SECTX+CO+LINENA+TRMROY+SECRX ;EXPECT LINE ENABLE, TERMINAL
1536 ;READY, CARRIER, SECONDARY
1537 ;TRANSMIT AND SECONDARY
1538 ;RECEIVE STATUS BITS SET
1539 ;ON ORIGINATE LINE
1540 012026 000001 XSCRX ;EXPECT SECONDARY RECEIVE
1541 ;TRANSITION ON ANSWER LINE
1542 012030 000001 XSCRX ;EXPECT SECONDARY RECEIVE
1543 ;TRANSITION ON ORIGINATE LINE
1544 012032 012044 T202H2 ;GO HERE ON ANSWER LINE STATUS ERROR
1545 012034 012050 T202H3 ;GO HERE ON ORIGINATE LINE STATUS ERROR
1546 012036 012054 T202H4 ;GO HERE ON ANSWER LINE TRANSITION ERROR
1547 012040 012060 T202H5 ;GO HERE ON ORIGINATE LINE TRANSITION ERROR
1548 012042 012064 T202I ;GO TO NEXT TEST IF NO ERRORS
1549 012044 104015 T202H2: ERRORS ;ANSWER LIN STATUS ERROR
1550 012046 000207 RTS PC ;CONTINUE CHECKING
1551 012050 104015 T202H3: ERRORS ;ORIGINATE LINE STATUS ERROR
1552 012052 000207 RTS PC ;CONTINUE CHECKING
1553 012054 104014 T202H4: ERRORT ;ANSWER LINE TRANSITION ERROR
1554 012056 000207 RTS PC ;CONTINUE CHECKING
1555 012060 104014 T202H5: ERRORT ;ORIGINATE LINE TRANSITION ERROR
1556 012062 000207 RTS PC ;CONTINUE CHECKING

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E06

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1558
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1561
1562 012064 013777 016244 004064 T2021: MOV LINANS,20HMLSR ;SET LINE COUNTER TO ANSWER LINE
1563 012072 042777 000004 004060 BIC #RS,20HMLSR ;CLEAR REQUEST TO SEND
1564 012100 013777 016242 004050 MOV LINORG,20HMLSR ;SET LINE COUNTER TO ORIGINATE LINE
1565 012106 042777 000010 004044 BIC #SECTX,20HMLSR ;CLEAR SECONDARY TRANSMIT
1566 012114 104022 WAITRN ;WAIT FOR TRANSITIONS TO OCCUR
1567
1568 ;CHECK FOR CORRECT STATUS AND TRANSITIONS ON
1569 ;SELECTED ORIGINATE AND ANSWER LINES
1570
1571 012116 104023 CKTRAN ;CHECK TRANSITIONS AND STATUS
1572 ;ON SELECTED ANSWER AND
1573 ;ORIGINATE LINES
1574 012120 000003 LINENA+TRMRDY ;EXPECT LINE ENABLE AND
1575 ;TERMINAL READY STATUS BITS SET
1576 ;ON ANSWER LINE
1577 012122 000003 LINENA+TRMRDY ;EXPECT LINE ENABLE AND
1578 ;TERMINAL READY STATUS BITS
1579 ;SET ON ORIGINATE LINE
1580 012124 000007 XCO+XCS+XSCRX ;EXPECT CARRIER, CLEAR TO SEND
1581 ;AND SECONDARY RECEIVE TRANSITIONS
1582 ;ON ANSWER LINE
1583 012126 000005 XCO+XSCRX ;EXPECT CARRIER AND SECONDARY
1584 ;RECEIVE TRANSITIONS ON
1585 ;ORIGINATE LINE
1586 012130 012142 T20212 ;GO HERE ON ANSWER LINE STATUS ERROR
1587 012132 012146 T20213 ;GO HERE ON ORIGINATE LINE STATUS ERROR
1588 012134 012152 T20214 ;GO HERE ON ANSWER LINE TRANSITION ERROR
1589 012136 012156 T20215 ;GO HERE ON ORIGINATE LINE TRANSITION ERROR
1590 012140 012162 T202J ;GO TO NEXT TEST IF NO ERRORS
1591 012142 104015 T20212: ERRORS ;ANSWER LINE STATUS ERROR
1592 012144 000207 RTS PC ;CONTINUE CHECKING
1593 012146 104015 T20213: ERRORS ;ORIGINATE LINE STATUS ERROR
1594 012150 000207 RTS PC ;CONTINUE CHECKING
1595 012152 104014 T20214: ERRORS ;ANSWER LINE TRANSITION ERROR
1596 012154 000207 RTS PC ;CONTINUE CHECKING
1597 012156 104014 T20215: ERRORS ;ORIGINATE LINE TRANSITION ERROR
1598 012160 000207 RTS PC ;CONTINUE CHECKING
  
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1600
1601
1602
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1605
1606
1607
1608
1609
1610 012162 104004          T202J: TYPE
1611 012164 016753          MDISC
1612 012166 012737 000340 177776 MOV #340,PS
1613 012174 012777 012620 003750 MOV #TRNTYP,JOHMVEC
1614 012202 012737 012222 016272 MOV #T202JS,RNGRET
1615
1616 012210 012777 000140 003740 MOV #SCNENA+INTENA,JOHMCSR
1617
1618 012216 005037 177776 T202JS: CLR PS
1619 012232 032737 000002 177570 BIT #2,SWR
1620 012230 001774          BEQ T202JS
1621
1622
1623
1624 012232 012737 000340 177776 MOV #340,PS
1625 012240 005077 003712          CLR JOHMCSR
1626 012244 013777 016242 003704 MOV LINORG,JOHMCSR
1627 012252 042777 000002 003700 BIC #TRMROD,JOHMLSR
1628 012260 104024          WAITS
1629 012262 104022          WAITRN

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;SET UP TO TEST DISCONNECT SEQUENCE
;THE PROGRAM WILL REQUEST THE OPERATOR TO SET SW01=1
;TO INITIATE THE DISCONNECT SEQUENCE
;THE OPERATOR MAY MANUALLY SWITCH THE DATA SETS FROM
;DATA TO TALK MODE AS MANY TIMES AS DESIRED
;BEFORE THE SWITCH SEETIN IS MADE
;ANY TRANSITIONS DETECTED DURING THIS TIME WILL BE
;REPORTED BY TYPEOUT

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```

;TYPE "SET SW01=1 TO
;TEST DISCONNECT"
;LOCK OUT INTERRUPTS
;SET UP TO DETECT TRANSITIONS
;SET UP DUMMY RETURN FOR RING
;FROM RING INTERRUPT
;ENABLE LINE SCANNER
;START SCANNER
;ENABLE INTERRUPTS
;MONITOR SW01 FOR DISCONNECT

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;DISCONNECT SEQUENCE REQUESTED

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;LOCK OUT INTERRUPTS
;STOP SCANNER
;SET LINE COUNTER TO SELECTED ORIGINATE LINE
;SET TERMINAL READY ON SELECTED LINE
;DELAY
;WAIT FOR TRANSITIONS TO OCCUR

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1631							
1632							
1633							
1634							
1635	012264	104023		CKTRAN			
1636							
1637							
1638	012266	000003		LINENA+TRMRDY			
1639							
1640							
1641	012270	000001		LINENA			
1642							
1643	012272	000000		0			
1644							
1645	012274	000000		0			
1646							
1647	012276	012310		T202J1			
1648	012300	012314		T202J2			
1649	012302	012320		T202J3			
1650	012304	012324		T202J4			
1651	012306	012330		T202JN			
1652	012310	104015		T202J1: ERRORS			
1653	012312	000207		RTS	PC		
1654	012314	104015		T202J2: ERRORS			
1655	012316	000207		RTS	PC		
1656	012320	104014		T202J3: ERROR			
1657	012322	000207		RTS	PC		
1658	012324	104014		T202J4: ERROR			
1659	012326	000207		RTS	PC		
1660							
1661	012330	104004		T202JN: TYPE			
1662	012332	016724		MT202A			
1663	012334	000137	011406	JMP	ST202B		
1664							

;CHECK FOR CORRECT STATUS AND TRANSITIONS ON SELECTED
 ;ORIGINATE AND ANSWER LINES

;CHECK TRANSITIONS AND STATUS
 ;ON SELECTED ANSWER AND
 ;ORIGINATE LINES
 ;EXPECT LINE ENABLE AND
 ;TERMINAL READY STATUS BITS
 ;SET ON ANSWER LINE
 ;EXPECT LINE ENABLE STATUS
 ;BIT SET ON ORIGINATE LINE
 ;EXPECT NO TRANSITIONS ON
 ;ANSWER LINE
 ;EXPECT NO TRANSITIONS ON
 ;ORIGINATE LINE
 ;GO HERE IF ANSWER LINE STATUS ERROR
 ;GO HERE IF ORIGINATE LINE STATUS ERROR
 ;GO HERE IF ANSWER LINE TRANSITION ERROR
 ;GO HERE IF ORIGINATE LINE TRANSITIONS ERROR
 ;GO TO END OF TEST IF NO ERRORS
 ;ANSWER LINE STATUS ERROR
 ;CONTINUE CHECKING
 ;ORIGINATE LINE STATUS ERROR
 ;CONTINUE CHECKING
 ;ANSWER LINE TRANSITION ERROR
 ;CONTINUE CHECKING
 ;ORIGINATE LINE TRANSITION ERROR
 ;CONTINUE CHECKING

;TYPE "202C TEST COMPLETE"

;GET NEW LINE NUMBERS
 ;RESTART TEST


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1666
1667
1668 ; DETECT AND RECORD TRANSITIONS ON SELECTED
1669 ; ORIGINATE AND ANSWER LINES
1670
1671 ; TRANSITION DATA IS STORED IN LOCATIONS ANSFLG AND ORGFLG
1672 ; FOR ANSWER AND ORIGINATE LINES RESPECTIVELY
1673 ; FORMAT OF DATA IS (FOR BOTH LINES)
1674
1675 ; BIT0=1, SECONDARY RECEIVE CAUSED INTERRUPT
1676 ; BIT1=1, CLEAR TO SEND CAUSED INTERRUPT
1677 ; BIT2=1, CARRIER CAUSED INTERRUPT
1678 ; BIT3=1, RING CAUSED INTERRUPT
1679 012340 017704 003612 TRANS: MOV 20HMSCR,R4 ; GET LINE NUMBER AND
1680 ; INTERRUPT FLAGS
1681 012344 010405 MOV R4,R5
1682 012346 042705 177760 BIC #177760,R5 ; EXTRACT LINE NUMBER
1683 012352 023705 016242 CMP LINORG,R5 ; DID ORIGINATE LINE INTERRUPT
1684 012356 001411 BEQ ORGTR ; IF YES, SERVICE
1685 012360 023705 016244 CMP LINANS,R5 ; DID ANSWER LINE INTERRUPT
1686 012364 001443 BEQ ANSTR ; IF YES, SERVICE
1687 012366 010577 003564 MOV R5,20HMSCR
1688 012372 017703 003562 MOV 20HMSR,R3
1689 012376 104016 ERRORN ; INTERRUPT ON INCORRECT LINE
1690 012400 000471 BR FATEX
1691
1692 ; RECORD TRANSITIONS FOR ORIGINATE LINE
1693
1694 012402 032704 100000 ORGTR: BIT #RINGF,R4 ; IF RING CAUSED INTERRUPT,
1695 012406 001403 BEQ ORGTR1 ; SET RING TRANSITION BIT
1696 012410 052737 000010 016250 BIS #10,ORGFLG
1697 012416 032704 040000 ORGTR1: BIT #COF,R4 ; IF CARRIER CAUSED INTERRUPT
1698 012422 001403 BEQ ORGTR2 ; SET CARRIER TRANSITION BIT
1699 012424 052737 000004 016250 BIS #4,ORGFLG
1700 012432 032704 020000 ORGTR2: BIT #CSF,R4 ; IF CLEAR TO SEND
1701 ; CAUSED INTERRUPT
1702 012436 001403 BEQ ORGTR3 ; SET CLEAR TO SEND
1703 ; TRANSITION BIT
1704 012440 052737 000002 016250 BIS #2,ORGFLG
1705 012446 032704 010000 ORGTR3: BIT #SECRXF,R4 ; IF SECONDARY RECEIVE
1706 ; CAUSED INTERRUPT
1707 012452 001403 BEQ ORGTR4 ; SET SECONDARY RECEIVE
1708 012454 052737 000001 016250 BIS #1,ORGFLG ; TRANSITION BIT
1709 012462 032704 170000 ORGTR4: BIT #RINGF+COF+CSF+SECRXF,R4
1710 ; IF NO INTERRUPT FLAGS SET
1711 012466 001044 BNE TRANEX ; EXIT TRANSITION DETECTION
1712 012470 104016 ORGTRR: ERRORN
1713 012472 000434 BR FATEX

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1715
1716
1717 ;RECORD TRANSITIONS FOR ANSWER LINE
1718 012474 032704 100000 ANSTR: BIT #RINGF,R4 ;IF RING CAUSED INTERRUPT,
1719 012500 001403 BEQ ANSTR1 ;SET RING TRANSITION BIT
1720 012502 052737 000010 016246 BIS #10,ANSFLG
1721 012510 032704 040000 ANSTR1: BIT #COF,R4 ;IF CARRIER CAUSED INTERRUPT
1722 012514 001403 BEQ ANSTR2 ;SET CARRIER TRANSITION BIT
1723 012516 052737 000004 016246 BIS #4,ANSFLG
1724 012524 032704 020000 ANSTR2: BIT #CSF,R4 ;IF CLEAR TO SEND
1725 ;CAUSED INTERRUPT
1726 012530 001403 BEQ ANSTR3 ;SET CLEAR TO SEND
1727 ;TRANSITION BIT
1728 012532 052737 000002 016246 BIS #2,ANSFLG
1729 012540 032704 010000 ANSTR3: BIT #SECRXF,R4 ;IF SECONDARY RECEIVE
1730 ;CAUSED INTERRUPT
1731 012544 001403 BEQ ANSTR4 ;SET SECONDARY RECEIVE
1732 012546 052737 000001 016246 BIS #1,ANSFLG ;TRANSITION BIT
1733 012554 032704 170000 ANSTR4: BIT #RINGF+COF+CSF+SECRXF,R4
1734 ;IF NO INTERRUPT FLAGS SET
1735 012560 001007 BNE TRANEX ;EXIT TRANSITION DETECTION
1736 012562 104016 ANSTRR: ERRORN
1737 012564 005037 016200 FATEX: CLR TSTNO
1738 012570 022626 POP2SP
1739 012572 000177 000000 FATRET: JMP #FATRET
1740 012576 000000
1741
1742 ;EXIT TRANSITION DETECTION
1743
1744 012600 005704 TRANEX: TST R4 ;IF RING FLAG WAS SET
1745 012602 100002 BPL .+6 ;SET UP SPECIAL RETURN
1746 012604 013716 016272 MOV RINGRET,(SP)
1747 012610 012777 000140 003340 TRANX1: MOV #SCNENA+INTENA,@OHMCSR ;RESTART SCANNER
1748 012616 000002 RTI
1749
1750 ;TYPE TRANSITION DATA AND RETURN
1751
1752 012620 017737 003332 013620 TRNTYP: MOV @OHMCSR,DATA1
1753 012626 017737 003326 013622 MOV @OHMLSR,DATA2
1754 012634 104004 TYPE
1755 012636 017365 MTRNDCT
1756 012640 104006 OCTASC
1757 012642 012646 TRNTAB
1758 012644 000761 BR TRANX1
1759 012646 000002 TRNTAB: 2
1760 012650 000006 6
1761 012652 013620 DATA1
1762 012654 000003 3
1763 012656 013622 DATA2
  
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1765
1766                                     ;INPUT ORIGINATE AND ANSWER LINES FROM TELETYPE KEYBOARD
1767
1768 012660 000005 GETLIN: RESET
1769 012662 012777 000100 003272 MOV #INTENA,@TKCSR
1770 012670 104013 INSTRG ;TYPE "ORIGINATE LINE-"
1771 012672 016632 MSELOR ;AND GET LINE NUMBER
1772 012674 000000 0
1773 012676 000017 17
1774 012700 016242 LINORG
1775 012702 104013 INSTRG ;TYPE "ANSWER LINE-"
1776 012704 016656 MSELANS ;AND GET LINE NUMBER
1777 012706 000000 0
1778 012708 000017 17
1779 012710 016242 LINANS
1780 012714 104013 TYPE
1781 012716 017256 MCRLF
1782 012720 000002 RTI ;RETURN TO CALLING ROUTINE
1783
1784                                     ;INITIALIZE INTERFACE
1785
1786 012722 000005 SETUPS: RESET
1787 012724 012777 000100 003230 MOV #INTENA,@TKCSR
1788 012732 012737 000340 177776 MOV #340,PS ;LOCK OUT ALL INTERRUPTS
1789 012740 011605 MOV (SP),RS
1790 012742 012537 013630 MOV (RS),NXTTS
1791 012746 012537 013610 MOV (RS)+,ERR1
1792 012752 010516 MOV RS,(SP)
1793 012754 012777 006000 003174 MOV #CLRSCN+CLRMUX,@OHMCSR ;CLEAR LINE SCANNER AND MULTIPLEXER
1794 012762 032777 000020 003166 SETUP1: BIT #BJSY,@OHMCSR ;WAIT FOR SCANNER TO CLEAR
1795 012770 001374 BNE SETUP1
1796 012772 005037 016172 CLR ERRFLG
1797
1798                                     ;ENABLE SELECTED LINES
1799                                     ;SET TERMINAL READY ON SELECTED ORIGINATE LINE
1800
1801 012776 013777 016242 003152 SETUP2: MOV LINORG,@OHMCSR ;SET UP TO ENABLE ORIGINATE LINE
1802                                     ;ORIGINATE LINE NUMBER
1803 013004 012777 000003 003146 MOV #LINENA+TRMRDY,@OHMLSR ;SET LINE ENABLE AND
1804                                     ;TERMINAL READY ON ORIGINATE LINE
1805 013012 013777 016244 003136 MOV LINANS,@OHMCSR ;SET LINE COUNTER TO ANSWER LINE
1806 013020 012777 000001 003132 MOV #LINENA,@OHMLSR ;SET LINE ENABLE ON ANSWER LINE
1807
1808                                     ;REQUEST OPERATOR TO DIAL SELECTED ANSWER TERMINAL
1809                                     ;SET UP TO RECEIVE INTERRUPTS
1810                                     ;START LINE SCANNER
1811
1812 013026 012777 012340 003116 MOV #TRANS,@OHMVEC ;SET UP INTERRUPT VECTOR
1813                                     ;FOR TRANSITION DETECTION
1814 013034 012777 000340 003112 MOV #340,@OHMLVL ;SET UP INTERRUPT SERVICE LEVEL
1815 013042 012777 000140 003106 MOV #SCNENA+INTENA,@OHMCSR ;START SCANNER, ENABLE INTERRUPTS
1816 013050 005037 016246 CLR ANSFLG ;CLEAR TRANSITION DETECTED FLAGS
1817 013054 005037 016250 CLR ORGFLG
1818 013060 012737 013110 016272 MOV #SETUP4,RNGRET ;SET UP RETURN FROM

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1819                                     ;DETECTION OF RING INTERRUPT
1820 013066 104004 TYPE                                ;REQUEST OPERATOR TO DIAL
1821 013070 016452 DIALM
1822 013072 005037 177776 CLR PS                                ;CLEAR PROCESSOR STATUS WORD
1823 013076 005037 016252 CLR TIME1                          ;CLEAR TIMER
1824 013102 012737 001000 016254 MOV #1000, TIME2          ;SET UP FOR 5 MINUTE DELAY
1825 013110 005737 016246 SETUP4: TST ANSFLG                ;IF TRANSITION HAS OCCURED,
1826 013114 001014 BNE SETUPB                               ;EXIT WAIT LOOP
1827 013116 005737 016250 TST ORGFLG
1828 013122 001011 BNE SETUPB
1829 013124 005237 016252 INC TIME1                          ;ALLOW OPERATOR 5 MINUTES TO DIAL
1830 013130 001367 BNE SETUP4
1831 013132 005337 016254 DEC TIME2
1832 013136 001364 BNE SETUP4
1833 013140 022626 POP2SP
1834 013142 000177 000442 JMP @ERR1
1835 013146 022626 SETUPB: POP2SP
1836 013150 000177 000454 JMP @NXTTS
1837 013154 012766 000340 000002 MOV #340, +2(SP)
1838 013162 000002 RTI
1839
1840                                     ;CHECK FOR RING INTERRUPT ON SELECTED ANSWER LINE
1841
1842 013164 011605 CKRNG: MOV (SP), R5
1843 013166 012537 013630 MOV (R5)+, NXTTS
1844 013172 012537 013610 MOV (R5)+, ERR1
1845 013176 012537 013612 MOV (R5)+, ERR2
1846 013202 010516 MOV R5, (SP)
1847 013204 012705 000010 MOV #10, R5                                ;EXPECT RING ONLY ON ANSWER LINE
1848 013210 013704 016246 MOV ANSFLG, R4                          ;GET ACTUAL TRANSITION DATA
1849 013214 013703 016244 MOV LINANS, R3                          ;SET UP LINE NUMBER
1850 013220 020504 CMP R5, R4                                ;DID RING CAUSE INTERRUPT
1851 013222 001402 BEQ CKRNG1                               ;ON ANSWER LINE
1852 013224 004777 000360 JSR PC, @ERR1
1853 013230 005005 CKRNG1: CLR R5
1854 013232 013704 016250 MOV ORGFLG, R4
1855 013236 013703 016242 MOV LINORG, R3
1856 013242 005704 TST R4                                ;IF TRANSITION OCCURED
1857 013244 001403 BEQ CKRNG2                               ;ON ORIGINATE LINE, ERROR
1858 013246 022626 POP2SP
1859 013250 000177 000336 JMP @ERR2
1860 013254 022626 CKRNG2: POP2SP
1861 013256 000177 000346 JMP @NXTTS

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1863									
1864	013262	005037	016246		WAITR:	CLR	ANSFLG		
1865	013266	005037	016250			CLR	ORGFLG		
1866	013272	012777	012340	002652		MOV	#TRANS, ZOHMVEC		
1867	013300	012737	013320	016272		MOV	#WAITR, RINGRET		
1868									; SET UP FOR RETURN
1869	013306	012777	000140	002642		MOV	#SCNENA+INTENA, ZOHMCSR		; FROM RING DETECTION
1870	013314	005037	177776			CLR	PS		; START SCANNER
1871	013320	005037	016252		WAITRR:	CLR	TIME1		
1872	013324	012737	000025	016254		MOV	#25, TIME2		
1873	013332	005237	016252		WAITR1:	INC	TIME1		; WAIT FOR TRANSITIONS OF
1874	013336	001375				BNE	WAITR1		; CARRIER AND CLEAR TO SEND
1875	013340	005337	016254			DEC	TIME2		
1876	013344	001372				BNE	WAITR1		
1877	013346	000002				RTI			
1878									
1879									
1890									; CHECK FOR CORRECT STATUS AND TRANSITIONS ON
1881									; SELECTED ORIGINATE AND ANSWER LINES
1882	013350	012737	000340	177776	CKTRN:	MOV	#340, PS		; LOCK OUT FURTHER INTERRUPTS
1883	013356	005077	002574			CLR	ZOHMCSR		; STOP LINE SCANNER
1884	013362	011605				MOV	(SP), R5		
1885	013364	012537	013620			MOV	(R5)+, DATA1		
1886	013370	012537	013622			MOV	(R5)+, DATA2		
1887	013374	012537	013624			MOV	(R5)+, DATA3		
1888	013400	012537	013626			MOV	(R5)+, DATA4		
1889	013404	012537	013610			MOV	(R5)+, ERR1		
1890	013410	012537	013612			MOV	(R5)+, ERR2		
1891	013414	012537	013614			MOV	(R5)+, ERR3		
1892	013420	012537	013616			MOV	(R5)+, ERR4		
1893	013424	012537	013630			MOV	(R5)+, NXTTS		
1894	013430	010516				MOV	R5, (SP)		
1895	013432	013705	013620			MOV	DATA1, R5		
1896	013436	013777	016244	002512		MOV	LINANS, ZOHMCSR		; SET LINE COUNTER TO ANSWER LINE
1897	013444	017704	002510			MOV	ZOHMLSA, R4		; GET ACTUAL ANSWER LINE STATUS
1898	013450	013703	016244			MOV	LINANS, R3		
1899	013454	020504				CMP	R5, R4		; COMPARE
1900	013456	001402				BEQ	CKTRN1		
1901	013460	004777	000124			JSR	PC, ZERR1		
1902	013464	013777	016242	002464	CKTRN1:	MOV	LINORG, ZOHMCSR		; SET LINE COUNTER TO ORIGINATE LINE
1903	013472	017704	002462			MOV	ZOHMLSA, R4		; GET ACTUAL ORIGINATE LINE STATUS
1904	013476	013705	013622			MOV	DATA2, R5		
1905	013502	013703	016242			MOV	LINORG, R3		
1906	013506	020504				CMP	R5, R4		; COMPARE
1907	013510	001402				BEQ	CKTRN2		
1908	013512	004777	000074			JSR	PC, ZERR2		

1910
 1911
 1912
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 1914 013516 105737 013625
 1915 013522 100003
 1916 013524 042737 000010 016246
 1917 013532 113704 016246
 1918 013536 113705 013624
 1919 013542 013703 016244
 1920 013546 020504
 1921 013550 001402
 1922 013552 004777 000036
 1923 013556 013704 016250
 1924 013562 013705 013626
 1925 013566 013703 016242
 1926 013572 020504
 1927 013574 001402
 1928 013576 004777 000014
 1929 013602 022626
 1930 013604 000177 000020
 1931 013610 000000
 1932 013612 000000
 1933 013614 000000
 1934 013616 000000
 1935 013620 000000
 1936 013622 000000
 1937 013624 000000
 1938 013626 000000
 1939 013630 000000

;CHECK FOR CORRECT TRANSITIONS ON
;SELECTED ORIGINATE AND ANSWER LINES

CKTRN2: TSTB DATA3+1
 BPL .+10
 BIC #10,ANSFLG
 MOVB ANSFLG,R4
 MOVB DATA3,R5
 MOV LINANS,R3
 CMP R5,R4
 BEQ CKTRN3
 JSR PC,@ERR3
 CKTRN3: MOV ORGFLG,R4
 MOV DATA4,R5
 MOV LINORG,R3
 CMP R5,R4
 BEQ CKTRN4
 JSR PC,@ERR4
 CKTRN4: POP2SP
 JMP @NXTTS

;GET TRANSITION DATA FOR

;DID CORRECT TRANSITIONS OCCUR

;GET TRANSITION DATA FOR

;DID CORRECT TRANSITIONS OCCUR

ERR1: 0
 ERR2: 0
 ERR3: 0
 ERR4: 0
 DATA1: 0
 DATA2: 0
 DATA3: 0
 DATA4: 0
 NXTTS: 0

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1946 013632
1947 013632 012777 000100 002322
1948 013640 005237 016176
1949 013644 012737 000001 016200
1950 013652 000005
1951 013654 005037 016270
1952 013660 005337 016270
1953 013664 001375
1954 013666 104004
1955 013670 017622
1956 013672 013701 000042
1957 013676 001501
1958 013700 000005
1959 013702 004711
1960 013704 000240
1961 013706 000240
1962 013710 000240
1963 013712 000240
1964 013714 000137 014102
1965
1966
1967
1968
1969
1970
1971 013720 011646
1972 013722 162716 000002
1973 013726 017616 000000
1974 013732 006316
1975 013734 042716 177001
1976 013740 062716 017640
1977 013744 017616 000000
1978 013750 000136

;END OF PASS
;UPDATE PASS COUNT
;TYPE END OF PASS MESSAGE

EOP:
MOV #100, @TKCSR
INC PASCNT ;UPDATE PASS COUNT
MOV #1, TSTNO ;START AT FIRST TEST OF GROUP
RESET ;CLEAR THE WORLD
CLR FILLA ;INIT COUNTER
IS: DEC FILLA ;COUNT THE CTR
BNE IS ;BR TIL STALL TIMES OUT
TYPE ;RING BELL
MEPASS
MOV 42, R1 ;ARE YOU ON ACT11?
BEQ TSTENT ;NO
RESET
LOGICAL: JSR PC, (R1)
NOP
NOP
NOP
NOP
JMP TSTENT ;GET ADDRESS OF FIRST TEST

;EMT DISPATCH SERVICE
;ARGUMENT OF EMT IS EXTRACTED
;AND USED AS OFFSET TO OBTAIN POINTER
;TO SELECTED SUBROUTINE

EMTSRV: MOV (SP), -(SP) ;GET PC OF RETURN
SUB #2, (SP) ;=PC OF EMT
MOV @ (SP), (SP) ;GET EMT
EMTOK: ASL (SP) ;MULTIPLY EMT ARG BY 2
BIC #177001, (SP) ;CLEAR UNWANTED BITS
ADD #EMTTAB, (SP) ;POINTER TO SUBROUTINE ADDRESS
MOV @ (SP), (SP) ;SUBROUTINE ADDRESS
JMP @ (SP) ;GO TO SUBROUTINE
    
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1880
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1888 013752 005737 001070 LOOP: TST XFLAG ;IS THERE AN XOR TESTER OUT THERE ?
1889 013756 100022 BPL 48 ;NO
1890 013760 013746 000004 MOV 4-(SP) ;SAVE 4
1891 013764 012737 014004 000004 MOV #18,4 ;SET UP SVC ROUTINE
1892 013772 005737 177060 TST 177060 ;GOT SOMETHING LIKE SLAVE SYNC
1893 013776 012637 000004 MOV (SP)+,4 ;YOU BETCHUM
1894 014002 000404 BR 28
1895 014004 022626 18: POP2SP ;RESTORE STACK
1896 014006 012637 000004 MOV (SP)+,4 ;RESTORE 4
1897 014012 000402 BR 38
1898 014014 000137 014076 28: JMP LOOPX ;GO TO NEXT TEST
1899 014020 000137 014102 38: JMP TSTENT ;GO
1900 014024 48:
1901 014024 005737 016172 TST ERRFLG ;IF ERROR OCCURED FLAG=1
1902 014030 001404 BEQ LOOPS ;CHECK FOR ESCAPE TO NEXT TEST
1903 014032 032737 002000 177570 BIT #SW10,SWR ;IF SW10=1,
1904 014040 001016 BNE LOOPX ;ESCAPE TO NEXT TEST
1905 014042 032737 040000 177570 LOOPS: BIT #SW14,SWR ;IF SW14=1,
1906 014050 001033 BNE LOOPL ;LOOP ON CURRENT TEST
1907 014052 032737 004000 177570 BIT #SW11,SWR ;IF SW11=1,
1908 014060 001006 BNE LOOPX ;INHIBIT ITERATIONS
1909 014062 005337 016204 DEC ICOUNT ;UPDATE ITERATION COUNT
1910 014066 001403 BEQ LOOPX ;IF ICOUNT=0, GO TO NEXT TEST
1911 014070 013716 016202 LOOPER: MOV RETURN,(SP) ;SET UP FOR RETURN TO CURRENT TEST
1912 014074 000002 RTI ;RETURN TO CURRENT TEST
1913 014076 005237 016200 LOOPX: INC TSTNO ;UPDATE TEST NUMBER
1914 014102 013705 016200 TSTENT: MOV TSTNO,R5 ;GET TEST NUMBER
1915 014106 006305 ASL R5 ;MULTIPLY TEST NUMBER BY 4
1916 014110 006305 ASL R5
1917 014112 063705 016232 ADD TSTPNT,R5 ;GET POINTER FOR TEST ENTRY
1918 014116 011537 016202 MOV (R5),RETURN ;GET STARTING ADDRESS OF NEXT TEST
1919 014122 001643 BEQ EOP ;IF ADDRESS=0, GO TO END OF PASS
1920 014124 012516 MOV (R5)+,(SP) ;PUT STARTING ADDRESS ON STACK
1921 014126 011537 016204 MOV (R5),ICOUNT ;GET ITERATION COUNT FOR TEST
1922 014132 005037 016172 CLR ERRFLG ;CLEAR ERROR OCCURED FLAG
1923 014136 000002 RTI ;GO TO TEST
1924 014140 012737 000001 016204 LOOPL: MOV #1,ICOUNT ;SET UP TO EXIT TEST AFTER LOOP
1925 014146 000750 BR LOOPER ;GO TO LOOP SERVICE
1926
1927
1928
1929
1930 014150 005737 016172 FREEZE: TST ERRFLG ;IF ERROR FLAG=0,
1931 014154 001413 BEQ FREEZX ;DO NOT TEST FOR ESCAPE
1932 014156 032737 002000 177570 BIT #SW10,SWR ;IF SW10=1,
1933 014164 001344 BNE LOOPX ;ESCAPE TO NEXT TEST

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2034	014166	032737	001000	177570		BIT	#SW09, SWR	: IF SW09=1
2035	014174	001403				BEQ	FREEZX	: FREEZE CURRENT DATA
2036	014176	017616	000000			MOV	@(SP), (SP)	: GET LOOPING ADDRESS
2037	014202	000002				RTI		: LOOP
2038	014204	062716	000002		FREEZX:	ADD	#2, (SP)	: CONTINUE IN CURRENT TEST
2039	014210	000002				RTI		

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;GENERAL ERROR SERVICE
;ONLY PC OF FAILING TEST IS OUTPUT TO TELEPRINTER

ERR: CLR ERRFLG ;ALWAYS TYPE PC+2
;OF TEST THAT FAILED
014212 005037 016172 CLR ERRMSG ;NO MESSAGE
014216 005037 014420 CLR ERTAB ;NO TABLE OF DATA
014222 005037 014432 BR ERRGEN ;OUTPUT ERROR MESSAGE
014226 000451

;TRANSITION DETECTION ERROR SERVICE
;FORMAT FOR ERROR TYPEOUT IS
;XXXXXX TRANSITION ERROR
;EXP REC LINE
;AA BB CC
;WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE
; AA=EXPECTED INTERRUPT FLAGS (CORRESPONDS TO 4 MSB OF CONTROL REGISTER)
; BB=RECEIVED INTERRUPT FLAGS (AS ABOVE)
; CC=LINE ON WHICH ERROR OCCURED

ERRT: CLR ERRFLG ;ALWAYS OUTPUT ALL DATA
MOV #MTRANE,ERRMSG ;TYPE "TRANSITION ERROR"
MOV #ERTAB1,ERTAB ;TABLE OF DATA
BR ERRGEN ;OUTPUT ERROR MESSAGE

;ON-LINE STATUS ERROR SERVICE
;FORMAT FOR LINE STATUS ERROR IS
;XXXX LINE ERROR
;EXP REC LINE
;AAA BBB CC
;WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE
; AAA=EXPECTED LINE STATUS AT TIME OF ERROR
; BBB=RECEIVED LINE STATUS AT TIME OF ERROR
; CC=LINE ON WHICH ERROR OCCURED

ERRS: CLR ERRFLG ;ALWAYS OUTPUT ALL DATA
MOV #MLINE1,ERRMSG ;TYPE "LINE ERROR"
MOV #ERTAB2,ERTAB ;EXP REC LINE"
BR ERRGEN ;TABLE OF DATA
;OUTPUT ERROR MESSAGE

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;FATAL TRANSITION ERROR
;FORMAT FOR FATAL ERROR TYPEOUT IS

;XXXXXX FATAL ERROR
;CSTAT LSTAT
;AAAAA BSB

;WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE
;AAAAA=RECEIVED CONTROL STATUS ON LINE THAT INTERRUPTED
;BBBB=RECEIVED LINE STATUS ON LINE THAT INTERRUPTED

014274 005037 016172          ERRN: CLR     EPCFLG          ;ALWAYS OUTPUT ALL DATA
014300 012737 017333 014420  MOV     #MFATAL,ERRMSG ;TYPE "FATAL ERROR"
                                ;CSTAT LSTAT
014306 012737 014532 014432  MOV     #ERTAB3,ERTAB  ;TABLE OF DATA
014314 000416                BR      ERRGEN        ;OUTPUT ERROR MESSAGE

;"CONTROL STATUS" ERROR SERVICE
;FORMAT FOR CONTROL STATUS ERROR IS

;XXXXXX STATUS ERROR
;EXP REC
;AAAAA BBBBBB

;WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE
;AAAAA=EXPECTED CONTROL STATUS AT TIME OF ERROR
;BBBBBB=RECEIVED(ACTUAL) CONTROL STATUS AT TIME OF ERROR

014316 012737 016274 014420  ERRCS: MOV     #MSTATE,ERRMSG ;TYPE "STATUS ERROR"
                                ;"EXP REC"
014324 012737 014544 014432  MOV     #ERTAB4,ERTAB  ;TABLE OF DATA
014332 000407                BR      ERRGEN        ;OUTPUT DATA

;LINE STATUS ERROR SERVICE
;FORMAT FOR LINE STATUS ERROR IS

;XXXX LINE ERROR
;EXP REC LINE SEL
;AAA DDD CC DO

;WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE
;AAA=EXPECTED LINE STATUS AT TIME OF ERROR
;BBB=RECEIVED LINE STATUS AT TIME OF ERROR
;CC=LINE ON WHICH ERROR OCCURED
;DO=THE LINE ON WHICH THE PROGRAM WAS OPERATING

014334 012737 016325 014420  ERRLS: MOV     #MLINER,ERRMSG
014342 012737 014556 014432  MOV     #ERTAB5,ERTAB
014350 000400                BR      ERRGEN

```

```

;GENERAL ERROR HANDLER
;TYPE PC+2 OF TEST THAT FAILED
;TYPE ERROR MESSAGE (IF ANY)
;TYPE DATA RELATING TO FAILURE (IF ANY)

11453 014352 032737 020000 177570 ERGEN: BIT #SW13,SWR ;IF SW13=1 DO NOT
11454 014360 001026 BNE .3 ;TYPE ERROR MESSAGE
11455 014362 021637 016224 CMP (SP),SAVPC ;SAME ERROR AGAIN
11456 014366 001402 BEQ +6
11457 014370 005037 016172 CLR ERRFLG
11458 014374 104005 SAVDSP
11459 014376 005737 016172 TST ERRFLG ;IF ERROR OCCURED FLAG=1,
11460 014402 001007 BNE .1 ;TYPE DATA ONLY
11461 014404 104006 OCTASC ;TYPE PC+2 OF CALL TO ERROR ROUTINE
11462 014406 014470 ERTAB0
11463 014410 005737 014420 TST ERRMSG
11464 014414 001407 BEQ .2
11465 014416 104004 TYPE ;TYPE ERROR MESSAGE
11466 014420 000000 ERRMSG: 0
11467 014422 005737 014432 .1: TST ERTAB
11468 014426 001402 BEQ .2
11469 014430 104006 OCTASC ;TYPE DATA
11470 014432 000000 ERTAB: 0
11471 014434 104007 .2: RESOS ;RESTORE RO-R5
11472 ;ERROR HALT SERVICE

1170 014436 032737 100000 177570 .3: BIT #SW15,SWR ;IF SW15=0 DO NOT
1171 014444 001405 BEQ .4 ;HALT ON ERROR
1172 014446 010046 PUSHRO ;SAVE RO ON STACK
1173 014450 013700 016224 MOV SAVPC,RO ;GET PC+2 OF CALL TO ERROR
1174 014454 000000 HALT ;HALT AND DISPLAY ADDRESS OF FAILING TEST
1175 014456 012600 POPRO ;RESTORE RO
1176 014460 012737 000001 016172 .4: MOV #1,ERRFLG ;SET ERROR OCCURED FLAG
1177 014466 000002 RTI ;RETURN TO TEST
    
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014470 000001
014472 000006
014474 016224
014476 000003
014500 000002
014502 016220
014504 000002
014506 016216
014510 000002
014512 016214
014514 000003
014516 000003
014520 016220
014522 000003
014524 016216
014526 000002
014530 016214
014532 000002
014534 000006
014536 016216
014540 000003
014542 016214
014544 000002
014546 000006
014550 016220
014552 000006
014554 016216
014556 000004
014560 000003
014562 016220
014564 000003
014566 016216
014570 000002
014572 016214
014574 000002
014576 016210

;TABLE S OF DATA FOR ERROR TYPEOUT
;TABLE FOR TRANSITION STATUS ERROR
ERTAB0: 1
SAVPC
ERTAB1: SAVRS ;CONTAINS EXPECTED TRANSITION STATUS
SAVR4 ;CONTAINS RECEIVED TRANSITION STATUS
SAVR3 ;CONTAINS NUMBER OF LINE WHERE ERROR OCCURED
SAVRS ;CONTAINS EXPECTED LINE STATUS
SAVR4 ;CONTAINS RECEIVED LINE STATUS
SAVR3 ;CONTAINS NUMBER OF LINE WHERE ERROR OCCURED
ERTAB3: SAVR4
SAVR3
ERTAB4: SAVRS ;CONTAINS EXPECTED CONTROL STATUS
SAVR4 ;CONTAINS RECEIVED CONTROL STATUS
ERTAB5: SAVRS ;CONTAINS EXPECTED LINE STATUS
SAVR4 ;CONTAINS RECEIVED LINE STATUS
SAVR3 ;CONTAINS NUMBER OF LINE WHERE ERROR OCCURED
SAVR1 ;CONTAINS NUMBER OF LINE UNDER TEST

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014600 017605 000000
014604 062716 000002
014610 012737 000010 015026
014616 012704 017521
014622 012537 016226
014626 012537 016230
014632 013537 015022
014636 104010
014640 005337 016226
014644 001370
014646 112714 000100
014652 104004
014654 017517
014656 000032

015026

```

; CONVERT OCTAL TO ASCII AND
; OUTPUT ON TELETYPE
OCTASN: MOV @ (SP), R5
ADD #2, (SP)
MOV #10, RADIX
MOV #MBCD+2, R4
MOV (R5)+, WDCNT
OCTAS1: MOV (R5)+, CHRCNT
MOV @ (R5)+, BINMAD
CONVERT
DEC WDCNT
BNE OCTAS1
MOV# #100, (R4)
TYPE
MBCD
RTI

```

; GET POINTER TO TABLE OF DATA

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; SET UP POINTER FOR CONVERTED DATA
; GET NUMBER OF WORDS TO BE CONVERTED
; GET NUMBER OF DIGITS IN WORD
; GET DATA TO BE CONVERTED
; CONVERT TO ASCII
; IF ALL DATA IS NOT CONVERTED
; CONTINUE
; PUT TERMINATOR AT END OF MESSAGE
; OUTPUT CONVERTED DATA
; TO TELETYPE
; RETURN TO CALLING ROUTINE

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2239                                     ;INTEGER BINARY TO ASCII CONVERSION COMMON ROUTINE
2240
2241 014650 013700 016230  BINASC: MOV    CHRCNT,R0      ;SET UP COUNT FOR DIGITS TO BE CONVERTED
2242 014664 012701 017624  BINASC: MOV    @TENTAB,R1     ;SET UP POINTER FOR TEMPORARY STORAGE
2243 014670 104011  BINASA: EXTRACT          ;EXTRACT ONE DIGIT
2244 014672 062737 000060 015024  ADD     #60,DIGIT    ;CONVERT FROM BCD TO ASCII
2245 014700 113721 015024  MOVB   DIGIT,(R1)+   ;STORE DIGIT
2246 014704 005300  DEC     R0          ;IF ALL DIGITS NOT DONE,
2247 014706 001370  BNE    BINASA      ;CONTINUE
2248 014710 114124  BINASB: MOVB   -(R1),(R4)+  ;REVERSE ORDER OF DIGITS
2249 014712 005337 016230  DEC     CHRCNT     ;IF ALL CHARACTERS ARE NOT
2250 014716 001374  BNE    BINASB    ;IN ORDER, CONTINUE
2251 014720 112724 000040  MOVB   #40,(R4)+  ;INSERT SPACE AFTER LAST DIGIT
2252 014724 000002  RTI           ;RETURN TO CALLING ROUTINE
2253
2254                                     ;SINGLE PRECISION UNSIGNED DIVIDE LOOP
2255
2256
2257 014726 005037 015024  DIVI:  CLR     DIVIDH
2258 014732 023737 015024 015026  DIVIU: CMP     DIVIDH,DIVIS
2259 014740 103027  BHIS   DIVIB
2260 014742 012737 000021 015002  MOV     #17,DIVCNT
2261 014750 000407  BR     DIVIC
2262 014752 023737 015024 015026  DIVIA: CMP     DIVIDH,DIVIS
2263 014760 103403  BLO   DIVIC
2264 014762 163737 015026 015024  SUB     DIVIS,DIVIDH
2265 014770 006137 015022  DIVIC: ROL     DIVIDL
2266 014774 006137 015024  ROL     DIVIDH
2267 015000 005327  DEC     (PC)+
2268 015002 000000  DIVCNT: 0
2269 015004 001362  BNE    DIVIA
2270 015006 006037 015024  ROR     DIVIDH
2271 015012 005137 015022  COM     DIVIDL
2272 015016 000002  RTI
2273 015020 000000  DIVIB: HALT
2274 015022 000000  DIVIDL: 0
2275 015024 000000  DIVIDH: 0
2276 015026 000000  DIVIS: 0
2277
2278                                     ;SAVE PC OF TEST THAT FAILED AND R0-R5
2279
2280 015030 016637 000004 016224  SVOSP: MOV     4(SP),SAVPC
2281
2282                                     ;SAVE R0-R5
2283
2284 015036 010537 016220  SVOS:  MOV     R5,SAVR5
2285 015042 010437 016216  MOV     R4,SAVR4
2286 015046 010337 016214  MOV     R3,SAVR3
2287 015052 010237 016212  MOV     R2,SAVR2
2288 015056 010137 016210  MOV     R1,SAVR1
2289 015062 010037 016206  MOV     R0,SAVR0
2290 015066 000002  RTI
    
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;RESTORE R0-R5
RSOS:  MOV SAVR0,R0
      MOV SAVR1,R1
      MOV SAVR2,R2
      MOV SAVR3,R3
      MOV SAVR4,R4
      MOV SAVR5,R5
      RTI

;TELETYPE OUTPUT ROUTINE
TYPERS: MOV @ (SP),R5 ;GET POINTER TO MESSAGE (ON STACK)
        ADD #2,(SP) ;CORRECT STACK FOR RETURN
TYPERA: TSTB @TPCSR ;WAIT FOR TELEPRINTER READY
        BPL TYPERA
        CMPB #12,-1(R5) ;WAS LAST ONE A L.F. ??
        BEQ IS ;BR IF YES
        CMPB #15,-1(R5) ;WAS LAST ONE A C.R. ??
        BEQ IS ;BR IF YES
        BR 2S ;CONTINUE IF NEITHER
        JSR PC,TYFILL ;GO OUT PUT FILLERS
        CMPB #100,(R5) ;IF CHARACTER IS NOT TERMINATOR, TYPE IT
        BNE TYPERA1
        RTI ;CHARACTER IS TERMINATOR, EXIT
TYPERA1: CMPB #42,(R5) ;IF CHARACTER=42,
        BEQ TYPECL ;TYPE LINE FEED
        CMPB #45,(R5) ;IF CHARACTER=45,
        BEQ TYPECL ;TYPE CARRIAGE RETURN
TYPERA2: MOVB (R5)+,@TPDBR ;GET CHARACTER
        BR TYPERA ;TYPE IT
TYPECL: BICB #40,(R5) ;CONVERT CODE OF 42 OR 45
        BISB #10,(R5) ;TO 12 OR 15
        BR TYPERA2 ;TYPE IT

;OUTPUT FILLERS AFTER <CR> OR <LF> CHAR IS OUT PUTTED.
TYFILL: MOVB FILL,FILLA ;GET FILL COUNT
        IS: MOVB FILL+1,@TPDBR ;OUT PUT ONE FILLER
        2S: TSTB @TPCSR ;WAIT FOR TTY TO FINISH OUTPUT
        BPL 2S ;BR IF TTY NOT DONE
        DECB FILLA ;COUNT ONE FILLER
        BNE IS ;BR TIL ALL DONE
        RTS PC ;RETURN TO CALLER ABOVE

;INPUT OCTAL CHARACTER STRING
;TERMINATOR IS CARRIAGE RETURN
;IF MORE THAN SEVEN (?) CHARACTERS INCLUDING
;CARRIAGE RETURN ARE TYPED, THE IN PUT WILL
;BE RE-REQUESTED
INSTR: CLR PS
    
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2346	015270	011605		MOV	(SP), R5		; GET POINTER TO ARGUMENTS
2347	015272	012537	015316	MOV	(R5)+, MSG		; GET MESSAGE TO BE TYPED
2348	015276	012537	015512	MOV	(R5)+, LOLIM		; GET LOWER LIMIT
2349	015302	012537	015514	MOV	(R5)+, HILIM		; GET UPPER LIMIT
2350	015306	012537	015516	MOV	(R5)+, STORE		; GET DATA STORAGE LOCATION
2351	015312	010516		MOV	R5, (SP)		; RESTORE STACK
2352	015314	104004					; TYPE MESSAGE
2353	015316	000000		INSTR1: TYPE			
2354	015320	012704	015520	MSG:	0		
2355	015324	012703	000007	MOV	#INBUF, R4		; SET UP CHARACTER INPUT BUFFER
2356	015330	105777	000626	MOV	#7, R3		; SET UP INRJT COUNT
2357	015334	100403		INSTRB: TSTB	@TKCSR		; WAIT FOR CHARACTER
2358	015336	005737	001724	BMI	INSTRB		
2359	015342	001772		TST	SINFL		
2360	015344	005037	001724	BEQ	INSTRB		
2361	015350	117714	000610	INSTRBB: CLR	SINFL		
2362	015354	142714	000200	MOV	@TKDBR, (R4)		; GET CHARACTER
2363	015360	122427	000015	BICB	#200, (R4)		; CLEAR BIT 8
2364	015364	001413		CMPB	(R4)+, #15		; IS CHARACTER TERMINATOR
2365	015366	117777	000572	BEQ	INSTR2		; IF IT IS, CONVERT INPUT STRING
2366	015374	105777	000566	MOV	@TKDBR, @TPDBR		; TYPE CHARACTER IF NOT TERMINATOR
2367	015400	100375		TSTB	@TPCSR		; WAIT TO FINISH TYPING
2368	015402	005303		BPL	INSTRC		
2369	015404	001351		DEC	R3		; UPDATE RECEIVED COUNT
2370	015406	104004		BNE	INSTRB		; AND CONTINUE
2371	015410	017252		INSTER: TYPE			; TYPE "?" AND RE-REQUEST INPUT
2372	015412	000740		MOVB			
2373				BR	INSTR1		
2374							; CONVERT ASCII STRING TO OCTAL
2375							
2376	015414	012704	015520	INSTR2: MOV	#INBUF, R4		; GET POINTER TO ASCII STRING
2377	015420	005003		CLR	R3		
2378	015422	122714	000015	CMPB	#15, (R4)		; IS TERMINATOR FIRST
2379							; CHARACTER IN STRING
2380	015426	001767		BEQ	INSTER		
2381	015430	121427	000060	INSTRD: CMPB	(R4), #60		; IS CHARACTER OCTAL DIGIT
2382	015434	002764		BLT	INSTER		; IF 67 >= CHAR >= 60
2383	015436	121427	000067	CMPB	(R4), #67		; CHARACTER IS OCTAL DIGIT
2384	015442	003361		BGT	INSTER		
2385	015444	142714	000060	BICB	#60, (R4)		; STRIP ASCII
2386	015450	152403		BISB	(R4)+, R3		; GENERATE OCTAL NUMBER
2387	015452	121427	000015	CMPB	(R4), #15		; IF END OF STRING, CHECK LIMITS
2388	015456	001404		BEQ	INSTR3		
2389	015460	006303		ASL	R3		; MULTIPLY DIGIT BY 10 (OCTAL)
2390	015462	006303		ASL	R3		
2391	015464	006303		ASL	R3		
2392	015466	000760		BR	INSTRD		; GET NEXT DIGIT
2393							
2394							; TEST NUMBER TO SEE IF IT IS WITHIN LIMITS
2395							
2396	015470	020337	015514	INSTR3: CMP	R3, HILIM		; TEST HI LIMIT
2397	015474	101344		BHI	INSTER		; IF R3 > HILIM, ERROR
2398	015476	020337	015512	CMP	R3, LOLIM		; TEST LOW LIMIT
2399	015502	103741		BLO	INSTER		; IF R3 < LOLIM, ERROR

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00 015504 010377 000006      MOV      R3, @STORE      ;STORE NUMBER
01 015510 000002          RTI                      ;EXIT
02 015512 000000          LCLIM: 0
03 015514 000000          HILIM: 0
04 015516 000000          STORE: 0
05 015520 000000          INBUF: 0
06 015542 015542          .*. +20
07                                     ;ENTER HERE ON POWER FAILURE
08
09
10 015542 010046          PFAIL:  MOV      R0, -(SP)      ;SAVE R0-R5 ON PROCESSOR STACK
11 015544 010146          MOV      R1, -(SP)
12 015546 010246          MOV      R2, -(SP)
13 015550 010346          MOV      R3, -(SP)
14 015552 010446          MOV      R4, -(SP)
15 015554 010546          MOV      R5, -(SP)
16 015556 013746 000024      MOV      R4, -(SP)
17 015562 010637 016222      MOV      SP, SAVSP          ;SAVE STACK POINTER
18 015566 012737 015600 000024  MOV      #RESTART, R4      ;SET UP FOR POWER UP TRAP
19 015574 000000          HALT                    ;HALT ON POWER DOWN NORMAL
20 015576 000776          BR       .-2
21                                     ;PROCESSOR WILL TRAP HERE WHEN POWER IS RESTORED
22
23
24 015600 013706 016222          RESTAR: MOV      SAVSP, SP      ;RESTORE STACK POINTER
25 015604 012605          MOV      (SP)+, R5        ;RESTORE R0-R5
26 015606 012604          MOV      (SP)+, R4
27 015610 012603          MOV      (SP)+, R3
28 015612 012602          MOV      (SP)+, R2
29 015614 012601          MOV      (SP)+, R1
30 015616 012600          MOV      (SP)+, R0
31 015620 012737 015542 000024  MOV      #PFAIL, R4      ;SET UP FOR POWER FAILURE
32 015626 005726          POP1SP
33 015630 104004          TYPE
34 015632 017431          MPFAIL
35 015634 012777 000100 000320  MOV      #100, @TKCSR
36 015642 005737 001560          TST     TIPFLG
37 015646 001002          BNE     RESTA1
38 015650 000137 001100          JMP     START0
39 015654 104004          RESTA1: TYPE
40 015656 017451          MPFI
41 015660 012716 000340          MOV      #340, (SP)
42 015664 005746          PUSH1SP
43 015666 000137 014102          JMP     TSTENT
44
45
46                                     ;THE FOLLOWING AUTO VECTORS USING THE FIRST BASE ADDRESS
47 XOR: 015672 013746 000020          MOV      20, -(SP)      ;SAVE 20
48 015676 013746 000022          MOV      22, -(SP)      ;SAVE 22
49 015702 012737 016074 000020  MOV      #24, 20        ;IOT INTR VECTOR
50 015710 012737 000340 000022  MOV      #340, 22       ;IOT INTR LVL
51 015716 012737 000300 013620  MOV      #300, DATA1
52 015724 012737 000302 013622  MOV      #302, DATA2
53 015732 013777 013622 175660 15.  MOV      DATA2, @DATA1

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2454	015740	012777	000004	175654		MOV	#IOT, @DATA2	; IOT TRAP
2455	015746	062737	000004	013620		ADD	#4, DATA1	
2456	015754	062737	000004	013622		ADD	#4, DATA2	
2457	015762	063727	013620	001000		CMP	DATA1, #1000	
2458	015770	001360				BNE	1\$	
2459	015772	012737	000000	016200		MOV	#0, TSTNO	; SET UP DEFAULT
2460	016000	012737	017744	016232		MOV	#TSTTBO, TSTPNT	
2461	016006	052737	000340	177776		BIS	#340, PS	; PREVENT INTERRUPTS
2462	016014	005077	000136			CLR	@DHMCSR	
2463	016020	012777	000100	000130		MOV	#INTENA, @DHMCSR	; SET INTERRUPT ENABLE
2464	016026	042737	000340	177776		BIC	#340, PS ; ALLOW INTERRUPTS	
2465	016034	052777	000200	000114		BIS	#DONE, @DHMCSR	; SET DONE..AND INTERRUPT
2466	016042	000240				NOP		
2467	016044	012637	000022			MOV	(SP)+, 22	; YOU DIDN'T INTERRUPT ?
2468	016050	012637	000020			MOV	(SP)+, 20	; RESTORE 20 & 22
2469	016054	005077	000076			CLR	@DHMCSR	; STOP ALL INTERRUPT
2470	016060	052737	000340	177776		BIS	#340, PS	
2471	016066	104012				ERROR		
2472	016070	000000				HALT		; YOU SHOULD HAVE INTERRUPTED
2473	016072	000426				BR	3\$	
2474	016074	011637	016152		2\$:	MOV	(SP), DHMVEC	; EXTRACT VECTOR +4
2475	016100	162737	000002	016152		SUB	#2, DHMVEC	; CREATE LVL
2476	016106	013737	016152	016154		MOV	DHMVEC, DHMLVL	; SAVE
2477	016114	162737	000002	016152		SUB	#2, DHMVEC	; CREATE AND SAVE VEC
2478	016122	012737	000340	177776		MOV	#340, PS	; PREVENT INTERRUPTS
2479	016130	005077	000022			CLR	@DHMCSR	
2480	016134	022626				POP2SP		
2481	016136	022626				POP2SP		
2482	016140	012637	000022			MOV	(SP)+, 22	; RESTORE 22
2483	016144	012637	000020			MOV	(SP)+, 20	; RESTORE 20
2484	016150	000207			3\$:	RTS	PC	
2485								

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016152 000300
016154 000302
016156 170500
016160 170502
016162 177560
016164 177562
016166 177564
016170 177566

016172 000000
016174 000000
016176 000000
016200 000000
016202 000000
016204 000000
016206 000000
016210 000000
016212 000000
016214 000000
016216 000000
016220 000000
016222 000000
016224 000000
016226 000000
016230 000000
016232 017744
016234 000000
016236 000000
016240 000000
016242 000000
016244 000000
016246 000000
016250 000000
016252 000000
016254 000000
016256 000000
016260 177777
016262 000000
016264 000000
016266 000002
016270 000000
016272 000000

016274 052123 052101 051525
016302 042440 051122 051117
016310 021045 054105 020120
016316 020040 051040 041505
016324 100
016325 114 047111 020105

;INDIRECT POINTERS

DHIVEC: 300
DHMLVL: 302
DHMCSR: 170500
DHMLSR: 170502
TKCSR: 177560
TKOBR: 177562
TPCSR: 177564
TPOBR: 177566

;MODEM CONTROL INTERRUPT VECTOR
;MODEM CONTROL ONTERRUPT PRIORITY
;MODEM CONTROL CONTROL STATUS REGISTER
;MODEM CONTROL CONTROL STATUS REGISTER

;PROGRAM VARIABLES

ERRFLG: 0
TRACON: 00
PASCNT: 00
TSTNO: 00
RETURN: 00
ICOUNT: 00
SAVR0: 00
SAVR1: 00
SAVR2: 00
SAVR3: 00
SAVR4: 00
SAVR5: 00
SAVSP: 00
SAVPC: 00
WRDCNT: 00
CHRCNT: 00
TSTPNT: TSTTBO
TSTMAX: 0
LINF LG: 00
LINE: 00
LINORG: 00
LINANS: 00
ANSFLG: 00
ORGF LG: 00
TIME1: 00
TIME2: 00
TIFLG: 00
LINSEL: 177777
SELSK: 00
SLMSK: 00
FILL: 00
FILLA: 00
RNGRET: 0

;FILL CHAR/COUNT
;TEMP STORAGE FOR FILL COUNT

MSTATE: .ASCII ;STATUS ERROR%*EXP REC;

MLINER: .ASCII ;LINE ERROR%*EXP REC LINE SEL;

2547	017010 017013 017020 017026 017034 017042	052103 045045 020066 051440 051109 022527	100 022442 044514 040503 052040 040042	030442 042516 047116 051505	M16: .ASCII ;X"X"16 LINE SCANNER TESTX"Q;
2548	017046 017054 017062 017070 017076 017104 017112 017120 017126	0221045 044104 020040 026455 020115 047522 043501 041511 022455	021045 026513 026440 047515 047503 020114 047516 026455 040042	055104 020102 026455 042504 052116 044504 052123 026455	MTITLE: .ASCII ;X"X"DZDMK-B -----MODEM CONTROL DIAGNOSTIC-----X"Q;
2549	017132 017140 017146	021045 051117 042523	042523 040440 051523	052103 042104 040055	MVECTOR: .ASCII ;X"VECTOR ADDRESS-Q;
2550	017154 017162 017170 017176	021045 042523 044507 040440	047503 020114 052123 042104	052116 042523 051103 042523	MPEGAD: .ASCII ;X"CONTROL REGISTER ADDRESS-Q;
2551	017204 017210 017216 017224 017232	051523 021045 021440 020124 042515	040055 044514 046105 040520 042524	042516 041505 040522 020122	MLINSL: .ASCII ;X"LINE SELECT PARAMETER -Q;
2552	017240 017242 017250	040055 021045 040055	042524	052123	MTEST: .ASCII ;X"TEST-Q;
2553	017252	020040	040077		MOM: .ASCII ; ?Q;
2554	017256	021045	100		MCRLF: .ASCII ;X"Q;
2555	017261 017266 017274 017302	045045 046107 042516 043514	051442 020105 041440 052040	047111 044514 041101 051505	MLINE: .ASCII ;X"SINGLE LINE CABLE TESTX"Q;
2556	017310 017314 017322 017330	022524 021045 047040 026522	040042 044514 046525 100	042516 042502	MLINEI: .ASCII ;X"LINE NUMBER-Q;
2557	017333 017340 017346 017354 017362	106 042440 021045 020124 052101	052101 051122 051503 046040 100	046101 051117 040524 052123	MFATAL: .ASCII ;FATAL ERRORX"CSTAT LSTATQ;
2558	017365 017372 017400 017406 017414 017422 017430	045 051516 020116 052103 051503 046040 100	052042 052111 042504 042105 040524 052123 100	040522 047511 042524 021045 020124 052101	MTRNDE: .ASCII ;X"TRANSITION DETECTEDX"CSTAT LSTATQ;
2559	017431 017436	045 051105	050042 043040	053517 044501	MPFAIL: .ASCII ;X"POWER FAILUREQ;

2560	017444	052514	042522	100
	017451	055	052503	051122
	017456	047105	020124	042524
	017464	052123	053440	046111
	017472	020114	042522	052123
	017500	051101	022524	040042
2561	017506	041536	100	
2562	017511	136	040126	
2563	017514	046136	100	
2564	017517	045	042	
2565		017621		
2566		017622		
2567	017622	040007		
2568	017624	000000		
2569		017636		
2570				
2571	017636	000000		
2572				
2573				
2574				
2575	017640	014316		
2576	017642	014334		
2577	017644	013752		
2578	017646	014150		
2579	017650	015122		
2580	017652	015030		
2581	017654	014600		
2582	017656	015070		
2583	017660	014850		
2584	017662	014726		
2585	017664	014212		
2586	017666	015264		
2587	017670	014230		
2588	017672	014252		
2589	017674	014274		
2590	017676	012660		
2591	017700	012722		
2592	017702	013164		
2593	017704	013262		
2594	017706	013350		
2595	017710	013320		
2596	017712	000000		
2597	017714	017744		
2598	017716	020146		
2599	017720	020210		
2600	017722	020216		
2601	017724	000000		
2602	017726	000000		
2603	017730	000000		
2604	017732	000000		
2605	017734	000037		
2606	017736	000007		
2607	017740	000001		
2608	017742	000000		

MPF1: .ASCII ; -CURRENT TEST WILL RESTARTX"0;

MCONTC: .ASCII ; :TC3;
MCONTV: .ASCII ; :TV3;
MCONTL: .ASCII ; :TL3;
MBCD: .ASCII ; :X";

.z. +100
.EVEN
MEPASS: 40007
TENTAB: 0
.z. +10

0

;EMT DISPATCH TABLE

EMTTAB: ERRCS
ERRLS
LOOP
FREEZE
TYPER
SVOSP
OCTASN
RSOS
BINASC
DIVI
ERR
INSTR
ERRT
ERRS
ERRN
GETLIN
SETUPS
CKRNG
MAITR
CKTRN
MAITR

ENTLIM: 0
TSTLST: TSTT80
TSTT91
TSTT82
TSTT83

GRO: NO-1
N1-100-1
N2-200-1
N3-300-1

DZDNK.P11

(2) 017744 001726
 (2) 017746 000001
 (2) 017750 001754
 (2) 017752 004000
 (2) 017754 002016
 (2) 017756 004000
 (2) 017760 002060
 (2) 017762 004000
 (2) 017764 002122
 (2) 017766 004000
 (2) 017770 002164
 (2) 017772 004000
 (2) 017774 002236
 (2) 017776 004000
 (2) 020000 002302
 (2) 020002 004000
 (2) 020004 002356
 (2) 020006 004000
 (2) 020010 002440
 (2) 020012 004000
 (2) 020014 002522
 (2) 020016 004000
 (2) 020020 002604
 (2) 020022 004000
 (2) 020024 002656
 (2) 020026 004000
 (2) 020030 002750
 (2) 020032 004000
 (2) 020034 003030
 (2) 020036 004000
 (2) 020040 003110
 (2) 020042 004000
 (2) 020044 003170
 (2) 020046 004000
 (2) 020050 003250
 (2) 020052 004000
 (2) 020054 003344
 (2) 020056 000400
 (2) 020060 003462
 (2) 020062 000400
 (2) 020064 003660
 (2) 020066 000400
 (2) 020070 004036
 (2) 020072 000200
 (2) 020074 004206
 (2) 020076 000200
 (2) 020100 004444
 (2) 020102 000200
 (2) 020104 004702
 (2) 020106 000200
 (2) 020110 005140
 (2) 020112 000200
 (2) 020114 005376
 (2) 020116 000200

TSTTBO: TO
 1
 T1
 TIMES
 T2
 TIMES
 T3
 TIMES
 T4
 TIMES
 T5
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 T7
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 T31
 TIMES
 T32
 TIMES

(2)	020120	005614
(2)	020122	000200
(2)	020124	006032
(2)	020126	000200
(2)	020128	006250
(2)	020130	004000
(2)	020132	006432
(2)	020134	004000
(2)	020136	006702
(2)	020138	004000
(2)	020140	000000
(2)	020142	007162
(2)	020144	000001
(2)	020146	007230
(2)	020148	004000
(2)	020150	007430
(2)	020152	004000
(2)	020154	007640
(2)	020156	004000
(2)	020158	010050
(2)	020160	004000
(2)	020162	010260
(2)	020164	004000
(2)	020166	010444
(2)	020168	004000
(2)	020170	010630
(2)	020172	004000
(2)	020174	000000
(2)	020176	011014
(2)	020178	000001
(2)	020180	000000
(2)	020182	011350
(2)	020184	000001
(2)	020186	000000
(2)	020188	000001
(2)	020190	000000
(2)	020192	000001
(2)	020194	000000
(2)	020196	000001
(2)	020198	000000
(2)	020200	000001
(2)	020202	000000
(2)	020204	000001
(2)	020206	000000
(2)	020208	000001
(2)	020210	000000
(2)	020212	000001
(2)	020214	000000
(2)	020216	011350
(2)	020218	000001
(2)	020220	000000
(2)	020222	000001
(2)	020224	000000
(2)	020226	000001
(2)	020228	000000
(2)	020230	000001
(2)	020232	000000
(2)	020234	000001
(2)	020236	000000
(2)	020238	000001
(2)	020240	000000
(2)	020242	000001
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(2)	020246	000001
(2)	020248	000000
(2)	020250	000001
(2)	020252	000000
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(2)	020258	000001
(2)	020260	000000
(2)	020262	000001
(2)	020264	000000
(2)	020266	000001
(2)	020268	000000
(2)	020270	000001
(2)	020272	000000
(2)	020274	000001
(2)	020276	000000
(2)	020278	000001
(2)	020280	000000
(2)	020282	000001
(2)	020284	000000
(2)	020286	000001
(2)	020288	000000
(2)	020290	000001
(2)	020292	000000
(2)	020294	000001
(2)	020296	000000
(2)	020298	000001
(2)	020300	000000

T33
TIMES
T34
TIMES
T35
TIMES
T36
TIMES
T37
TIMES
0
TSTTB1: T100
↓
T101
TIMES
T102
TIMES
T103
TIMES
T104
TIMES
T105
TIMES
T106
TIMES
T107
TIMES
0
TSTTB2: T200
↓
0
TSTTB3: T300
↓
0
ENDCOD: 0
.END

ANSFLG	016246	1720*	1723*	1728*	1732*	1816*	1825	1848	1864*	1916*	1917	2523*								
ANSTR	012474	1656	1718*																	
ANSTR	012562	1736*																		
ANSTR1	012510	1719	1721*																	
ANSTR2	012524	1722	1724*																	
ANSTR3	012540	1726	1729*																	
ANSTR4	012554	1731	1733*																	
BINASA	014670	2244*	2248*																	
BINASB	014710	2249*	2251*																	
BINASC	014660	2242*	2250*																	
BINARD	015022	80*	2230*																	
BUSY	000C30	85*	637	641	815	842	1033	1794												
CHRCNT	016230	2224*	2242	2250*	2516*															
CKRING	104021	144*	116*	1332																
CKRNG	013164	1842*	2569*																	
CKRNG1	013230	1851	1853*																	
CKRNG2	013254	1857	1860*																	
CKTRM	104023	146*	1193	1257	1361	1402	1446	1487	1527	1571	1635									
CKTRN	013350	1882*	2594																	
CKTRN1	013464	1900	1902*																	
CKTRN2	013516	1907	1914*																	
CKTRN3	013556	1921	1923*																	
CKTRN4	013502	1927	1929*																	
CLRMUX	002000	91*	794	909	911	913	915	938	971	1024	1108	1110	1112	1114						
CLRSCH	004000	1793																		
CO	000100	92*	814	841	1032	1793														
COF	040000	106*	917	1116	1196	1200	1364	1367	1405	1409	1490	1494	1530	1535						
CONVER	104010	95*	1697	1709	1721	1733														
CS	000040	135*	2231																	
CSF	020000	105*	917	1116	1196	1200	1367	1409	1490	1530										
CSTR1	001754	94*	1700	1709	1724	1733														
CSTR2	002016	580*																		
CSTR3	002060	593*																		
CSTR4	002122	606*																		
CSTR5	002164	620*																		
DATA1	013620	636*																		
DATA2	013622	262*	264*	267*	269	1752*	1761	1885*	1895	1935*	2451*	2453*	2455*	2457						
DATA3	013624	263*	264	265*	268*	1753*	1763	1886*	1904	1936*	2452*	2453	2454*	2456*						
DATA4	013626	1887*	1914	1918	1937*															
DHMC5R	016156	1809*	1924	1938*																
		290	291	295	351*	358*	367*	568	580*	581	584*	585	593*	594						
		597*	598	606*	607	610*	611	620*	621	625*	626	636*	637	640*						
		641	651*	654*	668*	671*	684*	686*	689*	713*	731*	738*	745*	746						
		763*	768*	771*	772	781*	794*	797*	798*	803*	804*	805	814*	815						
		819*	820*	821	837*	841*	842	844*	845*	846*	847*	849*	852*	853						
		876*	881*	882*	885*	886*	888*	891*	892	909*	911*	913*	915*	917*						
		919*	921*	928*	932*	938*	941*	971*	972*	975*	977*	986*	990*	992						
		996	1002	1008*	1010*	1024*	1025*	1029*	1032*	1033	1040*	1044*	1046	1050						
		1056	1062*	1065*	1108*	1110*	1112*	1114*	1116*	1118*	1120*	1184*	1242*	1248*						
		1249*	1352*	1354*	1395*	1437*	1439*	1480*	1520*	1562*	1564*	1616*	1625*	1626*						
		1679	1687*	1747*	1752	1793*	1794	1801*	1805*	1815*	1869*	1883*	1896*	1902*						
		2462*	2463*	2465*	2469*	2479*	2492*													
DHMLSR	016160	295*	296*	571	909*	911*	913*	915*	917*	919*	921*	931*	942	950*						
		951	958*	978*	1028*	1108*	1110*	1112*	1114*	1116*	1118*	1120*	1186*	1250*						

HEMT3	004036	876#		
HEMT3A	004060	880#	901	906
HEMT3B	004072	832#	884	
HEMT3C	004134	891#	904	
HEMT3D	004160	895#	897#	
HEMT3E	004172	898#	902#	
HEPASS	017122	1955	2357#	
HEATAL	017333	2101	2357#	
HLINE	017261	1098	2355#	
HLINEI	017314	1100	2356#	
HLINER	016325	2138	2356#	
HLINEI	016362	2093	2357#	
HLINSL	017210	298	2351#	
HPFAIL	017431	2434	2359#	
HPF1	017451	2440	2359#	
HQM	017252	2371	2353#	
HPG80	017154	287	2350#	
HULAY	016656	1776	2352#	
HUELOR	016632	1771	2352#	
HSG	015316	2347*	2353#	
HSTATE	016274	2118	2353#	
HTEST	017242	306	2352#	
HTITLE	017046	251	2348#	
HTRANE	016413	2064	2358#	
HTRNOE	017365	1755	2358#	
HT103A	016675	1288	2344#	
HT103T	016510	1148	2340#	
HT202A	016724	1662	2345#	
HT202T	016561	1315	2341#	
HUX1	004206	909#		
HUX1A	004234	909#		
HUX1B	004302	909#		
HUX1C	004340	909#		
HUX1D	004352	909#		
HUX1E	004372	909#		
HUX1F	004424	909#		
HUX11	007220	1108#		
HUX11A	007236	1108#		
HUX11B	007304	1108#		
HUX11C	007342	1108#		
HUX11D	007354	1108#		
HUX11E	007374	1108#		
HUX11F	007426	1108#		
HUX12	007430	1110#		
HUX12A	007446	1110#		
HUX12B	007514	1110#		
HUX12C	007552	1110#		
HUX12D	007564	1110#		
HUX12E	007604	1110#		
HUX12F	007636	1110#		
HUX13	007640	1112#		
HUX13A	007656	1112#		
HUX13B	007724	1112#		
HUX13C	007762	1112#		

MUX130	007774	1112#
MUX132	010014	1112#
MUX134	010046	1112#
MUX14	010050	1114#
MUX14A	010066	1114#
MUX14B	010134	1114#
MUX14C	010172	1114#
MUX14D	010204	1114#
MUX14E	010224	1114#
MUX14F	010256	1114#
MUX15	010260	1116#
MUX15A	010276	1116#
MUX15B	010330	1116#
MUX15C	010356	1116#
MUX15D	010370	1116#
MUX15E	010406	1116#
MUX15F	010442	1116#
MUX16	010444	1118#
MUX16A	010462	1118#
MUX16B	010514	1118#
MUX16C	010542	1118#
MUX16D	010554	1118#
MUX16E	010572	1118#
MUX16F	010626	1118#
MUX17	010630	1120#
MUX17A	010646	1120#
MUX17B	010700	1120#
MUX17C	010726	1120#
MUX17D	010740	1120#
MUX17E	010756	1120#
MUX17F	011012	1120#
MUX2	004444	911#
MUX2A	004472	911#
MUX2B	004540	911#
MUX2C	004576	911#
MUX2D	004610	911#
MUX2E	004630	911#
MUX2F	004662	911#
MUX3	004702	913#
MUX3A	004730	913#
MUX3B	004776	913#
MUX3C	005034	913#
MUX3D	005046	913#
MUX3E	005066	913#
MUX3F	005120	913#
MUX4	005140	915#
MUX4A	005166	915#
MUX4B	005234	915#
MUX4C	005272	915#
MUX4D	005304	915#
MUX4E	005324	915#
MUX4F	005356	915#
MUX5	005376	917#
MUX5A	005424	917#

SAVR1	016210	2219#	2298#	2508#											
SAVR2	016212	2220#	2299#	2509#											
SAVR3	016214	2221#	2300#	2510#											
SAVR4	016216	2222#	2301#	2511#											
SAVR5	016220	2223#	2302#	2512#											
SAVR6	016222	2224#	2303#	2513#											
SAVSP	104005	132#	2153#												
SCENR	000040	85#	1620#	621	625	626	636	640	990	1008	1044	1062	1242	1616	
SCNT1	006430	1747#	1869#	1007											
SCNT1A	006472	971#	993#												
SCNT1B	006473	977#	980#												
SCNT1C	006550	987#	1014#												
SCNT1D	006630	993#	1001#												
SCNT2	006700	1000#	1000#	1004	1008#										
SCNT2A	006734	1003#	1053#	1061											
SCNT2B	007030	1023#	1031#												
SCNT2C	007112	1041#	1049#												
SCNT2D	007132	1038#	1051#												
SCOPE	104002	1042#	1054#	1058	1062#										
		129#	857#	53	601	614	630	644	650	677	694	713	731	756	
		784#	830#	828	907	909	911	913	915	917	919	921	962	1015	
SCOPEF	104003	1069	1108	1110	1112	1114	1116	1118	1120	1120	1120	1120	1120	1120	
		130#	750#	776	809	825	861	900	909	911	913	915	917	919	
		921	947	955	998	1006	1052	1060	1108	1110	1112	1114	1116	1118	
		1120													
SECRX	000020	104#	921	1120	1405	1409	1530	1535							
SECRXF	010000	93#	1705	1709	1729	1733									
SECTX	000010	103#	915	921	1114	1120	1396	1405	1440	1521	1535	1565			
SELMSK	016262	743#	743	753#	755#	769	779#	909#	911#	913#	915#	917#	919#	921#	
		935#	939	959#	976#	987	1011#	1027#	1041	1063#	1108	1110	1112	1114	
		1116	1118	1120	2529#										
SETUP	104020	143#	1153	1320											
SETJP8	013146	1826	1835	1835#											
SETJPS	012722	1705#	2531												
SETJP1	012762	1794#	1795												
SETJP2	012776	1801#													
SETJP4	013110	1818	1825#	1830	1832										
SINGLE	000001	60#	909	911	913	915	917	919	921	1077#	1108	1110	1112	1114	
		1116	1118	1120											
SINTEL	001724	345#	369#	374#	2358	2360#									
SMSK	016264	909#	911#	913#	915#	1103#	1110#	1112#	1114#	2530#					
SP	%000006	71#	222#	213#	241	247	261#	304#	327#	1746#	1789	1792#	1837#	1842	
		1846#	1864	1894#	1971#	1972#	1973#	1974#	1975#	1976#	1977#	1978	1990#	1993	
		1996	2011#	2020#	2036#	2038#	2150	2224	2225#	2280	2305	2306#	2346	2351#	
		2410#	2411#	2412#	2413#	2414#	2415#	2416#	2417	2424#	2425	2426	2427	2428	
		2429	2430	2441#	2447#	2448#	2467	2458	2474	2482	2483				
ST	000200	703#	713#	722#	731#										
STACK	020422	78#	232	261	304										
START	001000	173	229#												
STARTN	001326	260	304#	352											
STARTO	001100	237	244	248#	2438										
STARTI	001142	256	259#												
STEP	000400	89#	771	798	804	820	846	852	882	886	891	909	911	913	
		915	917	919	921	932	977	1029	1108	1110	1112	1114	1116	1118	

TYPBR	015122	2305	2579	
TYPBR9	015133	2307	2308	2323
TYPBR1	015176	2316	2318	
TYPBR2	015212	2322	2326	
T0	001726	555	2609	
T1	001754	579	2620	
T10	002356	692	2620	
T100	007162	1094	2622	
T101	007220	1108	2633	
T102	007430	1110	2633	
T103	007640	1112	2633	
T103A	011054	1149	1153	
T103A1	011062	1156	1157	
T103B	011066	1155	1165	
T103B1	011076	1171	1175	
T103B2	011102	1173	1177	
T103C	011106	1169	1184	
T10301	011150	1210	1216	
T10302	011154	1212	1218	
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T10304	011164	1214	1222	
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T103E2	011320	1273	1279	
T103E3	011324	1274	1281	
T103E4	011330	1275	1283	
T104	010050	1114	2533	
T105	010260	1116	2633	
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T11	002440	713	2620	
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T13	002604	713	2620	
T14	002666	713	2620	
T15	002750	731	2620	
T16	003030	731	2620	
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T20	003170	731	2620	
T200	011014	1143	2635	
T201	011334	1286		
T202A	011410	1316	1320	
T202A1	011416	1323	1324	
T202B	011422	1322	1332	
T202B1	011432	1338	1342	
T202B2	011436	1340	1344	
T202C	011442	1336	1352	
T202D	011456	1354		
T20201	011520	1377	1382	
T20202	011524	1378	1384	
T20203	011530	1379	1386	
T20204	011534	1380	1388	

T2736	011540	1381	1395
T27	011602	1418	1423
T27	011606	1419	1425
T27	011612	1420	1427
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T27	011776	1505	1513
T27	012002	1506	1520
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T27	012050	1545	1551
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T27	012152	1558	1595
T27	012156	1559	1597
T27	012162	1590	1610
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T27	012222	1614	1619
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T27	012324	1650	1658
T21	003250	737	2620
T22	003344	761	2620
T23	003462	793	2620
T24	003520	836	2620
T25	004036	875	2620
T26	004236	909	2620
T27	004444	911	2620
T3	004550	605	2620
T30	004752	913	2620
T300	011350	1310	2638
T31	005140	915	2620
T32	005376	917	2620
T33	005614	919	2620
T34	006032	921	2620
T35	006250	927	2620
T36	006432	970	2620
T37	006702	1022	2620
T4	002122	619	2620
T5	002164	635	2620
T6	002226	649	2620
T7	002302	666	2620
VECSTA	001172	264	270

1620

H09

ADD	267	268	285	296	1976	2017	2038	2225	2245	2306	2455	2456	1108	1110	1112
ASL	753	779	909	911	913	915	917	919	921	959	1011	1063			
ASR	1114	1974	2015	2016	2389	2390	2391								
EXB	314														
	256	262	281	292	365	569	572	586	599	612	627	642	744	748	770
	774	807	823	839	838	909	911	913	915	917	919	921	940	945	953
	999	1004	1042	1038	1108	1110	1112	1114	1116	1118	1120	1246	1620	1684	1696
	1695	1698	1702	1707	1719	1722	1726	1731	1851	1857	1900	1907	1921	1927	1957
	2002	2010	2019	2031	2035	2151	2159	2163	2171	2310	2312	2319	2321	2359	2364
BGT	2384														
PLI	2397														
IS	2359														
BIC	311	321	584	597	610	625	640	655	672	688	713	731	739	762	795
	878	847	877	909	911	913	915	917	919	921	929	973	991	1033	1025
	1045	1062	1096	1109	1110	1112	1114	1116	1118	1120	1250	1438	1440	1563	1565
	1627	1692	1916	1975	2464										
BICB	2004	2362	2005												
BIS	330	650	654	667	671	683	689	713	731	771	797	798	804	820	845
	846	852	857	892	836	891	896	909	911	913	915	917	919	921	932
	950	977	989	990	945	1029	1043	1044	1049	1108	1110	1112	1114	1116	1118
	1120	1186	1353	1355	1396	1481	1521	1636	1699	1704	1708	1720	1723	1728	1732
	2461	2465	2470												
BISB	1010	1065	2325	2386											
BIT	259	290	291	591	595	594	598	607	611	621	626	637	641	743	769
	815	842	909	911	913	915	917	919	921	939	937	1033	1041	1108	1110
	1112	1114	1116	1118	1120	1244	1619	1694	1697	1700	1705	1709	1718	1721	1724
	1729	1733	1794	2003	2005	2007	2032	2034	2148	2170					
BLE	324														
PLT	2263	2399													
RTI	2202														
BNE	254	272	2357												
	249	270	274	318	347	354	361	592	595	608	622	638	755	783	800
	813	816	829	843	856	855	857	834	835	904	906	909	911	913	915
	917	919	921	934	961	500	1014	1031	1034	1038	1108	1110	1112	1114	1116
	1118	1120	1711	1735	1795	1826	1828	1830	1832	1874	1876	1953	2004	2006	2008
	2033	2149	2155	2233	2249	2251	2269	2316	2336	2369	2437	2458			
BPL	993	1047	1745	1915	1989	2308	2334	2367							
BR	237	258	657	674	652	713	731	1000	1054	1159	1178	1325	1345	1690	1713
	1758	1994	1997	2025	2049	2066	2086	2104	2121	2140	2261	2313	2323	2326	2372
	2392	2420	2473												
CLR	230	233	265	332	333	334	344	345	351	359	367	651	659	684	713
	731	738	741	763	764	818	837	851	876	870	909	911	913	915	917
	919	921	928	936	943	959	972	1025	1108	1110	1112	1114	1116	1118	1120
	1243	1248	1289	1618	1625	1737	1796	1816	1817	1822	1823	1853	1864	1865	1870
	1871	1883	1951	2022	2045	2047	2048	2063	2082	2100	2152	2257	2345	2360	2377
	2462	2469	2479												
CMP	269	323	346	353	360	364	747	773	806	822	858	897	909	911	913
	915	917	919	921	952	1003	1057	1109	1110	1112	1114	1116	1118	1120	1683
	1685	1850	1899	1906	1920	1926	2150	2258	2262	2396	2398	2457			
CMPB	855	894	2309	2311	2315	2318	2320	2363	2378	2381	2383	2387			
COM	242	2271													
DEC	754	782	799	812	828	864	866	883	903	905	909	911	913	915	917
	919	921	933	960	979	1013	1030	1067	1108	1110	1112	1114	1116	1118	1120

.EVEN	372	2566														
.IFEQ	909	911	913	915	917	919	921	1108	1110	1112	1114	1116	1118	1120		
.IFF	909	911	913	915	917	919	921	1108	1110	1112	1114	1116	1118	1120		
.IFT	909	911	913	915	917	919	921	1108	1110	1112	1114	1116	1118	1120		
.IFTF	909	911	913	915	917	919	921	1108	1110	1112	1114	1116	1118	1120		
.IIF	713	731	2620	2633												
.LIST	49	60	127	129	129	130	131	132	133	134	135	136	137	138	139	
	140	141	142	144	144	145	146	147	198	210	551	564	565	579	592	
	605	619	635	649	666	682	703	713	722	731	737	761	793	836	875	
	909	911	913	915	917	919	921	927	970	1022	1077	1093	1094	1108	1110	
.MACRO	1112	1114	1116	1118	1120	1142	1143	1236	1309	1310	2617	2620	2630	2633	1072	
	51	176	193	192	199	205	212	377	397	418	487	557	696	715		
.MLIST	1036	1135	1232	2611	2624											
	48	60	127	128	129	130	131	132	133	134	135	136	137	138	139	
	140	141	142	143	144	145	146	147	198	210	376	564	565	579	592	
	605	619	635	649	666	682	703	713	722	731	737	761	793	836	875	
	909	911	913	915	917	919	921	927	970	1022	1077	1093	1094	1108	1110	
.PAGE	1112	1114	1116	1118	1120	1142	1143	1236	1309	1310	2617	2620	2630	2633	1119	
.REPT	713	731	910	912	914	916	918	920	922	1109	1111	1113	1115	1117		
.TITLE	152	312	704	723	2618	2631										
	1															

ERRORS DETECTED: 0

*DZDHB, DZDHB/CRF=DZDHB
RUN-TIME: 21 38 8 SECONDS
CORE USED: 11K

