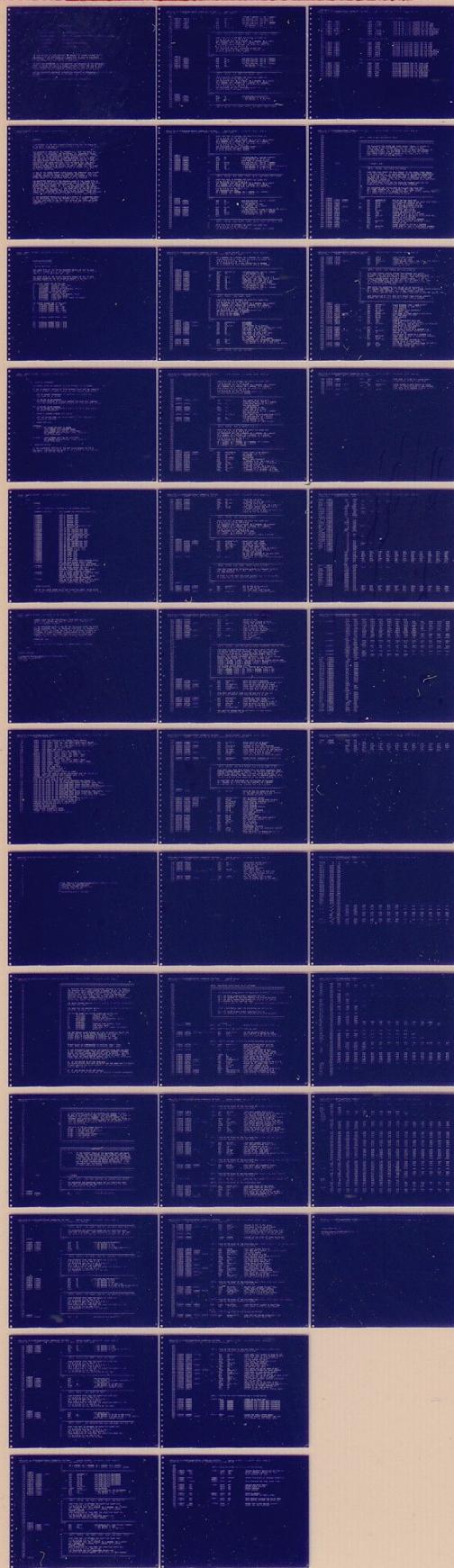


**PDP-11/70**

DIAGNOSTIC/BOOTSTRAP  
**MD-11-DEKBH-A**  
(M9301-YC) PATTERN

EP-DEKBH-A-DL  
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MADE IN USA



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IDENTIFICATION

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PRODUCT CODE! MAINDEC-11-DEKBH-A-D  
PRODUCT NAME! PDP-11/70 DIAGNOSTIC/BOOTSTRAP (M9381-YC) PATTERN  
DATE CREATED! 21-JULY-75  
MAINTAINER! DIAGNOSTIC ENGINEERING  
AUTHOR! DALE A. ROEDGER

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1) ABSTRACT

-----  
THIS PROGRAM IS THE ROM DIAGNOSTIC/BOOTSTRAP FOR THE M9301-YC  
IT IS MEANT TO BE FOR THE PDP-11/70 ONLY, AND WILL FAIL ON ALL  
OTHER PDP-11 PROCESSORS.

THE DIAGNOSTIC PORTION OF THE PROGRAM WILL TEST THE BASIC CPU,  
INCLUDING: THE BRANCHES, THE REGISTERS, ALL ADDRESSING MODES,  
AND MOST OF THE INSTRUCTIONS IN THE PDP-11 REPERTOIRE. IT WILL  
THEN SET THE STACK POINTER TO KERNEL D-SPACE M.A.R. 7, CHECK  
AND TURN ON, IF REQUESTED, MEMORY MANAGEMENT AND THE UNIBUS MAP,  
AND CHECK MEMORY FROM VIRTUAL ADDRESS 1000 TO 197776. AFTER  
MAIN MEMORY HAS BEEN VERIFIED, WITH THE CACHE OFF, THE CACHE  
MEMORY WILL BE TESTED TO VERIFY THAT "HITS" OCCUR PROPERLY.  
THEN MAIN MEMORY WILL BE SCANNED AGAIN TO INSURE THAT THE CACHE  
IS WORKING PROPERLY THROUGHOUT THE 28K OF MEMORY TO BE USED IN  
THE "BOOT" OPERATION.

IF ONE OF THE CACHE MEMORY TESTS FAILS, THE OPERATOR CAN ATTEMPT  
TO "BOOT" THE SYSTEM ANYWAY BY PRESSING "CONTINUE", THIS WILL  
CAUSE THE PROGRAM TO FORCE "MISSES" IN BOTH GROUPS OF THE CACHE  
BEFORE GOING TO THE BOOTSTRAP SECTION OF THE PROGRAM.

THE BOOTSTRAP PORTION OF THE PROGRAM LOOKS AT THE LOWER BYTE OF  
THE SWITCH REGISTER TO DETERMINE WHICH ONE OF 8 DEVICES AND WHICH  
DRIVE NUMBER TO ATTEMPT THE "BOOT" FROM, SWITCHES <02 | 00>  
SELECT THE DRIVE NUMBER (0 - 7), AND SWITCHES <06 | 03> SELECT  
THE DEVICE CODE (1 - 11). IF THE LOWER BYTE OF THE SWITCH REGIS-  
TER IS ZERO, THE PROGRAM WILL READ THE SET OF SWITCHES ON THE  
M9301-YC TO DETERMINE THE DEVICE AND DRIVE NUMBER. THESE SWITCHES  
CAN BE SET BY FIELD SERVICE TO SELECT A "DEFAULT BOOT" DEVICE.

IF THE BOOTSTRAP OPERATION FAILS AS A RESULT OF A HARDWARE ERROR  
IN THE PERIPHERAL DEVICE THE PROGRAM WILL DO A "RESET" INSTRU-  
CTION AND JUMP BACK TO THE TEST THAT SETS UP AND TURNS ON MEMORY  
MANAGEMENT AND TESTS MEMORY. THEN THE PROGRAM WILL ATTEMPT TO  
"BOOT" AGAIN.

.REM \

2) STARTING PROCEDURE

2.1 SWITCH SETTINGS

THE LOWER BYTE OF THE SWITCH REGISTER SHOULD BE SET TO HAVE THE DRIVE NUMBER (0 - 7) IN SWITCHES <02 : 00>, AND THE DEVICE CODE (1 - 11) IN SWITCHES <06 : 03>.

THE UPPER BYTE OF THE SWITCH REGISTER SHOULD BE SET TO HAVE THE BANK NUMBER OF THE 32K BLOCK OF MEMORY TO BE USED FOR THE BOOTSTRAP OPERATION (0 - 17) IN SWITCHES <15 : 12>.

THE DEVICE CODES ARE AS FOLLOWS:

- 1) TM11/TU10 MAGNETIC TAPE, TM11
- 2) TC11/TU56 DECTAPE, TC11-G
- 3) RK11/RK05 DECPACK DIS CARTRIDGE, RK11-D
- 4) RP11/RP03 DISK PACK, RP11-C
- 5) RESERVED FOR FUTURE DEVICE
- 6) RH70/TU16 MAGNETIC TAPE SYSTEM, THU16
- 7) RH70/RP04 DISK PACK, RWP04
- 10) RH70/RS04 FIXED HEAD DISK, RHS04 (OR HWS03)
- 11) RX11/RXB1 DISKETTE

THE MEMORY BLOCKS ARE AS FOLLOWS:

- 0) PHYSICAL MEMORY 0 - 28K
- 1) PHYSICAL MEMORY 32K - 68K
- 2) PHYSICAL MEMORY 64K - 92K
- 3) PHYSICAL MEMORY 96K - 124K
- 4) PHYSICAL MEMORY 128K - 156K
- .
- 13) PHYSICAL MEMORY 256K - 284K
- .
- 14) PHYSICAL MEMORY 384K - 412K
- 15) PHYSICAL MEMORY 416K - 444K
- 16) PHYSICAL MEMORY 448K - 476K
- 17) PHYSICAL MEMORY 480K - 508K

.REM \

## 2.2 STARTING ADDRESSES

THE NORMAL STARTING ADDRESS FOR THIS PROGRAM IS 177765000.

IF THE DIAGNOSTIC PORTION OF THIS PROGRAM FAILS AND THE OPERATOR WANTS TO ATTEMPT TO "BOOT" ANYWAY, HE MUST FOLLOW THESE STEPS:

- 1) SET UP MEMORY MANAGEMENT IF "BOOTING" INTO OTHER THAN THE LOWER 28K OF MEMORY.
- 2A) IF DEVICE IS ON MASSBUS;  
SET STACK POINTER TO A VALID ADDRESS AND LOAD THAT ADDRESS WITH THE MEMORY BANK NUMBER HE WOULD PUT INTO SWITCHES <15:12>.
- 2B) IF DEVICE IS ON UNIBUS;  
SET UP UNIBUS MAP REGISTERS 0 THRU 6 TO MAP TO SAME MEMORY AS MEMORY MANAGEMENT.
- 3) DEPOSIT ADDRESS 173000 INTO THE PC.
- 4) SET THE DEVICE CODE AND DRIVE NUMBER IN THE LOWER BYTE OF THE SWITCH REGISTER.
- 5) PRESS CONTINUE.

### EXAMPLES:

- A) RP04 -- SET STACK POINTER TO 40000  
LOAD 000000 INTO ADDRESS 40000  
LOAD 173000 INTO THE PC (177777707)  
SET 000070 INTO SWITCHES (RP04 DRIVE 0)  
PRESS "CONTINUE"
- B) RK05 -- LOAD 173000 INTO THE PC (177777707)  
SET 000030 INTO SWITCHES (RK05 DRIVE 0)  
PRESS "CONTINUE"

## 2.3 OPERATOR ACTION

IF THE DIAGNOSTIC PORTION OF THE ROM FAILS RECORD THE PC OF THE "HALT" INSTRUCTION AND REFER TO THE LISTING TO FIND OUT WHAT PORTION OF THE MACHINE FAILED.

.REM \

3) ERRORS

\*\*\*\*\*

3.1 LIST OF ERROR HALTS INDEXED BY THE ADDRESS DISPLAYED

| ADDRESS DISPLAYED | TEST NUMBER AND SUBSYSTEM UNDER TEST  |
|-------------------|---|
| 17765004          | TEST 1 BRANCH TEST  |
| 17765020          | TEST 2 BRANCH TEST  |
| 17765036          | TEST 3 BRANCH TEST  |
| 17765052          | TEST 4 BRANCH TEST  |
| 17765066          | TEST 5 BRANCH TEST  |
| 17765076          | TEST 6 BRANCH TEST  |
| 17765134          | TEST 7 REGISTER DATA PATH TEST  |
| 17765146          | TEST 10 BRANCH TEST   |
| 17765166          | TEST 11 CPU INSTRUCTION TEST  |
| 17765204          | TEST 12 CPU INSTRUCTION TEST  |
| 17765214          | TEST 13 CPU INSTRUCTION TEST  |
| 17765222          | TEST 14 "COM" INSTRUCTION TEST  |
| 17765236          | TEST 14 CPU INSTRUCTION TEST  |
| 17765260          | TEST 15 CPU INSTRUCTION TEST  |
| 17765270          | TEST 16 BRANCH TEST   |
| 17765312          | TEST 16 CPU INSTRUCTION TEST  |
| 17765346          | TEST 17 CPU INSTRUCTION TEST  |
| 17765360          | TEST 20 CPU INSTRUCTION TEST  |
| 17765374          | TEST 20 CPU INSTRUCTION TEST  |
| 17765450          | TEST 21 KERNEL P.A.R. TEST  |
| 17765474          | TEST 22 KERNEL P.D.R. TEST  |
| 17765518          | TEST 23 "JSR" TEST  |
| 17765520          | TEST 23 "JSR" TEST  |
| 17765530          | TEST 23 "RTS" TEST  |
| 17765542          | TEST 23 "RTI" TEST  |
| 17765550          | TEST 23 "JMP" TEST  |
| 17765570          | TEST 25 MAIN MEMORY DATA COMPARE ERROR  |
| 17766000          | TEST 25 MAIN MEMORY PARITY ERROR<br>NO RECOVERY POSSIBLE FROM THIS ERROR  |
| 17773644          | TEST 26 CACHE MEMORY DATA COMPARE ERROR   |
| 17773654          | TEST 26 CACHE MEMORY NO "HIT"<br>PRESSING "CONTINUE" HERE WILL CAUSE<br>"BOOT" ATTEMPT FORCING "MISSES"           |
| 17773736          | TEST 27 CACHE MEMORY DATA COMPARE ERROR   |
| 17773746          | TEST 27 CACHE MEMORY NO "HIT"<br>PRESSING "CONTINUE" HERE WILL CAUSE<br>"BOOT" ATTEMPT FORCING "MISSES"           |
| 17773764          | TEST 25 OR 26 CACHE MEMORY PARITY ERROR<br>PRESSING "CONTINUE" HERE WILL CASUE<br>"BOOT" ATTEMPT FORCING "MISSES" |

3.2 ERROR RECOVERY

MOST OF THE ABOVE ERROR HALTS ARE "HARD" FAILURES, WHICH MEANS  
THAT THERE IS NO RECOVERY FROM THEM, ESPECIALLY THE TWO (2) MAIN

,MAIN. MACY11 27(657) 18-JUN-75 07:18 PAGE 1-5  
DEKBHA.MAN

MEMORY HALTS ARE NOT RECOVERABLE. YOUR BEST BET IS TO TRY TO "BOOT" INTO ANOTHER 32K BANK OF MEMORY IF IT APPEARS TO BE A MAIN MEMORY FAILURE.

IF THE PROCESSOR HALTS IN ONE OF THE TWO CACHE TESTS THE ERROR CAN BE RECOVERED FROM. BY PRESSING "CONTINUE" THE PROGRAM WILL EITHER ATTEMPT TO FINISH THE TEST (IF AT EITHER: 17773644 OR 17773736) OR FORCE "MISSES" IN BOTH GROUPS OF THE CACHE AND ATTEMPT TO "BOOT" THE SYSTEM MONITOR WITH THE CACHE FULLY DISABLED (IF AT EITHER: 17773654, 17773744, OR 17773764).

4) EXECUTION TIME  
-----

THE RUN TIME FOR THIS PROGRAM IS APPROXIMATELY 3 SECONDS.

\END

ERRORS DETECTED: 0

\*,DEKBHA/NL:SEQ/NL:LOC/NL:BIN/NL:SYM=DEKBHA.MAN  
RUN-TIME: 0 1 0 SECONDS  
CORE USED: 4K

118 TEST1 THIS TEST VERIFIES THE UNCONDITIONAL BRANCH  
130 TEST2 TEST "SUB", MODE "0", AND "BHI", "BVS", "BHI", "BLOS"  
147 TEST3 TEST "DEC", MODE "0", AND "BPL", "BEQ", "BGE", "BGT", "BLE"  
169 TEST4 TEST "ROR", MODE "0", AND "BVC", "BHIS", "BHI", "BNE"  
190 TEST5 TEST "BHI", "BLT", AND "BLOS"  
210 TEST6 TEST "BLE" AND "BGT"  
228 TEST7 TEST REGISTER DATA PATH AND MODES "2", "3", "6"  
257 TEST10 TEST "ROL", "BCC", "BLT", AND MODE "6"  
277 TEST11 TEST "ADD", "INC", "COM", AND "BCC", "BLE"  
301 TEST12 TEST "ROR", "BIS", "ADD", AND "BLO", "BGE"  
324 TEST13 TEST "DEC" AND "BLOS", "BLT"  
343 TEST14 TEST "COM", "BIC", AND "BGT", "BGE", "BLE"  
368 TEST15 TEST "ADC", "CMP", "BIT", AND "BNE", "BGT", "BEQ"  
394 TEST16 TEST "MOVB", "SOB", "CLR", "TST" AND "BPL", "BNE"  
421 TEST17 TEST "ASR", "ASL"  
452 TEST20 TEST ASH, AND SWAB  
480 TEST21 TEST 16 KERNEL P,A,R.'S  
517 TEST22 TEST AND LOAD KIPDR'S  
545 TEST23 TEST "JSR", "RTS", "RTI", & "JMP"  
576 TEST24 LOAD AND TURN ON MEMORY MANAGEMENT AND THE UNIBUS MAP  
630 TEST25 TEST MAIN MEMORY FROM VIRTUAL 1000 TO 20K  
682 BOOTSTRAP ENTRY POINT IS AT 17773000  
707 CODE TO WAIT FOR TU10 TO COME ON LINE  
714 THIS IS THE CODE TO READ THE SWITCH REGISTER AND DECODE IT  
736 THIS IS THE START OF THE TM11/TU10 BOOT STRAP (MAGNETIC TAPE, TM11)  
754 THIS IS THE START OF THE TC11/TU96 BOOT STRAP (DECTAPE, TC11-G)  
767 THIS IS THE START OF THE RK11/RK09 BOOT STRAP (DECPACK DISK CARTRIDGE, RK11-D)  
775 THIS IS THE START OF THE RP11/RP03 BOOT STRAP (DISK PACK, RP11-C)  
780 THIS IS THE START OF THE COMMON READ CODE  
800 THIS IS THE START OF THE RH70/TU16 BOOT STRAP (MAGNETIC TAPE SYSTEM, THU16)  
822 THIS IS THE START OF THE RH70/RP04 BOOT STRAP (DISK PACK, RHP04)  
832 THIS IS THE START OF THE RH70/RS04 BOOT STRAP (FIXED HEAD DISK, RHS04)  
839 THIS IS THE START OF THE COMMON RH-70 CODE  
845 THIS IS THE START OF THE RX11/RX01 BOOT STRAP (FLOPPY DISK)  
872 THIS IS THE START RESERVED FOR A FUTURE DEVICE  
887 FUNCTION CODES FOR ALL OF THE DEVICES  
912 COMMAND AND STATUS REGISTER ADDRESS TABLE  
924 FUNCTION POINTER TABLE  
936 STARTING ADDRESS TABLE  
949 CACHE MEMORY DIAGNOSTIC TESTS  
967 TEST26 TEST CACHE DATA MEMORY  
1009 TEST27 TEST VIRTUAL 20K WITH CACHE ON

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;•  
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;•





PDP-11/70 DIAGNOSTIC/BOOTSTRAP (M93B1-VC) PATTERN MACY11 27(697) 17-JUN-75 18:04 PAGE 4  
DEKBHA.P11 TEST1 THIS TEST VERIFIES THE UNCONDITIONAL BRANCH

129 ;ooooooooooooooo  
130 .SBTTL TEST2 TEST "SUB", MODE "0", AND "BMI","BVS","BHI","BLOS"  
131 ;  
132 ; THE REGISTERS AND CONDITION CODES ARE ALL UNDEFINED WHEN  
133 ; THIS TEST IS ENTERED. UPON COMPLETION OF THIS TEST THE "SP"  
134 ; (R6) SHOULD BE ZERO AND ONLY THE "Z" FLIP-FLOP WILL BE SET.  
135 ;  
136 ;ooooooooooooooo  
137 165004 TST2:  
138  
139 165004 005006 CLR SP ;N=0,Z=1;V=0,C=0,SP=0000000  
140 165006 100403 BRI 15 ; V 324 BRANCH IF N=1  
141 165010 102402 BVS 15 ; V 324 BRANCH IF V=1  
142 165012 101001 BHI 15 ; V 321 BRANCH IF Z AND C ARE BOTH 0  
143 165014 101401 BLOS TST3 ; \* 329 BRANCH IF (Z XOR C)=1  
144 165016 000000 15: HALT  
145  
146 ;ooooooooooooooo  
147 .SBTTL TEST3 TEST "DEC", MODE "0", AND "BPL","BEQ","BGE","BGT","BLE"  
148 ;  
149 ; UPON ENTERING THIS TEST THE CONDITION CODES ARE:  
150 ; N = 0, Z = 1, V = 0, AND C = 0.  
151 ; THE REGISTERS ARE: R0 = ?, R1 = ? R2 = ?  
152 ; R3 = ? R4 = ? R5 = ? SP = 000000  
153 ; UPON COMPLETION OF THIS TEST THE CONDITION CODES WILL BE:  
154 ; N = 1, Z = 0, V = 0, AND C = 0  
155 ; THE REGISTERS AFFECTED BY THE TEST ARE:  
156 ; SP = 177777  
157 ;  
158 ;ooooooooooooooo  
159 165020 TST3:  
160 165020 005306 DEC SP ;N=1,Z=0;V=0,C=0,SP=177777  
161 165022 100204 BPL 15 ; V 321 BRANCH IF N=0  
162 165024 001403 BEQ 15 ; V 324 BRANCH IF Z=1  
163 165026 002002 BGE 15 ; V 322 BRANCH IF (N XOR V)=0  
164 165030 003001 BGT 15 ; V 322 BRANCH IF Z AND (N XOR V) ARE BOTH 0  
165 165032 003401 BLE TST4 ; \* 326 BRANCH IF (Z OR (N XOR V))=1  
166 165034 000000 15: HALT  
167  
168 ;ooooooooooooooo  
169 .SBTTL TEST4 TEST "ROR", MODE "0", AND "BVC","BHIS","BHI","BNE"  
170 ;  
171 ; UPON ENTERING THIS TEST THE CONDITION CODES ARE:  
172 ; N = 1, Z = 0, V = 0, AND C = 0.  
173 ; THE REGISTERS ARE: R0 = ?, R1 = ? R2 = ?  
174 ; R3 = ? R4 = ? R5 = ? SP = 177777  
175 ; UPON COMPLETION OF THIS TEST THE CONDITION CODES WILL BE:  
176 ; N = 0, Z = 0, V = 1, AND C = 1  
177 ; THE REGISTERS AFFECTED BY THE TEST ARE:  
178 ; SP = 077777  
179 ;  
180 ;ooooooooooooooo  
181 165036 TST4:  
182 165036 006006 ROR SP ;N=0,Z=0;V=1,C=1,SP=077777

PDP-11/73 DIAGNOSTIC/BOOTSTRAP (M9301-YC) PATTERN MACY11 27(097) 17-JUN-75 10:04 PAGE 5  
DEKBHA.P11 TEST4 TEST "ROR", MODE "0", AND "BVC","BHIS","BHI","BNE"

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183 165040 102003          BVC    15      ; V 321 BRANCH IF V=0
184 165042 103002          BHIS   15      ; V 321 BRANCH IF C=0
185 165044 101001          BHI    15      ; V 321 BRANCH IF C AND Z ARE BOTH 0
186 165046 001001          BNE    TST5    ; = 320 BRANCH IF Z=0
187 165050 000000          18:    HALT

188
189
190           ;oooooooooooooooooooooooooooooooooooooooooooo
191           .SBTTL TEST5 TEST "BHI", "BLT", AND "BLOs"
192
193           ;o
194           ;o UPON ENTERING THIS TEST THE CONDITION CODES ARE:
195           ;o N = 0, Z = 0, V = 1, AND C = 1.
196           ;o THE REGISTERS ARE: R0 = ? , R1 = ? R2 = ?
197           ;o R3 = ? R4 = ? R5 = ? SP = 077777
198           ;o UPON COMPLETION OF THIS TEST THE CONDITION CODES WILL BE:
199           ;o N = 1, Z = 1, V = 1, AND C = 1
200           ;o THE REGISTERS ARE ALL UNAFFECTED BY THE TEST.
201
202           ;oooooooooooooooooooooooooooooooooooooooooooo
203           TST5:
204           SEE
205           BHI    15      ;N=0,Z=1;V=1,C=1
206           SEN
207           BLT    15      ; V 321 BRANCH IF Z AND C ARE BOTH 0
208           BLOs   TST6    ;N=1,Z=1;V=1,C=1
209           18:    HALT    ; V 324 BRANCH IF (N XOR V)=1
210           ;o 325 BRANCH IF (Z OR C)=1
211           ;o ;STOP HERE IF A BRANCH FAILED
212
213           ;oooooooooooooooooooooooooooooooooooooooooooo
214           .SBTTL TEST6 TEST "BLE" AND "BGT"
215
216           ;o
217           ;o UPON ENTERING THIS TEST THE CONDITION CODES ARE:
218           ;o N = 1, Z = 1, V = 1, AND C = 1.
219           ;o THE REGISTERS ARE: R0 = ? , R1 = ? R2 = ?
220           ;o R3 = ? R4 = ? R5 = ? SP = 077777
221           ;o UPON COMPLETION OF THIS TEST THE CONDITION CODES WILL BE:
222           ;o N = 1, Z = 0, V = 1, AND C = 1
223           ;o THE REGISTERS ARE ALL UNAFFECTED BY THE TEST.
224
225           ;oooooooooooooooooooooooooooooooooooooooooooo
226           TST6:
227           CLE
228           BLE    15      ;N=1,Z=0;V=1,C=1
229           BGT    TST7    ; V 324 BRANCH IF {Z OR (N XOR V)}=1
230           18:    HALT    ; = 320 BRANCH IF Z AND (N XOR V) ARE BOTH 0
231           ;o ;STOP HERE IF A BRANCH FAILED
232
233           ;oooooooooooooooooooooooooooooooooooooooooooo
234           .SBTTL TEST7 TEST REGISTER DATA PATH AND MODES "2", "3", "6"
235
236           ;o
237           ;o WHEN THIS TEST IS ENTERED THE CONDITION CODES ARE:
238           ;o N = 1, Z = 0, V = 1, AND C = 1.
239           ;o THE REGISTERS ARE: R0 = ?, R1 = ?, R2 = ?
240           ;o R3 = ?, R4 = ?, R5 = ?, SP = 077777.
241           ;o UPON COMPLETION OF THIS TEST THE CONDITION CODES ARE:
242           ;o N = 0, Z = 1, V = 0, AND C = 0.
243           ;o THE REGISTERS ARE LEFT AS FOLLOWS:

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PDP-11/70 DIAGNOSTIC/BOOTSTRAP (M9381-YC) PATTERN MACY11 27(057) 17-JUN-75 18:04 PAGE 6  
DEKBHA.P11 TEST7 TEST REGISTER DATA PATH AND MODES "2", "3", "6"

237 ;\* R0 = 129292, R1 = 000000, R2 = 129292, R3 = 129292  
238 ;\* R4 = 129292, R5 = 129292, SP = 129292, AND MAPL00 = 129292  
239 ;\*  
240 ;!\*\*\*\*\*  
241 165076 TST7:  
242 165076 012706 129292  
243 165102 010600  
244 165104 010801  
245 165106 010102  
246 165110 010203  
247 165112 010304  
248 165114 010405  
249 165116 010537  
250 165120 170200  
251 165122 167701 177772  
252 165126 002401  
253 165130 001401  
254 165132 000000  
255  
256 ;!\*\*\*\*\*  
257 .SBTTL TEST10 TEST "ROL", "BCC", "BLT", AND MODE "6"  
258 ;\*  
259 ;\* WHEN THIS TEST IS ENTERED THE CONDITION CODES ARE:  
260 ;\* N = 0, Z = 1, V = 0, AND C = 0.  
261 ;\* THE REGISTERS ARE: R0 = 129292, R1 = 000000, R2 = 129292  
262 ;\* R3 = 129292, R4 = 129292, R5 = 129292, SP = 129292.  
263 ;\* MAPL00 = 129292  
264 ;\* UPON COMPLETION OF THIS TEST THE CONDITION CODES ARE:  
265 ;\* N = 0, Z = 0, V = 1, AND C = 1.  
266 ;\* THE REGISTERS ARE LEFT UNCHANGED EXCEPT FOR  
267 ;\* MAPL00 WHICH SHOULD NOW EQUAL 052924.  
268  
269 ;!\*\*\*\*\*  
270 165134 TST10:  
271 165134 006167 003040  
272 165140 103001  
273 165142 002401  
274 165144 000000  
275  
276 ;!\*\*\*\*\*  
277 .SBTTL TEST11 TEST "ADD", "INC", "COM", AND "BCS", "BLE"  
278 ;\*  
279 ;\* WHEN THIS TEST IS ENTERED THE CONDITION CODES ARE:  
280 ;\* N = 0, Z = 0, V = 1, AND C = 1.  
281 ;\* THE REGISTERS ARE: R0 = 129292, R1 = 000000, R2 = 129292  
282 ;\* R3 = 129292, R4 = 129292, R5 = 129292, SP = 129292.  
283 ;\* MAPL00 = 052924.  
284 ;\* UPON COMPLETION OF THIS TEST THE CONDITION CODES ARE:  
285 ;\* N = 0, Z = 1, V = 0, AND C = 0.  
286 ;\* THE REGISTERS ARE LEFT UNCHANGED EXCEPT FOR  
287 ;\* R3 WHICH NOW EQUALS 000000, AND R1 WHICH IS ALSO 000000  
288  
289  
290 165146 TST11:

PDP-11/70 DIAGNOSTIC/BOOTSTRAP (M9301-YC) PATTERN MACY11 27(657) 17-JUN-75 18:04 PAGE 7  
DEKBHA.P11 TEST11 TEST "ADD", "INC", "COM", AND "BCS", "BLE"

291  
292 165146 066703 003026 ADD MAPL0,R3 ; (MAPL0 = 052524) + (R3 = 125252)  
293 165152 005233 INC R3 ; N=1, Z=0, V=0, C=0, AND R3=177776  
294 165154 005103 COM R3 ; N=1, Z=0, V=0, C=0, AND R3=177777  
295 165156 060301 ADD R3,R1 ; N=0, Z=1, V=0, C=1, AND R3 = 000000  
296 165160 103401 BCS 15 ; N=0, Z=1, V=0, C=0, AND R1 = 000000  
297 165162 003401 BLE TST12 ; \* 326 BRANCH IF [Z OR (N XOR V)]=1  
298 165164 000000 15: HALT  
299  
300 ;oooooooooooooooooooooooooooooooooooo  
301 .SBTTL TEST12 TEST "ROR", "BIS", "ADD", AND "BLO", "BGE"  
302 ;  
303 ; WHEN THIS TEST IS ENTERED THE CONDITION CODES ARE:  
304 ; N = 0, Z = 1, V = 0, AND C = 0.  
305 ; THE REGISTERS ARE: R0 = 125252, R1 = 000000, R2 = 125252  
306 ; R3 = 000000, R4 = 125252, R5 = 125252, SP = 125252.  
307 ; UPON COMPLETION OF THIS TEST THE CONDITION CODES ARE:  
308 ; N = 0, Z = 1, V = 0, AND C = 0.  
309 ; THE REGISTERS ARE LEFT UNCHANGED EXCEPT FOR  
310 ; R3 WHICH SHOULD BE MODIFIED BACK TO 000000, AND  
311 ; R4 WHICH SHOULD NOW EQUAL 052525  
312 ;  
313 ;oooooooooooooooooooooooooooooooooooo  
314 165166 TST12:  
315 165166 006004 ROR R4 ; N=0,Z=0,V=1,C=0, AND R4 = 052525  
316 165170 050403 BIS R4,R3 ; N=0,Z=0,V=0,C=0, AND R3 = 052525  
317 165172 060503 ADD R3,R3 ; N=1,Z=0,V=0,C=0, AND R3 = 177777  
318 165174 005203 INC R3 ; N=0,Z=1,V=0,C=0, AND R3 = 000000  
319 165176 103401 BLO 15 ; V 324 BRANCH IF C=1  
320 165200 002001 BGE TST13 ; \* 328 BRANCH IF (N XOR V)=0  
321 165202 000000 15: HALT  
322  
323 ;oooooooooooooooooooooooooooooooooooo  
324 .SBTTL TEST13 TEST "DEC" AND "BLOS", "BLT"  
325 ;  
326 ; WHEN THIS TEST IS ENTERED THE CONDITION CODES ARE:  
327 ; N = 0, Z = 1, V = 0, AND C = 0.  
328 ; THE REGISTERS ARE: R0 = 125252, R1 = 000000, R2 = 125252  
329 ; R3 = 000000, R4 = 052525, R5 = 125252, SP = 125252.  
330 ; UPON COMPLETION OF THIS TEST THE CONDITION CODES ARE:  
331 ; N = 1, Z = 0, V = 0, AND C = 0.  
332 ; THE REGISTERS ARE LEFT UNCHANGED EXCEPT FOR  
333 ; R1 WHICH SHOULD NOW EQUAL 177777  
334 ;  
335 ;oooooooooooooooooooooooooooooooooooo  
336 165204 TST13:  
337 165204 005301 DEC R1 ; N=1,Z=0,V=0,C=0,R1=177777  
338 165206 101401 BLOS 15 ; V 324 BRANCH IF (Z OR C)=1  
339 165210 002401 BLT TST14 ; \* 326 BRANCH IF (N XOR V)=1  
340 165212 000000 15: HALT  
341  
342 ;oooooooooooooooooooooooooooooooooooo  
343 .SBTTL TEST14 TEST "COM", "BIC", AND "BGT", "BGET", "BLE"  
344 ;

345 ; WHEN THIS TEST IS ENTERED THE CONDITION CODES ARE:  
346 ; N = 1, Z = 0, V = 0, AND C = 0.  
347 ; THE REGISTERS ARE: R0 = 125252, R1 = 177777, R2 = 125252  
348 ; R3 = 000000, R4 = 052525, R5 = 125252, SP = 125252.  
349 ; UPON COMPLETION OF THIS TEST THE CONDITION CODES ARE:  
350 ; N = 0, Z = 0, V = 1, AND C = 1.  
351 ; THE REGISTERS ARE LEFT UNCHANGED EXCEPT FOR  
352 ; R0 WHICH SHOULD NOW EQUAL 052525, AND  
353 ; R1 WHICH SHOULD NOW EQUAL 052524  
354 ;  
355 ;-----  
356 165214  
357 165214 005100  
358 165216 101401  
359 165220 000000  
360 165222 040001  
361 165224 060101  
362 165226 003002  
363 165230 002001  
364 165232 003401  
365 165234 000000  
366  
367  
368 ;-----  
369 ;-----  
370 ; WHEN THIS TEST IS ENTERED THE CONDITION CODES ARE:  
371 ; N = 0, Z = 0, V = 1, AND C = 1.  
372 ; THE REGISTERS ARE: R0 = 052525, R1 = 052524, R2 = 125252  
373 ; R3 = 000000, R4 = 052525, R5 = 125252, SP = 125252.  
374 ; UPON COMPLETION OF THIS TEST THE CONDITION CODES ARE:  
375 ; N = 0, Z = 1, V = 0, AND C = 0.  
376 ; THE REGISTERS ARE NOW:  
377 ; R0 = 052525, R1 = 000000, R2 = 125252, R3 = 000000  
378 ; R4 = 052525, R5 = 052525, SP = 125252.  
379 ;  
380 ;-----  
381 165236  
382 165236 005501  
383 165240 020401  
384 165242 001005  
385  
386 165244 030105  
387 165246 003003  
388 165250 005105  
389 165252 160501  
390 165254 001401  
391 165256 000000  
392  
393 ;-----  
394 ;-----  
395 ;-----  
396 ;-----  
397 ;-----  
398 ;-----  
; WHEN THIS TEST IS ENTERED THE CONDITION CODES ARE:  
; N = 0, Z = 1, V = 0, AND C = 0.  
; THE REGISTERS ARE: R0 = 052525, R1 = 000000, R2 = 125252

TST14:  
COM R0 ;N=0,Z=0,V=0,C=1, AND R0 = 052525  
BLOS 25 ; \* 323 BRANCH IF (Z OR C)=1  
HALT ; STOP HERE IF BRANCH FAILED  
25: BIC R0,R1 ;N=1,Z=0,V=0,C=1, AND R1 = 125252  
ADD R1,R1 ;N=0,Z=0,V=1,C=1, AND R1 = 052524  
BGT 15 ; V 322 BRANCH IF Z AND (N XOR V) ARE BOTH 0  
BGE 15 ; V 322 BRANCH IF (N XOR V)=0  
BLE TST15 ; \* 326 BRANCH IF (Z OR (N XOR V))=1  
15: HALT

;-----  
.SDTTL TEST14 TEST "ADC", "CMP", "BIT", AND "BNE", "BGT", "BEQ"

16: ;-----  
; WHEN THIS TEST IS ENTERED THE CONDITION CODES ARE:  
; N = 0, Z = 0, V = 1, AND C = 1.  
; THE REGISTERS ARE: R0 = 052525, R1 = 052524, R2 = 125252  
; R3 = 000000, R4 = 052525, R5 = 125252, SP = 125252.  
; UPON COMPLETION OF THIS TEST THE CONDITION CODES ARE:  
; N = 0, Z = 1, V = 0, AND C = 0.  
; THE REGISTERS ARE NOW:  
; R0 = 052525, R1 = 000000, R2 = 125252, R3 = 000000  
; R4 = 052525, R5 = 052525, SP = 125252.

TST15:  
ADC R1 ;N=0,Z=0,V=0,C=0, AND R1 = 052525  
CMP R4,R1 ;N=0,Z=1;V=0,C=0  
BNE 15 ; V 322 BRANCH IF Z=0  
;R1 = 052525 R5 = 125252  
BIT R1,R5 ;N=0,Z=1;V=0,C=0  
BGT 15 ; V 322 BRANCH IF Z AND (N XOR V) ARE BOTH 0  
COM R5 ;N=0,Z=0,V=0,C=1, AND R5 = 052525  
SUB R5,R1 ;N=0,Z=1;V=0,C=0, AND R1 = 000000  
BEQ TST16 ; \* 326 BRANCH IF Z=1  
15: HALT

;-----  
.SDTTL TEST15 TEST "MOV8", "SOB", "CLR", "TST" AND "BPL", "BNE"

16: ;-----  
; WHEN THIS TEST IS ENTERED THE CONDITION CODES ARE:  
; N = 0, Z = 1, V = 0, AND C = 0.  
; THE REGISTERS ARE: R0 = 052525, R1 = 000000, R2 = 125252

```

399      ; R3 = 000000, R4 = 052525, RD = 052525, SP = 125252.
400      ; UPON COMPLETION OF THIS TEST THE CONDITION CODES ARE:
401      ; N = 0, Z = 1, V = 0, AND C = 0,
402      ; RD IS DECREMENTED BY A S0B INSTRUCTION TO 000000
403      ; R1 IS CLEARED AND THEN INCREMENTED AROUND TO 000000
404
405      ;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;
406 165268
407 165268 112700 177401
408 165264 100001
409 165266 000000
410 165270 077002
411 165272 005001
412 165274 005201
413 165276 077002
414 165300 005700
415 165302 001002
416 165304 005701
417 165306 001401
418 165310 000000
419
420
421      .SBTTL TEST17 TEST "ASR", "ASL"
422
423      ; WHEN THIS TEST IS ENTERED THE CONDITION CODES ARE:
424      ; N = 0, Z = 1, V = 0, AND C = 0.
425      ; THE REGISTERS ARE: RD = 125252, R1 = 000000, R2 = 125252
426      ; R3 = 000000, R4 = 052525, R5 = 052525, SP = 125252.
427      ; UPON COMPLETION OF THIS TEST THE CONDITION CODES ARE:
428      ; N = 0, Z = 0, V = 0, AND C = 0.
429      ; THE REGISTERS ARE LEFT UNCHANGED EXCEPT FOR
430      ; RD WHICH IS NOW EQUAL TO 000000,
431      ; R1 WHICH IS NOW 000001, AND
432      ; R2 WHICH IS NOW 000000.
433
434      ;;;;;;;;;;;;;;;;;;;
435 165312
436 165312 012700 100000
437 165316 005201
438 165320 012702 000020
439 165324 006200
440 165326 005500
441 165330 006301
442 165332 005501
443 165334 077205
444
445
446 165336 060001
447 165340 003401
448 165342 003001
449 165344 000000
450
451
452      ; R0=100000
        ; R1=000001
        ; SET COUNTER TO 16 DECIMAL
        ; RIGHT SHIFT RD, SIGN EXTEND (16 TIMES)
        ; ADD CARRY (0 UNTIL LAST TIME)
        ; LEFT SHIFT R1 (16 TIMES)
        ; ADD CARRY (0 UNTIL LAST TIME)
        ; LOOP BACK 19 DECIMAL TIMES
        ; AT THE END OF THE LOOP
        ; RD = 000000 AND R1 = 000001
        ; N=0, Z=0, V=0, C=0 R1=000001, R0=000000
        ; V 320 BRANCH IF [Z OR (N XOR V)]=1
        ; * 320 BRANCH IF Z AND (N XOR V) ARE BOTH 0
28: HALT
      ;;;;;;;;;;;;;;;;;;;
      .SBTTL TEST20 TEST ASH, AND SWAB
    
```

```

453 ;*
454 ;*
455 ;*
456 ;*
457 ;*
458 ;*
459 ;*
460 ;*
461 ;*
462 ;*
463 ;*****  

464 165346 TST20:  

465 165346 072127 300007      ASH    #7,R1      ;LEFT SHIFT BITS INTO BIT7  

466 165352 125701              TSTB   R1      ;N=0,Z=0;V=0,C=0, AND R1 = 000200  

467 165352 125701              -       -        ;LOWER BYTE SHOULD BE NEGATIVE  

468 165354 100401              BMI    15      ;N=1,Z=0,V=0,C=0  

469 165354 100401              HALT   -        ;+ 325 BRANCH IF N=1  

470 165356 000000              INC    R1      ;"ASH" MUST HAVE FAILED  

471 165360 000301              SWAB   R1      ;SWITCH BYTES OF R1, R1 = 100000  

472 165362 072127 177761      ASH    #0D15,R1  ;RIGHT SHIFT R1 15 PLACES SIGN EXTEND  

473 165362 072127 177761      ;N=1,Z=0;V=0,C=0, R1 = 177777  

474 165366 005201              INC    R1      ;N=0,Z=1;V=0,C=1, R1 = 000000  

475 165370 001401              BEQ    TST21   ;+ 326 BRANCH IF Z=1  

476 165370 001401              HALT   -        ;EITHER "SWAB" OR "ASH" FAILED  

477 165372 000000              -       -  

478 ;*****  

479 ;SBTTL TEST21 TEST 16 KERNEL P.A.R.'S  

480 ;*
481 ;*
482 ;*
483 ;*
484 ;*
485 ;*
486 ;*
487 ;*
488 ;*
489 ;*
490 ;*
491 ;*
492 ;*
493 ;*****  

494 165374 TST21:  

495 165374 012700 172340      MOV    #KIPAR0,R0  ;FIRST "PAR" TO BE CHECKED  

496 165400 012701 000020      MOV    #0D16,R1  ;DO KIPAR0 THRU KOPAR7  

497 165404 005105              COM    R5      ;R5=125252  

498 165406 010420              15:   MOV    R4,(R0)+  ;PAR=052525  

499 165410 020460 177776      CMP    R4,-2(R0)  ;DID IT LOAD PROPERLY?  

500 165414 001014              BNE    25      ;V BRANCH IF NO R0 = PAR + 2  

501 165416 105148              COMB   -(R0)   ;COMPLEMENT HIGH BYTE PAR=125125  

502 165420 120510              CMPB   R5,(R0)  ;CHECK THE HIGH BYTE  

503 165422 001011              BNE    25      ;V BRANCH IF BAD R0 = PAR + 1  

504 165424 120440              CMPB   R4,-(R0)  ;CHECK THE LOW BYTE DIDN'T CHANGE  

505 165426 001007              BNE    25      ;V BRANCH IF IT CHANGED R0 = PAR  

506 165430 105110              COMB   (R0)   ;COMPLEMENT THE LOW BYTE PAR=125252

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507 165432 120520      CMPB   R9,(R0)+    ;CHECK THE LOW BYTE
508 165434 001004      BNE    23        ; V BRANCH IF BAD R0 = PAR + 1
509 165436 120520      CMPB   R9,(R0)+    ;CHECK THE HIGH BYTE
510 165440 001002      BNE    23        ; V BRANCH IF IT FAILED R0 = PAR + 2
511 165442 077117      S0B    R1,1S      ;LOOP UNTIL KOPAR7 HAS BEEN TESTED
512 165444 000401      BR     TST22      ; O BRANCH TO NEXT TEST
513 165446 000000      23:   HALT      ;A P.A.R. FAILED TO HOLD THE RIGHT DATA
514
515
516
517           ;oooooooooooooooooooooooooooo
518           .SBTTL TEST22 TEST AND LOAD KIPDR'S
519           ;o
520           ;o WHEN THIS TEST IS ENTERED THE CONDITION CODES ARE:
521           ;o N = 0, Z = 1, V = 0, AND C = 0.
522           ;o THE REGISTERS ARE: R0 = 172480, R1 = 000000, R2 = 000000
523           ;o R3 = 000000, R4 = 052525, R5 = 125258, SP = 125252.
524           ;o UPON COMPLETION OF THIS TEST THE CONDITION CODES ARE:
525           ;o N = 0, Z = 1, V = 0, AND C = 0.
526           ;o THE REGISTERS THAT ARE MODIFIED ARE:
527           ;o R0 = 172300, R1 = 000000, R2 = 077406
528           ;o ALL KERNEL I-SPACE P.D.R.'S (172300 - 172316) + 077406
529
530           ;oooooooooooooooooooooooooooo
531 165450 012700 172320      TST22:
532 165454 012701 000010      MOV    SKIPDN7+2,R0    ;START WITH LAST "PDR"
533 165460 012702 077406      MOV    $0D8,R1    ;00 KIPDR7 THRU KIPDR0
534 165464 010240            13:   MOV    $077406,R2    ;PATTERN TO TEST "PDR'S"
535 165466 021002            CMP    (R0),R2    ;LOAD "PDR" UNDER TEST
536 165470 001401            BEQ    23        ;SEE IF THE DATA LOADED IS CORRECT
537 165472 000000            HALT   ;BRANCH IF THE DATA MATCHES
538
539           ;o "PDR" HAS FAILED
540 165474 077105            23:   S0B    R1,1S      ;R0 HAS THE ADDRESS OF THE BAD "PDR"
541
542           ;o R2 HAS THE EXPECTED DATA
543
544           ;oooooooooooooooooooooooooooo
545           .SBTTL TEST23 TEST "JSR", "RTS", "RTI", & "JMP"
546           ;o
547           ;o THIS TEST FIRST SETS THE STACK POINTER TO "KOPAR7" (172376),
548           ;o AND THEN VERIFIES THAT "JSR", "RTS", "RTI", AND "JMP"
549           ;o ALL WORK PROPERLY.
550
551           ;o ON ENTRY TO THIS TEST THE STACK POINTER "SP" IS INITIALIZED
552           ;o TO 172376 AND IS LEFT THAT WAY ON EXIT.
553
554
555 165476 012706 172376      TST23:
556
557 165476 012706 172376      MOV    SKOPAN7,SP    ;SET UP THE STACK POINTER
558 165502 024767 000002      JSR    PC,1S      ;TRY TO JSR TO 1S
559 165506 000000            109:  HALT   ;THE "JSR" MUST HAVE FAILED
560 165510 022716 165506      13:   CMP    $105,(SP)  ;WAS THE CORRECT ADDRESS PUSHED?

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PDP-11/70 DIAGNOSTIC/BOOTSTRAP (M9301-YC) PATTERN MACY11 27(657) 17-JUN-79 18:04 PAGE 12  
DEKBHA.P11 TEST23 TEST "JSR", "RTS", "RTI", & "JMP"

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561 165514 001401      BEQ    25      ;BRANCH IF YES
562 165516 000000      HALT   ;WRONG THING PUSHED ON STACK
563 165520 012716 165530 25:    MOV    #35,(SP) ;CHANGE THE ADDRESS ON THE STACK
564 165524 000207      RTS    PC     ;TRY TO RETURN TO JS
565 165526 000000      HALT   ;DID NOT RETURN PROPERLY
566 165530 005046      CLR    -(SP) ;PUSH A ZERO ON THE STACK
567 165532 012746 165542 35:    MOV    #45,-(SP) ;PUSH THE RETURN ADDRESS ON STACK
568 165536 000002      RTI    ;SEE IF AN "RTI" WORKS
569 165540 000000      HALT   ;THE "RTI" FAILED
570 165542 000137 165550 45:    JMP    0055 ;TRY TO "JMP"
571 165546 000000      HALT   ;THE "JMP" FAILED
572 165550              55:    ;ADDRESS TO "JMP" TO
573
574
575
576 ;*****SBTTL TEST24 LOAD AND TURN ON MEMORY MANAGEMENT AND THE UNIBUS MAP
577 ;
578 ; THIS TEST IS ONLY EXECUTED IF THE UPPER 4 BITS <15:12> OF
579 ; THE SWITCH REGISTER ARE NON-ZERO, THE TEST WILL LOAD MEMORY
580 ; MANAGEMENT TO RELOCATE TO THE 32K BLOCK NUMBER SPECIFIED,
581 ; IT WILL ALSO SET UP THE UNIBUS MAP REGISTERS 0 THRU 6 TO
582 ; RELOCATE THE UNIBUS ADDRESSES CORRECTLY. (IE. IF BITS <15:12>
583 ; SPECIFY BLOCK NUMBER 3, THEN YOU WANT TO BOOT INTO
584 ; MEMORY FROM 96K TO 128K. THE KIPAR'S WILL BE LOADED AS FOLLOWS:
585 ; KIPAR0 = 006000, KIPAR1 = 006200, KIPAR2 = 006400, KIPAR3 = 006600
586 ; KIPAR4 = 007000, KIPAR5 = 007200, KIPAR6 = 007400,)
587 ; KIPAR7 WILL ALWAYS EQUAL 177600.
588 ; THE UNIBUS MAP REGISTERS WILL THEN BE SET AS FOLLOWS!
589 ; MAPL0 = 000000, MAPH0 = 03, MAPL1 = 020000, MAPH1 = 03,
590 ; MAPL2 = 040000, MAPH2 = 03, MAPL3 = 060000, MAPH3 = 03,
591 ; MAPL4 = 100000, MAPH4 = 03, MAPL5 = 120000, MAPH5 = 03,
592 ; MAPL6 = 140000, MAPH6 = 03.
593
594
595 165550              TST24:
596 165550 013702 177570      MOV    #0#SHR,R2      ;READ THE SWITCH REGISTER
597 165554 001002          BNE    105      ;SKIP THE NEXT INSTRUCTION IF NOT ZERO
598 165556 013702 173024      MOV    #0#173024,R2 ;READ THE SWITCHES ON THE M9381
599 165562 072227 177776 105:   ASH    #2,R2      ;RIGHT SHIFT BITS <15:12> 2 PLACES
600 165566 042702 141777      BIC    #0#C030000,R2 ;LEAVE ONLY BITS <13:10> IN R2
601 165572 001433          BEQ    TST25      ;GO TO NEXT TEST IF R2 IS ZERO NOW
602
603 ; THIS NEXT PORTION OF CODE WILL BE RUN ONLY IF YOU ARE
604 ; BOOTING INTO MEMORY OTHER THAN PHYSICAL 0 TO 28K.
605
606 165574 012700 172340      MOV    #KIPAR0,R0      ;ADDRESS OF FIRST "PAR" TO LOAD
607 165600 012701 000007      MOV    #0#D7,M1      ;LOAD KIPAR0 THRU KIPAR6
608 165604 010220          15:    MOV    R2,(R0)+ ;LOAD THE KERNEL I-SPACE P,A,R.'S
609 165606 062702 000200      ADD    #200,M2      ;MAKE R2 POINT TO NEXT 4K BLOCK
610 165612 077104          S0B    R1,15      ;LOOP UNTIL KIPAR6 HAS BEEN LOADED
611 165614 012710 177600      MOV    #177600,(R0) ;MAP KIPAR7 TO I/O PAGE
612
613 ; NOW LOAD THE UNIBUS MAP TO REFERENCE THE SAME MEMORY
614 ; AS MEMORY MANAGEMENT DOES.

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PDP-11/70 DIAGNOSTIC/BOOTSTRAP (M9381-YC) PATTERN MACY11 27(657) 17-JUN-79 18:04 PAGE 13  
DEKBHA.P11 TEST24 LOAD AND TURN ON MEMORY MANAGEMENT AND THE UNIBUS MAP

PDP-11/70 DIAGNOSTIC/BOOTSTRAP (M9301-YC) PATTERN  
DEKBHA.P11 TEST25 TEST MAIN MEMORY FROM VIRTUAL 1000 TO 28K

|                                 |  |              |                                    |                                 |
|---------------------------------|--|--------------|------------------------------------|---------------------------------|
| 669 165750 005101               |  | COM R1       | ;COMPLEMENT BEFORE CHECKING        |                                 |
| 670 165752 020001               |  | CMP R0,R1    | ;IS THE DATA CORRECT?              |                                 |
| 671 165754 001401               |  | BEQ 53       | ;BRANCH IF YES                     |                                 |
| 672 165756 000000               |  | HALT         | ;R0=ADDRESS R1==DATA               |                                 |
| 673 165760 077206               |  | 53:          | SUB R2,45                          | ;LOOP UNTIL DONE                |
| 674 165762 012737 173762 000114 |  |              | MOV #CONT, #0114                   | ;SET PARITY VECTOR TO CODE THAT |
| 675                             |  |              |                                    | ;WILL TRY TO CONTINUE AND BOOT  |
| 676                             |  |              |                                    | ;IF THE CACHE FAILS.            |
| 677 165770 005046               |  | CLR -(SP)    | ;SET THE CYCLE FLAG TO ZERO        |                                 |
| 678 165772 000137 173606        |  | JMP #BTST26  | ;JUMP TO SECOND HALF OF THE ROM    |                                 |
| 679 165776 000000               |  | PAEHLT: HALT | ;HALTING HERE MEANS A PARITY ERROR |                                 |
| 680                             |  |              |                                    |                                 |

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681 .SBTTL  BOOTSTRAP ENTHY POINT IS AT 17773000
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705 173000 173000
706 173000 000405
707
708
709 .SBTTL  BASE2
710   , * BASE2
711   BR      START      ;BRANCH TO START OF BOOT STRAP
712
713
714 .SBTTL  CODE TO WAIT FOR TU10 TO COME ON LINE
715
716 173002 032761 000001 177776 WAIT:  BIT      #TUH,-2(R1)    ;IS THE SELECTED DRIVE ON LINE
717 173010 001774          BEQ      WAIT      ;WAIT FOR BIT TO BE SET BY DRIVE
718 173012 000430          BR       TU102     ;BRANCH TO CONTINUE BOOTING FROM TU10
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PDP-11/70 DIAGNOSTIC/BOOTSTRAP (M9301-YC) PATTERN MACY11 27(657) 17-JUN-75 18:04 PAGE 16  
DEKBHA.P11 THIS IS THE CODE TO READ THE SWITCH REGISTER AND DECODE IT

735

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737

738

739 173070 010211

740 173072 000743

741 173074 052311

742 173076 105711

743 173100 100376

744 173102 012761 177777 000002

745 173110 112311

746 173112 105711

747 173114 100376

748 173116 005711

749 173120 100563

750 173122 000417

.SBTTL THIS IS THE START OF THE TM11/TU10 BOOT STRAP (MAGNETIC TAPE, TM11)  
ICOMMAND REGISTER ADDRESS IS 172922

TU10: MOV R2,(R1) ;LOAD UNIT NUMBER INTO C.S.R.  
BR WAIT ;GO WAIT FOR SELECTED DRIVE TO COME ONLINE  
TU102: BIS (R3)+,(H1) ;'ON' REWIND COMMAND INTO C.S.R.  
;THIS COMMAND ALSO SETS BBB BPI 9 CHAN.  
18: TSTB (R1) ;SEE IF THE REWIND IS COMPLETE  
BPL 18 ;WAIT FOR BIT 87 OF C.S.R. TO BE SET  
MOV #1,2(R1) ;SET RECORD COUNTER TO SKIP ONE RECORD  
MOV# (R3)+,(H1) ;LOAD SPACE FORWARD COMMAND INTO C.S.R.  
28: TSTB (R1) ;SEE IF THE SPACE IS COMPLETE  
BPL 28 ;WAIT FOR BIT 87 OF C.S.R. TO BE SET  
TST (R1) ;CHECK THE ERROR FLAG FOR THE TM11/TU10  
BMI AGAIN ;RE-TRY BOOT IF THERE WAS AN ERROR  
BR CMNSGU ;BRANCH TO COMMON READ CODE IF NO ERRORS

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757 173124 010211

758 173126 052311

759 173130 005711

760 173132 100376

761 173134 005761 177776

762 173140 100153

763 173142 010211

764 173144 000406

TU56: MOV R2,(R1) ;LOAD UNIT NUMBER INTO C.S.R.  
BIS (R3)+,(H1) ;'ON' REWIND COMMAND INTO C.S.R.  
18: TST (R1) ;SEE IF ERROR BIT IS SET  
BPL 18 ;WAIT UNTIL BIT 15 OF C.S.R. IS SET  
TST #2(R1) ;IS THE ERROR 'END ZONE'  
BPL AGAIN ;BRANCH IF NOT 'END ZONE'  
MOV R2,(R1) ;RE-LOAD DRIVE NUMBER AND CLEAR REVERSE BIT  
BR CMNSGU ;BRANCH TO COMMON READ CODE

765

766

767

768

769

770 173146 J72227 000005

771 173152 010261 000006

772 173156 000401

.SBTTL THIS IS THE START OF THE RK11/RK05 BOOT STRAP (DECPACK DISK CARTRIDGE, RK11-D)  
ICOMMAND REGISTER ADDRESS IS 177404

RK05: ASH #5,R2 ;LEFT SHIFT UNIT NUMBER 5 PLACES  
MOV R2,6(H1) ;LOAD UNIT NUMBER INTO DEVICE  
BR CMNSGU ;BRANCH TO COMMON READ CODE

773

774

775

776

777

778 173160 010211

.SBTTL THIS IS THE START OF THE RP11/RP03 BOOT STRAP (DISK PACK, RP11-C)  
ICOMMAND REGISTER ADDRESS IS 176714

RP03: MOV R2,(R1) ;LOAD THE UNIT NUMBER INTO THE COMMAND REG.

779

780

781

782 173162 012761 177000 000002

783 173170 111311

784 173172 105711

785 173174 100376

786 173176 005711

787 173200 100007

788 173202 022704 000012

CMNSGU: MOV #512,,2(R1) ;LOAD WORD COUNT OF 512 WORDS  
MOVB (R3),(R1) ;LOAD READ FUNCTION INTO C.S.R.  
18: TSTB (R1) ;SEE IF FUNCTION IS COMPLETE  
BPL 18 ;WAIT UNTIL BIT 87 OF C.S.R. IS SET  
TST (R1) ;WERE THERE ANY ERRORS ON THE TRANSFER  
BPL 28 ;IF NO ERRORS BRANCH TO SEC. BOOT  
CMP #12,R4 ;IS THIS THE RH70/TU107

PDP-11/70 DIAGNOSTIC/BOOTSTRAP (M9381-YC) PATTERN  
DEKBHA.P11 THIS IS THE START OF THE COMMON READ CODE

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|     |        |        |               |       |              |                                 |  |
|-----|--------|--------|---------------|-------|--------------|---------------------------------|--|
| 789 | 173206 | 001130 |               | BNE   | AGAIN        | ;BRANCH IF NOT TO TRY AGAIN     |  |
| 790 | 173210 | 022761 | 001000 000014 | CMP   | \$FCE,14(R1) | ;WAS ERROR A FRAME COUNT ERROR? |  |
| 791 | 173216 | 001124 |               | BNE   | AGAIN        | ;BRANCH IF NOT TO TRY AGAIN     |  |
| 792 | 173220 | 005011 |               | 28:   | CLR          | (R1)                            | ;CLEAR COMMAND REGISTER THIS WILL STOP   |
| 793 |        |        |               |       |              |                                 | ;THE DECTAPE MOTION IF DEVICE WAS TU56   |
| 794 | 173222 | 005007 |               | CLR   | PC           |                                 | ;START SECONDARY BOOT AT VIRTUAL ZERO    |
| 795 |        |        |               |       |              |                                 |  |
| 796 | 173224 | 165000 |               | .WORD | 165000       |                                 | ;VECTOR TO THE START OF M9381 BOOTSTRAP  |
| 797 | 173226 | 000340 |               | .WORD | 000340       |                                 | ;PROCESSOR STATUS TO ASSUME AT BOOT TIME |
| 798 |        |        |               |       |              |                                 |  |
| 799 |        |        |               |       |              |                                 |  |

800 .SBTTL THIS IS THE START OF THE RH70/TU16 BOOT STRAP (MAGNETIC TAPE SYSTEM, TU16)  
801 ;COMMAND REGISTER ADDRESS IS 172448

|     |        |        |               |       |              |   |                                      |
|-----|--------|--------|---------------|-------|--------------|---|--------------------------------------|
| 802 |        |        |               | TU16: | MOV          | R0,R2                                     | ;COPY UNIT NUMBER INTO R2            |
| 803 | 173230 | 010902 |               | BIS   | \$001300,R2  | ;OR' \$001300 & FORMAT, WITH SLAVE NUMBER |                                      |
| 804 | 173232 | 022702 | 001300        | MOV   | R2,32(R1)    | ;LOAD UNIT NUMBER                         |                                      |
| 805 | 173236 | 010261 | 000032        | BIT   | \$MOL,12(R1) | ;IS THE MEDIUM ON LINE                    |                                      |
| 806 | 173242 | 022761 | 010000 000012 | 18:   | BEQ          | 18  | ;WAIT FOR BIT 12 OF DRIVE STATUS REG |
| 807 | 173250 | 001774 |               | MOVB  | (R3)+,(R1)   | ;ISSUE REWIND COMMAND                     |                                      |
| 808 | 173252 | 112311 |               | TSTB  | 12(R1)       | ;IS DRIVE READY BIT SET YET?              |                                      |
| 809 | 173254 | 105761 | 000012        | BPL   | 28           | ;WAIT FOR DRIVE READY BIT                 |                                      |
| 810 | 173260 | 100375 |               | MOVB  | (R3)+,(R1)   | ;ISSUE DRIVE CLEAR COMMAND                |                                      |
| 811 | 173262 | 112311 |               | TSTB  | 12(R1)       | ;IS DRIVE READY BIT SET?                  |                                      |
| 812 | 173264 | 105761 | 000012        | BPL   | 38           | ;WAIT UNTIL BIT 07 IS SET                 |                                      |
| 813 | 173270 | 100375 |               | MOV   | \$=1,6(R1)   | ;SET SKIP COUNT TO 1 RECORD               |                                      |
| 814 | 173272 | 012761 | 177777 000006 | MOVB  | (R3)+,(R1)   | ;ISSUE SPACE FORWARD COMMAND              |                                      |
| 815 | 173300 | 112311 |               | TSTB  | 12(R1)       | ;HAS THE DRIVE FINISHED THE SPACE?        |                                      |
| 816 | 173302 | 105761 | 000012        | BPL   | 48           | ;WAIT UNTIL BIT 07 IS SET                 |                                      |
| 817 | 173306 | 100375 |               | MOV   | (SP),34(R1)  | ;LOAD UPPER 6 BITS OF BUS ADDRESS         |                                      |
| 818 | 173310 | 011661 | 000034        | BR    | CHNSRM       | ;GO JOIN COMMON RH70 CODE                 |                                      |
| 819 | 173314 | 000415 |               |       |              |   |                                      |
| 820 |        |        |               |       |              |   |                                      |
| 821 |        |        |               |       |              |   |                                      |

822 .SBTTL THIS IS THE START OF THE RH70/RP84 BOOT STRAP (DISK PACK, RP84)  
823 ;COMMAND REGISTER ADDRESS IS 176700

|     |        |        |               |       |                |                                   |                                  |
|-----|--------|--------|---------------|-------|----------------|-----------------------------------|----------------------------------|
| 824 |        |        |               | RP84: | MOVB           | R0,10(R1)                         | ;SELECT UNIT NUMBER TO BOOT FROM |
| 825 | 173316 | 110061 | 000010        | MOVB  | (R3)+,(R1)     | ;ISSUE READ-IN PRESET COMMAND     |                                  |
| 826 | 173322 | 112311 |               | MOV   | \$14000,32(R1) | ;SET FMT22 & ECC INHIBIT BITS     |                                  |
| 827 | 173324 | 012761 | 314000 000032 | MOV   | (SP),30(R1)    | ;LOAD UPPER 6 BITS OF BUS ADDRESS |                                  |
| 828 | 173332 | 011661 | 000050        | BR    | CHNSRM         | ;GO JOIN THE COMMON RH70 CODE     |                                  |
| 829 | 173336 | 000404 |               |       |                |                                   |                                  |
| 830 |        |        |               |       |                |                                   |                                  |
| 831 |        |        |               |       |                |                                   |                                  |

832 .SBTTL THIS IS THE START OF THE RH70/RHS84 BOOT STRAP (FIXED HEAD DISK, RHS84)  
833 ;COMMAND REGISTER ADDRESS IS 172848

|     |        |        |        |        |             |                                   |                                     |
|-----|--------|--------|--------|--------|-------------|-----------------------------------|-------------------------------------|
| 834 |        |        |        | RHS84: | MOVB        | R0,10(R1)                         | ;LOAD THE DRIVE NUMBER TO BOOT FROM |
| 835 | 173340 | 110061 | 000010 | MOV    | (SP),30(R1) | ;LOAD UPPER 6 BITS OF BUS ADDRESS |                                     |
| 836 | 173344 | 011661 | 000030 |        |             |                                   |                                     |
| 837 |        |        |        |        |             |                                   |                                     |
| 838 |        |        |        |        |             |                                   |                                     |
| 839 |        |        |        |        |             |                                   |                                     |

840 .SBTTL THIS IS THE START OF THE COMMON RH-70 CODE

|     |        |        |               |         |        |                             |                                      |
|-----|--------|--------|---------------|---------|--------|-----------------------------|--------------------------------------|
| 841 | 173350 | 016161 | 000010 000016 | CMNSRH: | MOV    | 16(R1),16(R1)               | ;TURN OFF ANY ACTIVE ATTENTION FLAGS |
| 842 | 173356 | 000701 |               | BR      | CMNSGO | ;BRANCH TO COMMON READ CODE |                                      |

PDP-11/70 DIAGNOSTIC/BOOTSTRAP (M9381-YC) PATTERN MACY11 27(057) 17-JUN-75 18:04 PAGE 18  
DEKBHA.P11 THIS IS THE START OF THE COMMON RH-70 CODE

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843
844
845 .SBTTL THIS IS THE START OF THE RX11/RX81 BOOT STRAP (FLOPPY DISK)
846 |COMMAND REGISTER ADDRESS IS 177178
847
848 173360 042700 000000
849 173364 001401
850 173366 005203
851 173370 132711 000040
852 173374 001775
853 173376 111311
854 173400 012702 000002
855 173404 105711
856 173406 100376
857 173410 112761 000001 000002
858 173416 077206
859 173420 032711 100040
860 173424 001775
861 173426 100420
862 173430 112711 000003
863 173434 105711
864 173436 100376
865 173440 116122 000002
866 173444 105702
867 173446 100372
868 173450 009507
869
870
871
872 .SBTTL THIS IS THE START RESERVED FOR A FUTURE DEVICE
873
874 173452 000000
875 173454 000000
876 173456 000000
877 173460 000000
878 173462 000000
879 173464 000000
880 173466 000000
881
882
883 173470 000005
884 173472 000137 169550
885
RX01: BIC #0,R0 ;MAKE SURE UNIT NUMBER IS ZERO OR ONE
        BEQ 15 ;SKIP NEXT INST IF UNIT NUMBER IS ZERO
        INC R3 ;POINT TO UNIT ONE'S READ COMMAND
        BITB #40,(R1) ;IS THE "DONE" BIT SET?
        BEQ 15 ;WAIT UNTIL THE DONE BIT IS SET
        MOVB (R3),(R1) ;LOAD THE READ COMMAND
        MOV #2,R2 ;LOAD LOOP COUNT INTO R2
        TSTB (R1) ;IS THE "TR" BIT SET?
        BPL 25 ;WAIT UNTIL BIT 07 OF RXCS IS SET
        MOVB #0001,2(R1) ;LOAD SECTOR NUMBER OR TRACK ADDRESS
        SOB R2,25 ;LOOP BACK TO LOAD SECTOR NUMBER
        BIT #100040,(R1) ;CHECK FOR "ERROR" OR "DONE"
        BEQ 35 ;WAIT UNTIL BIT 15 OR BIT 05 OF RXCS IS SET
        BMI AGAIN ;BRANCH TO TRY AGAIN IF ERROR
        MOVB #0003,(R1) ;LOAD "EMPTY BUFFER" COMMAND INTO RXCS
        TSTB (R1) ;IS 'TR' BIT SET?
        BPL 45 ;WAIT UNTIL BIT 07 OF RXCS IS SET
        MOVB 2(R1),(R2) ;STORE DATA (R2 STARTS AT 0 & GOES TO 177)
        TSTB R2 ;IS BIT 07 OF MEMORY ADDRESS SET?
        BPL 45 ;BRANCH IF NOT 128 BYTES YET
        CLR PC ;START SECONDARY BOOT AT VIRTUAL ZERO
FUTDEV: HALT ;THERE IS NO BOOT YET
        ,WORD 000000 ;RESERVED FOR FUTURE BOOT EXPANSION
        ,WORD 000000 ;RESERVED FOR FUTURE BOOT EXPANSION
AGAIN: RESET ;CLEAR THE WORLD AFTER ERROR
        JMP OUTST24 ;GO SETUP MEMORY MANAGEMENT AND TEST
        ;MAIN MEMORY AND THE CACHE AGAIN,

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PDP-11/70 DIAGNOSTIC/BOOTSTRAP (M9301-YC) PATTERN MACY13 27(057) 17-JUN-79 18:04 PAGE 19  
DEKBHA.P11 THIS IS THE START RESERVED FOR A FUTURE DEVICE

886  
887 .SBTTL FUNCTION CODES FOR THE ALL OF THE DEVICES  
888  
889 173476 060017 TU1051 ,WORD 060017 ;REWIND SELECTED DRIVE AND SET 000 MPI  
890 173500 011 ,BYTE 011 ;SPACE FORWARD COMMAND FOR TU10  
891 173501 003 ,BYTE 003 ;READ COMMAND FOR TU10  
892  
893 173502 004003 TU5651 ,WORD 004003 ;SEARCH FOR BLOCK 0, REVERSE DIRECTION  
894 173504 RK0551 ,BYTE 005 ;READ COMMAND FOR TU56, RK05, RP63  
895 173504 005 RP0351 ,BYTE 005 ;READ COMMAND FOR TU56, RK05, RP63  
896  
897 173505 007 TU1651 ,BYTE 007 ;REWIND SELECTED DRIVE  
898 173506 011 ,BYTE 011 ;DRIVE CLEAR COMMAND  
899 173507 031 ,BYTE 031 ;SPACE FORWARD  
900 173510 071 ,BYTE 071 ;READ FORWARD  
901  
902 173511 021 RP0451 ,BYTE 021 ;READ-IN PRESET  
903 173512 071 RS0451 ,BYTE 071 ;READ COMMAND FOR RP04 & RS04  
904  
905 173513 007 RX0151 ,BYTE 007 ;READ SECTOR COMMAND FOR DRIVE ZERO  
906 173514 027 ,BYTE 027 ;READ SECTOR COMMAND FOR DRIVE ONE  
907  
908 173515 000 FUTDES1 ,BYTE 0 ;SPACE FOR FUTURE DEVICE COMMAND  
909 173516 000000 ,WORD 0 ;SPACE FOR MORE COMMANDS  
910

911

912

913

914 173520 172522  
915 173522 177342  
916 173524 177404  
917 173526 176714  
918 173530 220003  
919 173532 172440  
920 173534 176700  
921 173536 172040  
922 173540 177170

.SBTTL COMMAND AND STATUS REGISTER ADDRESS TABLE

CSRPTR: .WORD 172522 ;THIS IS THE C.S.R. ADDRESS FOR TU10  
.WORD 177342 ;THIS IS THE C.S.R. ADDRESS FOR THE TU56  
.WORD 177404 ;THIS IS THE C.S.R. ADDRESS FOR THE RK05  
.WORD 176714 ;THIS IS THE C.S.R. ADDRESS FOR THE RP03  
.WORD 0 ;THIS IS THE C.S.R. ADDRESS OF A FUTURE DEVICE  
.WORD 172440 ;THIS IS THE C.S.R. ADDRESS FOR THE RH70/TU16  
.WORD 176700 ;THIS IS THE C.S.R. ADDRESS FOR THE RH70/RP04  
.WORD 172040 ;THIS IS THE C.S.R. ADDRESS FOR THE RH70/RS04  
.WORD 177170 ;THIS IS THE C.S.R. ADDRESS FOR RX11/RX01

923

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925

926 173542 173476  
927 173544 173502  
928 173546 173504  
929 173550 173504  
930 173552 173519  
931 173554 173505  
932 173556 173511  
933 173560 173512  
934 173562 173513

.SBTTL FUNCTION POINTER TABLE

CMDPTR: .WORD TU10\$ ;POINTER TO FUNCTION TABLE FOR THE TU10  
.WORD TU56\$ ;POINTER TO FUNCTION TABLE FOR THE TU56  
.WORD RK05\$ ;POINTER TO FUNCTION TABLE FOR THE RK05  
.WORD RP03\$ ;POINTER TO FUNCTION TABLE FOR THE RP03  
.WORD FUT0ES ;POINTER TO FUNCTION TABLE FOR A FUTURE DEVICE  
.WORD TU16\$ ;POINTER TO FUNCTION TABLE FOR THE RH70/TU16  
.WORD RP04\$ ;POINTER TO FUNCTION TABLE FOR THE RH70/RP04  
.WORD RS04\$ ;POINTER TO FUNCTION TABLE FOR THE RH70/RS04  
.WORD RX01\$ ;POINTER TO FUNCTION TABLE FOR THE RX01

935

936

937

938 173564 173070  
939 173566 173124  
940 173570 173146  
941 173572 173160  
942 173574 173452  
943 173576 173230  
944 173602 173316  
945 173602 173340  
946 173604 173362

.SBTTL STARTING ADDRESS TABLE

ADDRS: .WORD TU10 ;STARTING ADDRESS FOR THE TH11/TU10  
.WORD TU56 ;STARTING ADDRESS FOR THE TC11/TU56  
.WORD RK05 ;STARTING ADDRESS FOR THE RK11/RK05  
.WORD RP03 ;STARTING ADDRESS FOR THE RP11/RP03  
.WORD FUTDEV ;STARTING ADDRESS FOR A FUTURE DEVICE  
.WORD TU16 ;STARTING ADDRESS FOR THE RH70/TU16  
.WORD RP04 ;STARTING ADDRESS FOR THE RH70/RP04  
.WORD RS04 ;STARTING ADDRESS FOR THE RH70/RS04  
.WORD RX01 ;STARTING ADDRESS FOR THE RX11/RX01

947



|                           |   |              |   |
|---------------------------|---|--------------|---|
| 1002 173660 003720        | TST   | (R0)+        | ;MOVE TO NEXT ADDRESS                         |
| 1003 173662 077221        | S0B   | R2,2S        | ;BRANCH IF NOT DONE                           |
| 1004 173664 012715 000044 | MOV   | #GRP1,(R5)   | ;FORCE REPLACE GROUP 1 AND FORCE MISS GROUP 0 |
| 1005 173670 005116        | COM   | (SP)         | ;COMPLEMENT THE CYCLE FLAG                    |
| 1006 173672 001391        | BNE   | 1S           | ;LOOP IF NOT DONE                             |
| 1007                      | ;oooooooooooooooooooooooooooo   |              |   |
| 1008                      | .SBTTL TEST27 TEST VIRTUAL 28K WITH CACHE ON                                  |              |   |
| 1009                      | ;o  |              |   |
| 1010                      | ;o THIS TEST CHECKS VIRTUAL MEMORY FROM 001000 THRU 197776                    |              |   |
| 1011                      | ;o TO INSURE THAT YOU CAN GET HITS ALL THE WAY UP THROUGH MAIN                |              |   |
| 1012                      | ;o MEMORY, IT STARTS WITH GROUP 1 ENABLED, THEN TESTS GROUP 0, AND            |              |   |
| 1013                      | ;o FINALLY CHECKS MEMORY WITH BOTH GROUPS ENABLED, IF ANY OF                  |              |   |
| 1014                      | ;o THE THREE PASSES FAIL THE TEST WILL HALT AT "CONT + 2", THEN               |              |   |
| 1015                      | ;o IF THE OPERATOR PURES "CONTINUE", THE PROGRAM WILL TRY TO                  |              |   |
| 1016                      | ;o BOOT WITH THE CACHE DISABLED.  |              |   |
| 1017                      | ;o  |              |   |
| 1018                      | ;o  |              |   |
| 1019                      | ;o UPON ENTRY THE REGISTERS WILL BE SET UP AS FOLLOWS:                        |              |   |
| 1020                      | ;o R0 = 001000 (ADDRESS), R1 = 3 (PASS COUNT), R2 = 67400 (MEMORY COUNTER),   |              |   |
| 1021                      | ;o R3 = 1000 (FIRST ADDRESS), R4 = 67400 (MEMORY COUNTER),                    |              |   |
| 1022                      | ;o R5 = 177746 (CONTROL REG.), SP = 172374 (POINTING TO CODE FOR CONTROL REG) |              |   |
| 1023                      | ;o  |              |   |
| 1024                      | ;o UPON COMPLETION OF THIS TEST MAIN MEMORY FROM VIRTUAL ADDRESS              |              |   |
| 1025                      | ;o 001000 THRU 197776 WILL CONTAIN ITS OWN VIRTUAL ADDRESS.                   |              |   |
| 1026                      | ;o  |              |   |
| 1027                      | ;oooooooooooooooooooooooooooo   |              |   |
| 1028 173674               | TST27:  |              |   |
| 1029 173674 012702 067400 | MOV   | 67400,R2     | ;COUNT STORAGE (28K - 1000 BYTES)             |
| 1030 173700 010300        | MOV   | R3,R0        | ;FIRST ADDRESS IS 1000 OCTAL                  |
| 1031 173702 010204        | MOV   | R2,R4        | ;SETUP COUNTER                                |
| 1032 173704 010020        | 1S:   | MOV (R0)+    | ;FILL MEMORY WITH ADDRESSES                   |
| 1033 173706 077402        | S0B   | R4,1S        | ;LOOP UNTIL DONE                              |
| 1034 173710 012716 000030 | MOV   | #GRP0,(SP)   | ;LOAD CODE TO FORCE GROUP 0 ONTO STACK        |
| 1035 173714 012701 000003 | MOV   | #3,R1        | ;SET PASS COUNT TO THREE                      |
| 1036 173720 010300        | 2S:   | MOV R3,R0    | ;FIRST ADDRESS                                |
| 1037 173722 010204        | MOV   | R2,R4        | ;COUNTER                                      |
| 1038 173724 005110        | 3S:   | COM (R0)     | ;DOUBLE COMPLEMENT DATA AND                   |
| 1039 173726 005110        | COM   | (R0)         | ;MAKE SURE IT IS IN THE CACHE,                |
| 1040 173730 020020        | CMP   | R0,(R0)+     | ;COMPARE DATA, AND SET BIT 0 IN HIT/MISS REG  |
| 1041                      |   |              | ;ALSO POINT TO NEXT ADDRESS                   |
| 1042 173732 001401        | BEQ   | 5S           | ;BRANCH IF DATA MATCHES                       |
| 1043 173734 000000        | HALT  |              | ;DATA DIDN'T MATCH R0 = ADDRESS + 2           |
| 1044 173736 006037 177752 | 5S:   | R0R 00177752 | ;WAS THE LAST MEMORY REFERENCE A HIT?         |
| 1045 173742 103402        | BCS   | 4S           | ;BRANCH IF YES                                |
| 1046 173744 000000        | HALT  |              | ;HIT FAILED TO OCCUR R0 = ADDRESS + 2         |
| 1047 173746 000407        | BR  | BOOTM:SS     | ;ABORT REST OF TEST IF "CONTINUE" PRESSED     |
| 1048 173750 077413        | 4S:   | S0B R4,3S    | ;LOOP UNTIL DONE                              |
| 1049 173752 011615        | MOV   | (SP),(R5)    | ;FORCE MISS GRP1 ON PASS 2, FULLY             |
| 1050                      |   |              | ;ENABLE CACHE ON PASS THREE,                  |
| 1051 173754 005016        | CLR   | (SP)         | ;GET READY TO FULLY ENABLE CACHE ON PASS 3    |
| 1052 173756 077120        | S0B   | R1,2S        | ;RUN THREE PASSES THRU THIS TEST              |
| 1053 173760 000404        | BR  | JUMP         | ;GO TO BOOT STRAP CODE                        |
| 1054                      |   |              |   |
| 1055                      |   |              |   |

PDP-11/70 DIAGNOSTIC/BOOTSTRAP (M9301-YC) PATTERN  
DEKBHA.P11 TEST27 TEST VIRTUAL 25K WITH CACHE ON

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|                           |            |              |   |
|---------------------------|------------|--------------|---|
| 1056 173762 000000        | CONT: HALT |              | ;STOP HERE IF THERE IS A CACHE ERROR      |
| 1057 173764 022626        | CMP        | (SP)+, (SP)+ | ;ADJUST STACK POINTER AFTER ABORT         |
| 1058 173766               | BOOTMISS:  |              |   |
| 1059 173766 012715 000014 | MOV        | #MISS,(H5)   | ;FORCE MISSES IN BOTH GROUPS OF CACHE     |
| 1060 173772 005726        | JUMP: TST  | (SP)+        | ;POINT TO UPPER SIX BITS OF BUS ADDRESS   |
| 1061                      |            |              | ;THAT DATA IS IN ADDRESS 1772376 (KOPAR7) |
| 1062 173774 000137 173000 | JMP        | 0#BOOT!      | ;GO TO BOOT STRAP ENTRY POINT             |
| 1063                      |            |              |   |
| 1064                      |            |              |   |
| 1065 000001               | .END       |              |   |

PDP-11/70 DIAGNOSTIC/BOOTSTRAP (M9301-YC) PATTERN  
DEKBHA.P11 CROSS REFERENCE TABLE

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|        |           |      |       |       |      |      |      |      |      |      |      |       |      |      |
|--------|-----------|------|-------|-------|------|------|------|------|------|------|------|-------|------|------|
| ADDRS  | 173564    | 733  | 9388  |       |      |      |      |      |      |      |      |       |      |      |
| AGAIN  | 173478    | 750  | 762   | 789   | 791  | 861  | 8838 |      |      |      |      |       |      |      |
| BASE1  | = 165000  | 18   | 116   |       |      |      |      |      |      |      |      |       |      |      |
| BASE2  | = 173000  | 18   | 704   | 964   |      |      |      |      |      |      |      |       |      |      |
| BOOT   | 173000    | 7058 | 1062  |       |      |      |      |      |      |      |      |       |      |      |
| BOOTMI | 173766    | 1000 | 1047  | 10588 |      |      |      |      |      |      |      |       |      |      |
| CMDPTR | 173542    | 732  | 9268  |       |      |      |      |      |      |      |      |       |      |      |
| CMNSGO | 173162    | 751  | 764   | 772   | 7828 | 842  |      |      |      |      |      |       |      |      |
| CMNSRH | 173358    | 819  | 829   | 8418  |      |      |      |      |      |      |      |       |      |      |
| CONT   | 173762    | 674  | 10568 |       |      |      |      |      |      |      |      |       |      |      |
| CSRPTR | 173520    | 731  | 9148  |       |      |      |      |      |      |      |      |       |      |      |
| DISPLA | = 177570  | 18   |       |       |      |      |      |      |      |      |      |       |      |      |
| FCE    | = 001000  | 18   | 798   |       |      |      |      |      |      |      |      |       |      |      |
| FUTDEV | 173452    | 8748 | 942   |       |      |      |      |      |      |      |      |       |      |      |
| FUTDES | 173515    | 9088 | 938   |       |      |      |      |      |      |      |      |       |      |      |
| GRP2   | = 000030  | 18   | 986   | 1034  |      |      |      |      |      |      |      |       |      |      |
| GRP1   | = 000044  | 18   | 1004  |       |      |      |      |      |      |      |      |       |      |      |
| INDMAP | 165128    | 2508 | 251   |       |      |      |      |      |      |      |      |       |      |      |
| JUMP   | 173772    | 1053 | 10688 |       |      |      |      |      |      |      |      |       |      |      |
| KDPAR0 | = 172368  | 18   |       |       |      |      |      |      |      |      |      |       |      |      |
| KDPAR7 | = 172376  | 18   | 557   |       |      |      |      |      |      |      |      |       |      |      |
| KIPAR0 | = 172348  | 18   | 495   | 686   |      |      |      |      |      |      |      |       |      |      |
| KIPAR7 | = 172356  | 18   |       |       |      |      |      |      |      |      |      |       |      |      |
| KIPDR0 | = 172308  | 18   |       |       |      |      |      |      |      |      |      |       |      |      |
| KIPDR7 | = 172316  | 18   | 531   |       |      |      |      |      |      |      |      |       |      |      |
| MAPL0  | = 170208  | 18   | 258   | 2718  | 292  | 618  |      |      |      |      |      |       |      |      |
| MISS   | = 000014  | 18   | 651   | 1059  |      |      |      |      |      |      |      |       |      |      |
| MMRD   | = 177572  | 18   | 6268  |       |      |      |      |      |      |      |      |       |      |      |
| MMR3   | = 172516  | 18   | 6258  |       |      |      |      |      |      |      |      |       |      |      |
| MOL    | = 010000  | 18   | 506   |       |      |      |      |      |      |      |      |       |      |      |
| N      | = 000003  | 1178 | 118   | 119   | 124  | 1298 | 138  | 131  | 136  | 1468 | 147  | 148   | 158  | 1688 |
|        |           | 169  | 178   | 188   | 1898 | 198  | 191  | 208  | 2098 | 218  | 211  | 228   | 2278 | 228  |
|        |           | 229  | 248   | 2568  | 257  | 258  | 269  | 2768 | 277  | 278  | 289  | 3088  | 351  | 362  |
|        |           | 313  | 3238  | 324   | 325  | 335  | 3428 | 343  | 346  | 355  | 3678 | 368   | 369  | 388  |
|        |           | 3938 | 394   | 395   | 485  | 4288 | 421  | 422  | 436  | 4518 | 452  | 453   | 463  | 4798 |
|        |           | 480  | 481   | 493   | 5168 | 517  | 518  | 529  | 5448 | 545  | 546  | 554   | 5758 | 576  |
|        |           | 577  | 594   | 6298  | 630  | 631  | 642  | 6668 | 667  | 668  | 683  | 10888 | 1089 | 1018 |
|        |           | 1027 |       |       |      |      |      |      |      |      |      |       |      |      |
| PAEHLT | 165776    | 648  | 6798  |       |      |      |      |      |      |      |      |       |      |      |
| PC     | = %000007 | 2498 | 5588  | 5648  | 719  | 7948 | 8688 |      |      |      |      |       |      |      |
| RK05   | 173146    | 7708 | 948   |       |      |      |      |      |      |      |      |       |      |      |
| RK05S  | 173504    | 8948 | 928   |       |      |      |      |      |      |      |      |       |      |      |
| RP03   | 173168    | 7788 | 941   |       |      |      |      |      |      |      |      |       |      |      |
| RP03S  | 173504    | 8958 | 929   |       |      |      |      |      |      |      |      |       |      |      |
| RP04   | 173316    | 8258 | 944   |       |      |      |      |      |      |      |      |       |      |      |
| RP04S  | 173511    | 9028 | 932   |       |      |      |      |      |      |      |      |       |      |      |
| RS04   | 173348    | 8358 | 945   |       |      |      |      |      |      |      |      |       |      |      |
| RS04S  | 173512    | 9038 | 933   |       |      |      |      |      |      |      |      |       |      |      |
| RX01   | 173368    | 8488 | 946   |       |      |      |      |      |      |      |      |       |      |      |
| RX01S  | 173513    | 9058 | 934   |       |      |      |      |      |      |      |      |       |      |      |
| RD     | = %000000 | 2438 | 244   | 3578  | 360  | 4878 | 4188 | 4138 | 414  | 4368 | 4398 | 4488  | 448  | 4958 |
|        |           | 4988 | 499   | 5018  | 502  | 504  | 5968 | 597  | 599  | 5318 | 5348 | 535   | 5868 | 5888 |
|        |           | 6118 | 6168  | 6208  | 6218 | 6558 | 6568 | 6688 | 661  | 662  | 6658 | 667   | 678  | 7168 |
|        |           | 7198 | 7238  | 724   | 7288 | 729  | 883  | 825  | 839  | 8488 | 9888 | 9918  | 9928 | 9938 |

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|        |          |          |      |      |      |      |      |      |      |      |      |      |       |      |
|--------|----------|----------|------|------|------|------|------|------|------|------|------|------|-------|------|
| TU56\$ | 173502   | 893#     | 927  |      |      |      |      |      |      |      |      |      |       |      |
| WAIT   | 173002   | 709#     | 710  | 740  |      |      |      |      |      |      |      |      |       |      |
| STN    | = 000030 | 10       | 117  | 126# | 129  | 138# | 143  | 146  | 162# | 165  | 168  | 182# | 186   | 189  |
|        |          | 202#     | 206  | 209  | 222# | 224  | 227  | 242# | 253  | 256  | 271# | 273  | 276   | 291# |
|        |          | 297      | 300  | 315# | 320  | 323  | 337# | 339  | 342  | 357# | 364  | 367  | 382#  | 390  |
|        |          | 393      | 407# | 417  | 420  | 436# | 448  | 491  | 465# | 476  | 479  | 495# | 512   | 516  |
|        |          | 531#     | 544  | 556# | 575  | 596# | 601  | 629  | 644# | 766  | 785# | 1086 | 1824# |      |
|        |          | = 174000 | 116# | 784# | 964# |      |      |      |      |      |      |      |       |      |

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| SOB    | 410  | 413  | 443  | 511  | 540 | 610 | 623 | 658  | 666  | 673  | 698 | 1001 | 1003 | 1033 | 1048 |
|--------|------|------|------|------|-----|-----|-----|------|------|------|-----|------|------|------|------|
|        | 1052 |      |      |      |     |     |     |      |      |      |     |      |      |      |      |
| SUB    | 251  | 389  |      |      |     |     |     |      |      |      |     |      |      |      |      |
| SWAB   | 471  | 730  |      |      |     |     |     |      |      |      |     |      |      |      |      |
| TST    | 414  | 416  | 727  | 749  | 759 | 761 | 786 | 1002 | 1003 |      |     |      |      |      |      |
| TSTB   | 467  | 743  | 747  | 764  | 809 | 812 | 816 | 855  | 863  | 866  | 903 |      |      |      |      |
| .BYTE  | 890  | 891  | 895  | 897  | 898 | 899 | 900 | 902  | 903  | 905  | 906 | 908  |      |      |      |
| .DSABL | 1    |      |      |      |     |     |     |      |      |      |     |      |      |      |      |
| .ENABL | 1    |      |      |      |     |     |     |      |      |      |     |      |      |      |      |
| .END   | 1065 |      |      |      |     |     |     |      |      |      |     |      |      |      |      |
| .ENDC  | 118  | 125  | 127  | 130  | 137 | 144 | 147 | 159  | 166  | 169  | 181 | 187  | 190  | 201  | 207  |
|        | 210  | 221  | 225  | 228  | 241 | 254 | 257 | 270  | 274  | 277  | 290 | 298  | 301  | 314  | 321  |
|        | 324  | 336  | 340  | 343  | 356 | 365 | 368 | 381  | 391  | 394  | 406 | 410  | 421  | 439  | 449  |
|        | 452  | 464  | 477  | 480  | 494 | 513 | 517 | 530  | 545  | 555  | 576 | 595  | 602  | 638  | 643  |
|        | 967  | 984  | 1009 | 1028 |     |     |     |      |      |      |     |      |      |      |      |
| .EQUIV | 1    |      |      |      |     |     |     |      |      |      |     |      |      |      |      |
| .IF    | 117  | 124  | 126  | 129  | 136 | 143 | 146 | 158  | 165  | 168  | 180 | 186  | 189  | 200  | 206  |
|        | 209  | 220  | 224  | 227  | 240 | 253 | 256 | 269  | 273  | 276  | 289 | 297  | 300  | 313  | 320  |
|        | 323  | 335  | 339  | 342  | 355 | 364 | 367 | 380  | 390  | 393  | 405 | 417  | 420  | 434  | 448  |
|        | 451  | 463  | 476  | 479  | 493 | 512 | 516 | 529  | 544  | 554  | 575 | 594  | 601  | 629  | 642  |
|        | 966  | 983  | 1008 | 1027 |     |     |     |      |      |      |     |      |      |      |      |
| .IFF   | 117  | 124  | 126  | 129  | 136 | 143 | 146 | 158  | 165  | 168  | 180 | 186  | 189  | 200  | 206  |
|        | 209  | 220  | 224  | 227  | 240 | 253 | 256 | 269  | 273  | 276  | 289 | 297  | 300  | 313  | 320  |
|        | 323  | 335  | 339  | 342  | 355 | 364 | 367 | 380  | 390  | 393  | 405 | 417  | 420  | 434  | 448  |
|        | 451  | 463  | 476  | 479  | 493 | 512 | 516 | 529  | 544  | 554  | 575 | 594  | 601  | 629  | 642  |
|        | 966  | 983  | 1008 | 1027 |     |     |     |      |      |      |     |      |      |      |      |
| .IIF   | 117  | 118  | 119  | 124  | 129 | 130 | 131 | 136  | 146  | 147  | 148 | 150  | 168  | 169  | 170  |
|        | 180  | 189  | 190  | 191  | 200 | 209 | 210 | 211  | 220  | 227  | 228 | 229  | 240  | 250  | 257  |
|        | 258  | 269  | 276  | 277  | 278 | 289 | 300 | 301  | 302  | 313  | 323 | 324  | 325  | 335  | 342  |
|        | 343  | 344  | 355  | 367  | 368 | 369 | 380 | 393  | 394  | 395  | 405 | 420  | 421  | 422  | 434  |
|        | 451  | 452  | 453  | 463  | 479 | 480 | 481 | 493  | 516  | 517  | 518 | 529  | 544  | 545  | 546  |
|        | 554  | 575  | 576  | 577  | 594 | 629 | 630 | 631  | 642  | 666  | 707 | 708  | 783  | 1000 | 1009 |
|        | 1010 | 1027 |      |      |     |     |     |      |      |      |     |      |      |      |      |
| .LIST  | 1    | 77   | 96   | 100  | 112 | 117 | 126 | 129  | 130  | 146  | 168 | 169  | 182  | 189  | 202  |
|        | 209  | 222  | 227  | 242  | 256 | 271 | 276 | 291  | 300  | 319  | 323 | 337  | 342  | 357  | 367  |
|        | 382  | 393  | 407  | 420  | 436 | 451 | 465 | 479  | 495  | 516  | 531 | 544  | 556  | 575  | 596  |
|        | 629  | 644  | 683  | 700  | 951 | 961 | 966 | 985  | 1000 | 1029 |     |      |      |      |      |
| .MACRO | 1    | 117  | 129  | 146  | 168 | 189 | 209 | 227  | 228  | 270  | 300 | 323  | 342  | 367  | 393  |
|        | 420  | 451  | 479  | 516  | 544 | 575 | 629 | 706  | 1000 |      |     |      |      |      |      |
| .NLIST | 1    | 77   | 96   | 100  | 112 | 117 | 126 | 129  | 138  | 146  | 160 | 168  | 182  | 189  | 202  |
|        | 209  | 222  | 227  | 242  | 256 | 271 | 276 | 291  | 300  | 319  | 323 | 337  | 342  | 357  | 367  |
|        | 382  | 393  | 407  | 420  | 436 | 451 | 465 | 479  | 495  | 516  | 531 | 544  | 556  | 575  | 596  |
|        | 629  | 644  | 683  | 700  | 951 | 961 | 966 | 985  | 1000 | 1029 |     |      |      |      |      |
| .PAGE  | 1    | 21   | 75   | 681  | 886 | 911 | 948 |      |      |      |     |      |      |      |      |
| .REPT  | 77   | 96   | 100  | 112  | 683 | 700 | 875 | 951  | 961  |      |     |      |      |      |      |
| .SBTTL | 118  | 130  | 147  | 169  | 190 | 210 | 228 | 297  | 277  | 301  | 324 | 343  | 368  | 394  | 421  |
|        | 452  | 480  | 517  | 545  | 576 | 630 | 682 | 787  | 714  | 736  | 754 | 767  | 775  | 788  | 888  |
|        | 822  | 832  | 839  | 845  | 872 | 887 | 912 | 924  | 936  | 949  | 967 | 1009 |      |      |      |
| .TITLE | 13   |      |      |      |     |     |     |      |      |      |     |      |      |      |      |
| .WORD  | 250  | 722  | 796  | 797  | 875 | 876 | 877 | 878  | 879  | 888  | 889 | 893  | 959  | 914  | 915  |
|        | 916  | 917  | 918  | 919  | 920 | 921 | 922 | 926  | 927  | 928  | 929 | 930  | 931  | 932  | 933  |
|        | 934  | 938  | 939  | 940  | 941 | 942 | 943 | 944  | 945  | 946  |     |      |      |      |      |

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ERRORS DETECTED: 0

\*DEKBHA,DEKBHA,LIS/SOL/CRF=DEKBHA,P11  
RUN-TIME: 10 15 2 SECONDS  
CORE USED: 9K