

TK50

TK50 FRONT END FUNC
CZTKAB0

AH-T772B-MC
1 OF 1 OCT 1985
COPYRIGHT © 1985

digital
MADE IN USA

The image shows a grid of 150 small, illegible data blocks arranged in 10 columns and 15 rows. Each block appears to contain technical information, possibly test results or component specifications, but the text is too small to read. The blocks are separated by thin white lines, forming a structured layout typical of a data table or a test report.

1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
25
26
27
28
29
30
31
32
33
34
35
36
37
38
39
40
41
42
43
44

.REM @

IDENTIFICATION

PRODUCT CODE: AC - T771B - MC
PRODUCT NAME: CZTKABO TK50 FRONT END FUNCTIONAL
PRODUCT DATE: 26 - JUL - 1985
MAINTAINER: TAPE AND OPTICAL DIAGNOSTIC ENGINEERING
AUTHOR: RAYMOND CHANG

THE INFORMATION IN THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT
NOTICE AND SHOULD NOT BE CONSTRUED AS A COMMITMENT BY DIGITAL
EQUIPMENT CORPORATION. DIGITAL EQUIPMENT CORPORATION ASSUMES NO
RESPONSIBILITY FOR ANY ERRORS THAT MAY APPEAR IN THIS DOCUMENT.

NO RESPONSIBILITY IS ASSUMED FOR THE USE OR RELIABILITY OF
SOFTWARE ON EQUIPMENT THAT IS NOT SUPPLIED BY DIGITAL OR ITS
AFFILIATED COMPANIES.

COPYRIGHT (C) 1985 BY DIGITAL EQUIPMENT CORPORATION

THE FOLLOWING ARE TRADEMARKS OF DIGITAL EQUIPMENT CORPORATION:

DIGITAL	PDP	UNIBUS	MASSBUS
DEC	DECUS	DECTAPE	

46
47
48
49
50
51
52
53

REVISION HISTORY

APRIL 1985

NEW RELEASE

JUNE 1985

REVISION B
ADDED CODE SO THAT PROGRAM CAN RUN ON
PDP - 11 UNIBUS FAMILY CPUS.

218
219
220
221
222
223
224
225
226
227
228
229
230
231
232
233
234
235
236
237
238
239
240
241
242
243
244
245
246
247
248
249
250
251
252
253
254
255
256
257
258
259
260
261
262
263
264
265
266
267
268
269
270
271
272
273
274

3 ERROR REPORTS

Error reports will have two basic formats as described below. It is anticipated that, due to program partitioning, it will be possible to unambiguously define a single FRU as the cause of any error condition.

3.1 Error Format 1

This basic format will be used by all host level testing.

```
CZTKA DVC FTL ERR eee ON UNIT ll TST tt SUB ss PC: xxxxxx
SA REG CONTENTS INCORRECT
IN INIT SEQUENCE STEP #: n
SA EXPECTED: yyyyyy SA RECEIVED: zzzzzz
```

*****FAILING FRU: CNTL

In this example, the fields have the following meanings:

- eee = discrete error number as defined by program
- ll = logical unit number assigned to unit-in-error during hardware questions
- tt = test number during which error occurred
- ss = subtest number
- xxxxxx = program location of error call
- n = step number of the UQSSP initialization sequence which detected the error condition
- yyyyyy = expected contents of SA register for this step
- zzzzzz = actual SA register contents

3.2 Error Format 2

This format will be used for errors detected by the Level 2 microdiagnostics.

The DUP "Receive Data" command is used to monitor the status of the Level 2 diagnostic and to collect results. It is valid to issue a "Receive Data" command at any time after the Level 2s have been started.

```
CZTKA DEV FTL ERR eee ON UNIT ll TST 09 SUB ss PC: xxxxxx
DRIVE ERROR
```

```
275
276          **** LEVEL 2 MICRODIAGNOSTIC DUMP ****
277
278          Program Status          *****
279          Drive Status             ***
280          Test Number              ***
281          Track Number             ***
282          TMSCP Command            ***
283          TMSCP Response Flag      ***
284          TMSCP Unit Flags         *****
285
286          Error Log (Tape Transfer Error)
287          Error Format               ***
288          Error Flags               ***
289          Event Code                *****
290          Error Retry Level         ***
291          Number of Reties          ***
292          Position (Low Order)      *****
293          Position (High Order)     *****
294          Controller Status         ***
295          Drive Error Code          ***
296          Drive Flags               ***
297          Track number              ***
298          Physical Block Number     *****
299          Logical Block Number      ***
300          Tape Count 0              ***
301          Tape Count 1              ***
302          Tape Count 2              ***
303          Drive State               *****
304          Read/Write State          *****
305          Operation Flags           *****
306
307          Blocks Written Channel 1   *****
308          Blocks Written Channel 2   *****
309          Blocks Read Channel 1      *****
310          Blocks Read Channel 2      *****
311          Soft Write Channel 1       *****
312          Soft Write Channel 2       *****
313          Ecc Corrected Channel 1    *****
314          Ecc Corrected Channel 2    *****
315          Read Repositions Channel 1 *****
316          Read Repositions Channel 2 *****
```

3.2.1 Program Status -

The program status word is only valid when read with a message number of 1. Upon an error, it will contain the status field of the TMSCP command which produced the error. Note that there are several diagnostic error codes which will be returned with a "ST_DIA" diagnostic status.

If the test completed without a fatal or hard error, this word will be zero (Normal Successful Completion).

275
276
277
278
279
280
281
282
283
284
285
286
287
288
289
290
291
292
293
294
295
296
297
298
299
300
301
302
303
304
305
306
307
308
309
310
311
312
313
314
315
316
317
318
319
320
321
322
323
324
325
326
327
328
329
330
331

617
618
619
620
621
622
623
624
625
626
627
628
629
630
631
632
633
634
635
636
637
638
639
640
641
642
643
644
645
646
647
648

4.4 Bad Parameters Error

This error code indicates that the parameters passed to the Level 2 diagnostic were invalid.

4.5 Bad Pattern

This subcode indicates an internal diagnostic error and should never be seen.

4.6 Response Address Error

This subcode indicates an internal firmware or diagnostic problem and should never be seen.

4.7 Host Address Error

This subcode indicates an internal firmware problem and should never be seen.

4.8 Unknown Error Log

This subcode indicates an internal firmware problem and should never be seen.


```

000340      PRI07== 340
000300      PRI06== 300
000240      PRI05== 240
000200      PRI04== 200
000140      PRI03== 140
000100      PRI02== 100
000040      PRI01== 40
000000      PRI0C== 0

;
;OPERATOR FLAG BITS
;
000004      EVL==      4
000010      LOT==     10
000020      ADR==     20
000040      IDU==     40
000100      ISR==    100
000200      UAM==    200
000400      BOE==    400
001000      PNT==   1000
002000      PRI==   2000
004000      IXE==   4000
010000      IBE==  10000
020000      IER==  20000
040000      LOE==  40000
100000      HOE== 100000
    
```



```
1197 ;:*****
1198 ;:*****
1199 ;
1200 ;GENERAL PURPOSE EQUATES
1201 ;
1202 ;:*****
1203 ;:*****
1204
1205 000004 VEC4 == 4 ;VECTOR FOUR - NXM TIMEOUTS, ETC.
1206 177560 RCSR == 177560 ;TERMINAL RECEIVE CONTROL/STATUS REGISTER ADDRESS
1207 177562 RBUF == 177562 ;TERMINAL RECEIVE BUFFER ADDRESS
1208
```

```

1210      ;;*****
1211      ;;*****
1212      ;
1213      ;MEMORY MANAGEMENT EQUATES
1214      ;
1215      ;;*****
1216      ;;*****
1217
1218      177572      MMUSRO ==      177572 ;STATUS REG 0
1219      177574      MMUSR1 ==      177574
1220      177576      MMUSR2 ==      177576
1221      172516      MMUSR3 ==      172516 ;SHOULD ONLY BE PRESENT ON 22 BIT CPU'S
1222
1223      172340      KPAR0  ==      172340 ;KERNEL MODE PAGE ADDRESS REG 0
1224      172342      KPAR1  ==      172342
1225      172344      KPAR2  ==      172344
1226      172346      KPAR3  ==      172346
1227      172350      KPAR4  ==      172350
1228      172352      KPAR5  ==      172352
1229      172354      KPAR6  ==      172354
1230      172356      KPAR7  ==      172356 ;ALWAYS FOR I/O PAGE
1231
1232      172300      KPDR0   ==      172300 ;KERNEL MODE PAGE DESCRIPTOR REG 0
1233      172302      KPDR1   ==      172302
1234      172304      KPDR2   ==      172304
1235      172306      KPDR3   ==      172306
1236      172310      KPDR4   ==      172310
1237      172312      KPDR5   ==      172312
1238      172314      KPDR6   ==      172314
1239      172316      KPDR7   ==      172316
1240      ;
1241      000001      MMON    ==      BIT0  ;ENABLE MMU - MMUSRO
1242      000020      MM22ON ==      BIT4  ;ENABLE 22 BIT MMU - MMUSR3
1243
    
```



```

1614      ;*****
1615      ;*****
1616      ;
1617      ;PROTECTION TABLE
1618      ;
1619      ;*****
1620      ;*****
1624
1625 023044      BGNPROT
          023044      L$PROT::
1626 023044 000000      .WORD 0
1627 023046 177777      .WORD -1
1628 023050 177777      .WORD -1
1629
1630 023052      ENDPROT
1631

```



```
3531      .SBTTL TEST 5: BR LEVEL TEST
3535
3536      ;;*****
3537      ;;*****
3538      ;
3539      ;TEST 5 - BR LEVEL TEST
3540      ;   THIS TEST INSURES THAT THE TK50 CAN NOT INTERRUPT
3541      ;   WHEN THE CPU PRIORITY IS SET TO 7.  THE TEST GOES
3542      ;   ONLY THROUGH THE FIRST STEP OF THE INIT SEQUENCE
3543      ;   SINCE THE CONTROLLER WILL "HANG" WAITING FOR THE
3544      ;   INTERRUPT ACKNOWLEDGE.
3545      ;
3546      ;;*****
3547      ;;*****
3551
3552      043714          BGNTST
043714      T5::
3553
3554      043714  032764  000001  000014      BIT      #DRPFLG,LUNFLG(R4)      ;IF UUT NOT DROPPED
3555      043722  001423                               BEQ      G05                      ; THEN DO TEST
3556      043724          RFLAGS  FLAGS                          ;ELSE GO GET SUPRVISOR FLAGS
043724          104421      TRAP    C$RFLA
043726          010037  002334      MOV      RO,FLAGS
3557      043732  032737  001000  002334      BIT      #PNT,FLAGS      ;SEE IF WE'RE PRINTING TEST NUMBERS
3558      043740  001412                               BEQ      1$                      ;NO, DON'T PRINT BYPASSED
3559      043742          PRINTF #BYPASS,L$TEST      ; ELSE PRINT THE TEST
043742          013746  002114      MOV      L$TEST,-(SP)
043746          012746  047626      MOV      #BYPASS,-(SP)
043752          012746  000002      MOV      #2,-(SP)
043756          010600      MOV      SP,RO
043760          104417      TRAP    C$PNTF
043762          062706  000006      ADD      #6,SP
3560      043766          1$:      EXIT    TST                      ;BYPASSED MESSAGE AND GET OUT
043766          104432      TRAP    C$EXIT
043770          000370      .WORD  L10023-.
3561
3562      043772  052764  000004  000014  G05:   BIS      #BRFLAG,LUNFLG(R4)      ;DO TEST WITH HIGH PRIORITY
3563      044000  012737  032540  002310      MOV      #CTRL,FRUIS          ;FAILING FRU IN CASE OF ERROR
3564      044006  012737  000001  002272      MOV      #1,ITRCNT           ;SET UP FOR ONE TEST ITERATION
3565      044014  022737  000001  002270      CMP      #1,PASCNT           ;IF FIRST PASS
3566      044022  001403                               BEQ      2$                      ; THEN START TEST
3567      044024  012737  000002  002272      MOV      #2,ITRCNT           ; ELSE DO 10 ITERATIONS
3568
3569      044032          2$:      SETPRI #PRI07                      ;CPU PRIORITY = 7
044032          012700  000340      MOV      #PRI07,RO
044036          104441      TRAP    C$SPRI
3570      044040  004737  036316      JSR      PC,VECTOR          ;SET UP VECTOR WITH INTERRUPT HANDLER
3571      044044  012705  000000      MOV      #0,R5              ;SET UP R5 AS INDEX TO STEP TABLES
3572      044050  012737  000001  002316      MOV      #1,INISTP          ;STEP 1 FOR ERROR PRINTOUT
3573      044056  016437  000004  002250      MOV      TKVEC(R4),STPTBL    ;PUT VECTOR IN STEP 1
3574      044064  006237  002250      ASR      STPTBL              ;DIVIDE BY TWO
3575      044070  006237  002250      ASR      STPTBL              ;DIVIDE BY FOUR
3576      044074  052737  104600  002250      BIS      #104600,STPTBL     ;REST OF STEP ONE
3577      044102  016437  000004  002260      MOV      TKVEC(R4),CMPTBL    ;STEP 1 COMPARE VALUE
3578
3579      044110  004737  036434      JSR      PC,STEP1          ;GO DO IT
3580      044114  005737  002320      TST      STEPST            ;IF STATUS OKAY
```

```

3581 044120 001412          BEQ      5$              ; THEN CONTINUE TEST
3582
3583 044122          ERRDF    14.,EMSG9,PRIINI      ; "SA CONTENTS IN ERROR"
      044122 104455      TRAP    C$ERDF
      044124 000016      .WORD   14
      044126 030465      .WORD   EMSG9
      044130 032740      .WORD   PRIINI
3584 044132          CKLOOP           ; LOOP ON ERROR?
      044132 104406      TRAP    C$CLP1
3585 044134          DODU    LOGUNT          ; DROP UUT
      044134 013700 002312  MOV    LOGUNT,R0
      044140 104451      TRAP    C$DODU
3586 044142          ESCAPE  TST              ; LEAVE TST
      044142 104410      TRAP    C$ESCAPE
      044144 000214      .WORD   L10023-.
3587
3588 044146 012737 000100 002326 5$:   MOV    #100,OUTER          ; SET UP FOR DELAY ROUTINE
3589 044154 016537 002260 002314      MOV    CMPTBL(R5),SAEXP    ; SET UP FOR COMPARE
3590 044162 012737 037200 002324 7$:   MOV    #16000.,INNER      ; SET UP INNER
3591 044170 004737 036406 9$:   JSR    PC,PDELAY          ; ELSE GIVE UUT SOME TIME
3592 044174 005737 002330      TST    TOUT              ; IF NO TIMEOUT YET
3593 044200 001770      BEQ    7$                ; THEN GO TAKE ANOTHER LOOK
3594
3595 044202 017464 000002 000012      MOV    @TKSA(R4),TKSASV(R4) ; GET SA CONTENTS
3596 044210 023764 002314 000012      CMP    SAEXP,TKSASV(R4)   ; IF CONTENTS OKAY
3597 044216 001412      BEQ    10$              ; THEN CHECK FOR INTERRUPT
3598
3599 044220          ERRDF    17.,EMSG9,PRIINI      ; "SA CONTENTS IN ERROR"
      044220 104455      TRAP    C$ERDF
      044222 000021      .WORD   17
      044224 030465      .WORD   EMSG9
      044226 032740      .WORD   PRIINI
3600 044230          CKLOOP           ; LOOP ON ERROR?
      044230 104406      TRAP    C$CLP1
3601 044232          DODU    LOGUNT          ; DROP UUT
      044232 013700 002312  MOV    LOGUNT,R0
      044236 104451      TRAP    C$DODU
3602 044240          ESCAPE  TST              ; LEAVE TST
      044240 104410      TRAP    C$ESCAPE
      044242 000116      .WORD   L10023-.
3603
3604 044244 032764 000002 000014 10$:  BIT    #INTFLG,LUNFLG(R4) ; IF NO INTERRUPT OCCURRED
3605 044252 001415      BEQ    20$              ; THEN CARRY ON WITH TEST
3606 044254 042764 000002 000014      BIC    #INTFLG,LUNFLG(R4) ; CLEAR FLAG IN CASE WE'RE LOOPING
3607 044262          ERRDF    18.,EMSG12,PRIINI    ; "INTRRPT WITH CPU PRIORITY =7"
      044262 104455      TRAP    C$ERDF
      044264 000022      .WORD   18
      044266 030601      .WORD   EMSG12
      044270 032740      .WORD   PRIINI
3608 044272          CKLOOP           ; LOOP ON ERROR?
      044272 104406      TRAP    C$CLP1
3609 044274          DODU    LOGUNT          ; DROP UUT
      044274 013700 002312  MOV    LOGUNT,R0
      044300 104451      TRAP    C$DODU
3610 044302          ESCAPE  TST              ; LEAVE TST
      044302 104410      TRAP    C$ESCAPE
      044304 000054      .WORD   L10023-.
    
```

```

3611
3612 044306          20$:  SETPRI  #PRI00          ;CPU PRIORITY = 0
      044306 012700 000000      MOV    #PRI00,R0
      044312 104441          TRAP   C$SPRI
3613 044314 000240          NOP
3614 044316 000240          NOP
3615 044320 042764 000002 000014 BIC    #INTFLG,LUNFLG(R4) ;DELAY FOR PENDING INTERRUPT
3616                                     ;CLEAR THE FLAG NOW
3617 044326 032764 000001 000014 BIT    #DRPFLG,LUNFLG(R4) ;HAS UUT BEEN DROPPED
3618 044334 001005          BNE    T5EXT          ;LEAVE NOW IF SO
3619 044336 005337 002272          DEC    ITRCNT         ;IF NO MORE ITERATIONS LEFT
3620 044342 001402          BEQ    T5EXT          ; THEN EXIT
3621 044344 000137 044032          JMP    2$            ; ELSE DO IT AGAIN
3622
3623 044350 004737 036266          T5EXT: JSR    PC,RSTVEC ;CATCH ILLEGAL INTERRUPTS
3624 044354          EXIT    TST
      044354 104432          TRAP   C$EXIT
      044356 000002          .WORD  L10023-.
3625
3626 044360          ENDTST
      044360          L10023:
      044360 104401          TRAP   C$ETST
  
```

3629
 3633
 3634
 3635
 3636
 3637
 3638
 3639
 3640
 3641
 3642
 3643
 3644
 3645
 3646
 3647
 3648
 3649
 3650
 3651
 3655
 3656 044362
 044362
 3657
 3658 044362 032764 000001 000014
 3659 044370 001423
 3660 044372
 044372 104421
 044374 010037 002334
 3661 044400 032737 001000 002334
 3662 044406 001412
 3663 044410
 044410 013746 002114
 044414 012746 047626
 044420 012746 000002
 044424 010600
 044426 104417
 044430 062706 000006
 3664 044434
 044434 104432
 044436 000542
 3665
 3666 044440 012737 032640 002310
 3667 044446 012737 000001 002272
 3668 044454 022737 000001 002270
 3669 044462 001403
 3670 044464 012737 000012 002272
 3671
 3672 044472 012705 000000
 3673 044476 012737 000001 002316
 3674 044504 016437 000004 002250
 3675 044512 006237 002250
 3676 044516 006237 002250
 3677 044522 013737 002250 002264
 3678 044530 052737 104400 002250
 3679 044536
 044536 104407

```

.SBTTL TEST 6: PURGE AND POLL TEST

;*****
;*****
;
;TEST 6 - PURGE AND POLL TEST
; THIS TEST WILL AGAIN RUN THROUGH THE INIT SEQUENCE, THIS
; TIME SETTING THE "PURGE AND POLL" BIT IN STEP 3. THIS
; SHOULD CAUSE THE PORT TO DMA VARIOUS DATA PATTERNS TO
; AND FROM THE COMMUNICATIONS AREA AND FINALLY LEAVE IT
; CLEARED BEFORE TRANSITIONING TO STEP 4. THE PROGRAM WILL
; HAVE FILLED THIS AREA WITH A BACKGROUND PATTERN OF ALL
; 1'S DATA PRIOR TO STARTING THE INIT. WHEN STEP 4 IS
; REACHED, THE PROGRAM WILL VERIFY THAT THE COMM AREA IS
; ALL 0'S, AND THAT THE 20 WORDS PRECEDING AND SUCCEEDING
; THE COMM AREA ARE UNTOUCHED. RING DEPTH USED IN THIS
; TEST IS THE MINIMUM.
;*****
;*****

BGNTST
T6::
BIT      #DRPFLG,LUNFLG(R4)   ;IF UUT NOT DROPPED
BEQ      G06                  ; THEN DO TEST
RFLAGS   FLAGS                ;ELSE GO GET SUPRVISOR FLAGS
TRAP     C$RFLA
MOV      R0,FLAGS
BIT      #PNT,FLAGS          ;SEE IF WE'RE PRINTING TEST NUMBERS
BEQ      1$                   ;NO, DON'T PRINT BYPASSED
PRINTF   #BYPASS,L$TEST      ; ELSE PRINT THE TEST
MOV      L$TEST,-(SP)
MOV      #BYPASS,-(SP)
MOV      #2,-(SP)
MOV      SP,R0
TRAP     C$PNTF
ADD      #6,SP
3664 1$: EXIT TST                ;BYPASSED MESSAGE AND GET OUT
TRAP     C$EXIT
.WORD    L10024-

G06: MOV      #CTRL,FRUIS      ;FAILING FRU IN CASE OF ERROR
MOV      #1,ITRCNT           ;SET UP FOR ONE TEST ITERATION
CMP      #1,PASCNT          ;IF FIRST PASS
BEQ      2$                   ; THEN START TEST
MOV      #10.,ITRCNT        ; ELSE DO 10 ITERATIONS

2$: MOV      #0,R5            ;SET UP R5 AS INDEX TO STEP TABLES
MOV      #1,INISTP          ;STEP 1 FOR ERROR PRINTOUT
MOV      TKVEC(R4),STPTBL    ;PUT VECTOR IN STEP 1
ASR      STPTBL              ;DIVIDE BY TWO
ASR      STPTBL              ;DIVIDE BY FOUR
MOV      STPTBL,CMPTBL+4     ;PUT VECTOR IN STEP 3 COMPARE
BIS      #104400,STPTBL     ;REST OF STEP ONE
READBUS  ;IF QBUS
TRAP     C$RDBU
    
```

```

3680 044540          BCOMPLETE      3$          : THEN BR FOR QBUS SET UP
      044540 103404          BCS          3$          :
3681 044542 012737 004700 002260          MOV          #4700,CMPTBL      : ELSE SET UP FOR UNIBUS
3682 044550 000403          BR           4$          :
3683 044552 012737 005700 002260 3$:          MOV          #B.S1!B.QB!B.DI!B.OD!B.MP,CMPTBL
3684          :STEP 1 COMPARE VALUE
3685 044560 012737 060050 002252 4$:          MOV          #COMMAR,STPTBL+2      :STEP 2 - COMM AREA ADDRESS
3686 044566 012737 010211 002262          MOV          #010211,CMPTBL+2      :STEP 2 COMPARE
3687 044574 012737 100000 002254          MOV          #B.PP,STPTBL+4      :STEP 3 - HIGH ADDRESS AND PRGE/POLL
3688 044602 112737 000040 002265          MOV          #40,CMPTBL+5      :REST OF STEP 3 COMPARE
3689 044610 012737 000000 002256          MOV          #0,STPTBL+6      :STEP 4
3690 044616 012737 040000 002266          MOV          #040000,CMPTBL+6      :STEP 4 COMPARE
3691
3692 044624 012737 000012 002306          MOV          #10.,CMARLG      :LENGTH OF COMM AREA FOR THIS TEST
3693 044632 004737 036560          JSR          PC,BAKPAT      :FILL COMM AREA WITH ALL 1'S DATA
3694
3695 044636 004737 036434          JSR          PC,STEP1      :GO DO IT
3696 044642 005737 002320          TST          STEPST      :IF STATUS OKAY
3697 044646 001412          BEQ          5$          : THEN CONTINUE TEST
3698
3699 044650          ERRDF       19.,MSG9,PRIINI      : "SA CONTENTS IN ERROR"
      044650 104455          TRAP        C$ERDF
      044652 000023          .WORD      19
      044654 030465          .WORD      MSG9
      044656 032740          .WORD      PRIINI
3700 044660          CKLOOP      :LOOP ON ERROR?
      044660 104406          TRAP        C$CLP1
3701 044662          DODU        LOGUNT      :DROP OUT
      044662 013700 002312          MOV          LOGUNT,RO
      044666 104451          TRAP        C$DODU
3702 044670          ESCAPE     TST          :LEAVE TST
      044670 104410          TRAP        C$ESCAPE
      044672 000306          .WORD      L10024-.
3703
3704 044674 005237 002316          5$:          INC          INISTP      :ADJUST STEP COUNTER
3705 044700 062705 000002          ADD          #2,R5      :ADJUST TABLE INDEX
3706 044704 012737 000100 002326 6$:          MOV          #100,OUTER      :SET UP FOR DELAY ROUTINE
3707 044712 016537 002260 002314          MOV          CMPTBL(R5),SAEXP      :SET UP FOR COMPARE
3708 044720 012737 037200 002324 7$:          MOV          #16000.,INNER      :SET UP INNER
3709 044726 017464 000002 000012          MOV          @TKSA(R4),TKSASV(R4) :GET SA CONTENTS
3710 044734 022705 000006          CMP          #6,R5      :ARE WE IN STEP 4?
3711 044740 001005          BNE          8$          :BRANCH IF NOT
3712 044742 033764 002314 000012          BIT          SAEXP,TKSASV(R4)      :JUST LOOK FOR STEP 4 BIT
3713 044750 001024          BNE          10$         :IT'S SET SO LET'S GO
3714 044752 000404          BR           9$          :STAY IN LOOP OTHERWISE
3715 044754 023764 002314 000012 8$:          CMP          SAEXP,TKSASV(R4)      :IF SA IS WHAT WE EXPECT
3716 044762 001417          BEQ          10$         : THEN MOVE ALONG
3717 044764 004737 036406          9$:          JSR          PC,PDELAY      : ELSE GIVE UUT SOME TIME
3718 044770 005737 002330          TST          TOUT      :IF NO TIMEOUT YET
3719 044774 001751          BEQ          7$          : THEN GO TAKE ANOTHER LOOK
3720
3721 044776          ERRDF       20.,MSG9,PRIINI      : "SA CONTENTS IN ERROR"
      044776 104455          TRAP        C$ERDF
      045000 000024          .WORD      20
      045002 030465          .WORD      MSG9
      045004 032740          .WORD      PRIINI
3722 045006          CKLOOP
    
```

```

045006 104406          TRAP  C#CLP1
3723 045010          DODU  LOGUNT
      045010 013700 002312  MOV  LOGUNT,R0
      045014 104451          TRAP  C#DODU
3724 045016          ESCAPE TST
      045016 104410          TRAP  C#ESCAPE
      045020 000160          .WORD L10024-.

3725
3726 045022 016574 002250 000002 10$:  MOV  STPTBL(R5),@TKSA(R4) ;WRITE NEXT STEP TO UUT
3727 045030 022705 000004          CMP  #4,R5 ;IF STEP 3
3728 045034 001404          BEQ  15$ ; THEN DO PURGE/POLL STUFF
3729 045036 022705 000006          CMP  #6,R5 ;IF NOT IN STEP 4
3730 045042 001314          BNE  5$ ; THEN GO BACK TO MAIN LOOP
3731 045044 000440          BR   20$ ; ELSE GO CHECK RESULTS
3732
3733 045046          15$:  DELAY 1 ;GIVE PORT SOME TIME
      045046 012727 000001          MOV  #1,(PC)+
      045052 000000          .WORD 0
      045054 013727 002116          MOV  L$DLY,(PC)+
      045060 000000          .WORD 0
      045062 005367 177772          DEC  -6(PC)
      045066 001375          BNE  -.4
      045070 005367 177756          DEC  -22(PC)
      045074 001367          BNE  -.20
3734 045076 017464 000002 000012          MOV  @TKSA(R4),TKSASV(R4) ;GET SA CONTENTS
3735 045104 001412          BEQ  16$ ;BRANCH IF OKAY
3736
3737 045106          ERRDF 21.,EMSG13,PRIINI ;SA NOT 0 IN PURGE/POLL
      045106 104455          TRAP  C#ERDF
      045110 000025          .WORD 21
      045112 030650          .WORD EMSG13
      045114 032740          .WORD PRIINI
3738 045116          CKLOOP
      045116 104406          TRAP  C#CLP1
3739 045120          DODU  LOGUNT
      045120 013700 002312  MOV  LOGUNT,R0
      045124 104451          TRAP  C#DODU
3740 045126          ESCAPE TST
      045126 104410          TRAP  C#ESCAPE
      045130 000050          .WORD L10024-.

3741
3742 045132 012774 000000 000002 16$:  MOV  #0,@TKSA(R4) ;WRITE 0'S TO SA
3743 045140 005774 000000          TST  @TKIP(R4) ;AND READ IP
3744 045144 000653          BR   5$ ;GO WAIT FOR NEXT TRANSITION
3745
3746 045146 004737 036610          20$:  JSR  PC,CHKCOM ;GO CHECK COMM AREA
3747 045152 032764 000001 000014          BIT  #DRPFLG,LUNFLG(R4) ;HAS UUT BEEN DROPPED
3748 045160 001005          BNE  T6EXT ;LEAVE NOW IF SO
3749 045162 005337 002272          DEC  ITRCNT ;IF NO MORE ITERATIONS LEFT
3750 045166 001402          BEQ  T6EXT ; THEN LEAVE TEST
3751 045170 000137 044472          JMP  2$ ; ELSE DO IT AGAIN
3752
3753 045174          T6EXT: EXIT TST
      045174 104432          TRAP  C#EXIT
      045176 000002          .WORD L10024-.

3754
3755 045200          ENDTST
    
```

045200
045200 104401

L10024:
TRAP C\$ETST

```

3758 .SBTTL TEST 7: MAXIMUM RING BUFFER TEST
3759
3760 045202 STARS
;*****
3761 045202 STARS
;*****
3762 ;
3763 ;TEST 7 - MAXIMUM RING BUFFER TEST
3764 ; THIS TEST IS SIMILAR TO TEST 5, BUT IT WILL UTILIZE
3765 ; THE MAXIMUM ALLOWABLE RING DEPTH AS SPECIFIED IN
3766 ; UQSSP. THIS VALUE IS EQUAL TO 128 COMMAND AND 128
3767 ; RESPONSE SLOTS OF 32 BITS PER SLOT.
3768 ;
3769 045202 STARS
;*****
3770 045202 STARS
;*****
3771
3772 045202 BGNTST
045202 T7::
3773
3774 045202 032764 000001 000014 BIT #DRPFLG,LUNFLG(R4) ;IF UUT NOT DROPPED
3775 045210 001423 BEQ G07 ; THEN DO TEST
3776 045212 RFLAGS FLAGS ;ELSE GO GET SUPRVISOR FLAGS
045212 104421 TRAP C#RFLA
045214 010037 002334 MOV R0,FLAGS
3777 045220 032737 001000 002334 BIT #PNT,FLAGS ;SEE IF WE'RE PRINTING TEST NUMBERS
3778 045226 001412 BEQ 1$ ;NO, DON'T PRINT BYPASSED
3779 045230 PRINTF #BYPASS,L$TEST ; ELSE PRINT THE TEST
045230 013746 002114 MOV L$TEST,-(SP)
045234 012746 047626 MOV #BYPASS,-(SP)
045240 012746 000002 MOV #2,-(SP)
045244 010600 MOV SP,R0
045246 104417 TRAP C#PNTF
045250 062706 000006 ADD #6,SP
3780 045254 1$: EXIT TST ;BYPASSED MESSAGE AND GET OUT
045254 104432 TRAP C#EXIT
045256 000542 .WORD L10025-.
3781
3782 045260 012737 032640 002310 G07: MOV #CTRL,FRUIS ;FAILING FRU IN CASE OF ERROR
3783 045266 012737 000001 002272 MOV #1,ITRCNT ;SET UP FOR ONE TEST ITERATION
3784 045274 022737 000001 002270 CMP #1,PASCNT ;IF FIRST PASS
3785 045302 001403 BEQ 2$ ; THEN START TEST
3786 045304 012737 000012 002272 MOV #10.,ITRCNT ; ELSE DO 10 ITERATIONS
3787
3788 045312 012705 000000 2$: MOV #0,R5 ;SET UP R5 AS INDEX TO STEP TABLES
3789 045316 012737 000001 002316 MOV #1,INISTP ;STEP 1 FOR ERROR PRINTOUT
3790 045324 016437 000004 002250 MOV TKVEC(R4),STPTBL ;PUT VECTOR IN STEP 1
3791 045332 006237 002250 ASR STPTBL ;DIVIDE BY TWO
3792 045336 006237 002250 ASR STPTBL ;DIVIDE BY FOUR
3793 045342 013737 002250 002264 MOV STPTBL,CMPTBL+4 ;PUT VECTOR IN STEP 3 COMPARE
3794 045350 052737 137400 002250 BIS #137400,STPTBL ;REST OF STEP ONE
3795 045356 READBUS ;IF QBUS
045356 1044J7 TRAP C#RDBU
3796 045360 BCOMPLETE 3$ ; THEN BR FOR QBUS SET UP
045360 103404 BCS 3$
3797 045362 012737 004700 002260 MOV #4700,CMPTBL ; ELSE SET UP FOR UNIBUS
    
```

3798	045370	000403			BR	4\$		
3799	045372	012737	005700	002260	3:	MOV	#B.S1!B.QB!B.DI!B.OD!B.MP,CMPTBL	
3800								;STEP 1 COMPARE VALUE
3801	045400	012737	060050	002252	4\$:	MOV	#COMMAR,STPTBL+2	;STEP 2 - COMM AREA ADDRESS
3802	045406	012737	010277	002262		MOV	#010277,CMPTBL+2	;STEP 2 COMPARE
3803	045414	012737	100000	002254		MOV	#B.PP,STPTBL+4	;STEP 3 - HIGH ADDRESS AND PRGE/POLL
3804	045422	112737	000040	002265		MOVB	#40,CMPTBL+5	;REST OF STEP 3 COMPARE
3805	045430	012737	000000	002256		MOV	#0,STPTBL+6	;STEP 4
3806	045436	012737	040000	002266		MOV	#040000,CMPTBL+6	;STEP 4 COMPARE
3807								
3808	045444	012737	001002	002306		MOV	#514.,CMARLG	;LENGTH OF COMM AREA FOR THIS TEST
3809	045452	004737	036560			JSR	PC,BAKPAT	;FILL COMM AREA WITH ALL 1'S DATA
3810								
3811	045456	004737	036434			JSR	PC,STEP1	;GO DO IT
3812	045462	005737	002320			TST	STEPST	;IF STATUS OKAY
3813	045466	001412				BEQ	5\$; THEN CONTINUE TEST
3814								
3815	045470					ERRDF	22.,EMSG9,PRINI	;"SA CONTENTS IN ERROR"
	045470	104455				TRAP	C\$ERDF	
	045472	000026				.WORD	22	
	045474	030465				.WORD	EMSG9	
	045476	032740				.WORD	PRINI	
3816	045500					CKLOOP		;LOOP ON ERROR?
	045500	104406				TRAP	C\$CLP1	
3817	045502					DODU	LOGUNT	;DROP UUT
	045502	013700	002312			MOV	LOGUNT,RO	
	045506	104451				TRAP	C\$DODU	
3818	045510					ESCAPE	TST	;LEAVE TST
	045510	104410				TRAP	C\$ESCAPE	
	045512	000306				.WORD	L10025-	
3819								
3820	045514	005237	002316		5\$:	INC	INISTP	;ADJUST STEP COUNTER
3821	045520	062705	000002			ADD	#2,R5	;ADJUST TABLE INDEX
3822	045524	012737	000100	002326	6\$:	MOV	#100,OUTER	;SET UP FOR DELAY ROUTINE
3823	045532	016537	002260	002314		MOV	CMPTBL(R5),SAEXP	;SET UP FOR COMPARE
3824	045540	012737	037200	002324	7\$:	MOV	#16000.,INNER	;SET UP INNER
3825	045546	017464	000002	000012		MOV	@TKSA(R4),TKSASV(R4)	;GET SA CONTENTS
3826	045554	022705	000006			CMP	#6,R5	;ARE WE IN STEP 4?
3827	045560	001005				BNE	8\$;BRANCH IF NOT
3828	045562	033764	002314	000012		BIT	SAEXP,TKSASV(R4)	;JUST LOOK FOR STEP 4 BIT
3829	045570	001024				BNE	10\$;IT'S SET SO LET'S GO
3830	045572	000404				BR	9\$;STAY IN LOOP OTHERWISE
3831	045574	023764	002314	000012	8\$:	CMP	SAEXP,TKSASV(R4)	;IF SA IS WHAT WE EXPECT
3832	045602	001417				BEQ	10\$; THEN MOVE ALONG
3833	045604	004737	036406		9\$:	JSR	PC,PDELAY	; ELSE GIVE UUT SOME TIME
3834	045610	005737	002330			TST	TOUT	;IF NO TIMEOUT YET
3835	045614	001751				BEQ	7\$; THEN GO TAKE ANOTHER LOOK
3836								
3837	045616					ERRDF	23.,EMSG9,PRINI	;"SA CONTENTS IN ERROR"
	045616	104455				TRAP	C\$ERDF	
	045620	000027				.WORD	23	
	045622	030465				.WORD	EMSG9	
	045624	032740				.WORD	PRINI	
3838	045626					CKLOOP		
	045626	104406				TRAP	C\$CLP1	
3839	045630					DODU	LOGUNT	
	045630	013700	002312			MOV	LOGUNT,RO	

```

3840 045634 104451          TRAP  C%DODU
      045636          ESCAPE TST
      045636 104410          TRAP  C$ESCAPE
      045640 000160          .WORD L10025-.

3841
3842 045642 016574 002250 000002 10$:  MOV  STPTBL(R5),@TKSA(R4)  ;WRITE NEXT STEP TO UUT
3843 045650 022705 000004          CMP  #4,R5                ;IF STEP 3
3844 045654 001404          BEQ  15$                 ; THEN DO PURGE/POLL STUFF
3845 045656 022705 000006          CMP  #6,R5                ;IF NOT IN STEP 4
3846 045662 001314          BNE  5$                  ; THEN GO BACK TO MAIN LOOP
3847 045664 000440          BR   20$                 ; ELSE GO CHECK RESULTS
3848
3849 045666          15$:  DELAY  1                    ;GIVE PORT SOME TIME
      045666 012727 000001          MOV  #1,(PC)+
      045672 000000          .WORD 0
      045674 013727 002116          MOV  L$DLY,(PC)+
      045700 000000          .WORD 0
      045702 005367 177772          DEC  -6(PC)
      045706 001375          BNE  -.4
      045710 005367 177756          DEC  -22(PC)
      045714 001367          BNE  -.20
3850 045716 017464 000002 000012          MOV  @TKSA(R4),TKSASV(R4) ;GET SA CONTENTS
3851 045724 001412          BEQ  16$                 ;BRANCH IF OKAY
3852
3853 045726          ERRDF  24.,EMSG13,PRIINI ;SA NOT 0 IN PURGE/POLL
      045726 104455          TRAP  C$ERDF
      045730 000030          .WORD 24
      045732 030650          .WORD EMSG13
      045734 032740          .WORD PRIINI
3854 045736          CKLOOP
      045736 104406          TRAP  C$CLP1
3855 045740          DODU  LOGUNT
      045740 013700 002312          MOV  LOGUNT,R0
      045744 104451          TRAP  C%DODU
3856 045746          ESCAPE TST
      045746 104410          TRAP  C$ESCAPE
      045750 000050          .WORD L10025-.

3857
3858 045752 012774 000000 000002 16$:  MOV  #0,@TKSA(R4)        ;WRITE 0'S TO SA
3859 045760 005774 000000          TST  @TKIP(R4)           ;AND READ IP
3860 045764 000653          BR   5$                  ;GO WAIT FOR NEXT TRANSITION
3861
3862 045766 004737 036610          20$:  JSR  PC,CHKCOM         ;GO CHECK COMM AREA
3863 045772 032764 000001 000014          BIT  #DRPFLG,LUNFLG(R4) ;HAS UUT BEEN DROPPED
3864 046000 001005          BNE  T7EXT               ;LEAVE NOW IF SO
3865 046002 005337 002272          DEC  ITRCNT              ;IF NO MORE ITERATIONS LEFT
3866 046006 001402          BEQ  T7EXT               ; THEN LEAVE TEST
3867 046010 000137 045312          JMP  2$                  ; ELSE DO IT AGAIN
3868
3869 046014          T7EXT: EXIT  TST
      046014 104432          TRAP  C$EXIT
      046016 000002          .WORD L10025-.

3870
3871 046020          ENDTST
      046020          L10025: TRAP  C$ETST
      046020 104401
    
```

```

3874          .SBTTL TEST 8: EXTENDED ADDRESS TEST
3875
3876 046022   STARS
              ;;*****
3877 046022   STARS
              ;;*****
3878
3879          ;
3880          ;TEST 8 - EXTENDED ADDRESS TEST
3881          ; THE FORMAT OF THIS TEST IS SIMILAR TO TEST 6, BUT THE
3882          ; PROGRAM WILL ESTABLISH THE COMMUNICATION AREA IN THE
3883          ; HIGHEST AVAILABLE MEMORY LOCATIONS. THIS WILL ALLOW
3884          ; TESTING OF THE UPPER SIX BITS OF ADDRESS LOGIC ON THE
3885          ; CONTROLLER BOARD.
3886 046022   STARS
              ;;*****
3887 046022   STARS
              ;;*****
3888
3889 046022   BGNTST
3890 046022   T8::
3891 046022 032764 000001 000014   BIT    #DRPFLG,LUNFLG(R4)   ;IF UUT NOT DROPPED
3892 046030 001423                   BEQ    G08                   ; THEN DO TEST
3893 046032                   RFLAGS  FLAGS                   ;ELSE GO GET SUPRVISOR FLAGS
              046032 104421           TRAP   C$RFLA
              046034 010037 002334   MOV    R0,FLAGS
3894 046040 032737 001000 002334   BIT    #PNT,FLAGS           ;SEE IF WE'RE PRINTING TEST NUMBERS
3895 046046 001412                   BEQ    1$                   ;NO, DON'T PRINT BYPASSED
3896 046050                   PRINTF  #BYPASS,L$TEST           ; ELSE PRINT THE TEST
              046050 013746 002114   MOV    L$TEST,-(SP)
              046054 012746 047626   MOV    #BYPASS,-(SP)
              046060 012746 000002   MOV    #2,-(SP)
              046064 010600           MOV    SP,R0
              046066 104417           TRAP   C$PNTF
              046070 062706 000006   ADD    #6,SP
3897 046074                   1$:   EXIT   TST                   ;BYPASSED MESSAGE AND GET OUT
              046074 104432           TRAP   C$EXIT
              046076 000646           .WORD  L10026-.
3898
3899 046100 005737 002274           G08:   TST    KTFLAG           ;IF MEMORY MANAGEMENT AVAILABLE
3900 046104 001002                   BNE    1$                   ; THEN DO TEST
3901 046106                   EXIT   TST                   ; ELSE GET OUT
              046106 104432           TRAP   C$EXIT
              046110 000634           .WORD  L10026-.
3902 046112 012737 032640 002310 1$:   MOV    #CTRL,FRUIS           ;FAILING FRU IN CASE OF ERROR
3903 046120 012737 000001 002272   MOV    #1,IIRCNT           ;SET UP FOR ONE TEST ITERATION
3904 046126 022737 000001 002270   CMP    #1,PASCNT           ;IF FIRST PASS
3905 046134 001403                   BEQ    2$                   ; THEN START TEST
3906 046136 012737 000012 002272   MOV    #10.,IIRCNT         ; ELSE DO 10 ITERATIONS
3907
3908 046144 004737 036742           2$:   JSR    PC,INTMMU           ;INITIALIZE MMU REGISTERS
3909 046150 012705 000000           3$:   MOV    #0,R5               ;SET UP R5 AS INDEX TO STEP TABLES
3910 046154 012737 000001 002316   MOV    #1,INISTP           ;STEP 1 FOR ERROR PRINTOUT
3911 046162 016437 000004 002250   MOV    TKVEC(R4),STPTBL    ;PUT VECTOR IN STEP 1
3912 046170 006237 002250           ASR    STPTBL               ;DIVIDE BY TWO
3913 046174 006237 002250           ASR    STPTBL               ;DIVIDE BY FOUR
    
```

3914	046200	013737	002250	002264	MOV	STPTBL,CMPTBL+4	;PUT VECTOR IN STEP 3 COMPARE
3915	046206	052737	111000	002250	BIS	#111000,STPTBL	;REST OF STEP ONE
3916	046214				READBUS		;IF QBUS
	046214	104407			TRAP	C#RDBU	
3917	046216				BCOMPLETE	11\$; THEN BR FOR QBUS SET UP
	046216	103404			BCS	11\$	
3918	046220	012737	004700	002260	MOV	#4700,CMPTBL	; ELSE SET UP FOR UNIBUS
3919	046226	000403			BR	12\$	
3920	046230	012737	005700	002260	11\$:	MOV	#B.S1!B.QB!B.DI!B.OD!B.MP,CMPTBL
3921							;STEP 1 COMPARE VALUE
3922	046236	012737	060050	002252	12\$:	MOV	#COMMAR,STPTBL+2
3923	046244	042737	160000	002252		BIC	#BIT15!BIT14!BIT13,STPTBL+2
3924							;CLEAR THE ACTIVE PAGE FIELD
3925	046252	012737	010222	002262	MOV	#010222,CMPTBL+2	;STEP 2 COMPARE
3926	046260	013737	172346	002332	MOV	KPAR3,TEMP	;GET RELOCATION VALUE
3927	046266	113737	002333	002254	MOVB	TEMP+1,STPTBL+4	;JUST THE HGH BYTE
3928	046274	006237	002254		ASR	STPTBL+4	;MAKE IT THE EXTENDED
3929	046300	006237	002254		ASR	STPTBL+4	; ADDRESS OF THE COMM AREA
3930	046304	052737	100000	002254	BIS	#B.PP,STPTBL+4	;NOW SET PURGE/POLL BIT
3931	046312	112737	000040	002265	MOVB	#40,CMPTBL+5	;REST OF STEP 3 COMPARE
3932	046320	012737	000000	002256	MOV	#0,STPTBL+6	;STEP 4
3933	046326	012737	040000	002266	MOV	#040000,CMPTBL+6	;STEP 4 COMPARE
3934							
3935	046334	012737	000022	002306	MOV	#18.,CMARLG	;LENGTH OF COMM AREA FOR THIS TEST
3936	046342	004737	036560		JSR	PC,BAKPAT	;FILL COMM AREA WITH ALL 1'S DATA
3937							
3938	046346	004737	036434		JSR	PC,STEP1	;GO DO IT
3939	046352	005737	002320		TST	STEPST	;IF STATUS OKAY
3940	046356	001412			BEQ	5\$; THEN CONTINUE TEST
3941							
3942	046360				ERRDF	25.,EMSG9,PRIINI	; "SA CONTENTS IN ERROR"
	046360	104455			TRAP	C#ERDF	
	046362	000031			.WORD	25	
	046364	030465			.WORD	EMSG9	
	046366	032740			.WORD	PRIINI	
3943	046370				CKLOOP		;LOOP ON ERROR?
	046370	104406			TRAP	C#CLP1	
3944	046372				DODU	LOGUNT	;DROP UUT
	046372	013700	002312		MOV	LOGUNT,R0	
	046376	104451			TRAP	C#DODU	
3945	046400				ESCAPE	TST	;LEAVE TST
	046400	104410			TRAP	C#ESCAPE	
	046402	000342			.WORD	L10026--	
3946							
3947	046404	005237	002316		5\$:	INC	INISTP
3948	046410	062705	000002		ADD	#2,R5	;ADJUST STEP COUNTER
3949	046414	012737	000100	002326	6\$:	MOV	#100,OUTER
3950	046422	016537	002260	002314	MOV	CMPTBL(R5),SAEXP	;SET UP FOR DELAY ROUTINE
3951	046430	012737	037200	002324	7\$:	MOV	#16000.,INNER
3952	046436	017464	000002	000012	MOV	@TKSA(R4),TKSASV(R4)	;SET UP INNER
3953	046444	022705	000006		CMP	#6,R5	;GET SA CONTENTS
3954	046450	001005			BNE	8\$;ARE WE IN STEP 4?
3955	046452	033764	002314	000012	BIT	SAEXP,TKSASV(R4)	;BRANCH IF NOT
3956	046460	001024			BNE	10\$;JUST LOOK FOR STEP 4 BIT
3957	046462	000404			BR	9\$;IT'S SET SO LET'S GO
3958	046464	023764	002314	000012	8\$:	CMP	SAEXP,TKSASV(R4)
3959	046472	001417			BEQ	10\$;STAY IN LOOP OTHERWISE
							;IF SA IS WHAT WE EXPECT
							; THEN MOVE ALONG

```

3960 046474 004737 036406          9$: JSR    PC,PDELAY      ; ELSE GIVE UUT SOME TIME
3961 046500 005737 002330          TST    TOUT          ;IF NO TIMEOUT YET
3962 046504 001751                   BEQ    7$            ; THEN GO TAKE ANOTHER LOOK
3963
3964 046506                   ERRDF  26.,EMSG9,PRIINI ;"SA CONTENTS IN ERROR"
      046506 104455          TRAP  C$ERDF
      046510 000032          .WORD 26
      046512 030465          .WORD EMSG9
      046514 032740          .WORD PRIINI
3965 046516                   CKLOOP
      046516 104406          TRAP  C$CLP1
3966 046520                   DODU  LOGUNT
      046520 013700 002312        MOV   LOGUNT,R0
      046524 104451          TRAP  C$DODU
3967 046526                   ESCAPE TST
      046526 104410          TRAP  C$ESCAPE
      046530 000214          .WORD L10026-.
3968
3969 046532 016574 002250 000002 10$: MOV   STPTBL(R5),@TKSA(R4) ;WRITE NEXT STEP TO UUT
3970 046540 022705 000004          CMP   #4,R5          ;IF STEP 3
3971 046544 001404                   BEQ   15$            ; THEN DO PURGE/POLL STUFF
3972 046546 022705 000006          CMP   #6,R5          ;IF NOT IN STEP 4
3973 046552 001314                   BNE  5$              ; THEN GO BACK TO MAIN LOOP
3974 046554 000440                   BR   20$            ; ELSE GO CHECK RESULTS
3975
3976 046556                   15$: DELAY 1          ;GIVE PORT SOME TIME
      046556 012727 000001        MOV   #1,(PC)+
      046562 000000          .WORD 0
      046564 013727 002116        MOV   L$DLY,(PC)+
      046570 000000          .WORD 0
      046572 005367 17777c        DEC   -6(PC)
      046576 001375          BNE   -.4
      046600 005367 177756        DEC   -22(PC)
      046604 001367          BNE   -.20
3977 046606 017464 000002 000012        MOV   @TKSA(R4),TKSASV(R4) ;GET SA CONTENTS
3978 046614 001412                   BEQ   16$            ;BRANCH IF OKAY
3979
3980 046616                   ERRDF  27.,EMSG13,PRIINI ;SA NOT 0 IN PURGE/POLL
      046616 104455          TRAP  C$ERDF
      046620 000033          .WORD 27
      046622 030650          .WORD EMSG13
      046624 032740          .WORD PRIINI
3981 046626                   CKLOOP
      046626 104406          TRAP  C$CLP1
3982 046630                   DODU  LOGUNT
      046630 013700 002312        MOV   LOGUNT,R0
      046634 104451          TRAP  C$DODU
3983 046636                   ESCAPE TST
      046636 104410          TRAP  C$ESCAPE
      046640 000104          .WORD L10026-.
3984
3985 046642 012774 000000 000002 16$: MOV   #0,@TKSA(R4)      ;WRITE 0'S TO SA
3986 046650 005774 000000          TST   @TKIP(R4)     ;AND READ IP
3987 046654 000653                   BR   5$              ;GO WAIT FOR NEXT TRANSITION
3988
3989 046656 004737 036610          20$: JSR    PC,CHKCOM    ;GO CHECK COMM AREA
3990 046662 032764 000001 000014        BIT   #DRPFLG,LUNFLG(R4) ;HAS UUT BEEN DROPPED
    
```

```
3991 046670 001021          BNE    T8EXT          ;LEAVE NOW IF SO
3992
3993 046672 062737 002000 172346  ADD    #2000,KPAR3    ;POINT TO NEXT 32KWORDS
3994 046700 103406          BCS    25$           ;DON'T ALLOW OVERFLOW IF 4 MBYTES
3995 046702 023737 002120 172346  CMP    L$HIME,KPAR3  ;IF THERE'S NO MORE MEMORY AVAILABLE
3996 046710 103402          BLO    25$           ; THEN CHECK FOR MORE ITERATIONS
3997 046712 000137 046150          JMP    3$            ; ELSE DO IT AGAIN
3998
3999 046716 005037 177572          25$:  CLR    MMUSRO    ;SHUT DOWN MEMORY MANAGEMENT
4000 046722 005337 002272          DEC    ITRCNT        ;IF NO MORE ITERATIONS LEFT
4001 046726 001402          BEQ    T8EXT        ; THEN LEAVE TEST
4002 046730 000137 046144          JMP    2$            ; ELSE DO IT AGAIN
4003
4004 046734 005037 177572          T8EXT: CLR    MMUSRO  ;MAKE SURE IT'S OFF
4005 046740          EXIT    TST
      046740 104432          TRAP   C$EXIT
      046742 000002          .WORD  L10026--
4006
4007 046744          ENDTST
      046744          L10026:
      046744 104401          TRAP   C$ETST
```

```

4010 .SBTTL TEST 9:GET DUST STATUS
4011
4012 046746 STARS
;*****
4013 046746 STARS
;*****
4014
4015 ;TEST 9 - GET DUST STATUS
4016 ; THE GET DUST STATUS TEST WILL REQUEST AND TEST THE DUST
4017 ; STATUS OF EACH UNIT UNDER TEST FOR TWO SPECIFIC CASES:
4018 ; 1) NO COMMAND MODIFIERS SET AND 2) ILLEGAL COMMAND
4019 ; MODIFIERS SET. DUST STATUS WILL BE RECEIVED FROM THE
4020 ; UNIT UNDER TEST AFTER THE PROGRAM ISSUES THE GET DUST
4021 ; STATUS COMMAND AVAILABLE IN DUP. THE RESPONSE PACKET
4022 ; RECEIVED FROM THE UNIT WILL BE TESTED AGAINST A KNOWN
4023 ; GOOD MASK.
4024 ;
4025 046746 STARS
;*****
4026 046746 STARS
;*****
4027
4028 046746 BGNTST
046746 T9::
4029
4030 046746 032764 000001 000014 BIT #DRPFLG,LUNFLG(R4) ;IS THE DRIVE AVAILABLE
4031 046754 001423 BEQ GO9 ;GO DO TEST IF AVAILABLE
4032 046756 RFLAGS FLG5 ;ELSE GO GET SUPRVISOR FLAGS
046756 104421 TRAP C$RFLA
046760 010037 002334 MOV RO,FLG5
4033 046764 032737 001000 002334 BIT #PNT,FLG5 ;SEE IF WE'RE PRINTING TEST NUMBERS
4034 046772 001412 BEQ 1$ ;NO, DON'T PRINT BYPASSED
4035 046774 PRINTF #BYPASS,L$TEST ; ELSE PRINT THE TEST
046774 013746 002114 MOV L$TEST,-(SP)
047000 012746 047626 MOV #BYPASS,-(SP)
047004 012746 000002 MOV #2,-(SP)
047010 010600 MOV SP,RO
047012 104417 TRAP C$PNTF
047014 062706 000006 ADD #6,SP
4036 047020 1$: EXIT TST ;BYPASSED MESSAGE AND GET OUT
047020 104432 TRAP C$EXIT
047022 000132 .WORD L10027-.
4037
4038 047024 042764 000010 000014 GO9: BIC #DUPFLG,LUNFLG(R4) ;CLEAR DUP FLAG
4039 047032 012737 032640 002310 MOV #CTRL,FRUIS ;SET UP FRU POINTER
4040 047040 004737 037042 JSR PC,PRTINT ;GO DO A PORT INITIALIZE
4041 047044 032764 000001 000014 BIT #DRPFLG,LUNFLG(R4) ;IS THE DRIVE AVAILABLE
4042 047052 001036 BNE T9EXT ;GET OUT IF NOT AVAILABLE
4043 047054 012705 002342 MOV #SCTRLC,R5 ;SET UP TO DO THE SCC COMMAND
4044 047060 012737 000001 002272 MOV #1,ITRCNT ;DO IT ONCE
4045 047066 012737 032663 023042 MOV #SCCMD,CURCMD ;SET UP COMMAND ASCII
4046 047074 004737 037274 JSR PC,CLSDRV ;GO ISSUE THE COMMAND
4047 047100 032764 000001 000014 BIT #DRPFLG,LUNFLG(R4) ;IS THE DRIVE AVAILABLE
4048 047106 001020 BNE T9EXT ;GET OUT IF NOT AVAILABLE
4049 047110 052764 000010 000014 BIS #DUPFLG,LUNFLG(R4) ;SET DUP FLAG FOR FOLLOWING COMMAND
4050 047116 012705 002456 MOV #GDUST,R5 ;SET UP TO DO GET DUST STATUS COMMAND
4051 047122 012737 000001 002272 MOV #1,ITRCNT
    
```

4052	047130	012737	032714	023042	MOV	#GDSCMD,CURCMD	:SET UP COMMAND ASCII
4053	047136	004737	037274		JSR	PC,CLSDRV	:GO ISSUE THE COMMAND
4054	047142	042764	000010	000014	BIC	#DUPFLG,LUNFLG(R4)	:CLEAR DUP FLAG
4055	047150				T9EXT: EXIT	TST	
	047150	104432			TRAP	C\$EXIT	
	047152	000002			.WORD	L10027-.	
4056	047154				ENDTST		
	047154				L10027:		
	047154	104401			TRAP	C\$ETST	

```

4058 .SBTTL TEST 10:EXECUTE LOCAL PROGRAM (Level II microdiagnostics)
4059
4060 047156 STARS
4061 047156 ;*****
STARS
;*****
;
;TEST 10 - EXECUTE LOCAL PROGRAM
; THIS TEST WILL INVOKE, VIA THE DUP EXECUTE LOCAL
; PROGRAM COMMAND, THE CONTROLLER RESIDENT LEVEL 2
; MICRODIAGNOSTICS.
;
4062 STARS
4063 ;*****
4064 STARS
4065 ;*****
4066
4067
4068 047156 STARS
4069 047156 ;*****
STARS
;*****

4070
4071 047156 BGNTST
047156 T10::
4072
4073 047156 032764 000001 000014 BIT #DRPFLG,LUNFLG(R4) ;IS THE DRIVE AVAILABLE
4074 047164 001423 BEQ GO10 ;GO DO TEST IF AVAILABLE
4075 047166 RFLAGS FLAGS ;ELSE GO GET SUPRVISOR FLAGS
047166 104421 TRAP C$RFLA
047170 010037 002334 MOV RO,FLAGS
4076 047174 032737 001000 002334 BIT #PNT,FLAGS ;SEE IF WE'RE PRINTING TEST NUMBERS
4077 047202 001412 BEQ 1$ ;NO, DON'T PRINT BYPASSED
4078 047204 PRINTF #BYPASS,L$TEST ; ELSE PRINT THE TEST
047204 013746 002114 MOV L$TEST,-(SP)
047210 012746 047626 MOV #BYPASS,-(SP)
047214 012746 000002 MOV #2,-(SP)
047220 010600 MOV SP,RO
047222 104417 TRAP C$PNTF
047224 062706 000006 ADD #6,SP
4079 047230 1$: EXIT TST ;BYPASSED MESSAGE AND GET OUT
047230 104432 TRAP C$EXIT
047232 000372 .WORD L10030-.

4080
4081 047234 042764 000010 000014 GO10: BIC #DUPFLG,LUNFLG(R4) ;CLEAR DUP FLAG
4082 047242 042764 000020 000014 BIC #ABTFLG,LUNFLG(R4) ;CLEAR ABORT FLAG
4083 047250 042764 000040 000014 BIC #CNTRLC,LUNFLG(R4) ;CLEAR CONTROL C FLAG
4084 047256 012737 032640 002310 MOV #CTRL,FRUIS ;SET UP FRU POINTER
4085 047264 004737 037042 JSR PC,PRTINT ;GO DO A PORT INITIALIZE
4086 047270 032764 000001 000014 BIT #DRPFLG,LUNFLG(R4) ;IS THE DRIVE AVAILABLE
4087 047276 001150 BNE TSTXEX ;GET OUT IF NOT AVAILABLE
4088 047300 012705 002342 MOV #SCTRLC,R5 ;SET UP TO DO THE SCC COMMAND
4089 047304 012737 000001 002272 MOV #1,ITRCNT ;DO IT ONCE
4090 047312 012737 032663 023042 MOV #SCCCMD,CURCMD ;SET UP COMMAND ASCII
4091 047320 004737 037274 JSR PC,CLSDRV ;GO ISSUE THE COMMAND
4092 047324 032764 000001 000014 BIT #DRPFLG,LUNFLG(R4) ;IS THE DRIVE AVAILABLE
4093 047332 001132 BNE TSTXEX ;GET OUT IF NOT AVAILABLE
4094 047334 012705 002406 MOV #ONLINE,R5 ;SET UP TO DO THE ONLINE COMMAND
4095 047340 012737 000001 002272 MOV #1,ITRCNT ;DO IT ONCE
4096 047346 012737 032670 023042 MOV #ONLCMD,CURCMD ;SET UP COMMAND ASCII
4097 047354 016465 000006 000004 MOV MSCPUN(R4),P.UNIT(R5) ;PUT THE UNIT NUMBER IN THE PACKET
4098 047362 004737 037274 JSR PC,CLSDRV ;GO ISSUE THE COMMAND
4099 047366 032764 000001 000014 BIT #DRPFLG,LUNFLG(R4) ;IS THE DRIVE AVAILABLE
    
```

```

4100 047374 001111      BNE      TSTXEX      ;GET OUT IF NOT AVAILABLE
4101 047376 052764 000010 000014      BIS      #DUPFLG,LUNFLG(R4) ;ALL FOLLOWING COMMANDS ARE DUP
4102 047404 012705 002476      MOV      #XLOCPR,R5      ;SET UP TO DO ELP COMMAND
4103 047410 012737 000001 002272      MOV      #1,I TRCNT      ;DO IT ONCE
4104 047416 012737 032721 023042      MOV      #ELPCMD,CURCMD   ;SET UP COMMAND ASCII
4105 047424 064737 037274      JSR      PC,CLSDRV        ;GO ISSUE THE COMMAND
4106 047430 032764 000001 000014      BIT      #DRPFLG,LUNFLG(R4) ;IS THE DRIVE AVAILABLE
4107 047436 001070      BNE      TSTXEX      ;GET OUT IF NOT AVAILABLE
4108 047440 032737 000200 177560 1$:      BIT      #BIT7,RCSR      ;CHECK FOR INPUT FROM KEYBOARD
4109 047446 001413      BEQ      2$            ;IF NONE, CONTINUE
4110 047450 013705 177562      MOV      RBUF,R5         ;GET DATA INPUT FROM KEYBOARD
4111 047454 042705 000200      BIC      #BIT7,R5        ;STRIP PARITY
4112 047460 022705 000003      CMP      #3,R5          ;CHECK FOR "CONTROL C" INPUT
4113 047464 001004      BNE      2$            ;IF NOT, CONTINUE
4114 047466 052764 000040 000014      BIS      #CNTRLC,LUNFLG(R4) ;SET "CONTROL C" FLAG
4115 047474 000432      BR       20$           ;GO ABORT MICRODIAGNOSTICS
4116 047476 012705 002524      MOV      #RCV DAT,R5     ;SET UP TO DO RECEIVE DATA COMMAND
4117 047502 012737 000001 002272      MOV      #1,I TRCNT      ;DO IT ONCE
4118 047510 012737 032726 023042      MOV      #RCV CMD,CURCMD ;SET UP COMMAND ASCII
4119 047516 004737 037274      JSR      PC,CLSDRV        ;GO ISSUE THE COMMAND
4120 047522 032764 000001 000014      BIT      #DRPFLG,LUNFLG(R4) ;IS THE DRIVE AVAILABLE
4121 047530 001033      BNE      TSTXEX      ;GET OUT IF NOT AVAILABLE
4122 047532 032764 000020 000014      BIT      #ABTFLG,LUNFLG(R4) ;ABORT LOCAL PROGRAM ?
4123 047540 001010      BNE      20$           ;YES, ISSUE ABORT COMMAND
4124 047542 005002      CLR      R2             ;NO, SET UP DELAY LOW COUNT
4125 047544 012703 000020      MOV      #20,R3         ;SET UP DELAY HIGH COUNT
4126 047550 005202      INC      R2             ;DELAY
4127 047552 001376      BNE      10$           ;
4128 047554 005303      DEC      R3             ;
4129 047556 001374      BNE      10$           ;
4130 047560 000727      BR       1$            ;
4131 047562 012705 002554      MOV      #ABORT,R5      ;POLL DIAGL2 PROGRESS
4132 047566 012737 000001 002272      MOV      #1,I TRCNT      ;SET UP TO DO ABORT COMMAND
4133 047574 012737 032733 023042      MOV      #ABT CMD,CURCMD ;DO IT ONCE
4134 047602 004737 037274      JSR      PC,CLSDRV        ;SET UP COMMAND ASCII
4135 047606 032764 000040 000014      BIT      #CNTRLC,LUNFLG(R4) ;GO ISSUE THE COMMAND
4136 047614 001401      BEQ      TSTXEX      ;CHECK IF ABORTING DUE TO "CONTROL C"
4137 047616      BREAK      ;IF NOT, EXIT
      047616 104422      TRAP     C$BRK
4138 047620      TSTXEX: EXIT      TST
      047620 104432      TRAP     C$EXIT
      047622 000002      .WORD   L10030-.
4139 047624      L10030: ENDTST
      047624 104401      TRAP     C$ETST
4140 047626      045      116      045 BYPASS:: .ASCIZ  /%N%A TEST %Z3%A BYPASSED%/
4141      .EVEN
4142 047662      ENDMOD
4143      .TITLE  PARAMETER CODING
4144
4145      .SBTTL  HARDWARE PARAMETER CODING SECTION
4146
4147      BGNMOD
4148
4149      ;++
4150      ; THE HARDWARE PARAMETER CODING SECTION CONTAINS MACROS
4151      ; THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES. THE
4152
4153
4154
4155
4156
4157
4158
    
```

```

4189 ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
4190 ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES. THE
4191 ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
4192 ; WITH THE OPERATOR.
4193 ;--
4194
4195 047662 BGNHRD
      047662 000044 .WORD L10031-L$HARD/2
      047664 L$HARD::
4196
4202
4203 047664 GPRMA TKIPAD,0,0,160002,177564,YES
      047664 000031 .WORD T$CODE
      047666 047722 .WORD TKIPAD
      047670 160002 .WORD T$LOLIM
      047672 177564 .WORD T$HILIM
4204 047674 GPRMD TKVECT,2,0,777,60,776,YES
      047674 001032 .WORD T$CODE
      047676 047737 .WORD TKVECT
      047700 000777 .WORD 777
      047702 000060 .WORD T$LOLIM
      047704 000776 .WORD T$HILIM
4205 047706 GPRMD TKUNT,4,0,777,0,251,YES
      047706 002032 .WORD T$CODE
      047710 047751 .WORD TKUNT
      047712 000777 .WORD 777
      047714 000000 .WORD T$LOLIM
      047716 000251 .WORD T$HILIM
4206
4207 047720 EXIT HRD
      047720 026004 .WORD T$CODE
4208
4209 047722 124 113 111 TKIPAD: .ASCIZ ?TKIP ADDRESS?
4210 047737 124 113 040 TKVECT: .ASCIZ ?TK VECTOR?
4211 047751 124 057 115 TKUNT: .ASCIZ ?T/MSCP UNIT NUMBER?
4212 .EVEN
4213
4214
4215 047774 ENDHRD
      047774 L10031: .EVEN
4216
4223
    
```

```

4226          .SBTTL  SOFTWARE PARAMETER CODING SECTION
4227
4228          ;**
4229          ; THE SOFTWARE PARAMETER CODING SECTION CONTAINS MACROS
4230          ; THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES.  THE
4231          ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
4232          ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
4233          ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
4234          ; WITH THE OPERATOR.
4235          ;--
4236
4237 047774          BGNSFT
          047774 000000          .WORD L10032-L$SOFT/2
          047776          L$SOFT::

4238
4245
4246          .EVEN
4247
4248 047776          ENDSFT
          047776          .EVEN
          L10032:

4249
4250
4260          ;*****
4261          ;*****
4262          ;
4263          ;COMMUNICATIONS AREA
4264          ; THIS IS THE COMMUNICATIONS AREA THAT IS USED
4265          ; THROUGHOUT THE PROGRAM IN TESTING THE PERMUTATIONS
4266          ; OF THE UQ-PORT INIT SEQUENCE.  IT IS ESSENTIAL THAT
4267          ; THIS AREA RESIDE IN AN 8KBYTE AREA OF MEMORY FREE
4268          ; OF DIAGNOSTIC CODE SO THAT IT MAY BE SUCCESSFULLY
4269          ; RELOCATED THROUGHOUT UPPER MEMORY VIA MEMORY MAN-
4270          ; AGEMENT.
4271          ;
4272          ;*****
4273          ;*****
4274
4275          060000          .-60000          ;START OF THE THIRD 8KBYTE BLOCK
4276          ;OF VIRTUAL MEMORY SPACE.  ACCESIBLE
4277          ;VIA PAR/PDR 2.
4278 060000          RDBUF::
4279 060000          COMMBF::
4280 060000          .BLKW 20.          ;BUFFER SPACE PRECEDING COMM AREA
4281 060050          COMMAR::
4282 060050          .BLKW 514.          ;MAXIMUM COMM AREA LENGTH
4283 062054          LASTBF::
4284 062054          .BLKW 20.          ;BUFFER SPACE SUCCEEDING COMM AREA
4288
4289 062124          LASTAD
          062124 000000          .EVEN
          062126 000000          .WORD 0
          062130          .WORD 0
          L$LAST::
4290 062130          ENDMOD
4291          .END
    
```


KPDR4 = 172310 G	L#LUN 002074 G	L2ELDF= 000156 G	L21 024417 G	MSCPER 040520
KPDR5 = 172312 G	L#MREV 002050 G	L2ELDS= 000166 G	L210 025073 G	MSCPUN= 000006 G
KPDR6 = 172314 G	L#NAME 002000 G	L2ELEC= 000140 G	L211 025137 G	MSCPVR= 000000 G
KPDR7 = 172316 G	L#PRIO 002042 G	L2ELEV= 000136 G	L212 025203 G	MSGLEN= 177774 G
KTEXT 036252	L#PROT 023044 G	L2ELFL= 000135 G	L213 025247 G	NEXT 040740
KTFLAG 002274 G	L#PRT 002112 G	L2ELFM= 000134 G	L214 025313 G	NOKT 036246
KTTEST 036102 G	L#REPP 002062 G	L2ELLB= 000162 G	L215 025357 G	NUPASS 040724
LASTBF 062054 G	L#REV 002010 G	L2ELOF= 000172 G	L216 025423 G	ONEFIL= 000001
LINE1 023420 G	L#RPT 035002 G	L2ELPB= 000160 G	L219 025467 G	ONLCMD 032670 G
LINE2 023454 G	L#SOFT 047776 G	L2ELP1= 000146 G	L22 024473 G	ONLINE 002406
LINE3 023534 G	L#SPC 002056 G	L2ELP2= 000150 G	L220 025533 G	OP.ABT= 000006 G
LINE4 023564 G	L#SPCP 002020 G	L2ELRL= 000144 G	L221 025601 G	OP.ELP= 000003 G
LINE5 023626 G	L#SPTP 002024 G	L2ELRT= 000145 G	L222 025646 G	OP.END= 000200 G
LINE6 023703 G	L#STA 002030 G	L2ELRW= 000170 G	L223 025710 G	OP.GDS= 000001 G
LINE7 023746 G	L#SW 002212 G	L2ELST= 000154 G	L224 025752 G	OP.ONL= 000011 G
LOE = 040000 G	L#TEST 002114 G	L2ELTN= 000157 G	L225 026011 G	OP.REC= 000005 G
LOGUNT 002312 G	L#TIML 002014 G	L2ELT0= 000163 G	L226 026053 G	OP.SCC= 000004 G
LOOP 037062	L#UNIT 002012 G	L2ELT1= 000164 G	L227 026115 G	OUTER 002326 G
LOT = 000010 G	L10000 002210	L2ELT2= 000165 G	L228 026154 G	OWN = 100000 G
LUNBLK 002212 G	L10001 002212	L2ER1 030763 G	L229 026213 G	O#APTS= 000000
LUNFLG= 000014 G	L10003 033542	L2ER10 031211 G	L23 024531 G	O#AU = 000000
L#ACP 002110 G	L10004 035000	L2ER11 031232 G	L230 026255 G	O#BGNR= 000001
L#APT 002036 G	L10005 036052	L2ER12 031246 G	L231 026317 G	O#BGNS= 000000
L#AU 041144 G	L10006 036060	L2ER13 031266 G	L232 026361 G	O#DU = 000001
L#AUT 002070 G	L10007 036070	L2ER14 031306 G	L233 026423 G	O#ERRT= 000001
L#AUTO= ***** GX	L10010 036100	L2ER15 031334 G	L234 026462 G	O#GNSW= 000000
L#CCP 002106 G	L10011 041076	L2ER16 031364 G	L235 026524 G	O#POIN= 000001
L#CLEA 041100 G	L10012 041126	L2ER17 031412 G	L236 026566 G	O#SETU= 000000
L#CO 002032 G	L10013 041142	L2ER18 031430 G	L237 026630 G	PAROFF 002300 G
L#DEPO 002011 G	L10014 041150	L2ER19 031451 G	L238 026672 G	PASCNT 002270 G
L#DESC 002150 G	L10015 041646	L2ER2 031003 G	L239 026731 G	PCKSIZ 003030 G
L#DESP 002076 G	L10016 041336	L2ER20 031467 G	L24 024571 G	PDELAY 036406 G
L#DEVP 002060 G	L10017 041602	L2ER21 031551 G	L240 026770 G	PDLYEX 036432
L#DISP 002124 G	L10020 042430	L2ER22 031631 G	L241 027027 G	PDRECV 040102 G
L#DLY 002116 G	L10021 043116	L2ER23 031720 G	L242 027071 G	PNT = 001000 G
L#DTP 002040 G	L10022 043712	L2ER24 032010 G	L244 027130 G	PRI = 002000 G
L#DTYP 002034 G	L10023 044360	L2ER25 032076 G	L245 027224 G	PRIDAT 033074 G
L#DU 041130 G	L10024 045200	L2ER26 032167 G	L246 027320 G	PRIERR 033146 G
L#DUT 002072 G	L10025 046020	L2ER27 032261 G	L247 027414 G	PRIEX 033536
L#DVTY 023412 G	L10026 046744	L2ER3 031023 G	L248 027510 G	PRIINI 032740 G
L#EF 002052 G	L10027 047154	L2ER4 031040 G	L249 027604 G	PRIIP 033122 G
L#ENVI 002044 G	L10030 047624	L2ER5 031057 G	L25 024631 G	PRIPAD 033022 G
L#ERRT 023052 G	L10031 047774	L2ER6 031107 G	L250 027700 G	PRISA 032764 G
L#ETP 002102 G	L10032 047776	L2ER7 031127 G	L251 027774 G	PRIVAD 033046 G
L#EXP1 002046 G	L2BRD1= 000024 G	L2ER8 031145 G	L252 030070 G	PRI00 = 000000 G
L#EXP4 002064 G	L2BRD2= 000030 G	L2ER9 031160 G	L253 030164 G	PRI01 = 000040 G
L#EXP5 002066 G	L2BWR1= 000014 G	L2ETBL 023062 G	L254 030260 G	PRI02 = 000100 G
L#HARD 047664 G	L2BWR2= 000020 G	L2MSG 023312	L26 024671 G	PRI03 = 000140 G
L#HIME 002120 G	L2CMD = 000010 G	L2REP1= 000050 G	L27 024731 G	PRI04 = 000200 G
L#MPCP 002016 G	L2CRC1= 000044 G	L2REP2= 000052 G	L28 024771 G	PRI05 = 000240 G
L#MPPT 002022 G	L2CRC2= 000046 G	L2RSP = 000011 G	L29 025026 G	PRI06 = 000300 G
L#HW 002202 G	L2DATA 040224 G	L2STA = 000002 G	MMON = 000001 G	PRI07 = 000340 G
L#ICP 002104 G	L2DRV = 000004 G	L2SWR1= 000034 G	MMUSRO= 177572 G	PRTDRV 037632 G
L#INIT 040620 G	L2DUMP 033544 G	L2SWR2= 000036 G	MMUSR1= 177574 G	PRTINT 037042 G
L#LADP 002026 G	L2ECC1= 000040 G	L2TRK = 000007 G	MMUSR2= 177576 G	P.BCNT= 000014 G
L#LAST 062130 G	L2ECC2= 000042 G	L2TST = 000006 G	MMUSR3= 172516 G	P.BUFF= 000020 G
L#LOAD 002100 G	L2ELDE= 000155 G	L2UNT = 000012 G	MM220N= 000020 G	P.CRF = 000000 G

P.EXT1= 000014 G	STEPST 002320 G	T\$EXCP= 000000	T\$\$SOF= 010032	WRCMD 032702 G
P.EXT2= 030015 G	STEP1 036434 G	T\$FLAG= 000041	T\$\$SRV= 010010	WRCMDE 033256 G
P.EXT3= 030016 G	STPTBL 002250 G	T\$GMAN= 000000	T\$\$SUB= 010017	WRDATA 002322 G
P.FLGS= 000011 G	STP1ER 036552	T\$HILI= 000251	T\$\$SM = 010001	WRER1 032347 G
P.IND1= 000020 G	STP1EX 036556	T\$LAST= 000001	T\$\$TES= 010030	WRER2 032401 G
P.IND2= 000022 G	SVCGBL= 000000	T\$LOLI= 000000	T1 041152 G	WRER3 032422 G
P.MOD = 000012 G	SVCINS= 000000	T\$LSYM= 010000	T1.1 041200	WRER4 032450 G
P.OPCD= 000010 G	SVCSUB= 000000	T\$LTNO= 000012	T1.2 041364	WRER5 032474 G
P.STS = 000012 G	SVCTAG= 000000	T\$NEST= 177777	T10 047156 G	WRER6 032525 G
P.UNIT= 000004 G	SVCTST= 000000	T\$NS0 = 000000	T2 041650 G	WRER7 032546 G
RBUF = 177562 G	S\$LSYM= 010000	T\$NS1 = 000005	T2EXT 042424	WRER8 032614 G
RCSR = 177560 G	S1 = 004000 G	T\$NS2 = 000002	T3 042432 G	WREX 033512 G
RCVCMO 032726 G	TEMP 002332 G	T\$PTNU= 000000	T3EXT 043112	WRINTO 033176 G
RCVDAT 002524	TF.BLK= 000010 G	T\$SAVL= 177777	T4 043120 G	WRPRTE 033222 G
RDBUF 060000 G	TIMOUT= 000024 G	T\$SEGL= 177777	T4EXT 043702	WRSEQE 033410 G
RDCMD 032707 G	TKIP = 000000 G	T\$SUBN= 000000	T5 043714 G	WRTOE 033466 G
RESPBF 002570 G	TKIPAD 047722	T\$TAGL= 177777	T5EXT 044350	WR1 023772 G
REWCMO 032675 G	TKIPSV= 000010 G	T\$TAGN= 010033	T6 044362 G	WR2 024035 G
RNGSTP= 000004 G	TKSA = 000002 G	T\$TEMP= 000000	T6EXT 045174	WR3 024071 G
RSPADD 023352	TKSASV= 000012 G	T\$TEST= 000012	T7 045202 G	WR4 024122 G
RSPBUF 002574 G	TKUNT 047751	T\$TSTM= 177777	T7EXT 046014	WR5 024147 G
RSPEND 003004 G	TKVEC = 000004 G	T\$TSTS= 000001	T8 046022 G	WR6 024211 G
RSPRNG 003004 G	TKVECT 047737	T\$\$AU = 010014	T8EXT 046734	WR7 024214 G
RSPSAV 023040 G	TOUT 002330 G	T\$\$CLE= 010012	T9 046746 G	WR8 024265 G
RSPSTP= 0C0104 G	TRAP4 036054 G	T\$\$DU = 010013	T9EXT 047150	WR9 024342 G
RSPTO 023402	TRP4FG 002276 G	T\$\$HAR= 010031	T9FLAG= ***** GX	W1 033322
RSTVEC 036266 G	TSTXEX 047620	T\$\$HW = 010000	UAM = 000200 G	XLOCPR 002476
SAEXP 002314 G	TXFER = 000005 G	T\$\$INI= 010011	UNERLG 023372	X\$ALWA= 000000
SCCCMD 032663 G	T\$ARGC= 000002	T\$\$MSG= 010004	VECTOR 036316 G	X\$FALS= 000040
SCTRLC 002342	T\$CODE= 026004	T\$\$PRD= 010002	VEC4 = 000004 G	X\$OFFS= 000400
SFPTBL 002212 G	T\$ERRN= 000033	T\$\$RPT= 010005	WRBUF 003036 G	X\$TRUE= 000020
START 040672				

. ABS. 062130 000 (RW,I,GBL,ABS,OVR)
000000 001 (RW,I,LCL,REL,CON)

Errors detected: 0

*** Assembler statistics

Work file reads: 353
Work file writes: 347
Size of work file: 34208 Words (134 Pages)
Size of core pool: 19402 Words (74 Pages)
Operating system: RSX-11M/PLUS (Under VAX/VMS)

Elapsed time: 00:18:51.24
CZTKAB.BIN,CZTKAB.LST/-SP/CR=SVC40R.MLB/ML,CZTKAB.MAC