

RK611

DUAL PORT LOGIC TEST
CZR6GB0

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FICHE 1 OF 2

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FICHE 2 OF 2

MADE IN USA

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TABLE OF CONTENTS

- 1.0 ABSTRACT
- 2.0 REQUIREMENTS
 - 2.1 HARDWARE
 - 2.2 PRELIMINARY TESTING & PROGRAMS
 - 2.3 RESTRICTIONS & OPERATOR ACTIONS
- 3.0 PROGRAM CONSIDERATIONS
 - 3.1 PDP-11 FAMILY COMPATIBILITY
 - 3.2 XXDP
 - 3.3 ACT/APT
 - 3.3.1 APT ETABLE DEFINITIONS
 - 3.4 MEMORY MANAGEMENT
 - 3.5 PARITY CHECK ENABLED
 - 3.6 BAD SECTORS
 - 3.7 EXECUTION TIME
 - 3.8 FAULT ISOLATION
 - 3.9 ERROR CORRECTION & FAILURE RATE ANALYSIS
 - 3.10 DEFAULT UNIBUS ADDRESSES & VECTORS
- 4.0 OPERATING PROCEDURE & CONTROL FUNCTIONS
 - 4.1 PROGRAM LOADING
 - 4.2 STARTING LOCATIONS
 - 4.3 CONSOLE SWITCH REGISTERS
 - 4.4 SOFTWARE SWITCH REGISTER
 - 4.5 INPUT DIALOGUE
 - 4.6 PROGRAM EXAMPLE
 - 4.7 HALTING THE PROGRAM
- 5.0 DRIVE DIAGNOSTIC FUNCTIONAL DESCRIPTION
 - 5.1 GENERAL
 - 5.2 TEST DESCRIPTIONS
- 6.0 ERROR REPORTING
 - 6.1 ERROR INTERPRETATION
 - 6.2 ERROR PRINTOUT EXAMPLE
- 7.0 DUAL PROCESSOR-DUAL CONTROLLER TESTING

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1.0 ABSTRACT

THE RK06 DUAL PORT LOGIC PERFORMS A SERIES OF TESTS WHICH VERIFY THAT THE DUAL PORT OPTION IS FUNCTIONING PROPERLY.

BOTH PORTS OF THE RK06 ARE CABLED TO THE SAME RK611 BY A STANDARD CABLE & THE DUAL PORT TEST SWITCH IS ENABLED ON THE DUAL PORT MODULE. THIS ARRANGEMENT ALLOWS THE DUAL PORT LOGIC TO BE TESTED FROM ONE PDP-11/RK611 TO A MAXIMUM OF 4 DRIVES.

THIS PROGRAM WILL TEST RK06/RK07 DRIVES WITHOUT OPERATOR INPUTS

2.0 REQUIREMENTS

2.1 HARDWARE

THE FOLLOWING HARDWARE IS REQUIRED TO RUN THE DISK DIAGNOSTIC:

PDP-11
CONSOLE TELETYPE
16K MEMORY
KW11-L OR KW11-P CLOCK
RK06 UNIBUS CONTROLLER (RK611)
1 TO 4 RK06/RK07 DRIVES
FORMATTED DISKPACKS

2.2 PRELIMINARY TESTING & PROGRAMS

1. THE RK611 DISKLESS CONTROLLER DIAGNOSTICS (ALL PARTS) SHOULD FIRST RUN SUCCESSFULLY ON BOTH PORTS.
2. THE RK06 DRIVE DIAGNOSTICS (ALL PARTS) SHOULD FIRST RUN SUCCESSFULLY.
3. THE RK611 FUNCTIONAL CONTROLLER DIAGNOSTIC SHOULD FIRST RUN SUCCESSFULLY.

2.3 RESTRICTIONS & OPERATOR ACTION

TO TEST THE RK06 DUAL PORT OPTION WITH THIS PROGRAM, THE DUAL PORT TEST SWITCH MUST BE ENABLED ON THE DUAL PORT MODULE.

THE CABLE FROM THE RK611 IS DAISY CHAINED TO BOTH PORTS OF RK06 UNDER TEST. ENABLING THE SWITCH ON THE DUAL PORT MODULE, GROUNDS BIT 0 OF THE UNIT SELECT LINES GOING TO THE PORT B INTERFACE & TIMING MODULE (M7706).

THE EFFECT OF THIS IS THAT BIT 0 UNIT SELECT IS COMPLEMENTED

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ON PORT B AND THE DRIVE APPEARS AS 2 SEPARATE UNITS TO THE RK611. EACH PORT OF THE RK06 WILL RESPOND TO A DIFFERENT DRIVE ADDRESS.

THE ADDRESS OF EACH PORT WILL DEPEND ON THE DRIVE ADDRESS. FOR THIS REASON, THE RESTRICTION IS MADE THAT ONLY EVEN NUMBERED UNIT SELECT PLUGS BE USED. IN THIS WAY PORT 'A' WILL RESPOND TO THE DRIVE ADDRESS, & PORT 'B' WILL RESPOND TO ADDRESS+1 (THE ADDRESS DEVELOPED BY THE SWITCH).

BECAUSE OF THE ABOVE CONSIDERATIONS, A MAXIMUM OF 4 DRIVES CAN BE TESTED BY THIS PROGRAM, WHICH WILL 'SEE' 8 DRIVES.

ANY OTHER DRIVES ON THE SYSTEM WHICH HAS ANY ADDRESS IN CONFLICT WITH EITHER OF THE TEST ADDRESSES MUST BE DESELECTED, BOTH PORTS SWITCHED OFF.

A FURTHER REQUIREMENT IS THE SYSTEM MUST HAVE EITHER A KW11-P OR KW11-L. IF NEITHER IS PRESENT AN ERROR MESSAGE WILL BE TYPED AND THE PROGRAM WILL JUMP TO THE END OF PASS.

3.0 PROGRAM CONSIDERATIONS

3.1 PDP-11 FAMILY COMPATIBILITY

THIS PROGRAM CAN BE USED BY THE PDP-11/04,05,10,20, 34,35,40,45,50,55 & 70.

IT IS COMPATABLE WITH THE LSI-11 INSTRUCTION SET AND CAN TEST THE RK06 ONLY IF THE DRIVE CONTROLLER FOR THE LSI-11 IS DESIGNED TO BE DIAGNOSTICALLY COMPATABLE WITH THE RK611.

3.2 XXDP

THIS PROGRAM SHOULD NOT BE CHAINED BY XXDP.

CHAIN MODE OPERATION (MONITOR)

BY DEFINITION, ANY PROGRAM THAT REQUIRES OPERATOR INTERVENTION SHOULD NOT BE CHAINED.

IN THIS CASE, OPERATOR INTERVENTION IS REQ'D TO ENABLE THE DUAL PORT TEST SWITCH & DAISY CHAINING BOTH PORTS TO THE SAME RK611.

DUMP MODE OPERATION (MANUAL)

1. INPUT DIALOGUE IF STARTED FROM 220.
2. IF THE LOADING MEDIUM ON DRIVE 0 IS AN RK06, IT WILL BE

TESTED. THE OPERATOR IS FIRST GIVEN A MESSAGE TO REPLACE
THE PACK ON DRIVE 0 WITH A SCRATCH PACK & TYPE <CR>
WHEN DONE.

3.3 ACT/APT

THIS PROGRAM IS ACT COMPATIBLE.

HOWEVER, IT SHOULD BE RUN ONLY IN DUMP MODE.

AUTOMATIC MODE (MONITOR)

BY DEFINITION ANY PROGRAM THAT REQUIRES OPERATOR INTERVENTION
SHOULD NOT BE RUN IN THE AUTO MODE.

DUMP MODE (MANUAL): INPUT DIALOGUE IF STARTED FROM 220.

3.3.1 APT ETABLE DEFINITIONS

THE FOLLOWING DEFINITIONS ARE VALID FOR SPECIFYING APT ENVIRONMENTAL
TABLE (ETABLE) ENTRIES. VIA RUNNING THE APT UTILITY PROGRAM "TSP":

1. SOFTWARE ENVIRONMENT:
 - =1 IF APT SCRIPT MODE
 - =0 IF STANDALONE MODE
2. ENVIRONMENT MODE: BYTE
 - BIT 7 = 1 ETABLE DOES SIZING
 - = 0 PROGRAM DOES SIZING
 - BIT 6 = 1 SPOOL MESSAGES TO APT IF SCRIPT MODE
 - = 0 DON'T SPOOL TO APT
 - BIT 5 = 1 SUPPRESS CONSOLE OUTPUT
 - = 0 ALLOW CONSOLE OUTPUT
 - BITS 4-0 NOT USED
3. SWITCH 1 (SOFTWARE SWITCH REGISTER)

IF ENVIRONMENT MODE BIT 7 (SIZING BIT) IS SET TO 1, THE SOFTWARE
SWITCH REGISTER WILL BE USED, INSTEAD OF THE HARDWARE CONSOLE
SWITCH REGISTER. REGARDLESS OF WHICH ONE IS USED, ALL BITS
DEFINED IN SECTIONS 4.3 & 4.4 (SWITCH REGISTER OPTIONS) MAY USED
WHEN RUNNING IN STANDALONE MODE.
IN APT SCRIPT MODE, HOWEVER, BIT 14 (LOOP ON TEST) MUST ALWAYS
BE SET TO 0.
4. SWITCH 2 (USER SWITCH REGISTER)

NOT USED
5. CPU OPTIONS:

NOT USED
6. MEMORY TYPES 1-4 AND MAX MEMORY ADDRESSES

NOT USED

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7. INTERRUPT VECTOR 1:
USED WHEN ENVIRONMENT MODE BIT 7=1. DEFAULT = 210
8. BUS PRIORITY 1:
USED WHEN ENVIRONMENT MODE BIT 7=1. DEFAULT = 5
9. INTERRUPT VECTOR 2:
NOT USED
10. BUS PRIORITY 2:
NOT USED
11. BASE ADDRESS:
USED WHEN ENVIRONMENT MODE BIT 7 = 1. DEFAULT = 177440
12. DEVICE MAP:
USED WHEN ENVIRONMENT MODE BIT 7 = 1. EACH BIT SET TO
1 IN BITS 0-7 WILL SELECT THE CORRESPONDING DRIVE TO BE
TESTED. BITS 8-15 ARE NOT USED.
- NOTE: IN THIS PROGRAM, ONLY EVEN NUMBERED DRIVES CAN BE
TESTED (0,2,4,6)
13. CONTROLLER DESCRIPTORS:
NOT USED.
14. DRIVE DESCRIPTOR CODES (IN WORDS):
NOT USED

3.4 MEMORY MANAGEMENT
MEMORY MANAGEMENT IS NOT USED.

3.5 PARITY CHECK ENABLED

IF THE MEMORY PARITY CHECK OPTION IS AVAILABLE ON THE SYSTEM,
THE PROGRAM WILL RUN WITH MEMORY CHECK ENABLED.

3.6 BAD SECTOR

THE PROGRAM WILL COMPARE DATA ERRORS WITH THE BAD SECTOR
INFORMATION CONTAINED ON CYLINDER 410, HEAD 2. PRINTOUTS
OF DATA ERRORS DUE TO BAD SECTORS/TRACKS WILL BE MASKED OUT.

3.7 EXECUTION TIME

THE EXECUTION TIME IS APPROX. 2.5 MIN. BASED ON THE PDP 11/50.

3.8 FAULT ISOLATION

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TO BE DETERMINED.

3.9 ERROR CORRECTION AND FAILURE RATE ANALYSIS

THIS PROGRAM WILL NOT DO ERROR CORRECTION OR FAILURE RATE ANALYSIS.

3.10 DEFAULT UNIBUS ADDRESSES & VECTORS

THE FOLLOWING IS A LIST OF ALL DEFAULT ADDRESSES & VECTORS OF ALL HARDWARE TO BE USED & THEIR MEMORY ADDRESSES WHERE THEY CAN BE CHANGED.

	LOCATION	DEFAULT CONTENTS
RK06-RK07 BUSS ADDRESS	1264	177440
CONTROLLER INTERRUPT VECTOR	1314	210
CONTROLLER PRIORITY	1316	240
P-CLOCK STATUS REG	1320	172540
P-CLOCK SET BUFFER	1322	172542
P-CLOCK READ BUFFER	1324	172544
L-CLOCK STATUS REG	1326	177546
L-CLOCK INTERRUPT VECTOR	1330	100
P-CLOCK INTERRUPT VECTOR	1332	104
TTY KB STATUS REG	1144	177560
TTY KB BUFFER	1146	177562
TTY PRINTER STATUS REG	1150	177564
TTY PRINTER BUFFER	1152	177566

4.0 OPERATING PROCEDURE & CONTROL FUNCTIONS

4.1 PROGRAM LOADING

THE PROGRAM CAN BE LOADED FROM PAPER TAPE USING STANDARD PROCEDURE FOR ABSOLUTE LOADER TAPES; OR FROM ANY MEDIA SUPPORTED BY XXDP.

4.1.1 LOAD THE STARTING ADDRESS (SEE SEC 4.2).

4.1.2 SET SWITCH REGISTERS AS DESIRED (SEE SEC 4.3).

4.1.3 INSTALL CALBE(S) & SET DRIVES TO BE TESTED IN THE 'LOAD' CONDITION & WITH BOTH PORTS SELECTED & WRITE LOCK DISABLED. DRIVES NOT TO BE TESTED MUST HAVE BOTH PORTS DESELECTED. ALSO, THE DUAL PORT TEST SWITCH ON THE DUAL PORT MODULE MUST BE ENABLED.

NOTE: THE DRIVE WILL NOT RESPOND TO THE 'START SPINDLE' COMMAND IF THE RUN/STOP SWITCH IS IN THE 'STOP'

POSITION.

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4.1.4 PRESS 'START'

THE PROGRAM WILL IDENTIFY ITSELF AND WILL BEGIN A DIALOGUE WITH THE OPERATOR TO DETERMINE DRIVES TO BE TESTED (SEE SEC 4.5).

THE PROGRAM BEGINS TESTING ONLY THOSE DRIVES SPECIFIED BY THE INPUT DIALOGUE. IF A SPECIFIED DRIVE CANNOT BE FOUND BY THE PROGRAM IT WILL BE FLAGGED AS AN ERROR THAT THE DRIVE WAS NOT AVAILABLE. THEN BEGINNING WITH THE LOWEST NUMERICAL DRIVE AND PROCEEDING IN SEQUENTIAL ORDER, ALL VALID DRIVES WILL BE TESTED. ONE PASS THROUGH THE TEST SEQUENCE WILL BE

PERFORMED ON EACH DRIVE BEFORE MOVING TO THE NEXT DRIVE IN SEQUENCE. THE DRIVE TO BE TESTED WILL BE TYPED AT THE BEGINNING OF EACH PASS. "END OF PASS" WILL BE TYPED AFTER TESTING ALL DRIVES.

4.2 STARTING LOCATIONS

LOCATION 200 - STARTING ADDRESS TO DEFAULT THE BUSS ADDRESS & THE CONTROLLER INTERRUPT VECTOR & TEST ALL DRIVES IN THE 'DRIVE PRESENT' CONDITION.

NOTE: THE DRIVE PRESENT CONDITION IS:

- A. HEADS MANUALLY LOADED
- B. BOTH PORTS SELECTED
- C. WRITE LOCK DISABLED
- D. DRIVE READY INDICATOR ON

LOCATION 220 - STARTING ADDRESS TO INPUT TESTING PARAMETERS VIA THE INPUT DIALOGUE. BUSS ADDRESS & CONT. INTERRUPT VECTOR INPUTTED ONLY ON 1ST PASS.

IMPORTANT: FOR VARIATIONS OF THE ABOVE, SEE XXDP, ACT/APT CONSIDERATIONS IN SECTIONS 3.2 & 3.3.

THE PROGRAM WILL DETERMINE WHETHER THE DRIVE IS AN RK06/RK07 WITHOUT OPERATOR INPUTS.

4.3 SWITCH REGISTER

THE SWITCHES ARE USED TO PROVIDE CONTROL FUNCTIONS.

SWITCH FUNCTION

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15  HALT ON ERROR
14  LOOP ON TEST
13  INHIBIT ERROR TYPEOUT
12  BYPASS DRIVE AFTER 20 ERRORS

11  INHIBIT ITERATION
10  BELL ON ERROR
9   LOOP ON ERROR
8   LOOP ON TEST IN SW<07:00>

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4.3.1 SW<15>

THE PROGRAM HALTS ON ENCOUNTERING AN ERROR, AFTER TYPING OUT THE ERROR MESSAGE AND PERTINENT INFORMATION. PRESSING "CONTINUE" CONTINUES OPERATION OF THE PROGRAM.

4.3.2 SW<14>

THE PROGRAM LOOPS ON THE TEST THAT IS BEING EXECUTED WHEN THE SWITCH IS PUT ON. THIS SWITCH IS NORMALLY USED ALONG WITH SW15.

4.3.3 SW<13>

THIS SWITCH INHIBITS ALL ERROR MESSAGES. NORMALLY USED WHEN LOOPING ON TEST (SW14) OR LOOPING ON ERROR (SW9). WITH SW<13>SET, SW<15> SHOULD NOT BE SET

4.3.4 SW<12>

THIS SWITCH BYPASSES A GIVEN DRIVE AFTER 20 ERRORS HAVE BEEN DETECTED.

4.3.5 SW<11>

EACH TEST WILL BE EXECUTED ONLY ONCE. NORMALLY AFTER THE FIRST PASS, EACH SUBTEST IS ITERATED A NUMBER OF TIMES (USUALLY 50, 5 IN SOME CASES). SETTING THIS SWITCH INHIBITS ITERATIONS, SO THAT QUICK PASSES CAN BE MADE.

4.3.6 SW<10>

RINGS A BELL ON ERROR. USEFUL WHEN ERROR TYPEOUT IS INHIBITED.

4.3.7 SW<09>

THIS SWITCH PROVIDES THE TIGHTEST POSSIBLE SCOPE LOOP FOR

482 ERRORS. IF THE PROGRAM DETECTS AN ERROR, IT WILL LOOP BACK
483 TO THE BEGINNING OF TEST.
484
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488 4.3.8 SW<08>
489

490 THIS SWITCH IS USED TO SELECT A PARTICULAR TEST (AS PER
491 SW<00-7>) FOR EXECUTION AND SUBSEQUENT LOOPING. THUS IF
492 TEST 15 IS TO BE SELECTED THE SWITCH SETTING WOULD BE
493 000415. IT SHOULD BE NOTED THAT BEFORE SELECTING & LOOPING
494 TEST 15, ALL THE PREVIOUS TESTS (1-14) WILL BE EXECUTED.
495

496 4.4 'SOFTWARE' SWITCH REGISTER
497

498 IF THE PROGRAM IS BEING RUN ON A SWITCHLESS PROCESSOR
499 (I.E. AN 11/04 OR 11/34) THE PROGRAM WILL DETERMINE THAT
500 THE HARDWARE SWITCH REGISTER IS NOT PRESENT AND WILL USE
501 A 'SOFTWARE' SWITCH REGISTER. THE 'SOFTWARE' SWITCH
502 REGISTER IS LOCATED AT LOCATION 176 (8). THE SETTINGS
503 OF THE 'SOFTWARE' SWITCHES ARE CONTROLLED THROUGH A
504 KEYBOARD ROUTINE WHICH IS CALLED BY TYPING A 'CONTROL G'.
505 THE PROGRAM WILL RECOGNIZE THE 'CONTROL G' AT ANY TIME
506 EXCEPT WHEN THE PROGRAM IS AT A HIGHER PRIORITY PROCESSING
507 AN RK06 INTERRUPT. THE 'SOFTWARE' SWITCH VALUES ARE
508 ENTERED AS AN OCTAL NUMBER IN RESPONSE TO THE PROMPT
509 FROM THE SWITCH ENTRY ROUTINE:
510

511 SWR = NNNNNN NEW =
512

513 EACH TIME SWITCH SETTING ARE ENTERED, THE ENTIRE SWITCH
514 REGISTER IMAGE MUST BE ENTERED. LEADING ZEROS ARE NOT
515 REQUIRED. 'RUBOUT' AND 'CONTROL U' FUNCTIONS MAY BE
516 USED TO CORRECT TYPING ERRORS DURING SWITCH ENTRY.
517

518 ON PROCESSORS WITH HARDWARE SWITCH REGISTERS, THE
519 'SOFTWARE' SWITCH REGISTER MAY BE USED. IF THE PROGRAM
520 FINDS ALL 16 SWITCHES IN THE 'UP' POSITION, ALL SWITCH
521 REGISTER REFERENCES WILL BE TO THE 'SOFTWARE' REGISTER
522 AND THE PROCEDURES DESCRIBED ABOVE MUST BE FOLLOWED.
523

524 4.5 INPUT DIALOGUE
525

526 THE DIALOGUE WILL BE DONE INTERACTIVELY. THE PROGRAM WILL
527 REQUEST A PARAMETER BY CONSOLE TYPEOUT. THE PARAMETER MAY
528 THEN BE ENTERED AS SPECIFIED BELOW OR ALLOWED TO DEFAULT
529 BY A CARRIAGE RETURN. UNRECOGNIZED OR ILLEGAL RESPONSES WILL
530 BE ECHOED BACK FOLLOWED BY "?". THE PROPER RESPONSE MAY
531 THEN BE ENTERED.
532

533 IMPORTANT: FOR VARIATIONS OF THE ABOVE, SEE XXDP, ACT/APT
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535 CONSIDERATIONS IN SECTIONS 3.2 & 3.4.
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4.5.1 DRIVE SELECTION

THE REQUEST WILL BE:

DRIVES TO BE TESTED:

THE DEFAULT RESPONSE IS CARRIAGE RETURN TO TEST ALL DRIVES
IN THE 'DRIVE PRESENT' CONDITION.

THE OPERATOR CAN ALSO TYPE IN THE SPECIFIC DRIVE NUMBERS
TO BE TESTED, SEPARATED BY COMMAS & TERMINATED BY A CARRIAGE
RETURN.

E.G. DRIVES TO BE TESTED (EVEN NOS. ONLY): 0,4

NOTES: 1. FOR VARIATIONS OF THE ABOVE, SEE XXDP, ACT/APT
CONSIDERATIONS IN SECTIONS 3.2 & 3.3.

2. SEE 'RESTRICTIONS & OPERATOR ACTION' IN SECTION 2.3.

4.5.2 BUS ADDRESS

THE REQUEST WILL BE:

TYPE IN BUSS ADDRESS IF NOT 177440

THE DEFAULT IS A CARRIAGE RETURN

4.5.3 CONTROLLER INTERRUPT VECTOR

THE REQUEST WILL BE:

TYPE IN CONTROLLER INTERRUPT VECTOR IF NOT 210

THE DEFAULT IS A CARRIAGE RETURN.

4.5.4 EXAMPLE OF PROGRAM DIALOGUE

THE EXAMPLE SHOWN IS FOR A PROGRAM STARTED AT ADDRESS 220.
ALL OPERATOR RESPONSES ARE UNDERLINED.

UNIBUS RK06-RK07 DUAL PORT DRIVE DIAGNOSTIC

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DRIVES TO BE TESTED: 0,4<CR>

TYPE IN BUSS ADDRESS IF NOT 177440 <CR>

TYPE IN CONTROLLER INTERRUPT VECTOR IF NOT 210 <CR> _____

WILL TEST DRIVES:

0
4

DRIVE 0

(THE REST IS IDENTICAL TO THE EXAMPLE SHOWN IN 4.6 BELOW)

4.6 PROGRAM EXAMPLE

THE FOLLOWING IS AN EXAMPLE OF A PROGRAM STARTED AT THE
DEFAULT ADDRESS (200) & WITH 2 DRIVES ON THE LINE.

UNIBUS RK06-RK07 DUAL PORT DRIVE DIAGNOSTIC

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WILL TEST DRIVES:

0
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DRIVE 0

DRIVE SERIAL NO. AAA
CARTRIDGE SERIAL NO. BBB

DRIVE 4

DRIVE SERIAL NO. CCC
CARTRIDGE SERIAL NO. DDD

END PASS #1

WILL TEST DRIVES:

0
4

DRIVE 0

DRIVE 4

END PASS # 2

(ETC)

THE ABOVE ASSUMES NO ERRORS DETECTED.
THE NUMBER OF PASSES IS DETERMINED BY ACT/APT/XXDP

NOTES: 1. FOR VARIATIONS OF THE ABOVE, SEE XXDP, ACT/APT
CONSIDERATIONS IN SECTIONS 3.2 & 3.3.

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2. SEE 'RESTRICTIONS & OPERATOR ACTION' IN SECTION 2.3.

4.7 HALTING THE PROGRAM

THE PROGRAM PROVIDES A METHOD OF HALTING ITSELF SUCH THAT THE CARTRIDGE AND/OR DRIVE IS NOT LEFT IN ON UNDETERMINED STATE; IE: HEADS UNLOADED OR INVALID FORMAT.

TO PROPERLY HALT, TYPE CONTROL-C (↑C) ON THE CONSOLE.

IF HEADS ARE LOADED, THE PROGRAM WILL:

1. ECHO ↑C
2. TYPE "CPU HALTED"
3. HALT THE PROGRAM

IF HEADS ARE NOT LOADED, THE PROGRAM WILL:

1. ECHO ↑C
2. TYPE 'HALT PENDING, PLEASE WAIT'
3. WILL LOAD HEADS
4. TYPE 'CPU HALTED'
5. HALT THE PROGRAM

NOTES:

1. OPERATING THE 'CONTINUE' SWITCH ON THE CPU CONSOLE WILL RETURN THE PROGRAM TO TEST 1 WHERE TESTING WILL BEGIN WITH THE 1'ST DRIVE AGAIN.

5.0 DUAL PORT DRIVE DIAGNOSTIC FUNCTIONAL DESCRIPTION

5.1 GENERAL

A. BASIC CONTROLLER TESTS, SIZING & SETUP

THESE TESTS DO BASIC CONTROLLER REGISTER REFERENCE TESTS, CHECKS OPERATOR INPUTS AGAINST DRIVES ON THE LINE OR DE-FAULTS TO TEST ALL DRIVES SEEN ON THE LINE WITHIN THE RESTRICTIONS DESCRIBED IN SECTION 2.3. CHECKS ARE MADE ON THE EXISTENCE OF EITHER AN L OR P CLOCK.

B. DUAL PORT TESTS

THESE TESTS VERIFY THE ABILITY OF THE DRIVE TO OPERATE IN THE DUAL PORT MODE. RELEASE, TIMEOUT & PROPER INTER-ACTION BETWEEN THE PORTS ARE VERIFIED.

C. METHOD TO DETERMINE THAT THE DRIVE IS IN NEUTRAL OR SEIZED.

THE PROGRAM DOES A 'SELECT DRIVE' COMMAND TO PORT 'A'. IF MESSAGE AO RETURNS WITH THE 'DRIVE AVAILABLE' BIT NOT SET, IT ASSUMES PORT 'B' HAS SEIZED THE DRIVE.

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IF MESSAGE AD RETURNS WITH THE 'DRIVE AVAILABLE' BIT SET, IT ASSUMES THE DRIVE WAS IN NEUTRAL OR ALREADY SEIZED BY PORT 'A'.

AFTER A TIMEOUT OF 1 SECOND BY PORT 'A', ASSUMING NO FURTHER COMMANDS, THE DRIVE SHOULD BECOME AVAILABLE TO PORT 'B'.

THERE IS NO SPECIFIC METHOD TO VERIFY THE DRIVE IS IN NEUTRAL BECAUSE THE ACT OF DOING A SELECT DRIVE COMMAND TO CHECK FOR NEUTRAL SEIZES THE DRIVE & TAKES IT OUT OF NEUTRAL.

'DSC' & 'ATTN' DO NOT ASSERT AT THE END OF A TIMEOUT IF THERE IS NO PREVIOUS PORT REQUEST MADE.

THEY WILL ASSERT, HOWEVER, IF A PORT REQUESTED WHILE SEIZED BY THE OTHER PORT & A TIMEOUT OR RELEASE OCCURS ON THE OTHER PORT.

5.2 TEST DESCRIPTIONS

BASIC CONTROLLER TESTS, SIZING & SETUP

TEST 1 REFERENCE ALL CONTROLLER REGISTERS

THIS TEST VERIFIES THAT ALL THE CONTROLLER REGISTERS CAN BE ACCESSED. THE INABILITY TO BE ACCESSED WILL RESULT IN A TIMEOUT TRAP WITH AN ERROR MESSAGE. ANY ERROR IN THIS TEST WILL RESULT IN ABORTING ALL OTHER TESTS AND JUMPING TO 'END OF PASS'

TEST 2 SIZE THE BUSS

THIS TEST IS ENTERED ONLY IF 'DRIVE SELECTION' IS DEFAULTED EITHER BY RUNNING IN THE AUTO MODE OR A 200 START IN THE MANUAL MODE.
EVERY EVEN NUMBERED DRIVE (0,2,4,6) IS ADDRESSED. CONTROLLER ERROR (CERR) IS EXAMINED AND IF NOT SET, THE DRIVE WILL BE TESTED AS AN RK06. IF SET, THE PROGRAM WILL BYPASS TESTING THAT DRIVE ONLY IF THE ERROR WAS A RESULT OF MDS, UFE OR NED BEING SET; OR BOTH NED & DRA RESET INDICATING THE OTHER PORT IS ACCESSED.
IF CERR DUE TO DTYE, DRIVE WILL BE TESTED AS RK07.

TEST 3 VERIFY OPERATOR DRIVE SELECTIONS

THIS TEST IS ENTERED ONLY IF DRIVE SELECTION IS NOT DEFAULTED. EVERY EVEN NUMBERED DRIVE IS ADDRESSED & CONTROLLER ERROR (CERR) IS EXAMINED. IF NOT SET, THE PROGRAM WILL ASSUME THE DRIVE IS PRESENT AS AN RK06 IF CERR WAS SET, THAT DRIVE WILL BE BYPASSED ONLY IF THE ERROR WAS A RESULT OF MDS OR UFE SET OR BOTH

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NED & DRA RESET (WRONG PORT). IF CERR IS A RESULT OF NED ONLY, IT IS CHECKED AGAINST THE INPUTTED INFORMATION TO VERIFY IT WAS NOT SPECIFIED. IF CERR DUE TO DTYE, DRIVE WILL BE TESTED AS RK07.

TEST 4 FIND NEXT DRIVE TO BE TESTED

THIS TEST FINDS THE NEXT DRIVE PRESENT & PUTS THAT ADDRESS IN 'SUNIT' & \$TMP4 IS SET TO CDT IF DRIVE IS RK07. THROUGHOUT THE FOLLOWING TESTS, THE DRIVE TESTED IS THE DRIVE WHOSE ADDRESS IS IN 'SUNIT'.

DUAL PORT TESTS

TEST 5 TEST PORT 'A' SEIZE & TIMEOUT

VERIFY THAT THE DRIVE CAN BE SEIZED & THAT THE PORT TIMEOUT RELEASES THE DRIVE.

- A. SET VOLUME VALID FOR BOTH PORTS & DO A RECAL COMMAND
- B. A SELECT DRIVE COMMAND IS ISSUED THRU PORT 'A'. THE PROGRAM VERIFIES THE DRIVE HAS BEEN SEIZED BY 'DRIVE AVAILABLE' SET.
- C. A SELECT DRIVE COMMAND IS ISSUED THRU PORT 'B' THE PROGRAM VERIFIES THAT 'DRIVE AVAILABLE' IS NOT SET FOR PORT 'B' & THAT CERR IS SET.
- D. VERIFY THAT FOR ALL MESSAGES REQUESTED THRU PORT 'B' (MSG A0-A3, B0-B3) THAT MESSAGE 0 ALWAYS RETURNS FROM PORT B WHILE PORT 'A' IS SEIZED.
- E. WAIT FOR THE PORT TIMEOUT TO OCCUR ON PORT 'A' BY CONTINUOUSLY CHECKING FOR ATTN ON PORT B. AFTER ATTN-B IS REC'D, A DRIVE SELECT COMMAND IS ISSUED THRU PORT B & "DRIVE AVAILABLE" IS CHECKED TO BE SET IN MESSAGE A0.

MEASURE THE DURATION OF THE TIMEOUT & TYPE THE VALUE FOR THE FIRST PASS ONLY.
- F. VERIFY THAT ONLY PORT 'B' GETS 'DSC' & 'ATTN'.
- G. VERIFY THE DRIVE CLEAR COMMAND CLEARS 'DSC' & 'ATTN' ON PORT 'B' BUT DOES NOT RELEASE THE DRIVE FROM PORT 'B'.

TEST 6 TEST PORT 'B' SEIZE & TIMEOUT

THE PREVIOUS TEST IS REPEATED FOR PORT 'B'

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TEST 7 PRINT DRIVE SERIAL NUMBER

THIS TEST READS & PRINTS THE DRIVE SERIAL # FROM MSG A3
IN BCD ON THE 1'ST PASS ONLY.
IT ALSO TESTS THAT THE SERIAL # READ THRU BOTH PORTS
ARE THE SAME.

TEST 10 TEST PORT 'A' COMMAND SEIZE & ATTENTION

VERIFY THE OPERATION OF 'DSC' & 'ATTN' BITS AFTER A COMMAND.

- A. ISSUE A SEEK COMMAND TO CYLINDER 10 THRU PORT 'A'.
- B. VERIFY SEIZURE & THAT 'DSC' & 'ATTN' SETS FOR PORT 'A'
ONLY AFTER THE SEEK HAS COMPLETED.
- C. VERIFY 'ATTN' REMAINS SET BEYOND TIMEOUT
- D. VERIFY A DRIVE CLEAR COMMAND RESETS 'DSC' & 'ATTN'
& DOES NOT RELEASE THE DRIVE FROM PORT 'A'.

TEST 11 TEST PORT 'B' COMMAND SEIZE & ATTENTION

THE PREVIOUS TEST IS REPEATED FOR PORT 'B'
BUT THE SEEK IS TO CYLINDER 0.

TEST 12 TEST RESET PORT 'A' ATTENTION BY DRIVE CLEAR COMMAND

VERIFY THAT A DRIVE CLEAR COMMAND CLEARS ONLY THE ATTENTION BIT OF
THE SEIZING PORT

- A. SET EACH PORT'S ATTENTION BIT BY PERFORMING SEEK
COMMANDS TO CYLINDER 0 & ALLOWING TIMEOUTS.
- B. SEIZE THE DRIVE THRU PORT 'A' & ISSUE A DRIVE CLEAR COMMAND
VERIFY THAT 'DSC' & 'ATTN' FOR PORT 'A' HAVE BEEN CLEARED
- C. SEIZE THE DRIVE THRU PORT 'B' & VERIFY 'DSC' & 'ATTN'
HAVE NOT CLEARED

TEST 13 TEST RESET PORT 'B' ATTENTION BY DRIVE CLEAR COMMAND

THE PREVIOUS TEST IS REPEATED FOR PORT 'B'

TEST 14 TEST RELEASE, DRIVE SEIZED BY PORT 'A'

- A. SEIZE THE DRIVE THRU PORT 'A'

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- B. ISSUE A RELEASE USING RKCS2 THRU PORT 'A'
- C. VERIFY PORT 'B' CAN ACCESS THE DRIVE IMMEDIATELY & THAT NEITHER PORT SEES 'DSC' OR 'ATTN'

TEST 15 TEST RELEASE, DRIVE SEIZED BY PORT 'B'
THE PREVIOUS TEST IS REPEATED FOR PORT 'B'

TEST 16 TEST RELEASE FROM PORT 'A' WITH PORT 'B' REQUESTING

- A. PORT 'A' SEIZES THE DRIVE & DOES A SEEK TO SELF COMMAND. THE PROGRAM VERIFIES 'DSC' & 'ATTN' ON PORT 'A' ONLY ON COMPLETION
- B. PORT 'B' TRIES TO ACCESS THE DRIVE. THE PROGRAM VERIFIES DRIVE NOT AVAILABLE
- C. A RELEASE BY PORT 'A' IS ISSUED. VERIFY PORT 'B' CAN ACCESS THE DRIVE IMMEDIATELY & THAT 'DSC' & 'ATTN' ARE SEEN ON PORT 'B'.
- D. VERIFY PORT 'A' 'DSC' & 'ATTN' REMAINS SET AFTER RELEASE
- E. THE PROGRAM ISSUES A DRIVE CLEAR COMMAND TO PORT 'B' & VERIFIES 'DSC' & 'ATTN' RESETS.
- F. THE PROGRAM THEN VERIFIES THAT PORT 'B' DOES NOT SEE FURTHER (MULTIPLE) ATTENTIONS FROM WHAT WOULD HAVE BEEN NORMAL TIMEOUT FROM PORT 'A'.

TEST 17 TEST RELEASE FROM PORT 'B' WITH PORT 'A' REQUESTING
THE PREVIOUS TEST IS REPEATED FOR PORT 'B'

TEST 20 TEST RELEASE FROM REQUESTING PORT 'B' INHIBITS 'ATTN'

- A. PORT 'A' SEIZES THE DRIVE
- B. PORT 'B' ATTEMPTS TO SEIZE THE DRIVE
- C. PORT 'B' & PORT 'A' RELEASE THE DRIVE, IN THAT ORDER
- D. THE PROGRAM VERIFIES THAT NEITHER PORT 'A' OR 'B' ATTENTION BITS SET

TEST 21 TEST RELEASE FROM REQUESTING PORT 'A' INHIBITS 'ATTN'
THE PREVIOUS TEST IS REPEATED FOR PORT 'B'

TEST 22 TEST RELEASE BY PORT 'B' WHEN SEIZED BY PORT 'A'

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VERIFY THAT A RELEASE ISSUED BY ONE PORT IS NOT RECOGNIZED IF THE DRIVE IS SEIZED BY THE OTHER PORT

- A. SEIZE THE DRIVE THRU PORT 'A'.
- B. ISSUE A RELEASE THRU PORT 'B' & VERIFY DRIVE STILL SEIZED BY PORT 'A'.

TEST 23 TEST RELEASE BY PORT 'A' WHEN SEIZED BY PORT 'B'
THE PREVIOUS TEST IS REPEATED FOR PORT 'B'

TEST 24 TEST COMMAND FOLLOWED BY IMMEDIATE RELEASE FROM PORT 'A'

- A. ISSUE A SEEK COMMAND TO CYL 10 FROM PORT 'A' & AN IMMEDIATE RELEASE TO PORT A.
- B. VERIFY THE DRIVE IS AVAILABLE TO PORT 'B' & PORT 'B' RECEIVES 'ATTN'
- C. VERIFY PORT 'A' DOES NOT RAISE ATTN WHEN THE SEEK IS COMPLETED.

TEST 25 COMMAND & IMMEDIATE RELEASE FROM PORT 'B'

THE PREVIOUS TEST IS REPEATED FOR PORT 'B'
BUT THE SEEK IS TO CYLINDER 0.

TEST 26 TEST TIMEOUT RETRIGGER THRU PORT 'A'

VERIFY THAT THE PORT TIMEOUT ONE-SHOT CAN BE RETRIGGERED.

- A. PORT 'A' SEIZES THE DRIVE
- B. THE PROGRAM WAITS 500MS & RE-SEIZES THE DRIVE THRU PORT 'A'
- C. PORT 'B' ATTEMPTS TO SEIZE THE DRIVE & THE PROGRAM VERIFIES THAT FULL TIMEOUT TOOK PLACE FROM STEP 'B' ABOVE.

TEST 27 TEST TIMEOUT RETRIGGER THRU PORT 'B'

THE PREVIOUS TEST IS REPEATED FOR PORT 'B'

TEST 30 TEST PORT 'A' TIMER INHIBIT

- A. PORT 'A' SEIZES THE DRIVE
- B. PORT 'B' ATTEMPTS TO SEIZE THE DRIVE

C. PORT 'A' RELEASES THE DRIVE
 D. PORT 'A' ATTEMPTS TO GET THE DRIVE BACK.
 THE PROGRAM VERIFIES THAT PORT 'A' CANNOT ACCESS
 THE DRIVE FOR APPROX 1 SEC

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TEST 31 TEST PORT 'B' TIMER INHIBIT

THE PREVIOUS TEST IS REPEATED FOR PORT 'B'

TEST 32 TEST UNLOAD COMMAND TIMER INHIBIT THRU PORT 'A'

VERIFY THAT THE UNLOAD COMMAND THRU A PORT, SEIZES THAT
 PORT FOR AS LONG AS HEADS ARE UNLOADED & RELEASE IS NOT
 ISSUED.

- A. ISSUE AN UNLOAD COMMAND THRU PORT 'A'.
 VERIFY DRIVE UNLOADS & ATTENTION IS SET.
- B. DELAY FOR 5 SECONDS & VERIFY DRIVE
 NOT AVAILABLE TO PORT 'B' TO INSURE TIMERS INHIBITED.
- C. ISSUE A RELEASE FROM PORT 'A'. VERIFY DRIVE BECOMES
 AVAILABLE TO PORT 'B'
- D. LOAD HEADS FROM PORT 'B' & VERIFY 'ATTN-B' AT COMPLETION

TEST 33 TEST UNLOAD COMMAND TIMER INHIBIT THRU PORT 'B'

THE PREVIOUS TEST IS REPEATED FOR PORT 'B'

TEST 34 TEST RECAL COMMAND TIMER INHIBIT THRU PORT 'A'

VERIFY THAT THE RECAL COMMAND THRU A PORT, SEIZES THAT
 PORT FOR AS LONG AS THE RECAL IS IN PROGRESS & A
 RELEASE IS NOT ISSUED.

- A. ISSUE A RECAL COMMAND FROM THE LAST CYL THRU PORT 'A'
- B. VERIFY PORT 'B' CANNOT SEIZE THE DRIVE UNTIL
 PORT 'A' RECEIVES ATTN.
 THIS INSURES THAT THE TIMERS WERE INHIBITED.

TEST 35 TEST RECAL COMMAND TIMER INHIBIT THRU PORT 'B'

THE PREVIOUS TEST IS REPEATED FOR PORT 'B'

TEST 36 READ & SAVE BAD SECTOR INFO & TYPE PACK SERIAL

H02

CZR6GBD RK611 DU PORT LGC TST MACY11 30A(1052) 03-JAN-78 08:56 PAGE 20
CZR6GB.P11 03-JAN-78 08:46

SEQ 0020

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THIS TEST VERIFIES THAT CYL 632 (1456 FOR RK07), TRACK 2 CAN BE READ.

THIS AREA CONTAINS BAD SECTOR INFO WHICH IS WRITTEN BY THE FACTORY DURING MANF. ALL BAD SECTOR INFO (BSE) WILL BE STORED AT THIS TIME TO MASK FUTURE READ HEADER OR DATA ERROR PRINTOUTS.

SECTORS 0,2,4,6,8 CONTAIN IDENTICAL INFO FOR 22 SECTOR HARDWARE DETECTED
SECTORS 10,12,14,16,18,20 CONTAIN IDENTICAL INFO FOR 22 SECTOR SOFTWARE

IF BSE INFO CANNOT BE READ, OR IF AFTER READING THE BSE INFO IT IS DETERMINED THAT AN ALIGNMENT CARTRIDGE IS USED,

A MESSAGE WILL BE TYPED INDICATING THAT ALL FUTURE FORMAT AND READ-WRITE TESTS WILL BE BYPASSED. THIS IS DONE SO AS NOT TO DESTROY BSE INFO OR AN ALIGNMENT PACK BY WRITING

THE PACK SERIAL # IS TYPED IN OCTAL & FOR THE FIRST PASS ONLY.

TEST 37 DATA TESTS

VERIFY UNIQUE DATA CAN BE WRITTEN THRU EITHER PORT & READ BACK CORRECTLY THRU BOTH PORTS.

- A. ALL 1'S ARE WRITTEN THRU PORT 'A' ON CYL 0, SECTOR 0, TRACK 0 & VERIFIED BY READING BACK THRU BOTH PORTS.
- B. ALL 1'S ARE WRITTEN THRU PORT 'B' ON CYL 10, SECTOR 0, TRACK 0 & VERIFIED BY READING BACK THRU BOTH PORTS.
- C. THE PROGRAM CHECKS THAT CYL 0 WAS NOT OVERWRITTEN BY READING & VERIFYING ALL 0'S THRU PORT 'B'.

TEST 40 ALTERNATING SEEK INTERACTION TEST

THIS TEST VERIFIES THAT THERE ARE NO TIMING INTERACTION PROBLEMS BETWEEN SEEKS FROM BOTH PORTS.

- A. PORT 'A' SEIZES THE DRIVE & SEEKS TO CYLINDER 0 & RELEASES THE DRIVE AFTER 'ATTN' IS RECEIVED. THE PROGRAM VERIFIES THAT UNTIL ATTN IS REC'D, PORT 'B' SEES 'CONTROLLER ERROR' & 'DRIVE NOT AVAILABLE'.
- B. PORT 'B' SEIZES THE DRIVE & SEEKS TO THE LAST CYL & RELEASES THE DRIVE AFTER 'ATTN' IS RECEIVED. THE PROGRAM VERIFIES THAT UNTIL ATTN IS REC'D, PORT 'A' SEES 'CONTROLLER ERROR' & 'DRIVE NOT AVAILABLE'.
- C. THE ABOVE IS REPEATED FOR A PATTERN OF CONVERGING SEEKS TOWARD THE CENTER OF THE CARTRIDGE.
- D. THE PROGRAM VERIFIES MULTIPLE ATTENTIONS OR ERRORS DO NOT OCCUR AS A RESULT OF TIMING PROBLEMS.

6.0 ERROR REPORTING

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6.1 ERROR INTERPRETATION

WHENEVER AN ERROR MESSAGE IS PRINTED OUT, ALL REGISTERS AND OTHER DATA PERTAINING TO THE ERROR ARE ALSO GIVEN. MSG A(00), MSG B(01), RKER, RKBA...ETC. INDICATE THE CONTENTS OF THE CORRESPONDING REGISTERS AT THE TIME OF ERROR.

EVERY ERROR MESSAGE CONTAINS A PC. THIS PC INDICATES THE POSITION IN PROGRAM WHERE THE ERROR CALL IS LOCATED. THE ERROR MESSAGE, BECAUSE OF PRACTICAL CONSIDERATIONS IS MADE SHORT AND MEANINGFUL. THE USER IS ADVISED TO LOOK UP THE PC IN THE PROGRAM LISTING, WHERE HE WILL FIND MORE INFORMATION ABOUT THE ERROR. IN MANY INSTANCES, A SINGLE FAULT WILL GIVE RISE TO MORE THAN ONE ERROR REPORT. A LITTLE DELIBERATION AND CAREFUL EXAMINATION OF THE DATA GIVEN WILL BE CERTAINLY

VERY HELPFUL IN PINPOINTING THE FAULT. A BRIEF EXPLANATION OF WHAT IS BEING CHECKED IN THE TEST IS GIVEN AT THE BEGINNING OF EVERY TEST. ALL THE NUMBERS GIVEN WITH ERROR MESSAGES ARE IN OCTAL.

NOTE

NO ERROR LOGGING OR OPERATION HISTORY IS PROVIDED.

6.2 ERROR PRINTOUT EXAMPLE:

MESSAGE AD ERROR
AFTER START SPINDLE CMD & FWD SET

	TEST NO.	PC				
	000014	016530				
	EXPECT					
AD	B0	A1	B1	A2	B2	B3
030144	100000	013704	000001			
	ACTUAL					
	140144	100000	101744	000001		
RKCS1	RKCS2	RKASOF	RKER	RKDS	RKDC	
040200	000100	010000	000000	000000	000000	

THE ABOVE EXAMPLE SHOWS EXPECTED & ACTUAL DATA FOR MESSAGE REGISTERS AD, B0, A1 & B1.

MESSAGES A2, B2 & B3 WILL BE TYPED OUT ONLY AS REQUIRED IF THE CYLINDER DIFFERENCE/OFFSET, CYLINDER ADDRESS & HEAD & SECTOR INFORMATION IS A VARIABLE PARAMETER OF THE TEST.

7.0 DUAL PROCESSOR-DUAL CONTROLLER TESTING

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THIS PROGRAM AS DESCRIBED, TESTS THRU A SINGLE CPU & RK611
IN A SYNCHRONOUS MANNER.

TO RUN IN A DYNAMIC MANNER, I.E.: DUAL CPU/DUAL RK611
WITH BOTH PORTS CONTENDING FOR THE DRIVE ASYNCHRONOUSLY,
THE FOLLOWING PROGRAMS SHOULD BE USED:

CZR6M SUBSYSTEM VERIFICATION PART 1 TEST 22
CZR6P PERFORMANCE EXERCISER

EACH OF THE ABOVE PROGRAMS REQUIRES A SPECIAL STARTING ADDRESS
TO ENTER THE DUAL PORT TESTING MODE.

TEST 22 IN THE SUBSYSTEM VERIFICATION PROGRAM CAN BE SET UP
SO THAT ONE PORT BECOMES THE WRITER OF A SPECIFIED DATA PATTERN
OVER A SPECIFIED RANGE OF HEADS & CYLINDERS, WHILE THE
OTHER PORT BECOMES THE READER.
THE ROLES OF EACH PORT CAN THEN BE REVERSED THRU ANOTHER SETUP.

THERE ARE MANY POSSIBLE WAYS OF DYNAMICALLY TESTING DUAL
PORT BY THE ABOVE TEST.
THE OPERATOR IS URGED TO READ THE PROGRAM DOCUMENT.

THE PERFORMANCE EXERCISER SHOULD BE USED TO RANDOMLY
TEST ALL POSSIBLE DUAL PORT OPERATIONS.
THIS PROGRAM WILL MOST SIMULATE ACTUAL ON-LINE DUAL PORT OPERATION.
THE OPERATOR IS URGED TO READ THE PROGRAM DOCUMENT.
[END OF SPECIFICATION]

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```
; *** PGM REV 030 ***  
; 29-SEP-77 PATCH ALL ERROR LOGS  
  
.NLIST CND,MC,MD  
.LIST ME  
.ENABL ABS,AMA  
  
;DEFINE SYSMAC MACROS  
  
$SWR= 167400 ;DEFINE SWITCHES 15,14,13,11,10,9,8  
$TN= 1 ;SET FIRST TEST NO. TO 1
```

```
.TITLE CZR6GB0 RK611 DU PORT LGC TST  
;*COPYRIGHT (C) 1976,1977  
;*DIGITAL EQUIPMENT CORP.  
;*MAYNARD, MASS. 01754  
;*PROGRAM BY GARY PAPIAZIAN  
;*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC  
;*PACKAGE (MAINDEC-11-DZQAC-C3), JAN 19, 1977.  
;*
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```
.SBTTL OPERATIONAL SWITCH SETTINGS  
;*  
;* SWITCH USE  
;*-----  
;* 15 HALT ON ERROR  
;* 14 LOOP ON TEST  
;* 13 INHIBIT ERROR TYPEOUTS  
;* 12 ABORT DRIVE AFTER 20 ERRORS  
;* 11 INHIBIT ITERATIONS  
;* 10 BELL ON ERROR  
;* 9 LOOP ON ERROR  
;* 8 LOOP ON TEST IN SWR<7:0>
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.SBTTL SUMMARY OF STARTING LOCATIONS  
;*  
;* 200 DEFAULT PARAMETERS  
;* 220 INPUT PARAMETERS  
;* 240 ODT11  
;*
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1231          .SBTTL BASIC DEFINITIONS
1232
1233          ;*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
1234          001100  STACK= 1100
1235          .EQUIV EMT,ERROR          ;;BASIC DEFINITION OF ERROR CALL
1236          .EQUIV IOT,SCOPE        ;;BASIC DEFINITION OF SCOPE CALL
1237
1238          ;*MISCELLANEOUS DEFINITIONS
1239          000011  HT= 11          ;;CODE FOR HORIZONTAL TAB
1240          000012  LF= 12          ;;CODE FOR LINE FEED
1241          000015  CR= 15          ;;CODE FOR CARRIAGE RETURN
1242          000200  CRLF= 200       ;;CODE FOR CARRIAGE RETURN-LINE FEED
1243          177776  PS= 177776     ;;PROCESSOR STATUS WORD
1244          .EQUIV PS,PSW
1245          177774  STKLMT= 177774 ;;STACK LIMIT REGISTER
1246          177772  PIRQ= 177772  ;;PROGRAM INTERRUPT REQUEST REGISTER
1247          177570  DSWR= 177570  ;;HARDWARE SWITCH REGISTER
1248          177570  DDISP= 177570 ;;HARDWARE DISPLAY REGISTER
1249
1250          ;*GENERAL PURPOSE REGISTER DEFINITIONS
1251          000000  R0= %0          ;;GENERAL REGISTER
1252          000001  R1= %1          ;;GENERAL REGISTER
1253          000002  R2= %2          ;;GENERAL REGISTER
1254          000003  R3= %3          ;;GENERAL REGISTER
1255          000004  R4= %4          ;;GENERAL REGISTER
1256          000005  R5= %5          ;;GENERAL REGISTER
1257          000006  R6= %6          ;;GENERAL REGISTER
1258          000007  R7= %7          ;;GENERAL REGISTER
1259          000006  SP= %6         ;;STACK POINTER
1260          000007  PC= %7         ;;PROGRAM COUNTER
1261
1262          ;*PRIORITY LEVEL DEFINITIONS
1263          000000  PR0= 0          ;;PRIORITY LEVEL 0
1264          000040  PR1= 40         ;;PRIORITY LEVEL 1
1265          000100  PR2= 100       ;;PRIORITY LEVEL 2
1266          000140  PR3= 140       ;;PRIORITY LEVEL 3
1267          000200  PR4= 200       ;;PRIORITY LEVEL 4
1268          000240  PR5= 240       ;;PRIORITY LEVEL 5
1269          000300  PR6= 300       ;;PRIORITY LEVEL 6
1270          000340  PR7= 340      ;;PRIORITY LEVEL 7
1271
1272          ;*"SWITCH REGISTER" SWITCH DEFINITIONS
1273          100000  SW15= 100000
1274          040000  SW14= 40000
1275          020000  SW13= 20000
1276          010000  SW12= 10000
1277          004000  SW11= 4000
1278          002000  SW10= 2000
1279          001000  SW09= 1000
1280          000400  SW08= 400
1281          000200  SW07= 200
1282          000100  SW06= 100
1283          000040  SW05= 40
1284          000020  SW04= 20
1285          000010  SW03= 10
1286          000004  SW02= 4

```

BASIC DEFINITIONS

1287 000002
1288 000001
1289
1290
1291
1292
1293
1294
1295
1296
1297
1298
1299
1300
1301 100000
1302 040000
1303 020000
1304 010000
1305 004000
1306 002000
1307 001000
1308 000400
1309 000200
1310 000100
1311 000040
1312 000020
1313 000010
1314 000004
1315 000002
1316 000001
1317
1318
1319
1320
1321
1322
1323
1324
1325
1326
1327
1328
1329 000004
1330 000010
1331 000014
1332 000014
1333 000014
1334 000020
1335 000024
1336 000030
1337 000034
1338 000060
1339 000064
1340 000240
1341
1342

SW01= 2
SW00= 1
.EQUIV SW09,SW9
.EQUIV SW08,SW8
.EQUIV SW07,SW7
.EQUIV SW06,SW6
.EQUIV SW05,SW5
.EQUIV SW04,SW4
.EQUIV SW03,SW3
.EQUIV SW02,SW2
.EQUIV SW01,SW1
.EQUIV SW00,SW0

.*DATA BIT DEFINITIONS (BIT00 TO BIT15)

BIT15= 100000
BIT14= 40000
BIT13= 20000
BIT12= 10000
BIT11= 4000
BIT10= 2000
BIT09= 1000
BIT08= 400
BIT07= 200
BIT06= 100
BIT05= 40
BIT04= 20
BIT03= 10
BIT02= 4
BIT01= 2
BIT00= 1
.EQUIV BIT09,BIT9
.EQUIV BIT08,BIT8
.EQUIV BIT07,BIT7
.EQUIV BIT06,BIT6
.EQUIV BIT05,BIT5
.EQUIV BIT04,BIT4
.EQUIV BIT03,BIT3
.EQUIV BIT02,BIT2
.EQUIV BIT01,BIT1
.EQUIV BIT00,BIT0

.*BASIC "CPU" TRAP VECTOR ADDRESSES

ERRVEC= 4 ;: TIME OUT AND OTHER ERRORS
RESVEC= 10 ;: RESERVED AND ILLEGAL INSTRUCTIONS
TBITVEC= 14 ;: "T" BIT
TRTVEC= 14 ;: TRACE TRAP
BPTVEC= 14 ;: BREAKPOINT TRAP (BPT)
IOTVEC= 20 ;: INPUT/OUTPUT TRAP (IOT) **SCOPE**
PWRVEC= 24 ;: POWER FAIL
EMTVEC= 30 ;: EMULATOR TRAP (EMT) **ERROR**
TRAPVEC= 34 ;: "TRAP" TRAP
TKVEC= 60 ;: TTY KEYBOARD VECTOR
TPVEC= 64 ;: TTY PRINTER VECTOR
PIRQVEC= 240 ;: PROGRAM INTERRUPT REQUEST VECTOR

.SBTTL RK06 CONTROLLER REGISTER DEFINITION

```

1343
1344
1345
1346          000000          RKCS1= 0          ; CONTROL AND STATUS REGISTER 1
1347          000002          RKWC= 2          ; WORD COUNT REGISTER
1348          000004          RKBA= 4          ; BUS ADDRESS REGISTER
1349          000006          RKDA= 6          ; DESIRED TRACK SECTOR REGISTER
1350          000010          RKCS2= 10         ; CONTROL AND STATUS REGISTER 2
1351          000012          RKDS= 12         ; DRIVE STATUS REGISTER
1352          000014          RKER= 14         ; ERROR REGISTER
1353          000016          RKASOF= 16        ; ATTENTION SUMMARY AND OFFSET REGISTER
1354          000020          RKDC= 20         ; DESIRED CYLINDER REGISTER
1355          000024          RKDB= 24         ; DATA BUFFER
1356          000026          RKMR1= 26        ; MAINTENANCE REGISTER 1
1357          000034          RKMR2= 34        ; MAINTENANCE REGISTER 2 (MESSAGE LINE A)
1358          000036          RKMR3= 36        ; MAINTENANCE REGISTER 3 (MESSAGE LINE B)
1359          000030          RKECPS= 30       ; ECC POSITION INFORMATION
1360          000032          RKECPT= 32       ; ECC PATTERN INFORMATION
1361
1362          .SBTTL CONTROL AND STATUS REGISTER 1 BITS (RKCS1:0)
1363
1364          ; DRIVE COMMANDS
1365
1366          000001          SELDRV= 1          ; SELECT DRIVE (GET STATUS)
1367          000003          PACK= 3           ; PACK ACKNOWLEDGE
1368          000005          CLEAR= 5          ; DRIVE CLEAR
1369          000007          UNLOAD= 7         ; UNLOAD
1370          000011          SRTSPL= 11        ; START SPINDLE
1371          000013          RECAL= 13        ; RECALIBRATE
1372          000015          OFFSET= 15       ; OFFSET
1373          000017          SEEK= 17         ; SEEK
1374          000021          RDDATA= 21       ; READ DATA
1375          000023          WRDATA= 23       ; WRITE DATA
1376          000025          RDHEAD= 25      ; READ HEADER
1377          000027          WRHEAD= 27      ; WRITE HEADER AND DATA
1378          000031          WRTCHK= 31      ; WRITE CHECK
1379
1380          000001          GO= BIT0          ; GO BIT
1381          000100          IE= BIT6          ; INTERRUPT ENABLE
1382          000200          RDY= BIT7          ; CONTROLLER READY
1383          000400          BA16= BIT8         ; BUS ADDRESS BIT 16
1384          001000          BA17= BIT9         ; BUS ADDRESS BIT 17
1385          002000          CDT= BIT10        ; CONTROLLER DRIVE TYPE (0=RK06,1=RK07)
1386          004000          CTO= BIT11        ; CONTROLLER TIMEOUT
1387          010000          CFMT= BIT12       ; CONTROLLER DRIVE FORMAT (0=22 SECTOR, 1=20 SECTOR)
1388          020000          DCPAR= BIT13      ; SERCON PARITY ERROR DETECTED BY CONTROLLER
1389          040000          DI= BIT14        ; DRIVE INTERRUPT
1390          100000          CERR= BIT15       ; CONTROLLER ERROR
1391          100000          CCLR= BIT15       ; CONTROLLER CLEAR
1392
1393          .SBTTL CONTROL AND STATUS REGISTER 2 BITS (RKCS2:10)
1394
1395          000007          DRVMSK= 7          ; MASK FOR DRIVE SELECTION CODE
1396          000010          RLS= BIT3         ; DESELECT OR RELEASE DRIVE IN BITS 0-2
1397          000020          BAI= BIT4         ; BUS ADDRESS INCREMENT INHIBIT
1398          000040          SCLR= BITS        ; SUBSYSTEM CLEAR CONTROLLER AND ALL DRIVES

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1399	000100	IR=	BIT6	: INPUT READY
1400	000200	OR=	BIT7	: OUTPUT READY
1401	000400	UFE=	BIT8	: UNIT FIELD ERROR
1402	001000	MDS=	BIT9	: MULTIPLE DRIVE SELECT
1403	002000	PGE=	BIT10	: PROGRAMMING ERROR
1404	004000	NEM=	BIT11	: NON-EXISTENT MEMORY
1405	010000	NED=	BIT12	: NON-EXISTENT DRIVE
1406	020000	UPE=	BIT13	: UNIBUS PARITY ERROR
1407	040000	WCE=	BIT14	: WRITE CHECK ERROR
1408	100000	DLT=	BIT15	: DATA LATE ERROR
1409				
1410		.SBTTL	ERROR REGISTER BIT DEFINITION (RKER:14)	
1411				
1412	000001	ILF=	BIT0	: ILLEGAL FUNCTION CODE
1413	000002	SKI=	BIT1	: SEEK INCOMPLETE
1414	000004	NXF=	BIT2	: NON-EXECUTABLE FUNCTION
1415	000010	DRPAR=	BIT3	: DRIVE DETECTED SERCON PARITY ERROR
1416	000020	FMTE=	BIT4	: FORMAT ERROR
1417	000040	DTYPE=	BIT5	: DRIVE TYPE ERROR
1418	000100	ECH=	BIT6	: ECC HARD
1419	000200	BSE=	BIT7	: BAD SECTOR ERROR
1420	000400	HVRC=	BIT8	: HEADER VRC ERROR
1421	001000	COE=	BIT9	: CYLINDER ADDRESS OVERFLOW ERROR
1422	002000	IDAE=	BIT10	: INVALID DISK ADDRESS ERROR: HEAD/CYL
1423	004000	WLE=	BIT11	: WRITE LOCK ERROR
1424	010000	DTE=	BIT12	: DRIVE TIMING ERROR
1425	020000	OPI=	BIT13	: OPERATION (SEARCH) INCOMPLETE
1426	040000	UNS=	BIT14	: DRIVE UNSAFE
1427	100000	DCK=	BIT15	: DATA CHECK
1428				
1429		.SBTTL	STATUS REGISTER BIT DEFINITION (RKDS:12)	
1430				
1431	000001	DRA=	BIT0	: DRIVE AVAILABLE (CONTROLLER IS SET IF THIS BIT IS RESET)
1432				
1433	000004	OFST=	BIT2	: DRIVE OFFSET
1434	000010	ACLO=	BIT3	: AC LOW
1435	000020	DCLO=	BIT4	: DC LOW
1436	000040	DROT=	BIT5	: DRIVE OFF TRACK
1437	000100	VV=	BIT6	: VOLUME VALID
1438	000200	DRDY=	BIT7	: DRIVE READY
1439	000400	DDT=	BIT8	: DRIVE TYPE (0=RK06,1=RK07)
1440	004000	WRL=	BIT11	: WRITE LOCK
1441	020000	PIP=	BIT13	: POSITIONING IN PROGRESS
1442	040000	DSC=	BIT14	: DRIVE STATUS CHANGE
1443	100000	SVAL=	BIT15	: STATUS VALID
1444				
1445		.SBTTL	MAINTENANCE REGISTER 1 BIT DEFINITION (RKMR1:22)	
1446				
1447	000017	MESMSK=	17	: MESSAGE MASK
1448	000020	PAT=	BIT4	: FORCE EVEN PARITY ON SERCON MESSAGE LINES
1449	000040	DMD=	BIT5	: DIAGNOSTIC MODE
1450	000100	MSP=	BIT6	: MAINTENANCE SECTOR PULSE
1451	000200	MIND=	BIT7	: MAINTENANCE INDEX
1452	000400	MCLK=	BIT8	: MAINTENANCE CLOCK
1453	001000	MERD=	BIT9	: MAINTENANCE ENCODED READ DATA
1454	002000	MEWD=	BIT10	: MAINTENANCE ENCODED WRITE DATA

1455	004000	PCA=	BIT11	; PRECOMPENSATION ADVANCE
1456	010000	PCD=	BIT12	; PRECOMPENSATION DELAY
1457	020000	ECCW=	BIT13	; ECC WORD IS BEING READ OR WRITTEN
1458	040000	WRTGAT=	BIT14	; WRITE GATE
1459	100000	RDGATE=	BIT15	; READ GATE
1460				
1461		.SBTTL	DEFINITION OF DRIVE STATUS BYTE 00 MESSAGE A (RKMR2:34)	
1462				
1463	000040	D.DRA=	BITS	; DRIVE AVAILABLE
1464	000100	D.VV=	BIT6	; VOLUME VALID
1465	000200	D.DRDY=	BIT7	; DRIVE READY
1466	000400	D.DDT=	BIT8	; DRIVE TYPE (0=RK06,1=RK07)
1467	001000	D.FORM=	BIT9	; DRIVE FORMAT
1468	002000	D.OFF=	BIT10	; OFFSET ON
1469	004000	D.WRL=	BIT11	; WRITE LOCK
1470	010000	D.SPIN=	BIT12	; SPINDLE ON
1471	020000	D.PIP=	BIT13	; POSITIONING IN PROGRESS
1472	040000	D.DSC=	BIT14	; DRIVE STATUS CHANGE
1473				
1474		.SBTTL	DEFINITION OF DRIVE STATUS BYTE 01 MESSAGE A (RKMR2:34)	
1475				
1476	000020	D.SSP=	BIT4	; SERVO SIG PRESENT
1477	000040	D.HDHM=	BITS	; HEADS HOME
1478	000100	D.BRHM=	BIT6	; BRUSHES HOME
1479	000200	D.DOOR=	BIT7	; DOOR INTERLOCKED
1480	000400	D.CART=	BIT8	; CARTRIDGE INTERLOCK
1481	001000	D.SPOK=	BIT9	; SPEED OK
1482	002000	D.FWD=	BIT10	; FORWARD
1483	004000	D.REV=	BIT11	; REVERSE
1484	010000	D.LOAD=	BIT12	; HEADS LOADING
1485	020000	D.RTZ=	BIT13	; RETURN TO ZERO
1486	040000	D.UNLD=	BIT14	; HEADS UNLOADING
1487				
1488		.SBTTL	DEFINITION OF DRIVE STATUS BYTE 00 MESSAGE B (RKMR3:36)	
1489				
1490	000040	D.IDAE=	BITS	; INVALID DISK ADDRESS ERROR:HEAD/CYL
1491	000100	D.ACLO=	BIT6	; AC LOW
1492	000200	D.FLT=	BIT7	; DRIVE FAULT
1493	000400	D.ILF=	BIT8	; ILLEGAL FUNCTION CODE
1494	001000	D.PAR=	BIT9	; DRIVE DETECTED SERCON PARITY ERROR
1495	002000	D.SKI=	BIT10	; SEEK INCOMPLETE
1496	004000	D.WLE=	BIT11	; WRITE LOCK ERROR
1497	010000	D.SPLS=	BIT12	; SPEED LOSS
1498	020000	D.DROT=	BIT13	; DRIVE OFF TRACK
1499	040000	D.UNS=	BIT14	; R/W UNSAFE
1500				
1501		.SBTTL	DEFINITION OF DRIVE STATUS BYTE 01 MESSAGE B (RKMR3:36)	
1502				
1503	000020	D.SECT=	BIT4	; SECTOR ERROR
1504	000040	D.WCUR=	BITS	; WRITE CURRENT AND NO WRITE GATE
1505	000100	D.WGAT=	BIT6	; WRITE GATE AND NO TRANSISTIONS
1506	000200	D.HDFL=	BIT7	; HEAD FAULT
1507	000400	D.MHD=	BIT8	; MULTIPLE HEAD SELECT
1508	001000	D.XERR=	BIT9	; INDEX ERROR
1509	002000	D.TIB=	BIT10	; TRIBIT ERROR
1510	004000	D.PLO=	BIT11	; PLO ERROR

E03

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 30
DEFINITION OF DRIVE STATUS BYTE 01 MESSAGE B (RKMR3:36)

SEQ 0030

1511	010000	D.NMOV= BIT12	;SEEK AND NO MOTION
1512	020000	D.LIMD= BIT13	;LIMIT DETECT ON SEEK
1513	040000	D.SUNS= BIT14	;SERVO UNSAFE
1514			
1515		.SBTTL COMMON MASKS AND OTHER BITS: MESSAGE A (RKMR2:34)	
1516			
1517	000007	M.DRV= 7	;DRIVE CODE, ALL BYTES
1518	077770	M.SER= 77770	;DRIVE SERIAL #, BYTE 11
1519			
1520		.SBTTL COMMON MASKS AND OTHER BITS: MESSAGE B (RKMR3:36)	
1521			
1522	000003	M.ID= 3	;BYTE ID, ALL BYTES
1523	040000	M.ALGN= BIT14	;ALIGN SIGN, BYTE 10
1524	000760	M.SECT= 760	;SECTOR COUNT, BYTE 11
1525	007000	M.HEAD= 7000	;HEAD DECODE, BYTE 11
1526	100000	M.PAR= BIT15	;PARITY, MESS A/B, ALL BYTES

F03

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 31
COMMON MASKS AND OTHER BITS: MESSAGE B (RKMR3:36)

SEQ 0031

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1527
1528          .SBTTL TRAP CATCHER
1529
1530          000000          .=0
1531          ;*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
1532          ;*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
1533          ;*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS
1534          .=174
1535 000174 000000  DISPREG: .WORD 0          ;;SOFTWARE DISPLAY REGISTER
1536 000176 000000  SWREG:   .WORD 0          ;;SOFTWARE SWITCH REGISTER
1537          .SBTTL STARTING ADDRESS(ES)
1538 000200 000137 010050  JMP      @#START ;;JUMP TO STARTING ADDRESS OF PROGRAM
1539          .=220
1540 000220 000137 010040  JMP      PARSRT  ;INPUT PARAMETERS
1541
1542          .=240
1543 000240 000137 067264  JMP      0.ODT   ;ENTER ODT11
1544
1545          .SBTTL ACT11 HOOKS
1546
1547          ;*****
1548          ;HOOKS REQUIRED BY ACT11
1549          $SVPC=.          ;SAVE PC
1550          .=46
1551 000046 042772  $ENDAD          ;;1)SET LOC.46 TO ADDRESS OF $ENDAD IN .SEOP
1552          .=52
1553 000052 100000  .WORD 100000    ;;2)SET LOC.52 TO 100000
1554          .=$SVPC          ;; RESTORE PC
1555          .=1000
1556          .SBTTL APT PARAMETER BLOCK
1557
1558          ;*****
1559          ;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
1560          ;*****
1561          .SX=.          ;;SAVE CURRENT LOCATION
1562          .=24          ;;SET POWER FAIL TO POINT TO START OF PROGRAM
1563 000024 000200  200          ;;FOR APT START UP
1564          .=44          ;;POINT TO APT INDIRECT ADDRESS PNTR.
1565 000044 001000  $APTHDR      ;;POINT TO APT HEADER BLOCK
1566          .=$X          ;;RESET LOCATION COUNTER
1567          ;*****
1568          ;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
1569          ;INTERFACE SPEC.
1570
1571 001000  $APTHD:
1572 001000 000000  $HIPTS: .WORD 0          ;; TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
1573 001002 001210  $MADR:  .WORD $MAIL      ;; ADDRESS OF APT MAILBOX (BITS 0-15)
1574 001004 000430  $TSTM:  .WORD 280.      ;; RUN TIM OF LONGEST TEST
1575 001006 001130  $PASTM: .WORD 600.      ;; RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
1576 001010 001130  $UNITM: .WORD 600.      ;; ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
1577 001012 000042  .WORD $ETEND-$MAIL/2 ;;LENGTH MAILBOX-ETABLE(WORDS)
1578
1579
1580          .LIST MD
1581
1582

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1583 ;USE LOOP X TO OMIT SUBCLR
1584 ;
1585
1586 .MACRO LOOP A
1587 SCOP1
1588 MOV #STACK,SP ;RESTORE STK PTR
1589
1590 .IF B A
1591 JSR PC,SUBCLR
1592 ERROR 24 ;CERR AFTER SCLR
1593
1594 .ENDC
1595 .ENDM LOOP
1596
1597 ;
1598 ;THIS MACRO FILLS EXPECTED MSG A0, B0, A1, B1, A2, B2 & B3 WITH STANDARD BITS
1599 ;A=D.DSC AFTER ATTN OR 0 AFTER DRIVE CLEAR OR ANY IMPLIED SEEKS
1600 ;NOTE: A CAN BE ANY BIT COMBINATION DESIRED & INCLUDES D.DRA
1601 ;
1602 .MACRO F.EAB A
1603
1604 MOV #A!D.SPIN!D.DRDY!D.VV> E.A0 ;EXPECTED MSG A0
1605 CLR E.B0 ;EXPECTED MSG B0
1606 MOV #D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
1607 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
1608 CLR E.A2 ;EXPECTED MSG A2
1609 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
1610 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
1611 .ENDM F.EAB
1612
1613 ;
1614 ;THIS MACRO ASSUMES DRIVE MSG A0, B0, A1, B1 WILL ALWAYS BE TESTED
1615 ;USE A,C,D,E FOR MSG A0, B0, A1, B1 ERROR NUMBERS RESP.
1616 ;USE G=T.A2 TO READ MSG A2 & PUT INFO INTO 'CYLDIF'
1617 ; H=T.B2 TO READ MSG B2 & PUT INFO INTO 'CYLADD'
1618 ; I=T.B3 TO READ MSG B3 & PUT INFO INTO 'SECTOR' & 'HEADA'
1619
1620 ;USE F=<ERROR DESCRIPTION>
1621
1622 .MACRO CHECK A,C,D,E,F,G,H,I
1623
1624 JSR PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
1625 .WORD G!H!I ;& MSGS SPECIFIED HERE
1626 ERROR A ;MSG A0 ERROR F
1627 ERROR C ;MSG B0 ERROR
1628 ERROR D ;MSG A1 ERROR
1629 ERROR E ;MSG B1 ERROR
1630 .ENDM CHECK
1631
1632 ;
1633 ;A=CYL DIFF/OFFSET ERROR #
1634 ;B=CYL ADDR ERROR #
1635 ;C=<ERROR DESCRIPTION>
1636
1637 .MACRO CWD2 A,B,C,?D,?E
1638 MOV #2,RKMR1(R5)

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      JSB      PC,GSTAT
      TST      CYLDIF      ;SEE IF MSG A2=0
      BEQ      D           ;BR IF YES
      ERROR    A           ;MSG A2 NOT CLEARED C
D:     TST      CYLADD     ;SEE IF MSG B2=0
      BEQ      E           ;BR IF YES
      ERROR    B           ;MSG B2 NOT CLEARED C
E:
.ENDM      CWD2

.MACRO     STDER1      B
      F.EAB      <D.DRA!B>
      CHECK      165,166,167,170,<AFTER TIMEOUT>,0,0,0
.ENDM

.MACRO     STDER2      B
      F.EAB      <D.DRA!B>
      CHECK      211,212,213,214,<AFTER RELEASE ISSUED>,0,0,0
.ENDM

.MACRO     DRCLR      C,?A
      MOV      #CCLR,RKCS1(R5)
      MOV      $UNIT,RKCS2(R5) ;DRIVE#
      ADD      UNITB,RKCS2(R5) ;ADD 1 IF ON PORT B
      MOV      #CLEAR,HCS1
      JSR      PC,DOCMD      ;DO DRIVE CLEAR CMD & GET CONTR RDY
      ERROR    151          ;NO RDY AFTER DRIVE CLEAR CMD
      JSR      PC,TSTATN    ;TEST FOR ATTN
      BR       A
      ERROR    154          ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
A:
      .IF      B           C
      F.EAB      D.DRA
      CHECK      33,34,35,36,<AFTER DRV CLEAR CMD>,T.A2,T.B2,0
      .ENDC
      .ENDM      DRCLR

;USE CALIB      X      TO OMIT CHECKING MSGS A0, B0, A1, B1, A2 & B2
.MACRO     CALIB      A,?C
      MOV      #CCLR,RKCS1(R5)
      MOV      $UNIT,RKCS2(R5)
      ADD      UNITB,RKCS2(R5) ;ADD 1 IF ON PORT B
      MOV      #RECAL,HCS1
      JSR      PC,DOCMD      ;DO RECAL CMD & GET CONTR RDY
      ERROR    124          ;RDY NOT SET AFTER RECAL CMD

      MOV      #1,RKMR1(R5) ;SELECT WORD 1
      JSR      PC,GSTAT
      BIT      #D.RTZ,HMR2
    
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1695      BNE      C
1696      ERROR   70          ;RTZ NOT SET DURING RECAL CMD
1697      MOV     T10,TEMP2    ;SETUP TIMEOUT
1698      JSR     PC,FATT1      ;FIND ATTN
1699      ERROR   55          ;NO ATTN AFTER RECAL CMD
1700      .IF B
1701      A
1702      F.EAB   <D.DSC!D.DRA>
1703      CHECK  221,66,222,67,<AFTER RECAL CMD>,T.A2,T.B2,T.B3
1704      CWD2   47,50,<AFTER RECAL CMD>
1705      .ENDC
1706      DRCLR
1707      .ENDM   CALIB
1708
1709      ;
1710      ;QUICK START SPINDLE
1711      ;
1712      .MACRO  QKSRT
1713
1714      JSR     PC,SUBCLR
1715      ERROR   24          ;CERR AFTER SCLR
1716
1717      MOV     #SRTSPL,HCS1
1718      JSR     PC,DOCMD      ;DO START SPINDLE CMD & GET CONTR RDY
1719      ERROR   121         ;RDY NOT SET AFTER ST SPIN CMD.
1720
1721      MOV     T100,TEMP2    ;SETUP TIMEOUT
1722      JSR     PC,FATT1      ;FIND ATTN
1723      ERROR   74          ;NO ATTN AFTER ST SPIN CMD.
1724
1725      CLR     UNLD
1726
1727      .ENDM   QKSRT
1728
1729      ;
1730      ; A=WRHEAD/<CFMT!WRHEAD>
1731      ; USE WRHDR <A>,X TO OMIT CHECKING AO, BO, A1 & B1
1732      ;
1733      .MACRO  WRHDR  A,C,?D
1734
1735      MOV     #<A>,HCS1
1736      JSR     PC,DATCMD      ;DO DATA X FOR CMD & GET CONTR RDY
1737      ERROR   200         ;NO RDY AFTER WRITE HEADER CMD
1738      JSR     PC,GSTAT      ;GET FRESH STATUS
1739      BIT     #CERR,HCS1
1740      BEQ    D
1741      ERROR   201         ;CERR AFTER WRITE HEADER CMD
1742      TYPE   MSG21         ;ABORTING BALANCE OF TESTS
1743      JMP    $EOP
1744
1745      D:
1746      .IF B
1747      C
1748      F.EAB   D.DRA
1749      CHECK  37,40,41,42,<AFTER WRITE HEADER CMD>,0,0,0
1750      .ENDC
1751      .ENDM   WRHDR

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1751
1752
1753 ; A=RDHEAD/<CFMT!RDHEAD>
1754 ; USE RDHDR <A>,X TO OMIT CHECKING A0, B0, A1, B1
1755
1756 ; MACRO RDHDR A,C,?D,?E
1757
1758     MOV     #RHTAB,RO
1759     MOV     #<A>,HCS1
1760     JSR    PC, DATCMD           ; DO DATA X FOR CMD & GET CONTR RDY
1761     ERROR  171                 ; NO RDY AFTER READ HEADER CMD
1762     BIT     #CERR,HCS1
1763     BEQ    D
1764     ERROR  174                 ; CERR AFTER READ HEADER CMD
1765
1766 D:     MOV     RKDB(R5),(RO)+    ; 1'ST WORD FROM SILO TO RHTAB
1767     MOV     RKDB(R5),(RO)+    ; 2'ND WORD
1768     MOV     RKDB(R5),(RO)+    ; 3'RD WORD
1769
1770
1771     BIT     #DLT,RKCS2(R5)
1772     BEQ    E
1773     JSR    PC,GSTAT
1774     ERROR  173                 ; DLT AFTER READ HEADER CMD
1775
1776 E:
1777 .IF   B      C
1778     F.EAB  D.DRA
1779     CHECK 301,271,302,272,<AFTER READ HEADER CMD>,T.A2,T.B2,0
1780
1781 .ENDC
1782
1783 .ENDM RDHDR
1784
1785 ; MACRO HDCHK3 ?A
1786
1787     RDHDR  RDHEAD
1788     CMP    RHTAB,TOCYL       ; CHECK WORD 0 ONLY, CYL#
1789     BEQ    A                 ; BR IF SAME
1790     ERROR  51                 ; WRONG CYL# ON HEADER
1791
1792 A:
1793
1794 .ENDM HDCHK3
1795
1796 ; A=TOCYL/FRCYL , B=HEAD#, C = 0 FOR 22 SECTOR, 1 FOR 20 SECTOR
1797
1798 ; MACRO HDTBL A,B,C
1799
1800     MOV     A,CALADD         ; SETUP
1801     MOV     #B,HEAD         ; TO FILL
1802     MOV     #C,FORMAT       ; HEADER
1803     JSR    PC,FHDTAB        ; TABLE
1804
1805 .ENDM HDTBL
1806
;

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1807 ;QUICK SEEK. ENTER WITH CYL# IN RKDC
1808 ;
1809 .MACRO QKSEEK ?A
1810
1811 MOV #SEEK,HCS1
1812 JSR PC,DOCMD ;DO SEEK CMD & GET CONTR READY
1813 ERROR 131 ;NO RDY AFTER SEEK CMD
1814
1815 MOV T50000,TEMP1 ;SETUP TIMEOUT
1816 JSR PC,FATT2 ;FIND ATTN
1817 ERROR 132 ;NO ATTN AFTER SEEK CMD
1818
1819 BIT #CERR,HCS1
1820 BEQ A
1821 ERROR 210 ;CERR AFTER SEEK CMD
1822
1823 A: F.EAB <D.DSC!D.DRA>
1824 CHECK 161,162,163,164,<AFTER SEEK CMD>,0,0,0
1825
1826 .ENDM QKSEEK
1827
1828
1829 ;
1830 ;A=WRDATA/<CFMT!WRDATA>
1831 ;C=ADDR TO JMP TO ATTEMPT TO WRITE ON ANOTHER SECTOR
1832 ;D=ADDR TO JMP TO BYPASS TEST
1833 ;E: IF BLANK WILL CHECK A0, B0, A1 & B1 AT THE END OF WRITING
1834 ;E: IF NON BLANK WILL OMIT CHECKING A0 THRU B1
1835 ;
1836
1837 .MACRO WDATA A,C,D,E,?F,?G,?H,?I
1838
1839 MOV #<A>,HCS1
1840 JSR PC,DATCMD ;DO DATA X FOR CMD & GET CONTR RDY
1841 ERROR 11 ;NO RDY AFTER WRITE DATA CMD
1842 JSR PC,GSTAT ;GET FRESH STATUS
1843 BIT #CERR,HCS1
1844 BEQ I ;BR IF NO ERRORS
1845
1846 BIT #BSE,HER ;SEE IF BAD SECTOR FLAG
1847 BEQ G ;BR IF NO
1848 JSR PC,TRUERR ;ELSE SEE IF SECTOR LISTED IN BSE TABLE
1849 BR H ;RETURN HERE IF NO
1850
1851 INC SECTOR ;RETURN HERE IF YES
1852 CMP SECTOR,#10. ;ARE 10 CONSEC. SECTORS BAD
1853 BNE F ;BR IF NO
1854 ERROR 46 ;ABORTING TEST DETECTED 10 BAD SECTORS
1855 JMP D ;BYPASS TEST
1856 F: MOV #CCLR,RKCS1(R5) ;TRY ANOTHER SECTOR
1857 JMP C
1858 G: ERROR 12 ;CERR WITH WRITE DATA CMD
1859 F.EAB D.DRA
1860 CHECK 52,23,53,25,<AFTER WRITE DATA CMD>,T.A2,T.B2,0
1861 TYPE MSG21 ;ABORTING BALANCE OF TESTS
1862 JMP $EOP

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H: ERROR 43 ;BAD SECTOR NOT LISTED IN TABLE
I:
.IF B E
F.EAB D.DRA
CHECK 52,23,53,25,<AFTER WRITE DATA CMD>,T.A2,T.B2,0
.ENDC
.ENDM WDATA

;A=RDDATA/<CFMT!RDDATA>
;USE RDATA <A>,X TO OMIT CHECKING A0, B0, A1 & B1
;

.MACRO RDATA A,C,?D,?E,?F,?G,?H

MOV #<A>,HCS1
JSR PC,DATCMD ;DO DATA X FOR CMD & GET CONTR RDY
ERROR 13 ;NO RDY AFTER READ DATA CMD
JSR PC,GSTAT ;GET FRESH STATUS
BIT #CERR,HCS1
BEQ G
BIT #BSE,HER ;SEE IF BAD SECTOR
BEQ E
ERROR 65 ;DETECTED BSE IN READ BUT NOT IN WRITE CMD.
BR H

E: BIT #DCK,HER ;SEE IF DATA CHECK ERROR
BEQ F
ERROR 21 ;DATA CHECK ERROR AFTER READ CMD (ECC)
BR H

F: ERROR 14 ;CERR AFTER READ DATA CMD.

H: F.EAB D.DRA
CHECK 54,26,56,30,<AFTER READ DATA CMD>,T.A2,T.B2,0
TYPE MSG21 ;ABORTING BAL OF TESTS
JMP \$EOP

G:
.IF B C
F.EAB D.DRA
CHECK 54,26,56,30,<AFTER READ DATA CMD>,T.A2,T.B2,0
.ENDC
.ENDM RDATA

;A=WRTCHK/<CFMT!WRTCHK>
;C=EXPECTED DATA FOR TYPEOUT
;USE WRCHK <A>,DATA0,X TO OMIT CHECKING A0, B0, A1 & B1
;

.MACRO WRCHK A,C,D,?E,?F

MOV #<A>,HCS1
JSR PC,DATCMD ;DO DATA X FOR CMD & GET CONTR RDY

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1919          ERROR 15          ;NO RDY AFTER WRITE CHECK CMD
1920          JSR   PC,GSTAT    ;GET FRESH STATUS
1921          BIT   #CERR,HCS1
1922          BEQ   F
1923          BIT   #WCE,HCS2    ;SEE IF WRITE CHECK ERROR
1924          BEQ   E
1925          MOV   RKDB(R5),WD1  ;ACTUAL WORD FOR PRINTOUT
1926          MOV   C,WD2        ;EXPECTED WORD FOR TYPEOUT
1927          ERROR 16          ;WCE AFTER WRITE CMD
1928          BR    F
1929
1930 E:          ERROR 22          ;CERR AFTER WRITE CHECK CMD
1931          F.EAB D.DRA
1932          CHECK 57,31,60,32,<AFTER WRITE CHECK CMD>,T.A2,T.B2,0
1933          TYPE  #MSG21       ;ABORTING BALANCE OF TESTS
1934          JMP   $EOP
1935
1936 F:
1937 .IF        B           D
1938          F.EAB D.DRA
1939          CHECK 57,31,60,32,<AFTER WRITE CHECK CMD>,T.A2,T.B2,0
1940
1941 .ENDC
1942 .ENDM     WRCHK
1943
1944
1945 .MACRO    EOPGM
1946
1947          SCOPE
1948          CLR   $ESCAPE
1949          MOV   #1,$TIMES
1950          MOV   #STACK,$SP
1951          INC  $DEVCT        ;INCR COUNT FOR # DRIVES CHECKED
1952          CMP  DRIVS,$DEVCT  ;ARE ALL DRIVES PRESENT TESTED?
1953          BEQ  $EOP1+2      ;BR IF YES
1954          JMP  NUDRV        ;ELSE TEST NEXT DRIVE PRESENT
1955
1956 $EOP1:    SCOPE
1957 .ENDM     EOPGM
1958
1959 .MACRO    QKPACK ?A
1960          MOV   #CCLR,RKCS1(R5)
1961          MOV   $UNIT,RKCS2(R5)
1962          ADD  UNITB,RKCS2(R5)
1963          MOV   #PACK,HCS1
1964          JSR  PC,DOCMD      ;DO PACK CMD & GET CONTR RDY
1965          ERROR 116        ;CONTR NOT RDY AFT PACK CMD
1966
1967          BIT   #D.VV,HMR2
1968          BNE  A
1969          ERROR 27          ;VV NOT SET AFTER PACK CMD
1970
1971 A:        .ENDM     QKPACK
1972
1973 ;
1974

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:MACRO TO RELEASE PORT A OR B
:RLS PORT A:  RELEAS 0,1,A,B (A,B,C,D IF USED WITH MACROS BELOW)
:RLS PORT B:  RELEAS 1,C,B,A (B,A,D,C IF USED WITH MACROS BELOW)
:
.MACRO  RELEAS  W,X,Y,Z,?J
        MOV     $UNIT,RKCS2(R5) ;SETUP FOR PORT Y
        MOV     #W,UNITB
        ADD     UNITB,RKCS2(R5)
        MOVB    #'Y,MSG19A
        ADD     #RLS,RKCS2(R5) ;RELEASE PORT Y
        MOV     #SELD, HCS1
        JSR     PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
        ERROR   117 ;NO RDY AFTER SEL DRV CMD

        MOV     $UNIT,RKCS2(R5) ;SETUP FOR PORT Z
        MOV     #X,UNITB
        ADD     UNITB,RKCS2(R5)
        MOVB    #'Z,MSG19A
        MOV     #SELD, HCS1
        JSR     PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
        ERROR   117 ;NO RKY AFTER SEL DRV CMD

        BIT     #D.DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT Z
        BNE     J ;BR IF YES
        ERROR   71 ;PORT Z NOT AVAIL AFTER PORT Y RLS

J:
.ENDM  RELEAS

.MACRO  SETP   A,C
        MOV     #A,UNITB ;SETUP PORT C
        MOVB    #'C,MSG19A
        MOV     TIMER,COUNT
        JSR     PC,TMO ;DO TIMEOUT

        JSR     PC,SUBCLR
        ERROR   24 ;CERR AFTER SCLR
    
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        JSR      PC,DRAV          ;SEE IF DRIVE AVAIL
        ERROR   45                ;PORT C NOT AVAIL AFTER TMO
    .ENDM     SETP

    .MACRO    REPTST
    ;*
    ;*      THE PREVIOUS TEST IS REPEATED FOR PORT 'B'.
    ;*
    .ENDM     REPTST

    .MACRO    REPTA
    ;*
    ;*      THE PREVIOUS TEST IS REPEATED FOR PORT 'B',
    ;*      BUT THE SEEK IS TO CYLINDER 0
    ;*
    .ENDM     REPTA

    .MACRO    HEADER
    ;A & B=0 FOR PORT A OR 1 FOR PORT B
    ;C & D=A FOR PORT A OR B FOR PORT B
    .ENDM     HEADER

    .MACRO    SUNIT
    MOV      $UNIT,RKCS2(R5)
    ADD     UNITB,RKCS2(R5)
    .ENDM     SUNIT

    ;A & B=0 FOR PORT A OR 1 FOR PORT B
    ;C & D=A FOR PORT A OR B FOR PORT B

    .MACRO    TST5      A,B,C,D,E
    SETP     A,C
    MOV      #5$, $ESCAPE
    F.EAB    D.DRA
    CHECK    77,100,101,102,<AFTER TIMEOUT>,0,0,0
    MOV      #B,UNITB          ;SELECT PORT D BEFORE TIMEOUT OR RELEASE
    MOV      #'D,MSG19A        ;SETUP ERROR MSG FOR PORT D
    CLC
    CLR      $ESCAPE
    JSR      PC,DRAV          ;SEE IF DRIVE AVAIL
    BR       1$              ;BR IF NOT AVAIL
    ERROR   103              ;PORT D AVAIL BEFORE TMO OR RELEASE

1$:      BIT      #CERR,HCS1
    BNE     6$
    ERROR   130              ;CERR NOT SET AFTER SEL DRIVE & DRIVE NOT AVAIL
6$:      MOV      #<D.SPIN!D.VV>,E.A0
    MOV      E.A0,E.A1        ;MSG A & B SHOULD ALWAYS RETURN SAME
    BIS     E.DD†,E.A1
    CLR     E.B0              ;WORD 0 FOR PORT D
    CLR     E.B1
    MOV      #5$, $ESCAPE
    CHECK    104,105,106,107,<WHILE PORT D UNAVAILABLE>,0,0,0
    
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	TST	\$PASS	
	BEQ	8\$;BR IF FIRST PASS
	JMP	5\$;ELSE EXIT TEST
8\$:	CLR	\$ESCAPE	
	MOV	#CCLR,RKCS1(R5)	
	MOV	#A,UNITB	;SETUP FOR PORT C AGAIN
	MOVB	#'C,MSG19A	
	MOV	#360,COUNT	;SETUP 4 SEC TIMEOUT
	JSR	PC,CLKON	;TURN ON CLOCK
	JSR	PC,DRAV	
	ERROR	45	;PORT C NOT AVAIL AFTER TIMEOUT
	MOV	#B,UNITB	;SELECT PORT D BEFORE TIMEOUT OR RELEASE
	MOVB	#'D,MSG19A	
	JSR	PC,DRAV	;SEE IF PORT D DRIVE AVAIL
	BR	3\$;BR IF NOT AVAIL
	ERROR	103	;PORT D AVAIL BEFORE TMO OR RELEASE
3\$:	MOV	#CCLR,RKCS1(R5)	
	MOV	\$UNIT,R4	
	ADD	UNITB,R4	
	JSR	PC,FATT3	
	ERROR	110	;NO ATTN ON PORT D TO ALLOW SEIZE
	JSR	PC,CLKOF	
	JSR	PC,DRAV	;SEE IF PORT D DRIVE AVAIL
	ERROR	45	;PORT D NOT AVAIL
	MOV	#5\$, \$ESCAPE	
	F.EAB	<D.OSC!D.DRA>	
	CHECK	77,100,101,102,<AFTER TIMEOUT>,0,0,0	
	CLR	\$ESCAPE	
	MOV	#A,UNITB	;SETUP FOR PORT C
	MOVB	#'C,MSG19A	
	JSR	PC,TSTATN	;TEST FOR ATTN ON PORT C
	BR	4\$	
	ERROR	111	;PORT C ATTN SET W/O REQUEST PENDING
4\$:	MOV	#5\$, \$ESCAPE	
	MOV	#B,UNITB	;SETUP FOR PORT D
	MOVB	#'D,MSG19A	
	DRCLR		
	MOV	#360,R1	
	SUB	COUNT,R1	;R1-COUNT=R1
	MULT	#17.,R1	;MULT BY 16.66 MS
	TYPE	E	;PORT TIMEOUT
	MOV	R1,-(SP)	;PUSH BINARY ONTO STACK
	JSR	PC,\$S2D	;CONVERT TO ASCII
	JSR	PC,\$SUPRS	;TYPE IT
	TYPE	,MSG22	;MS
5\$:	CLR	\$ESCAPE	
	JSR	PC,CLKOF	

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.ENDM TST5

;A & B=0 FOR PORT A OR 1 FOR PORT B
;C & D=A FOR PORT A OR B FOR PORT B
;
;MACRO TST10 A,B,C,D,E,F,?G
;SETP A,C
;MOV #F,RKDC(R5) ;SEEK TO CYL F
;QKSEEK
;JSR PC,RDCYLA ;READ CYL ADDR
;CMP CYLADD,#F ;SEE IF CYL F
;BEQ G ;BR IF YES
;MOV #E,FRCYL
;MOV #F,TOCYL
;MOV #F,CALDIF
;MOV #F,RKDC(R5) ;REFRESH RKDC
;ERROR 224 ;DID NOT SEEK TO CYL F

G:
;MOV #150,COUNT
;JSR PC,TMO ;DO 2.5 SEC TIMEOUT
;JSR PC,FATT2
;ERROR 112 ;ATTN CLEARED AFTER TMO
;DRCLR
;JSR PC,DRAV
;ERROR 113 ;PORT C NOT AVAIL AFTER DRIVE CLEAR CMD

.ENDM TST10

;A & B=0 FOR PORT A OR 1 FOR PORT B
;C & D=A FOR PORT A OR B FOR PORT B
;
;MACRO TST12 A,B,C,D
;SETP A,C
;QKSEEK
;MOV TIMER,COUNT
;JSR PC,TMO ;DO 1.5 SEC TIMEOUT
;MOV #B,UNITB ;SETUP PORT D
;MOVB #'D,MSG19A
;JSR PC,DRAV
;ERROR 45 ;PORT D NOT AVAIL AFTER TMO

;STDER1 D
;QKSEEK
;MOV TIMER,COUNT
;JSR PC,TMO
;MOV #A,UNITB ;SETUP PORTC
;MOVB #'C,MSG19A
;JSR PC,DRAV
;ERROR 45 ;PORT C NOT AVAIL AFTER TMO

;STDER1 D.DSC
;DRCLR
;MOV TIMER,COUNT
;JSR PC,TMO ;DO 1.5 SEC TIMEOUT
;MOV #B,UNITB ;SETUP PORT D

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2182          MOVB    #'D,MSG19A
2183          JSR     PC,TSTATN
2184          ERROR   114          ;ATTN RESET ON PORT D AFTER DR CLR CMD
2185                                     ;ON PORT C
2186
2187          STDER1  D.DSC
2188          .ENDM   TST12
2189
2190          ;A & B=0 FOR PORT A OR 1 FOR PORT B
2191          ;C & D=A FOR PORT A OR B FOR PORT B
2192
2193          .MACRO  TST14  A,B,C,D
2194          SETP   A,C
2195          RELEAS A,B,C,D
2196          JSR   PC,TSTATN
2197          BR    1$
2198          ERROR 115          ;ATTN SET IN PORT D AFTER RLS OF PORT C
2199
2200          1$:     F.EAB  D.DRA
2201          CHECK  133,134,135,136,<AFTER RELEASED TO PORT D>,0,0,0
2202
2203          MOV    #A,UNITB          ;ADDRESS PORT C
2204          MOVB  #'C,MSG19A
2205          JSR   PC,TSTATN
2206          SKIP  R,<GOTO NEXT TST>
2207          ERROR 115          ;ATTN SET AFTER RLS ISSUED
2208          STDER2 D.DRA
2209          .ENDM   TST14
2210
2211          ;A & B=0 FOR PORT A OR 1 FOR PORT B
2212          ;C & D=A FOR PORT A OR B FOR PORT B
2213
2214          .MACRO  TST16  A,B,C,D
2215          RELEAS B,A,D,C
2216          STDER2 D.DRA
2217          JSR   PC,SUBCLR
2218          ERROR 24          ;CERR AFTER SCLR
2219          QKSEEK
2220
2221          MOV    #B,UNITB          ;SETUP PORT D
2222          MOVB  #'D,MSG19A
2223          JSR   PC,DRAV          ;SEE IF DRIVE AVAIL
2224          BR    1$
2225          ERROR 103          ;PORT D AVAIL BEFORE TMO OR RELEASE
2226
2227          1$:     BIT    #CERR,HCS1
2228          BNE   2$
2229          ERROR 130          ;CERR NOT SET AFTER SEL DRIVE CMD
2230                                     ;& NO DRA
2231
2232          2$:     JSR   PC,TSTATN
2233          BR    3$
2234          ERROR 115          ;ATTN SET IN PORT D AFTER RLS FROM PORT C
2235
2236          3$:     MOV    #CCLR,RKCS1(R5)
2237          RELEAS A,B,C,D

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JSR PC,TSTATN
ERROR 122 ;NO ATTN IN PORT D AFTER RLS FROM PORT C

STDER2 <D,DSC!D,DRA>
RELEAS B,A,D,C
JSR PC,TSTATN
ERROR 123 ;ATTN CLEARED IN PORT C AFT RLS FROM PORT D

STDER2 <D,DSC!D,DRA>
RELEAS A,B,C,D
DRCLR
MOV TIMER,COUNT
JSR PC,TMO ;DO 1.5 SEC TIMEOUT ON PORT D
JSR PC,TSTATN
SKIP R,<GOTO NEXT TST>
ERROR 144 ;MULT ATTN ON PORT D

.ENDM TST16

;A & B=0 FOR PORT A OR 1 FOR PORT B
;C & D=A FOR PORT A OR B FOR PORT B

.MACRO TST20 A,B,C,D
RELEAS B,A,D,C
STDER2 D,DRA
JSR PC,SUBCLR
ERROR 24 ;CERR AFTER SCLR

MOV #B,UNITB ;SETUP PORT D
MOVB #'D,MSG19A
JSR PC,DRAV ;SEE IF DRIVE AVAIL
BR 1$
ERROR 103 ;PORT D AVAIL BEFORE TMO OR RELEASE

1$: BIT #CERR,HCS1
BNE 2$
ERROR 130 ;CERR NOT SET BY NO DRA

2$: JSR PC,TSTATN
BR 3$
ERROR 115 ;ATTN SET IN PORT D AFTER RLS FROM PORT C

3$: MOV #CCLR,RKCS1(R5)
RELEAS B,A,D,C
RELEAS A,B,C,D
MOV TIMER,COUNT
JSR PC,TMO ;DO 1.5 SEC TIMEOUT ON PORT D
JSR PC,TSTATN
BR 4$
ERROR 115 ;ATTN SET ON PORT D AFTER RLS FROM PORT D

4$: STDER2 0
MOV #A,UNITB ;SETUP PORT C
MOVB #'C,MSG19A
JSR PC,TSTATN

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```
SKIP R,<GOTO NEXT TST>  
ERROR 115 ;ATTN SET ON PORT C, AFTER RLS FROM PORT C  
.ENDM TST20
```

```
;A & B=0 FOR PORT A OR 1 FOR PORT B  
;C & D=A FOR PORT A OR B FOR PORT B
```

```
.MACRO TST22 A,B,C,D  
SETP A,C  
RELEAS B,A,D,C  
STDER2 D.DRA  
.ENDM TST22
```

```
;A & B=0 FOR PORT A OR 1 FOR PORT B  
;C & D=A FOR PORT A OR B FOR PORT B
```

```
.MACRO TST24 A,B,C,D,E  
RELEAS B,A,D,C  
JSR PC,SUBCLR  
ERROR 24 ;CERR AFTER SCLR
```

```
MOV #E,RKDC(R5)  
MOV #SEEK,HCS1  
JSR PC,DOCMD ;DO SEEK CMD & GET CONTR READY  
ERROR 131 ;NO RDY AFTER SEEK CMD  
RELEAS A,B,C,D
```

```
MOV T50000,TEMP1  
JSR PC,FATT2  
ERROR 152 ;NO ATTN ON PORT D AFTER SEEK & RLS FROM PORT C
```

```
F.EAB <D.DRA!D.DSC>  
CHECK 145,146,147,150,<AFTER SEEK & RLS FROM PORT C>,0,0,0  
RELEAS B,A,D,C  
JSR PC,TSTATN  
BR 15  
ERROR 160 ;ATTN ON PORT C AFTER SEEK & RLS FROM PORT C
```

```
15: F.EAB D.DRA  
CHECK 145,146,147,150,<AFTER SEEK & RLS FROM PORT C>,0,0,0  
.ENDM TST24
```

```
;A & B=0 FOR PORT A OR 1 FOR PORT B  
;C & D=A FOR PORT A OR B FOR PORT B
```

```
.MACRO TST26 A,B,C,D  
JSR PC,SUBCLR  
ERROR 24 ;CERR AFTER SCLR
```

```
RELEAS B,A,D,C  
JSR PC,SUBCLR  
ERROR 24 ;CERR AFTER SCLR
```

```
MOV #30.,COUNT
```

```
2350 JSR PC,TMO ;DO 500MS TIMEOUT
2351 JSR PC,DRAV ;RE-SEIZE DRIVE THRU PORT C
2352 ERROR 45 ;PORT C NOT AVAIL AFTER TIMEOUT
2353 STDER1 0
2354 MOV #-1,COUNT
2355 JSR PC,CLKON ;TURN ON CLOCK
2356 MOV #B,UNITB ;SETUP PORT D
2357 JSR PC,DRAV ;SEE IF DRV AVAIL
2358 BR 1$ ;BR IF NO
2359 ERROR 103 ;PORT C AVAIL BEFORE TMO OR RLS
2360 1$: MOVB #'D,MSG19A
2361 MOV $UNIT,R4
2362 ADD UNITB,R4
2363 JSR PC,FATT3
2364 ERROR 110 ;NO ATTN ON PORT D TO ALLOW SEIZE
2365
2366 JSR PC,CLKOF ;TURN CLOCK OFF
2367 COM COUNT ;GET ACTUAL COUNT OF TIMEOUT
2368 CMP COUNT,#35. ;COMPARE COUNT AGAINST APPROX 1 SEC
2369 SKIP GE,<GO TO NEXT TEST>
2370 ERROR 153 ;TIMEOUT DID NOT RE-TRIGGER
2371 .ENDM TST26
2372
2373
2374 ;A & B=0 FOR PORT A OR 1 FOR PORT B
2375 ;C & D=A FOR PORT A OR B FOR PORT B
2376
2377 .MACRO TST30 A,B,C,D
2378 JSR PC,SUBCLR ;CERR AFTER SCLR
2379 ERROR 24
2380 RELEAS B,A,D,C
2381 JSR PC,SUBCLR ;CERR AFTER SCLR
2382 ERROR 24
2383
2384 MOV #B,UNITB ;SETUP FOR PORT D
2385 MOVB #'D,MSG19A
2386 JSR PC,DRAV ;PORT D TRIES TO SEIZE THE DRIVE
2387 BR 1$ ;BR IF NOT AVAIL
2388 ERROR 103 ;PORT D AVAIL BEFORE TMO OR RELEASE
2389
2390 1$: MOV #CLR,RKCS1(R5)
2391 RELEAS A,B,C,D
2392 MOV #A,UNITB ;SETUP FOR PORT C
2393 MOVB #'C,MSG19A
2394 JSR PC,DRAV
2395 BR 2$
2396 ERROR 103 ;PORT C AVAIL BEFORE TMO OR RELEASE
2397
2398 2$: MOV #CLR,RKCS1(R5)
2399 MOV #-1,COUNT
2400 JSR PC,CLKON
2401 MOV $UNIT,R4
2402 ADD UNITB,R4
2403 JSR PC,FATT3
2404 ERROR 110 ;NO ATTN ON PORT C TO ALLOW SEIZE
2405
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```

JSR PC,CLKOF ;TURN CLOCK OFF
COM COUNT ;GET ACTUAL COUNT OF TIMEOUT
CMP COUNT,#35. ;COMPARE AGAINST APPROX 1 SEC
SKIP GE,<GO TO NEXT TST>
ERROR 153 ;TIMEOUT DID NOT RE-TRIGGER
.ENDM TST30

```

```

;A & B=0 FOR PORT A OR 1 FOR PORT B
;C & D=A FOR PORT A OR B FOR PORT B

```

```

.MACRO TST32 A,B,C,D
RELEAS B,A,D,C
JSR PC,SUBCLR ;CERR AFTER SCLR
ERROR 24

INC UNLD ;USED FOR VALID HALT
MOV #2$,SESCAPE ;UNLOAD CMD
MOV #UNLOAD,HCS1
BIS $TMP4,HCS1
MOV HCS1,RKCS1(R5)
MOV T10,TEMP1 ;SETUP TIMEOUT
JSR PC,FATT2 ;FIND ATTN
ERROR 73 ;NO ATTN AFTER UNLD CMD

MOV #300,COUNT
JSR PC,TM0 ;DO 5 SEC DELAY
MOV #B,UNITB ;SETUP FOR PORT D
MOVB #'D,MSG19A
JSR PC,DRAW
BR 1$
ERROR 155 ;PORT D AVAIL BEFORE RLS WHEN UNLOADED
;UNLOAD DID NOT INHIBIT TIMERS

```

```
1$: MOV #CCLR,RKCS1(R5)
```

```
2$: CLR $SESCAPE
QKSRT
CLR UNLD
```

```
.ENDM TST32
```

```

;A & B=0 FOR PORT A OR 1 FOR PORT B
;C & D=A FOR PORT A OR B FOR PORT B

```

```

.MACRO TST32A A,B,C,D
RELEAS B,A,D,C
JSR PC,SUBCLR ;CERR AFTER SCLR
ERROR 24

MOV #5$,SESCAPE ;SEEK TO LAST CYL
MOV LC,RKDC(R5)
QKSEEK
DRCLR

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```

MOV #RECAL,HCS1 ;RECAL COMMAND
BIS $TMP4,HCS1
MOV HCS1,RKCS1(R5)
MOV $UNIT,R4
ADD UNITB,R4
1$: BITB ATTN(R4),RKASOF+1(R5) ;SEE IF ATTN SET
    BNE 3$ ;BR IF YES

MOV #B,UNITB ;SETUP FOR PORT D
MOVB #'D,MSG19A
JSR PC,DRVAV ;SEE IF DRV AVAIL
BR 2$ ;RETURN HERE IF NO
ERROR 177 ;PORT D AVAIL
;RECAL DID NOT INHIBIT TIMERS

2$: MOV #CLR,RKCS1(R5)
MOV #A,UNITB
MOVB #'C,MSG19A ;SETUP PORT C
JSR PC,DRVAV ;SEE IF DRV AVAIL
ERROR 203 ;PORT C NOT REMAIN AVAIL DURING RECAL
BR 1$

3$: JSR PC,GSTAT
BIT #D.PIP,HMR2 ;SEE IF ANY MOTION
BEQ 4$ ;BR IF NO
ERROR 72 ;PIP SET AFTER ATTN REC'D FROM RECAL

4$: MOV #120,COUNT
JSR PC,TMO ;DO 2 SEC DLY
MOV #B,UNITB ;SETUP FOR PORT D
MOVB #'D,MSG19A
JSR PC,DRVAV ;SEE IF DRV NOW AVAIL
ERROR 45 ;PORT D NOT AVAIL AFTER TMO

5$: CLR $ESCAPE
.ENDM $ST32A

.NLIST MD

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001100 000000
001102 000
001103 000
001104 000000
001106 000000
001110 000000
001112 000000
001114 000
001115 001
001116 000000
001120 000000
001122 000000
001124 000000
001126 000000
001130 000000
001132 000000
001134 000
001135 000
001136 000000
001140 177570
001142 177570
001144 177560
001146 177562
001150 177564
001152 177566
001154 000
001155 002
001156 012
001157 000
001160 000000
001162 000000
001164 000000
001166 000000
001170 000000
001172 000000
001174 000000
001176 000000
001200 177607 000377
001204 077
001205 015
001206 000012

001210
001210 000000

.SBTTL COMMON TAGS

*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
*USED IN THE PROGRAM.

.=1100

\$CMTAG: .WORD 0 ;: START OF COMMON TAGS
\$TSTNM: .BYTE 0 ;: CONTAINS THE TEST NUMBER
\$ERFLG: .BYTE 0 ;: CONTAINS ERROR FLAG
\$ICNT: .WORD 0 ;: CONTAINS SUBTEST ITERATION COUNT
\$LPADR: .WORD 0 ;: CONTAINS SCOPE LOOP ADDRESS
\$LPERR: .WORD 0 ;: CONTAINS SCOPE RETURN FOR ERRORS
\$ERTTL: .WORD 0 ;: CONTAINS TOTAL ERRORS DETECTED
\$ITEMB: .BYTE 0 ;: CONTAINS ITEM CONTROL BYTE
\$ERMAX: .BYTE 1 ;: CONTAINS MAX. ERRORS PER TEST
\$ERRPC: .WORD 0 ;: CONTAINS PC OF LAST ERROR INSTRUCTION
\$GDADR: .WORD 0 ;: CONTAINS ADDRESS OF 'GOOD' DATA
\$BDADR: .WORD 0 ;: CONTAINS ADDRESS OF 'BAD' DATA
\$GDDAT: .WORD 0 ;: CONTAINS 'GOOD' DATA
\$BDDAT: .WORD 0 ;: CONTAINS 'BAD' DATA
 .WORD 0 ;: RESERVED--NOT TO BE USED
\$AUTOB: .BYTE 0 ;: AUTOMATIC MODE INDICATOR
\$INTAG: .BYTE 0 ;: INTERRUPT MODE INDICATOR
\$SWR: .WORD 0 DSWR ;: ADDRESS OF SWITCH REGISTER
\$DISPLAY: .WORD 0 DDISP ;: ADDRESS OF DISPLAY REGISTER
\$TKS: 177560 ;: TTY KBD STATUS
\$TKB: 177562 ;: TTY KBD BUFFER
\$TPS: 177564 ;: TTY PRINTER STATUS REG. ADDRESS
\$TPB: 177566 ;: TTY PRINTER BUFFER REG. ADDRESS
\$NULL: .BYTE 0 ;: CONTAINS NULL CHARACTER FOR FILLS
\$FILLS: .BYTE 2 ;: CONTAINS # OF FILLER CHARACTERS REQUIRED
\$FILLC: .BYTE 12 ;: INSERT FILL CHARS. AFTER A "LINE FEED"
\$STPFLG: .BYTE 0 ;: "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
\$TMP0: .WORD 0 ;: USER DEFINED
\$TMP1: .WORD 0 ;: USER DEFINED
\$TMP2: .WORD 0 ;: USER DEFINED
\$TMP3: .WORD 0 ;: USER DEFINED
\$TMP4: .WORD 0 ;: USER DEFINED
\$TMP5: .WORD 0 ;: USER DEFINED
\$TIMES: 0 ;: MAX. NUMBER OF ITERATIONS
\$ESCAPE: 0 ;: ESCAPE ON ERROR ADDRESS
\$BELL: .ASCIZ <207><377><377> ;: CODE FOR BELL
\$QUES: .ASCII /?/ ;: QUESTION MARK
\$CRLF: .ASCII <15> ;: CARRIAGE RETURN
\$LF: .ASCIZ <12> ;: LINE FEED

.SBTTL APT MAILBOX-ETABLE

\$EVEN
\$MAIL: ;: APT MAILBOX
\$MSGTY: .WORD AMSGTY ;: MESSAGE TYPE CODE

2556	001212	000000	\$FATAL: .WORD	AFATAL	:: FATAL ERROR NUMBER
2557	001214	000000	\$TESTN: .WORD	ATESTN	:: TEST NUMBER
2558	001216	000000	\$PASS: .WORD	APASS	:: PASS COUNT
2559	001220	000000	\$DEVCT: .WORD	ADEVCT	:: DEVICE COUNT
2560	001222	000000	\$UNIT: .WORD	AUNIT	:: I/O UNIT NUMBER
2561	001224	000000	\$MSGAD: .WORD	AMSGAD	:: MESSAGE ADDRESS
2562	001226	000000	\$MSGLG: .WORD	AMSLG	:: MESSAGE LENGTH
2563	001230		\$ETABLE:		:: APT ENVIRONMENT TABLE
2564	001230	000	\$ENV: .BYTE	AENV	:: ENVIRONMENT BYTE
2565	001231	000	\$ENVM: .BYTE	AENVM	:: ENVIRONMENT MODE BITS
2566	001232	000000	\$SWREG: .WORD	ASWREG	:: APT SWITCH REGISTER
2567	001234	000000	\$USWR: .WORD	AUSWR	:: USER SWITCHES
2568	001236	000000	\$CPUOP: .WORD	ACPUOP	:: CPU TYPE, OPTIONS
2569			*		BITS 15-11=CPU TYPE
2570			*		11/04=01, 11/05=02, 11/20=03, 11/40=04, 11/45=05
2571			*		11/70=06, PDQ=07, Q=10
2572			*		BIT 10=REAL TIME CLOCK
2573			*		BIT 9=FLOATING POINT PROCESSOR
2574			*		BIT 8=MEMORY MANAGEMENT
2575	001240	000	\$MAMS1: .BYTE	AMAMS1	:: HIGH ADDRESS, M.S. BYTE
2576	001241	000	\$MTYP1: .BYTE	AMTYP1	:: MEM. TYPE BLK#1
2577			*		MEM. TYPE BYTE -- (HIGH BYTE)
2578			*		900 NSEC CORE=001
2579			*		300 NSEC BIPOLAR=002
2580			*		500 NSEC MOS=003
2581	001242	000000	\$MADR1: .WORD	AMADR1	:: HIGH ADDRESS, BLK#1
2582			*		MEM. LAST ADDR.=3 BYTES, THIS WORD AND LOW OF "TYPE" ABOVE
2583	001244	000	\$MAMS2: .BYTE	AMAMS2	:: HIGH ADDRESS, M.S. BYTE
2584	001245	000	\$MTYP2: .BYTE	AMTYP2	:: MEM. TYPE, BLK#2
2585	001246	000000	\$MADR2: .WORD	AMADR2	:: MEM. LAST ADDRESS, BLK#2
2586	001250	000	\$MAMS3: .BYTE	AMAMS3	:: HIGH ADDRESS, M.S. BYTE
2587	001251	000	\$MTYP3: .BYTE	AMTYP3	:: MEM. TYPE, BLK#3
2588	001252	000000	\$MADR3: .WORD	AMADR3	:: MEM. LAST ADDRESS, BLK#3
2589	001254	000	\$MAMS4: .BYTE	AMAMS4	:: HIGH ADDRESS, M.S. BYTE
2590	001255	000	\$MTYP4: .BYTE	AMTYP4	:: MEM. TYPE, BLK#4
2591	001256	000000	\$MADR4: .WORD	AMADR4	:: MEM. LAST ADDRESS, BLK#4
2592	001260	000000	\$VECT1: .WORD	AVECT1	:: INTERRUPT VECTOR#1, BUS PRIORITY#1
2593	001262	000000	\$VECT2: .WORD	AVECT2	:: INTERRUPT VECTOR#2, BUS PRIORITY#2
2594	001264	177440	\$BASE: .WORD	ABASE	:: BASE ADDRESS OF EQUIPMENT UNDER TEST
2595	001266	000000	\$DEVN: .WORD	ADEVN	:: DEVICE MAP
2596	001270	000000	\$CDW1: .WORD	ACDW1	:: CONTROLLER DESCRIPTION WORD#1
2597	001272	000000	\$CDW2: .WORD	ACDW2	:: CONTROLLER DESCRIPTION WORD#2
2598	001274	000000	\$DDW0: .WORD	ADDW0	:: DEVICE DESCRIPTOR WORD#0
2599	001276	000000	\$DDW1: .WORD	ADDW1	:: DEVICE DESCRIPTOR WORD#1
2600	001300	000000	\$DDW2: .WORD	ADDW2	:: DEVICE DESCRIPTOR WORD#2
2601	001302	000000	\$DDW3: .WORD	ADDW3	:: DEVICE DESCRIPTOR WORD#3
2602	001304	000000	\$DDW4: .WORD	ADDW4	:: DEVICE DESCRIPTOR WORD#4
2603	001306	000000	\$DDW5: .WORD	ADDW5	:: DEVICE DESCRIPTOR WORD#5
2604	001310	000000	\$DDW6: .WORD	ADDW6	:: DEVICE DESCRIPTOR WORD#6
2605	001312	000000	\$DDW7: .WORD	ADDW7	:: DEVICE DESCRIPTOR WORD#7
2606	001314		\$ETEND:		
2607			.MEXIT		
2608		177440	ABASE=	177440	:: DEFAULT BUSS ADDRESS
2609	001314	000210	RKVEC:	210	:: DEFAULT CONTROLLER INTERRUPT VECTOR
2610	001316	000240	RKPRI:	PR5	:: PRIORITY
2611	001320	172540	PKS:	172540	:: P-CLOCK STATUS REG

2612	001322	172542	PKSB:	172542	;P-CLOCK SET BUFFER
2613	001324	172544	PKRB:	172544	;P-CLOCK READ BUFFER
2614	001326	177546	LKS:	177546	;L-CLOCK STATUS REG.
2615					
2616	001330	000100	LCVEC:	100	;L-CLOCK INTERRUPT VECTOR
2617	001332	000104	PCVEC:	104	;P-CLOCK INTERRUPT VECTOR.
2618					
2619		000114	MEMVEC=	114	;MEMORY PARITY VECTOR
2620		172100	MEMBAS=	172100	;MEMORY PARITY OPTION CSR START ADDR
2621	001334	000000	TRAPPC:	0	;PC FOR MEM CHECK ENABLE TRAP
2622					
2623	001336	000000	PARAM:	0	;1 FOR 220 START, NO DEFAULT
2624	001340	000000	FTITLE:	0	;FLAG FOR PRINTING OUT 1ST PROGRAM TITLE
2625					
2626	001342	000000	DRVPTR:	0	;CONTAINS THE POINTER TO THE DRIVE FLAG
2627					; (DRIV0-DRIV7) OF THE DRIVE TO BE CHECKED NEXT.
2628		000040	SPBAR=	40	;SPACEBAR
2629	001344	000000	FRCYL:	0	;FROM CYLINDER
2630	001346	000000	TOCYL:	0	;TO CYLINDER
2631	001350	000000	CCYL:	0	;CURRENT CYL. USED IN N SQUARE TEST
2632	001352	000000	PCYL:	0	;PREV CYL. USED IN N SQUARE TEST
2633	001354	000000	CALDIF:	0	;CALC CYL DIFF USED IN N SQUARE TEST
2634	001356	000000	CYLDIF:	0	;CYL DIFF, RIGHT JUSTIFIED FROM RKMR3
2635	001360	000000	CYLADD:	0	;CYL ADDR, RIGHT JUSTIFIED FROM RKMR3
2636	001362	000000	CALADD:	0	;CYL ADDR USED IN FHDTAB ROUTINE
2637					
2638	001364	000074	HZ:	60.	;60 FOR 60 CPS
2639					;50 FOR 50 CPS
2640	001366	000000	COUNT:	0	;LOADED TO 50 OR 60 TO COUNT TO 1 SEC
2641					;OR ANY OTHER NUMBER TO COUNT OFF FRACTIONAL SECOND
2642	001370	000000	SEC:	0	;SECOND COUNTER
2643	001372	000000	TIMUP:	0	;FLAG TO INDICATE TIME IS UP
2644	001374	000000	SECTOR:	0	;SECTOR COUNT, RIGHT JUSTIFIED FROM RKMR3
2645					
2646	001376	000001	T1:	1	;TIMEOUT CONSTANTS
2647	001400	000012	T10:	10.	
2648	001402	000062	T50:	50.	
2649	001404	000764	T500:	500.	
2650	001406	010000	T100:	10000	
2651	001410	011610	T5000:	5000.	
2652	001412	141520	T50000:	50000.	
2653					
2654					
2655	001414	000000	WD1:	0	;ACTUAL HEADER/DATA WORD
2656	001416	000000	WD2:	0	;EXPECTED DATA WORD
2657	001420	000000	HEAD:	0	;HEAD NUMBER
2658	001422	000000	HEAD#:	0	;HEAD # FROM H.B3, RT. JUSTIFIED
2659	001424	000000	HD1:	0	;SHIFTED HEAD# FOR FORMATTER ROUTINE
2660	001426	000000	FORMAT:	0	;FORMAT TYPE
2661	001430	000000	FMT1:	0	;SHIFTED FORMAT FOR FORMATTER ROUTINE
2662	001432	000000	WDCNT:	0	;WORD COUNT
2663					
2664	001434	000000	DATA0:	0	;ALL 0'S
2665	001436	052525	DATA01:	52525	;0101 PATT
2666	001440	177777	DATA1:	177777	;ALL 1'S
2667	001442	133467	DPAT1:	133467	

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2668 001444 070627          DPAT2: 70627
2669
2670 001446 000000          WORD: 0          ;HEADER/DATA WORD
2671 001450 000000          HDWD: 0          ;HEADER WORD FROM RKDB
2672
2673 001452 000000          BSERR: 0         ;CANNOT READ BSE INFO WHEN SET
2674 001454 000000          LIMERR: 0        ;LIMIT DETECT ERROR FLAG
2675 001456 000000          BYPCERR: 0       ;SET TO 1 TO BYPASS CKCERR IN 'GSTAT1'
2676 001460 000000          CHKFLG: 0       ;WORDS TO BE TESTED
2677
2678 001462 000102          HDTAB: .BLKW 66. ;CALCULATED HEADER WORD TABLE
2679 001666 000102          RHTAB: .BLKW 66. ;FILLED AFTER READ HEADER CMD
2680 002072 000102          SRTTAB: .BLKW 66. ;ABOVE RHTAB SORTED STARTING FORM
2681                                     ;SECTOR 0 BY SORT ROUTINE
2682 002276 000400          BSE22H: .BLKW 256. ;22 SECTOR HARDWARE BSE INFO.
2683 003276 000400          BSE22S: .BLKW 256. ;22 SECTOR SOFTWARE BSE INFO.
2684 004276 000400          RDTAB: .BLKW 256. ;FILLED AFTER READ DATA CMD
2685
2686 005276 000000          UNLD: 0          ;SET TO 0 IF HEADS ARE LOADED
2687                                     ;SET TO 1 IF HEADS UNLOADED
2688 005300 000000          BADHDR: 0        ;SET TO 0 IF FORMATTING OK
2689                                     ;SET TO 1 IF FORMATTING ALTERED
2690 005302 000000          HPEND: 0         ;SET TO 0 IF HALT NOT PENDING
2691                                     ;SET TO 1 IF HALT PENDING
2692
2693                                     ;THE ABOVE 3 FLAGS ARE USED
2694                                     ;BY 'STOP' ROUTINE TO BRING
2695                                     ;THE CPU TO A VALID HALT.
2696
2697
2698 005304 001 002 004 ATTN: .BYTE 1,2,4,10,20,40,100,200 ;ATN 0-7 RESP.
2699 005307 010 020 040
2700 005312 100 200
2701                                     .EVEN
2702
2703
2704                                     ;THE FOLLOWING ARE HOLDING REGISTERS FOR THE RK611 REGISTERS
2705                                     ;THEY ARE LOADED AFTER RDY IS REC'D FROM WRDY ROUTINE.
2706
2707
2708 005314 000000          HCS1: 0          ;HOLD RKCS1
2709 005316 000000          HCS2: 0          ;HOLD RKCS2
2710 005320 000000          HWC: 0          ;HOLD RKWC
2711 005322 000000          HBA: 0          ;ETC.
2712 005324 000000          HDA: 0
2713 005326 000000          HDS: 0
2714 005330 000000          HER: 0
2715 005332 000000          HASOF: 0
2716 005334 000000          HDC: 0
2717 005336 000000          HDB: 0
2718 005340 000000          HMR1: 0
2719 005342 000000          HMR2: 0
2720 005344 000000          HMR3: 0
2721 005346 000000          HPOS: 0
2722 005350 000000          HPAT: 0
2723

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2724
2725 005352 000000      TEMP1: 0          ;TEMPORARY STORAGE.
2726 005354 000000      TEMP2: 0
2727 005356 000000      TEMP3: 0
2728 005360 000000      TEMP4: 0
2729 005362 000000      TEMP5: 0
2730
2731      ;THE FOLLOWING ARE HOLDING REGISTERS FOR MSGA(0-3) & MSGB(0-3).
2732
2733 005364 000000      H.A0: 0
2734 005366 000000      H.B0: 0
2735 005370 000000      H.A1: 0
2736 005372 000000      H.B1: 0
2737 005374 000000      H.A2: 0
2738 005376 000000      H.B2: 0
2739 005400 000000      H.A3: 0
2740 005402 000000      H.B3: 0
2741
2742      ;THE FOLLOWING ARE 'EXPECTED' REGISTER FOR THE ABOVE.
2743
2744 005404 000000      E.A0: 0
2745 005406 000000      E.B0: 0
2746 005410 000000      E.A1: 0
2747 005412 000000      E.B1: 0
2748 005414 000000      E.A2: 0
2749 005416 000000      E.B2: 0
2750 005420 000000      E.A3: 0
2751 005422 000000      E.B3: 0
2752
2753      ;THE FOLLOWING ARE IDENTIFIERS FOR DRIVE MSG WORDS TO BE TESTED.
2754
2755      T.A2=BIT0          ;TEST MSG A2 IF SET
2756      T.B2=BIT1
2757      T.B3=BIT2
2758
2759      ;ALL THE FLAGS BELOW ARE CLEARED INITIALLY BY THE CLRFLG ROUTINE.
2760
2761 005424 000000      DDUMP: 0          ;FLAG - SET WHEN IN DDP DUMP MODE
2762 005426 000000      DDPCH: 0         ;FLAG - SET WHEN IN DDP CHAIN MODE
2763 005430 000000      ACT11: 0        ;FLAG - SET WHEN IN ACT11 MODE OF OPERATION
2764 005432 000000      PPTP: 0         ;FLAG - SET WHEN PROGRAM LOADED BY PAPER TAPE
2765 005434 000000      DRIVS: 0        ;CONTAINS THE NUMBER OF DRIVES PRESENT
2766
2767      ;THE FLAGS BELOW ARE SET TO 1 TO INDICATE THAT A PARTICULAR DRIVE
2768      ;IS PRESENT AND IS TO BE TESTED.
2769      ;ONLY DRIVES 0,2,4,6 CAN BE TESTED.
2770
2771 005436 000000      DRIV0: 0         ;FLAG SET TO 1 WHEN DRIVE 0 PRESENT
2772 005440 000000      DRIV1: 0         ;FOR DRIVE 1
2773 005442 000000      DRIV2: 0         ;FOR DRIVE 2
2774 005444 000000      DRIV3: 0         ;FOR DRIVE 3
2775 005446 000000      DRIV4: 0         ;FOR DRIVE 4
2776 005450 000000      DRIV5: 0         ;FOR DRIVE 5
2777 005452 000000      DRIV6: 0         ;FOR DRIVE 6
2778 005454 000000      DRIV7: 0         ;FOR DRIVE 7
2779

```

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 54
APT MAILBOX-ETABLE

SEQ 0054

2780 005456 000000
2781 005460 000000
2782 005462 000000
2783 005464 000000
2784
2785 005466 000132

LCLKF: 0
PCLKF: 0
SIZFLG: 0
UNITB: 0

TIMER: 90.

;L-CLOCK FLAG PRESENT FLAG
;P-CLOCK FLAG PRESENT FLAG
;SET IF DEFAULT DO SIZING IN TEST 1
;0 FOR PORT A: DRIVES 0,2,4,6
;1 FOR PORT B: DRIVES 1,3,5,7
;TIMER FOR 1.5 SEC

.SBTTL ERROR POINTER TABLE

;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
;*LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
;*NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
;*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

;* EM ;:POINTS TO THE ERROR MESSAGE
;* DH ;:POINTS TO THE DATA HEADER
;* DT ;:POINTS TO THE DATA
;* DF ;:POINTS TO THE DATA FORMAT

\$ERRTB:

;ERROR 1 EM2 ;DR # IN RKCS2 CANNOT BE READ BACK CORRECTLY IN RKMR2
DH1
DT1
DF1

;ERROR 2 EM5 ;DETECTED MDS
DH1
DT1
DF1

;ERROR 3 EM6 ;DETECTED UFE
DH1
DT1
DF1

;ERROR 4 EM7 ;DETECTED DRA & NED RESET (WRONG PORT SELECTED?)
DH1
DT1
DF1

;ERROR 5
0
0
0

;ERROR 6 EM9 ;DR NOT PRESENT BUT SPECIFIED BY OPERATOR
DH1
DT1
DF1

;ERROR 7 EM10 ;ABORT TEST, COULD NOT REFERENCE CONTROLLER REGISTER
DH1
DT1
DF1

2786
2787
2788
2789
2790
2791
2792
2793
2794
2795
2796
2797
2798
2799
2800 005470
2801
2802
2803 005470 057111
2804 005472 062540
2805 005474 065512
2806 005476 066314
2807
2808
2809 005500 057330
2810 005502 062540
2811 005504 065512
2812 005506 066314
2813
2814
2815 005510 057351
2816 005512 062540
2817 005514 065512
2818 005516 066314
2819
2820
2821 005520 057372
2822 005522 062540
2823 005524 065512
2824 005526 066314
2825
2826 005530 000000
2827 005532 000000
2828 005534 000000
2829 005536 000000
2830
2831
2832 005540 057636
2833 005542 062540
2834 005544 065512
2835 005546 066314
2836
2837
2838 005550 057712
2839 005552 062540
2840 005554 065512
2841 005556 066314

2842			;ERROR 10		
2843	005560	057775		EM11	;DRA & NED BOTH SET
2844	005562	062540		DH1	
2845	005564	065512		DT1	
2846	005566	066314		DF1	
2847			;ERR 11		
2848	005570	060041		EM12	;NO RDY
2849	005572	064302		DH27	;AFTER WRITE DATA CMD
2850	005574	065512		DT1	
2851	005576	066444		DF10	
2852			;ERR 12		
2853	005600	060435		EM21	;CERR SET
2854	005602	064302		DH27	
2855	005604	065512		DT1	
2856	005606	066444		DF10	
2857			;ERR 13		
2858	005610	060041		EM12	;NO RDY
2859	005612	064252		DH26	;AFTER READ DATA CMD
2860	005614	065512		DT1	
2861	005616	066444		DF10	
2862			;ERR 14		
2863	005620	060435		EM21	;CERR SET
2864	005622	064252		DH26	
2865	005624	065512		DT1	
2866	005626	066444		DF10	
2867			;ERR 15		
2868	005630	060041		EM12	;NO RDY
2869	005632	064432		DH32	;AFTER WRITE CHECK CMD
2870	005634	065512		DT1	
2871	005636	066444		DF10	
2872			;ERR 16		
2873	005640	062220		EM80	;WRITE CHECK ERROR SET
2874	005642	064432		DH32	;AFTER WRITE CHECK CMD
2875	005644	065624		DT6	
2876	005646	066334		DF3	
2877			;ERR 17		
2878	005650	060121		EM14	;UNEXP MEM PARITY TRAP
2879	005652	063176		DH8	;TEST #, TRAP PC
2880	005654	065552		DT3	
2881	005656	066330		DF2	
2882			;ERR 20		
2883	005660	062257		EM82	;READ DATA NOT COMPARE WITH WRITE DATA
2884	005662	064252		DH26	;AFTER READ DATA CMD
2885	005664	065624		DT6	
2886					
2887	005666	066334		DF3	
2888			;ERR 21		
2889	005670	062331		EM83	;DATA CHECK ERROR
2890	005672	064252		DH26	
2891	005674	065512		DT1	
2892					
2893	005676	066444		DF10	
2894			;ERR 22		
2895	005700	060435		EM21	;CERR SET
2896	005702	064432		DH32	;AFTER WRITE CHECK CMD
2897	005704	065512		DT1	

2898	005706	066444		DF10	
2899			;ERR 23		
2900	005710	060352		EM18	;MSG B0 ERROR
2901	005712	064302		DH27	;AFTER WRITE DATA CMD
2902	005714	066050		DT13	
2903	005716	066574		DF21	
2904			;ERROR 24		
2905	005720	060435		EM21	;CERR SET
2906	005722	064052		DH21	;AFTER SCLR
2907	005724	065512		DT1	
2908	005726	066444		DF10	
2909			;ERR 25		
2910	005730	060414		EM20	;MSG B1 ERROR
2911	005732	064302		DH27	
2912	005734	066050		DT13	
2913	005736	066574		DF21	
2914			;ERR 26		
2915	005740	060352		EM18	
2916	005742	064252		DH26	;AFTER READ DATA CMD
2917	005744	066050		DT13	
2918	005746	066574		DF21	
2919			;ERROR 27		
2921	005750	060664		EM24	;VOL VALID NOT SET
2922	005752	063774		DH19	;AFTER PACK CMD
2923	005754	065512		DT1	
2924	005756	066444		DF10	
2925			;ERR 30		
2926	005760	060414		EM20	;MSG B1 ERROR
2927	005762	064252		DH26	;AFTER READ DATA CMD.
2928	005764	066050		DT13	
2929	005766	066574		DF21	
2930			;ERR 31		
2931	005770	060352		EM18	;MSG B0 ERROR
2932	005772	064432		DH32	;AFTER WRITE CHECK CMD
2933	005774	066050		DT13	
2934	005776	066574		DF21	
2935			;ERR 32		
2936	006000	060414		EM20	;MSG B1 ERROR
2937	006002	064432		DH32	
2938	006004	066050		DT13	
2939	006006	066574		DF21	
2940			;ERR 33		
2941	006010	060331		EM17	;AO ERROR
2942	006012	064100		DH22	;AFTER DRIVE CLR CMD
2943	006014	066050		DT13	
2944	006016	066574		DF21	
2945			;ERR 34		
2946	006020	060352		EM18	;B0 ERROR
2947	006022	064100		DH22	
2948	006024	066050		DT13	
2949	006026	066574		DF21	
2950			;ERR 35		
2951	006030	060373		EM19	
2952	006032	064100		DH22	
2953	006034	066050		DT13	

2954	006036	066574		DF21	
2955			;ERR 36		
2956	006040	060414		EM20	
2957	006042	064100		DH22	
2958	006044	066050		DT13	
2959	006046	066574		DF21	
2960			;ERR 37		
2961	006050	060331		EM17	;AD ERROR
2962	006052	064720		DH39	;AFTER WRITE HEADER CMD
2963	006054	066050		DT13	
2964	006056	066574		DF21	
2965			;ERR 40		
2966	006060	060352		EM18	
2967	006062	064720		DH39	
2968	006064	066050		DT13	
2969	006066	066574		DF21	
2970			;ERR 41		
2971	006070	060373		EM19	
2972	006072	064720		DH39	
2973	006074	066050		DT13	
2974	006076	066574		DF21	
2975			;ERR 42		
2976	006100	060414		EM20	
2977	006102	064720		DH39	
2978	006104	066050		DT13	
2979	006106	066574		DF21	
2980			;ERR 43		
2981	006110	060771		EM26	;BSE ERROR IN WRITE CMD NOT ON BSE TABLE
2982	006112	064302		DH27	;AFTER WRITE DATA CMD
2983	006114	065512		DT1	
2984	006116	066444		DF10	
2985			;ERR 44		
2986	006120	060157		EM15	;WCE AT CYL 411, TRK 2, SEC 21
2987	006122	062540		DH1	
2988	006124	065512		DT1	
2989	006126	066360		DF4	
2990			;ERR 45		
2991	006130	061364		EM31	;PORT NOT AVAIL
2992	006132	062742		DH5	;AFTER TIMEOUT
2993	006134	065512		DT1	
2994	006136	066444		DF10	
2995			;ERR 46		
2996	006140	060717		EM25	;DETECTED 10 BAD SECTORS
2997	006142	064302		DH27	;AFTER WRITE DATA CMD.
2998	006144	065512		DT1	
2999	006146	066444		DF10	
3000			;ERROR 47		
3001	006150	061675		EM39	;CYL DIFF/OFFSET IN RKMR2 NOT CLEARED
3002	006152	063723		DH17	;AFTER RECAL CMD
3003	006154	066130		DT14	
3004	006156	066630		DF22	
3005			;ERROR 50		
3006	006160	061744		EM40	;CYL ADDR IN RKMR3 NOT CLEARED
3007	006162	063723		DH17	;AFTER RECAL COMD
3008	006164	066130		DT14	
3009	006166	066630		DF22	

3010			;ERR 51	EM93	;WRONG CYL# IN HEADER WORD (MISPOSITION)
3011	006170	062457		DH25	;AFTER SEEK CMD
3012	006172	064227		DT9	
3013	006174	066004		DF20	
3014	006176	066550			
3015			;ERR 52	EM17	;MSG AO ERROR
3016	006200	060331		DH27	;AFTER WRITE DATA CMD
3017	006202	064302		DT13	
3018	006204	066050		DF21	
3019	006206	066574			
3020			;ERR 53	EM19	;MSG A1 ERROR
3021	006210	060373		DH27	
3022	006212	064302		DT13	
3023	006214	066050		DF21	
3024	006216	066574			
3025			;ERR 54	EM17	;MSG AO ERROR
3026	006220	060331		DH26	;AFTER READ DATA CMD
3027	006222	064252		DT13	
3028	006224	066050		DF21	
3029	006226	066574			
3030			;ERROR 55	EM13	;NO ATTN
3031	006230	060077		DH17	;AFTER RECAL CMD
3032	006232	063723		DT1	
3033	006234	065512		DF10	
3034	006236	066444			
3035			;ERR 56	EM19	;MSG A1 ERROR
3036	006240	060373		DH26	
3037	006242	064252		DT13	
3038	006244	066050		DF21	
3039	006246	066574			
3040			;ERR 57	EM17	;MSG AO ERROR
3041	006250	060331		DH32	;AFTER WRITE CHECK CMD
3042	006252	064432		DT13	
3043	006254	066050		DF21	
3044	006256	066574			
3045			;ERR 60	EM19	;MSG A1 ERROR
3046	006260	060373		DH32	
3047	006262	064432		DT13	
3048	006264	066050		DF21	
3049	006266	066574			
3050			;ERR 61	EM17	;AO ERROR
3051	006270	060331		DH30	;AFTER READ HEADER CMD
3052	006272	064353		DT13	
3053	006274	066050		DF21	
3054	006276	066574			
3055			;ERR 62	EM18	
3056	006300	060352		DH30	
3057	006302	064353		DT13	
3058	006304	066050		DF21	
3059	006306	066574			
3060			;ERR 63	EM19	
3061	006310	060373		DH30	
3062	006312	064353		DT13	
3063	006314	066050		DF21	
3064	006316	066574			
3065			;ERR 64		

3066	006320	060414	EM20	
3067	006322	064353	DH30	
3068	006324	066050	DT13	
3069	006326	066574	DF21	
3070				;ERR 65
3071	006330	061050	EM27	
3072	006332	062540	DH1	
3073	006334	065512	DT1	
3074	006336	066314	DF1	
3075				;ERR 66
3076	006340	060352	EM18	
3077	006342	063723	DH17	
3078	006344	066050	DT13	
3079	006346	066574	DF21	
3080				;ERR 67
3081	006350	060414	EM20	
3082	006352	063723	DH17	
3083	006354	066050	DT13	
3084	006356	066574	DF21	
3085				;ERR 70
3086	006360	062152	EM74	
3087	006362	065007	DH41	
3088	006364	065512	DT1	
3089	006366	066444	DF10	
3090				;ERR 71
3091	006370	061364	EM31	
3092	006372	064464	DH35	
3093	006374	065512	DT1	
3094	006376	066444	DF10	
3095				;ERR 72
3096	006400	061343	EM30	
3097	006402	062676	DH4	
3098	006404	065512	DT1	
3099	006406	066444	DF10	
3100				;ERR 73
3101	006410	060077	EM13	
3102	006412	063747	DH18	
3103	006414	065512	DT1	
3104	006416	066444	DF10	
3105				;ERR 74
3106	006420	060077	EM13	
3107	006422	063272	DH10	
3108	006424	065512	DT1	
3109	006426	066444	DF10	
3110				;ERR 75
3111	006430	060457	EM22	
3112	006432	062540	DH1	
3113	006434	065512	DT1	
3114	006436	066314	DF1	
3115				;ERR 76
3116	006440	060564	EM23	
3117	006442	062540	DH1	
3118	006444	065512	DT1	
3119	006446	066314	DF1	
3120				;ERR 77
3121	006450	060331	EM17	

; DETECTED BSE IN READ BUT NOT IN WRITE CMD.

; BO ERROR
; AFTER RECAL CMD

; RTZ NOT SET
; DURING RECAL CMD

; PORT NOT AVAIL
; AFTER RLS

; PIP SET
; AFTER ATTN REC'D FROM RECAL

; NO ATTN
; AFTER UNLOAD CMD

; NO ATTN
; AT END OF HEAD LOADING

; NO DRIVS IN \$DEVN

; NO DRIVS ON BUSS

; AO ERROR

3122	006452	062742	DH5		
3123	006454	066050	DT13		
3124	006456	066574	DF21		
3125				;ERR 100	
3126	006460	060352	EM18		
3127	006462	062742	DH5		
3128	006464	066050	DT13		
3129	006466	066574	DF21		
3130				;ERR 101	
3131	006470	060373	EM19		
3132	006472	062742	DH5		
3133	006474	066050	DT13		
3134	006476	066574	DF21		
3135				;ERR 102	
3136	006500	060414	EM20		
3137	006502	062742	DH5		
3138	006504	066050	DT13		
3139	006506	066574	DF21		
3140				;ERR 103	
3141	006510	061407	EM32		;PORT AVAIL
3142	006512	063513	DH14		;BEFORE TMO OR RLS
3143	006514	065512	DT1		
3144	006516	066444	DF10		
3145				;ERR 104	
3146	006520	060331	EM17		;AO ERROR
3147	006522	063545	DH15		;WHILE PORT UNAVAIL
3148	006524	066050	DT13		
3149	006526	066574	DF21		
3150				;ERR 105	
3151	006530	060352	EM18		
3152	006532	063545	DH15		
3153	006534	066050	DT13		
3154	006536	066574	DF21		
3155				;ERR 106	
3156	006540	060373	EM19		
3157	006542	063545	DH15		
3158	006544	066050	DT13		
3159	006546	066574	DF21		
3160				;ERR 107	
3161	006550	060414	EM20		
3162	006552	063545	DH15		
3163	006554	066050	DT13		
3164	006556	066574	DF21		
3165				;ERR 110	
3166	006560	060077	EM13		;NO ATTN
3167	006562	063574	DH16		;TO ALLOW PORT TO SEIZE
3168	006564	065512	DT1		
3169	006566	066444	DF10		
3170				;ERR 111	
3171	006570	061426	EM33		;ATTN SET
3172	006572	064132	DH23		;W/O REQUEST PENDING
3173	006574	065512	DT1		
3174	006576	066444	DF10		
3175				;ERR 112	
3176	006600	060077	EM13		;NO ATTN
3177	006602	062742	DH5		;AFT TMO

3178	006604	065512	DT1	
3179	006606	066444	DF10	
3180			;ERR 113	
3181	006610	061364	EM31	;PORT NO AVAIL
3182	006612	064100	DH22	;AFTER DRIVE CLEAR CMD
3183	006614	065512	DT1	
3184	006616	066444	DF10	
3185			;ERR 114	
3186	006620	061451	EM34	;ATTN CLEARED
3187	006622	064162	DH24	;BY DR CLR TO OTHER PORT.
3188	006624	065512	DT1	
3189	006626	066444	DF10	
3190			;ERR 115	
3191	006630	061426	EM33	;ATTN SET
3192	006632	064464	DH35	;AFT RELEASE ISSUED
3193	006634	065512	DT1	
3194	006636	066444	DF10	
3195			;ERROR 116	
3196	006640	060041	EM12	;CONT NOT RDY
3197	006642	063774	DH19	;AFTER PACK CMD
3198	006644	065512	DT1	
3199	006646	066444	DF10	
3200			;ERROR 117	
3201	006650	060041	EM12	;CONT NOT RDY
3202	006652	064017	DH20	;AFTER SEL DR CMD
3203	006654	065512	DT1	
3204	006656	066444	DF10	
3205			;ERROR 120	
3206	006660	060041	EM12	
3207	006662	064052	DH21	;AFTER SUBSYS CLEAR
3208	006664	065512	DT1	
3209	006666	066444	DF10	
3210			;ERROR 121	
3211	006670	060041	EM12	
3212	006672	063217	DH9	;AFTER START SPINDLE CMD
3213	006674	065512	DT1	
3214	006676	066444	DF10	
3215			;ERR 122	
3216	006700	060077	EM13	;NO ATTN
3217	006702	064572	DH37	;AFT RLS & REQUEST PENDING
3218	006704	065512	DT1	
3219	006706	066444	DF10	
3220			;ERR 123	
3221	006710	061451	EM34	;ATTN CLEARED
3222	006712	064464	DH35	;AFTER RELEASE
3223	006714	065512	DT1	
3224	006716	066444	DF10	
3225			;ERROR 124	
3226	006720	060041	EM12	
3227	006722	063723	DH17	;AFTER RECAL CMD
3228	006724	065512	DT1	
3229	006726	066444	DF10	
3230			;ERR 125	
3231	006730	062131	EM73	;CTO SET
3232	006732	062366	EM84	;WHILE WAITING FOR OR REC'D CONTR RDY. MSG A&B BAD
3233	006734	065512	DT1	

3234	006736	066374	DF5	
3235			;ERR 126	
3236	006740	062177	EM79	;NED SET
3237	006742	062366	EM84	
3238	006744	065512	DT1	
3239	006746	066374	DF5	
3240			;ERR 127	
3241	006750	057330	EM5	;MDS SET
3242	006752	062366	EM84	
3243	006754	065512	DT1	
3244	006756	066374	DF5	
3245			;ERROR 130	
3246	006760	061500	EM35	;CERR NOT SET
3247	006762	064511	DH36	;AFT SEL DRV CMD & NO DRA
3248	006764	065512	DT1	
3249	006766	066444	DF10	
3250			;ERROR 131	
3251	006770	060041	EM12	;NO RDY
3252	006772	064227	DH25	;AFTER SEEK CMD
3253	006774	065512	DT1	
3254	006776	066444	DF10	
3255			;ERROR 132	
3256	007000	060077	EM13	;NO ATTN
3257	007002	064227	DH25	
3258	007004	065512	DT1	
3259	007006	066444	DF10	
3260			;ERR 133	
3261	007010	060331	EM17	;AO ERROR
3262	007012	064464	DH35	;AFTER RLS ISSUED
3263	007014	066050	DT13	
3264	007016	066574	DF21	
3265			;ERR 134	
3266	007020	060352	EM18	
3267	007022	064464	DH35	
3268	007024	066050	DT13	
3269	007026	066574	DF21	
3270			;ERR 135	
3271	007030	060373	EM19	
3272	007032	064464	DH35	
3273	007034	066050	DT13	
3274	007036	066574	DF21	
3275			;ERR 136	
3276	007040	060414	EM20	
3277	007042	064464	DH35	
3278	007044	066050	DT13	
3279	007046	066574	DF21	
3280			;ERROR 137	
3281	007050	061675	EM39	;CYL DIFF/OFFSET IN RKMR2 NOT CLEARED
3282	007052	064227	DH25	
3283	007054	065512	DT1	
3284	007056	066444	DF10	
3285			;ERR 140	
3286	007060	060331	EM17	;MSG AO ERROR
3287	007062	065433	DH51	;AFTER SEEK TO SELF
3288	007064	066050	DT13	
3289	007066	066574	DF21	

3290					
3291	007070	060352	;ERR 141	EM18	
3292	007072	065433		DH51	
3293	007074	066050		DT13	
3294	007076	066574		DF21	
3295			;ERR 142	EM19	
3296	007100	060373		DH51	
3297	007102	065433		DT13	
3298	007104	066050		DF21	
3299	007106	066574			
3300			;ERR 143	EM20	
3301	007110	060414		DH51	
3302	007112	065433		DT13	
3303	007114	066050		DF21	
3304	007116	066574			
3305			;ERR 144	EM37	;MULT ATTN
3306	007120	061571		DH5	;AFTER TMO
3307	007122	062742		DT1	
3308	007124	065512		DF10	
3309	007126	066444			
3310			;ERR 145	EM17	;AO ERROR
3311	007130	060331		DH38	;AFTER SEEK & IMMED RELEASE
3312	007132	064646		DT13	
3313	007134	066050		DF21	
3314	007136	066574			
3315			;ERR 146	EM18	
3316	007140	060352		DH38	
3317	007142	064646		DT13	
3318	007144	066050		DF21	
3319	007146	066574			
3320			;ERR 147	EM19	
3321	007150	060373		DH38	
3322	007152	064646		DT13	
3323	007154	066050		DF21	
3324	007156	066574			
3325			;ERR 150	EM20	
3326	007160	060414		DH38	
3327	007162	064646		DT13	
3328	007164	066050		DF21	
3329	007166	066574			
3330			;ERROR 151	EM12	;NO RDY
3331	007170	060041		DH22	;AFTER CLEAR CMD
3332	007172	064100		DT1	
3333	007174	065512		DF10	
3334	007176	066444			
3335			;ERR 152	EM13	;NO ATTN
3336	007200	060077		DH38	;AFTER SEEK & IMMED RLS
3337	007202	064646		DT1	
3338	007204	065512		DF10	
3339	007206	066444			
3340			;ERR 153	EM38	;RE-TRIGGER NO GOOD
3341	007210	061622		DH43	;AFTER RE-SEIZE MIDWAY THRU TMO
3342	007212	065154		DT1	
3343	007214	065512		DF10	
3344	007216	066444			
3345			;ERROR 154		

N05

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 65
ERROR POINTER TABLE

SEQ 0065

3346 007220 062002
3347 007222 064100
3348 007224 065512
3349 007226 066444
3350

EM55
DH22
DT1
DF10

;ATTN NOT CLEARED

;ERR 155

B06

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78
ERROR POINTER TABLE

08:56 PAGE 66

SEQ 0066

3351 007230 061275
3352 007232 065312
3353 007234 065512

EM29
DH45
DT1

;PORT AVAIL-TIMERS NOT INHIB.
;BEFORE RLS WHEN UNLOADED

3354	007236	066444	DF10	
3355			;ERR 156	
3356	007240	060077	EM13	;NO ATTN
3357	007242	064227	DH25	;AFTER SEEK CMD
3358	007244	065512	DT1	
3359	007246	066444	DF10	
3360			;ERR 157	
3361	007250	061364	EM31	;PORT NOT AVAIL
3362	007252	064227	DH25	;AFTER SEEK CMD
3363	007254	065512	DT1	
3364	007256	066444	DF10	
3365			;ERROR 160	
3366	007260	061426	EM33	;ATTN SET
3367	007262	064646	DH38	;AFTER SEEK & IMMED RLS
3368	007264	065512	DT1	
3369	007266	066444	DF10	
3370			;ERR 161	
3371	007270	060331	EM17	;MSG AD ERROR
3372	007272	064227	DH25	;AFTER SEEK CMD
3373	007274	066050	DT13	
3374	007276	066574	DF21	
3375			;ERR 162	
3376	007300	060352	EM18	
3377	007302	064227	DH25	
3378	007304	066050	DT13	
3379	007306	066574	DF21	
3380			;ERR 163	
3381	007310	060373	EM19	
3382	007312	064227	DH25	
3383	007314	066050	DT13	
3384	007316	066574	DF21	
3385			;ERR 164	
3386	007320	060414	EM20	
3387	007322	064227	DH25	
3388	007324	066050	DT13	
3389	007326	066574	DF21	
3390			;ERR 165	
3391	007330	060331	EM17	;MSG AD ERROR
3392	007332	062742	DH5	;AFTER TMO
3393	007334	066050	DT13	
3394	007336	066574	DF21	
3395			;ERR 166	
3396	007340	060352	EM18	
3397	007342	062742	DH5	
3398	007344	066050	DT13	
3399	007346	066574	DF21	
3400			;ERR 167	
3401	007350	060373	EM19	
3402	007352	062742	DH5	
3403	007354	066050	DT13	
3404	007356	066574	DF21	
3405			;ERR 170	
3406	007360	060414	EM20	
3407	007362	062742	DH5	
3408	007364	066050	DT13	
3409	007366	066574	DF21	

3410			; ERROR 171	
3411	007370	060041	EM12	; NO RDY
3412	007372	064353	DH30	; AFTER READ HEADER CMD
3413	007374	065512	DT1	
3414	007376	066444	DF10	
3415			; ERROR 172	
3416	007400	061364	EM31	; PORT NOT AVAIL
3417	007402	064464	DH35	; AFT RELEASE
3418	007404	065512	DT1	
3419	007406	066444	DF10	
3420			; ERROR 173	
3421	007410	062035	EM63	; DLT SET
3422	007412	064353	DH30	
3423	007414	065512	DT1	
3424	007416	066504	DF15	
3425			; ERROR 174	
3426	007420	060435	EM21	; CERR SET
3427	007422	064353	DH30	
3428	007424	065512	DT1	
3429	007426	066504	DF15	
3430			; ERROR 175	
3431	007430	061675	EM39	; CYL DIFF NOT CLEARED
3432	007432	063272	DH10	; AT END OF HEAD LOADING
3433	007434	065512	DT1	
3434	007436	066444	DF10	
3435			; ERROR 176	
3436	007440	061744	EM40	; CYL ADDR NOT CLEARED.
3437	007442	063272	DH10	
3438	007444	065512	DT1	
3439	007446	066444	DF10	
3440			; ERROR 177	
3441	007450	061275	EM29	; PORT AVAIL-TIMERS NOT INHIB
3442	007452	065007	DH41	; DURING RECAL CMD
3443	007454	065512	DT1	
3444	007456	066444	DF10	
3445			; ERROR 200	
3446	007460	060041	EM12	; NO RDY
3447	007462	064720	DH39	; AFTER WRITE HEADER CMD
3448	007464	065512	DT1	
3449	007466	066504	DF15	
3450			; ERROR 201	
3451	007470	060435	EM21	; CERR SET
3452	007472	064720	DH39	
3453	007474	065512	DT1	
3454	007476	066504	DF15	
3455			; ERROR 202	
3456	007500	062056	EM65	; READ HEADER ERROR
3457	007502	062540	DH1	
3458	007504	065670	DT7	
3459	007506	066464	DF14	
3460			; ERROR 203	
3461	007510	061364	EM31	; PORT NOT AVAIL
3462	007512	065007	DH41	; DURING RECAL CMD
3463	007514	065512	DT1	
3464	007516	066444	DF10	
3465			; ERROR 204	

3466	007520	000000	0	
3467	007522	000000	0	
3468	007524	000000	0	
3469	007526	000000	0	
3470			;ERROR 205	
3471	007530	000000	0	
3472	007532	000000	0	
3473	007534	000000	0	
3474	007536	000000	0	
3475			;ERROR 206	
3476	007540	061526	EM36	;CYL ADDR IN RKMR3 INCORRECT
3477	007542	064227	DH25	;AFTER SEEK CMD
3478	007544	065736	DT8	
3479	007546	066420	DF6	
3480			;ERROR 207	
3481	007550	061526	EM36	;CYL ADDR IN RKMR3 INCORRECT
3482	007552	064227	DH25	;AFTER SEEK CMD
3483	007554	065556	DT4	
3484	007556	066420	DF6	
3485			;ERROR 210	
3486	007560	060435	EM21	;CERR SET
3487	007562	064227	DH25	
3488	007564	065512	DT1	
3489	007566	066444	DF10	
3490			;ERR 211	
3491	007570	060331	EM17	;MSG AD ERROR
3492	007572	064464	DH35	;AFTER RLS ISSUED
3493	007574	066050	DT13	
3494	007576	066574	DF21	
3495			;ERR 212	
3496	007600	060352	EM18	
3497	007602	064464	DH35	
3498	007604	066050	DT13	
3499	007606	066574	DF21	
3500			;ERR 213	
3501	007610	060373	EM19	
3502	007612	064464	DH35	
3503	007614	066050	DT13	
3504	007616	066574	DF21	
3505			;ERR 214	
3506	007620	060414	EM20	
3507	007622	064464	DH35	
3508	007624	066050	DT13	
3509	007626	066574	DF21	
3510			;ERROR 215	
3511	007630	000000	0	
3512	007632	000000	0	
3513	007634	000000	0	
3514	007636	000000	0	
3515			;ERROR 216	
3516	007640	000000	0	
3517	007642	000000	0	
3518	007644	000000	0	
3519	007646	000000	0	
3520			;ERROR 217	
3521	007650	000000	0	

3522	007652	000000	0	
3523	007654	000000	0	
3524	007656	000000	0	
3525			; ERROR 220	
3526	007660	000000	0	
3527	007662	000000	0	
3528	007664	000000	0	
3529	007666	000000	0	
3530			; ERROR 221	
3531	007670	060331	EM17	; MSG A0 ERROR
3532	007672	063723	DH17	
3533	007674	066050	DT13	
3534	007676	066574	DF21	
3535			; ERROR 222	
3536	007700	060373	EM19	; MSG A1 ERROR
3537	007702	063723	DH17	
3538	007704	066050	DT13	
3539	007706	066574	DF21	
3540			; ERROR 223	
3541	007710	060435	EM21	; CERR SET
3542	007712	063024	DH7	; AFT SCLR
3543	007714	065512	DT1	
3544	007716	066444	DF10	
3545			; ERR 224	
3546	007720	061526	EM36	; CYL ADDR IN RKMR3 BAD
3547	007722	063336	DH12	; AFT SEEK CMD
3548	007724	065556	DT4	
3549	007726	066420	DF6	
3550			; ERROR 225	
3551	007730	000000	0	
3552	007732	000000	0	
3553	007734	000000	0	
3554	007736	000000	0	
3555			; ERROR 226	
3556	007740	060041	EM12	; NO RDY
3557	007742	064252	DH26	; AFTER READ DATA CMD
3558	007744	065512	DT1	
3559	007746	066444	DF10	
3560			; ERROR 227	
3561	007750	060435	EM21	; CERR SET
3562	007752	064252	DH26	
3563	007754	065512	DT1	
3564	007756	066504	DF15	
3565			; ERROR 230	
3566	007760	060266	EM16	; CANNOT READ BSE INFO
3567	007762	063427	DH13	; ON SEC 10, 12, 14, 16, 18, 20
3568	007764	065512	DT1	
3569	007766	066524	DF17	
3570			; ERROR 231	
3571	007770	000000	0	
3572	007772	000000	0	
3573	007774	000000	0	
3574	007776	000000	0	
3575			; ERROR 232	
3576	010000	000000	0	
3577	010002	000000	0	


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3578 010004 000000 0
3579 010006 000000 0
3580 ;ERROR 233
3581 010010 060266 EM16 ;CANNOT READ BSE INFO
3582 010012 065103 DH42 ;ON SECT 0,2,4,6,8
3583 010014 065512 DT1
3584 010016 066524 DF17
3585 ;ERROR 234
3586 010020 000000 0
3587 010022 000000 0
3588 010024 000000 0
3589 010026 000000 0
3590 ;ERROR 235
3591 010030 062100 EM69 ;ALIGN CARTRIDGE USED
3592 010032 065233 DH44 ;WILL BYPASS FORMAT & ALL R/W TESTS
3593 010034 065512 DT1
3594 010036 066444 DF10
3595
3596 .SBTTL PROGRAM SETUP
3597
3598 010040 012737 000001 001336 PARSRT: MOV #1,PARAM ;SET FLAG FOR 220 START: INPUT PARAMETERS
3599 010046 000402 BR PRGSRT ;START PROGRAM
3600
3601 010050 005037 001336 START: CLR PARAM ;200 START, DEFAULT
3602 010054 000005 PRGSRT: RESET ;CLEAR ALL INT ENABLE & INIT
3603 010056 012706 001100 MOV #STACK,SP ;SETUP STACK POINTER
3604 010062 012746 000000 MOV #PRO,-(SP) ;PSW LOADED TO BE
3605 010066 012746 010074 MOV #1$,-(SP) ;LSI-11 COMPATABLE
3606 010072 000002 RTI ;ENABLE ALL INTERRUPTS
3607
3608 010074 004737 052260 1$: JSR PC,$TKINT ;SETUP KB VECTOR ADDR, PRIORITY 4
3609 ;& TURN ON KB INTERRUPT
3610
3611 ;*** CPU PRIORITY LEVEL NOW AT 0 ***
3612 ;*** ANY DEVICE WHICH SETS ITS ***
3613 ;*** INTERRUPT ENABLE BIT WILL ***
3614 ;*** SERVICED. ***
3615
3616 ;CLOCK INTERRUPTS WILL CHANGE CPU PRIORITY TO LEVEL 6 (IN 'ST5')
3617 ;RK06 CONTROLLER INTERRUPTS WILL CHANGE CPU PRIORITY TO LEVEL 5 IN 'SETINT')
3618 ;KEYBOARD INTERRUPTS WILL CHANGE CPU PRIORITY TO LEVEL 4 (SEE ABOVE)
3619
3620 ;ALL 'SYSMAC' TRAPS WILL CHANGE CPU PRIORITY TO LEVEL 7 (SEE BELOW)
3621
3622 ;SYSMAC 'SETUP'
3623
3624 .SBTTL INITIALIZE THE COMMON TAGS
3625 ;;CLEAR THE COMMON TAGS ($CMTAG) AREA
3626 010100 012706 001100 MOV #SCMTAG,R6 ;;FIRST LOCATION TO BE CLEARED
3627 010104 005026 CLR (R6)+ ;;CLEAR MEMORY LOCATION
3628 010106 022706 001140 CMP #SWR,R6 ;;DONE?
3629 010112 001374 BNE -6 ;;LOOP BACK IF NO
3630 010114 012706 001100 MOV #STACK,SP ;;SETUP THE STACK POINTER
3631 ;;INITIALIZE A FEW VECTORS
3632 010120 012737 050366 000020 MOV #SCOPE,#IOTVEC ;;IOT VECTOR FOR SCOPE ROUTINE
3633 010126 012737 000340 000022 MOV #340,#IOTVEC+2 ;;LEVEL 7

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3634 010134 012737 050646 000030      MOV      #ERROR, @#EMTVEC      ;; EMT VECTOR FOR ERROR ROUTINE
3635 010142 012737 000340 000032      MOV      #340, @#EMTVEC+2    ;; LEVEL 7
3636 010150 012737 054476 000034      MOV      #STRAP, @#TRAPVEC    ;; TRAP VECTOR FOR TRAP CALLS
3637 010156 012737 000340 000036      MOV      #340, @#TRAPVEC+2   ;; LEVEL 7
3638 010164 012737 050122 000024      MOV      #SPWRDN, @#PWRVEC    ;; POWER FAILURE VECTOR
3639 010172 012737 000340 000026      MOV      #340, @#PWRVEC+2    ;; LEVEL 7
3640 010200 013737 042740 042732      MOV      SENDCT, $EOPCT      ;; SETUP END-OF-PROGRAM COUNTER
3641 010206 005037 001174          CLR      $TIMES              ;; INITIALIZE NUMBER OF ITERATIONS
3642 010212 005037 001176          CLR      $ESCAPE            ;; CLEAR THE ESCAPE ON ERROR ADDRESS
3643 010216 112737 000001 001115      MOV      #1, $SERMAX         ;; ALLOW ONE ERROR PER TEST
3644 010224 012737 010224 001106      MOV      #., $SLPADR         ;; INITIALIZE THE LOOP ADDRESS FOR SCOPE
3645 010232 012737 010232 001110      MOV      #., $SLPERR         ;; SETUP THE ERROR LOOP ADDRESS
3646          ;; SIZE FOR A HARDWARE SWITCH REGISTER, IF NOT FOUND OR IT IS
3647          ;; EQUAL TO A "-1" SETUP FOR A SOFTWARE SWITCH REGISTER.
3648 010240 013746 000004          MOV      @#ERRVEC, -(SP)     ;; SAVE ERROR VECTOR
3649 010244 012737 010300 000004      MOV      #64$, @#ERRVEC     ;; SET UP ERROR VECTOR
3650 010252 012737 177570 001140      MOV      #DSWR, SWR          ;; SETUP FOR A HARDWARE SWICH REGISTER
3651 010260 012737 177570 001142      MOV      #DDISP, DISPLAY     ;; AND A HARDWARE DISPLAY REGISTER
3652 010266 022777 177777 170644      CMP      #-1, @SWR           ;; TRY TO REFERENCE HARDWARE SWR
3653 010274 001012          BNE      66$                ;; BRANCH IF NO TIMEOUT TRAP OCCURRED
3654          ;; AND THE HARDWARE SWR IS NOT = -1
3655 010276 000403          BR       65$                ;; BRANCH IF NO TIMEOUT
3656 010300 012716 010306 64$:      MOV      #65$, (SP)         ;; SET UP FOR TRAP RETURN
3657 010304 000002          RTI                          ;;
3658 010306 012737 000176 001140 65$:      MOV      #SWREG, SWR        ;; POINT TO SOFTWARE SWR
3659 010314 012737 000174 001142      MOV      #DISPREG, DISPLAY   ;;
3660 010322 012637 000004 66$:      MOV      (SP)+, @#ERRVEC    ;; RESTORE ERROR VECTOR
3661          ;;
3662 010326 005037 001216          CLR      $PASS              ;; CLEAR PASS COUNT
3663 010332 132737 000200 001231      BITB    #APTSIZE, $ENVM     ;; TEST USER SIZE UNDER APT
3664 010340 001403          BEQ     67$                ;; YES, USE NON-APT SWITCH
3665 010342 012737 001232 001140      MOV      #SSWREG, SWR       ;; NO, USE APT SWITCH REGISTER
3666 010350          67$:
3667 010350 012737 010414 000004 MEMPAR: MOV      #1$, ERRVEC         ;; SETUP TIMEOUT VECTOR
3668 010356 012737 000340 000006      MOV      #PR7, ERRVEC+2
3669          ;;
3670 010364 012701 172100          MOV      #MEMBAS, R1        ;; ADDR OF MEM CSR
3671 010370 005011 3$:      CLR      (R1)               ;; SEE IF CAN REFERENCE
3672 010372 012711 000001          MOV      #1, (R1)           ;; SET ENABLE BIT IF YES
3673 010376 012737 050024 000114      MOV      #MEMERR, MEMVEC    ;; LOAD VECTOR IF NO TIMEOUT
3674 010404 012737 000340 000116      MOV      #PR7, MEMVEC+2
3675 010412 000401          BR       2$                ;;
3676          ;;
3677 010414 022626 1$:      CMP      (SP)+, (SP)+       ;; ADJ STACK
3678 010416 062701 000002 2$:      ADD     #2, R1              ;; TRY NEXT CSR
3679 010422 020127 172140      CMP     R1, #MEMBAS+40     ;; SEE IF TRIED ALL
3680 010426 001360          BNE     3$                ;; BR IF NO
3681 010430 012737 000006 000004      MOV      #ERRVEC+2, ERRVEC  ;; RESTORE TRAP CATCHER
3682 010436 005037 000006          CLR      ERRVEC+2
3683          ;;
3684 010442 004737 043026          JSR     PC, CLRFLG         ;; CLEAR DDUMP THRU UNITB
3685 010446 005037 001220          CLR     $DEVCT
3686 010452 005037 001222          CLR     $UNIT
3687
3688
3689          ;; FIND OUT IF XXDP, ACT, APT; CHAIN OR DUMP MODE

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3690 ;
3691 ;
3692 010456 005737 000042 START1: TST 42
3693 010462 001014 BNE 1$ ;BR IF AUTO
3694 010464 004737 043046 JSR PC,TITLE ;MANUAL, TYPE PROG ID
3695 010470 123727 000041 000013 CMPB 41,#13 ;13=LOADED BY XXDP
3696 010476 001010 BNE 2$
3697 010500 005237 005424 INC DDUMP ;SET RK06 DUMP MODE FLAG
3698 010504 104401 055725 TYPE MSG2 ;REPLACE DRO PACK W/SCRATCH & DO<CR>
3699 010510 000137 010524 JMP ST2
3700 010514 000137 010570 1$: JMP ST3
3701 010520 005237 005432 2$: INC PPTP ;SET ACT/APT/PTP DUMP MODE FLAG
3702 ;
3703 ;
3704 ;CHECK IF ALL PARAMETERS DEFAULTED. IF NOT, BEGIN INPUT DIALOGUE
3705 ;WITH OPERATOR. THE REPLY TO 'DRIVES TO BE TESTED' SHOULD BE
3706 ;DRIVE NOS. SEPERATED BY COMMAS & TERMINATED BY <CR>
3707 ; EX: DRIVES TO BE TESTED: 0,2,4<CR>
3708 ;FOR DUAL PORT, ONLY EVEN NUMBERED DRIVES MUST BE TESTED
3709 ;
3710 010524 005737 001336 ST2: TST PARAM
3711 010530 001002 BNE 1$ ;BR IF 220 START
3712 010532 000137 010622 JMP ST4 ;200 START, DEFAULT & SIZE THE BUSS
3713 010536 104401 055776 1$: TYPE MSG3 ;DRIVES TO BE TESTED
3714 010542 004737 043126 JSR PC,GDRVS ;GET DR NOS.
3715 010546 104401 056051 TYPE MSG4 ;BUSS ADDR
3716 010552 004737 043274 JSR PC,GBA ;GET BA
3717 010556 104401 056103 TYPE MSG5 ;CONT INT VECTOR
3718 010562 004737 043322 JSR PC,GINT ;GET INT VECTOR
3719 010566 000427 BR ST5
3720 ;
3721 ;
3722 ;AUTO MODE
3723 ;CHECK IF LOADED BY XXDP OR OTHER. SET FLAGS & NO INPUT DIALOGUE.
3724 ;DEFAULT ALL PARAMETERS. TEST ONLY THOSE DRIVES THAT ARE READY
3725 ;ON THE BUSS
3726 ;
3727 ;
3728 010570 123727 000041 000013 ST3: CMPB 41,#13 ;13=LOADED BY XXDP
3729 010576 001007 BNE 1$
3730 010600 005237 005426 INC DDPCH ;SET RK06 CHAIN MODE FLAG
3731 010604 004737 043046 JSR PC,TITLE
3732 010610 104401 056205 TYPE MSG7 ;DRO NOT TSTD
3733 010614 000402 BR ST4
3734 010616 005237 005430 1$: INC ACT11 ;SET ACT AUTO FLAG.
3735 ;
3736 010622 012737 177440 001264 ST4: MOV #177440,$BASE ;DEFAULT VALUE
3737 010630 012737 000210 001314 MOV #210,RKVEC ;DEFAULT VALUE
3738 010636 004737 043354 JSR PC,SETINT
3739 010642 005237 005462 INC SI2FLG ;DO "SIZE THE BUSS" TEST
3740 ;
3741 010646 005037 005276 ST5: CLR UNLD ;INITIALIZE FLAGS
3742 010652 005037 005300 CLR BADHDR ;USED IN 'STOP' ROUTINE
3743 010656 005037 005302 CLR HPEND ;FOR VALID PROGRAM HALTS
3744 010662 005037 001176 CLR $ESCAPE
3745 010666 005037 001172 CLR $TMP5 ;CLEAR RK07 FLAG

```

```

3746 010672 012737 005436 001342      MOV      #DRIVO,DRVPTR      ;SETUP
3747 010700 005037 001220      CLR      $DEVCT           ;NO. OF DRVS DONE
3748 010704 005037 001222      CLR      $UNIT           ;CURRENT DRV UNDER TEST
3749 010710 005037 005464      CLR      UNITB           ;PORT A TESTING
3750 010714 112737 000101 056560  MOVB     #'A,MSG19A       ;PORT A TESTING
3751 010722 012737 010770 000004  MOV      #1$,ERRVEC      ;SETUP TIMEOUT ERROR VECTOR
3752 010730 005777 170372      TST     @LKS             ;SEE IF L-CLOCK THERE
3753 010734 005237 005456      INC     LCLKF           ;PRESENT, SET FLAG.
3754 010740 013700 001330      MOV     LCVEC,RO        ;VECTOR ADDR
3755 010744 012737 011026 000004  MOV     #2$,ERRVEC
3756 010752 005777 170342      TST     @PKS             ;SEE IF P-CLOCK THERE
3757 010756 005237 005460      INC     PCLKF           ;PRESENT, SET FLAG
3758 010762 013700 001332      MOV     PCVEC,RO        ;VECTOR ADDR
3759 010766 000412
3760
3761 010770 022626 011032 000004  1$:     CMP     (SP)+,(SP)+      ;L-CLOCK NOT THERE, CLEAR STACK
3762 010772 012737 011032 000004  MOV     #4$,ERRVEC
3763 011000 005777 170314      TST     @PKS             ;SEE IF P-CLOCK THERE
3764 011004 005237 005460      INC     PCLKF           ;PRESENT, SET FLAG
3765 011010 013700 001332      MOV     PCVEC,RO        ;VECTOR ADDR
3766 011014 012720 047154 000004  3$:     MOV     #CLOCK,(RO)+    ;SERVICE ROUTINE FOR CLOCKS
3767 011020 012710 000300      MOV     #PR6,(RO)
3768 011024 000407      BR      TST1            ;GO TO NEXT TEST
3769
3770 011026 022626 011032 000004  2$:     CMP     (SP)+,(SP)+      ;P-CLOCK NOT THERE, CLEAR STACK
3771 011030 000771      BR      3$
3772
3773 011032 022626 011032 000004  4$:     CMP     (SP)+,(SP)+      ;NEITHER CLOCK THERE, CLEAR STACK
3774 011034 104401 056360      TYPE    MSG13           ;NO CLOCKS PRESENT TESTS BYPASSED
3775 011040 000137 042644      JMP     $EOP
3776
3777

```

.SBTTL BASIC CONTROLLER TESTS, SIZING & SETUP

```

*****
*TEST 1 REFERENCE ALL CONTROLLER REGISTERS

```

```

*
* THIS TEST VERIFIES THAT ALL THE CONTROLLER REGISTERS
* CAN BE ACCESSED. THE INABILITY TO BE ACCESSED WILL
* RESULT IN A TIMEOUT TRAP WITH AN ERROR MESSAGE. ANY
* ERROR IN THIS TEST WILL RESULT IN ABORTING ALL OTHER
* TESTS AND JUMPING TO 'END OF PASS'

```

```

*****

```

3778									
3779									
3780									
3781									
3782									
3783									
3784									
3785									
3786									
3787									
3788									
3789									
3790	011044	000004				TST1:	SCOPE		
3791	011046	012737	000001	001174			MOV #1,\$TIMES	::DO 1 ITERATION	
3792	011054	012706	001100				MOV #STACK,SP	::RESTORE STK PTR	
3793									
3794	011060	012746	000000				MOV #PRO,-(SP)	::RESET PSW TO PRIORITY 0	
3795	011064	012746	011072				MOV #SS,-(SP)	::& MAKE IT LSI COMPATABLE	
3796	011070	000002					RTI		
3797	011072					SS:			
3798									
3799	011072	012737	011216	000004			MOV #1\$,ERRVEC	::SETUP TIMEOUT ERROR VECTOR	
3800	011100	013705	001264				MOV \$BASE,R5	::SETUP INDEX REG.	
3801	011104	005765	000000				TST RKCS1(R5)	::REFERENCE ALL THE	
3802	011110	005765	000010				TST RKCS2(R5)	::CONTROLLER REGISTERS	
3803	011114	005765	000002				TST RKWC(R5)		
3804	011120	005765	000004				TST RKBA(R5)		
3805	011124	005765	000006				TST RKDA(R5)		
3806	011130	005765	000012				TST RKDS(R5)	::TIMEOUTS IN THIS SECTION	
3807	011134	005765	000014				TST RKER(R5)	::INDICATE THAT THE CONTROLLER	
3808	011140	005765	000016				TST RKASOF(R5)	::REGISTERS CANNOT BE READ.	
3809	011144	005765	000020				TST RKDC(R5)	::TESTING SHOULD NOT PROCEED	
3810	011150	005765	000024				TST RKDB(R5)	::UNTIL THIS IS REMEDIED.	
3811	011154	005765	000026				TST RKMR1(R5)		
3812	011160	005765	000034				TST RKMR2(R5)		
3813	011164	005765	000036				TST RKMR3(R5)		
3814	011170	005765	000030				TST RKECPS(R5)		
3815	011174	005765	000032				TST RKECPT(R5)		
3816									
3817	011200	012737	047736	000004			MOV #BADTMO,ERRVEC	::SETUP TIMEOUT HANDLER	
3818	011206	012737	000340	000006			MOV #PR7,ERRVEC+2		
3819	011214	000404					BR TST2	::GO TO NEXT TEST	
3820									
3821	011216	022626				1\$:	CMP (SP)+,(SP)+	::RESTORE STACK POINTER	
3822	011220	104007					ERROR 7	::ABORT-COULD NOT REFERENCE CONTROLLER REGISTER	
3823	011222	000137	042704				JMP \$EOP1		
3824									
3825									
3826									
3827									
3828									
3829									
3830									
3831									
3832									
3833									

```

*****
*TEST 2 SIZE THE BUSS

```

```

*
* THIS TEST IS ENTERED ONLY IF 'DRIVE SELECTION' IS DEFAULTED
* EITHER BY RUNNING IN THE AUTO MODE OR A 200 START IN THE
* MANUAL MODE.
* EVERY EVEN NUMBERED DRIVE (0,2,4,6) IS ADDRESSED.
* CONTROLLER ERROR (CERR) IS EXAMINED AND IF NOT SET, THE

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```

3834      *      DRIVE WILL BE TESTED AS AN RK06.  IF SET, THE PROGRAM WILL BYPASS
3835      *      TESTING THAT DRIVE ONLY IF THE ERROR WAS A RESULT OF
3836      *      MDS, UFE OR NED BEING SET; OR BOTH NED & DRA RESET IN-
3837      *      DICATING THE OTHER PORT IS ACCESSED.
3838      *      IF CERR DUE TO DTYE, DRIVE WILL BE TESTED AS AN RK07.
3839      *
3840      *
3841      *      *****
3841      *      †ST2:  SCOPE
3842      *      MOV      #1,$TIMES      ;;DO 1 ITERATION
3843      *      MOV      #STACK,SP    ;;RESTORE STK PTR
3844      *
3845      *      INC      BYPCERR      ;;DO NOT DO 'CKCERR' ROUTINE
3846      *
3847      *
3848      *      BITB     #BIT7,$ENVM   ;;SEE IF USE APT SELECTED DRIVES
3849      *      BNE     14$          ;;BR IF YES
3850      *      JMP     12$          ;;ELSE DO NORM SIZING OR VERIFY
3851      *
3852      *      14$:  TYPE     ,MSG10  ;;WILL TEST DRIVES
3853      *      CLR     DRIVS      ;;# OF DRIVES PRESENT
3854      *      CLR     RO         ;;DRV ADDR
3855      *      MOV     #DRIVO,R1   ;;DRV FLAG
3856      *      MOV     $DEV M,R2   ;;APT DEVICE MAP
3857      *
3858      *      15$:  BIT      #BIT0,R2 ;;SEE IF DRV IN DEVICE MAP
3859      *      BEQ     16$          ;;BR IF NO
3860      *      CMP     #BIT0,R0    ;;SEE IF ODD # DRIVE
3861      *      BEQ     11$          ;;BR IF NO
3862      *
3863      *      TYPE     ,EM28      ;;ONLY EVEN # ALLOWED IN $DEV M
3864      *      HALT                    ;;RELOAD $DEV M & PRESS 'CONTINUE'
3865      *      JMP     PRGSRT      ;;RESTART
3866      *      11$:  INC     DRIVS   ;;ELSE INCR DRIVE COUNT
3867      *      INC     (R1)        ;;& SET DRIVE PRESENT FLAG
3868      *      TYPE     ,SCRLF
3869      *      MOV     RO,-(SP)    ;;SAVE RO FOR TYPEOUT
3870      *      ;;TYPE DRIVE #
3871      *      ;;GO TYPE--OCTAL ASCII
3872      *      .BYTE  1           ;;TYPE 1 DIGIT(S)
3873      *      .BYTE  0           ;;SUPPRESS LEADING ZEROS
3874      *
3875      *      16$:  CMP     (R1)+,(R1)+ ;;ADV PTR TO NEXT EVEN #
3876      *      ADD     #2,RO        ;;ADD DRV ADDR TO NEXT EVEN #
3877      *      CMP     #8,RO        ;;ALL 4 TESTED?
3878      *      BEQ     17$          ;;BR IF YES
3879      *
3880      *      ROR     R2           ;;ELSE GET NEXT BIT OFF DEVICE MAP
3881      *      BR     15$          ;;& TRY AGAIN
3882      *
3883      *      17$:  TST     DRIVS   ;;SEE IF MORE DRIVES PRESENT
3884      *      BEQ     18$          ;;BR IF NO
3885      *      JMP     VERIFY      ;;ELSE EXIT TEST & SETUP FOR RK07'S
3886      *
3887      *      18$:  ERROR   75      ;;NO DRIVES FOUND IN $DEV M
3888      *      HALT                    ;;SETUP CORRECTLY & PRESS 'CONTINUE'
3889      *      JMP     STS         ;;TO TRY AGAIN

```

```

3890 011414 000137 012076          20$: JMP      VERIFY          ;DO NOT SIZE, GO TO NEXT TEST
3891
3892 011420 012765 000040 000010 12$: MOV      #SCLR,RKCS2(R5) ;SUBSYSTEM CLEAR
3893 011426 013737 001400 005352  MOV      T10,TEMP1        ;SET TIMEOUT
3894 011434 004737 043466          JSR      PC,FRDY          ;FIND RDY
3895 011440 104120          ERROR    120             ;RDY NOT SET BY END OF SCLR
3896 011442 005737 005462          TST      SIZFLG          ;SIZE BUS?
3897 011446 001762          BEQ      20$             ;BR IF NO
3898 011450 104401 056273          TYPE    ,MSG10          ;WILL TEST DRIVES
3899 011454 005037 005434          CLR      DRVS           ;# OF DRIVES PRESENT
3900 011460 005000          CLR      RO             ;DRV ADDR
3901 011462 012701 005436          MOV      #DRIVO,R1      ;DRV FLAG
3902 011466
3903 011466 104415          1$: SCOP1
3904 011470 012706 001100          MOV      #STACK,SP      ;RESTORE STK PTR
3905
3906 011474 012765 000040 000010  MOV      #SCLR,RKCS2(R5) ;SUBSYSTEM CLEAR
3907 011502 013737 001400 005352  MOV      T10,TEMP1        ;SET TIMEOUT
3908 011510 004737 043466          JSR      PC,FRDY          ;FIND RDY
3909 011514 104120          ERROR    120             ;RDY NOT SET BY END OF SCLR
3910 011516 010065 000010  MOV      RO,RKCS2(R5)    ;SELECT THE DRIVE ADDR
3911 011522 012737 000001 005314  MOV      #SELDRV,HCS1
3912 011530 053737 001170 005314  BIS      $TMP4,HCS1      ;ADD CDT IF RK07
3913 011536 013765 005314 000000  MOV      HCS1,RKCS1(R5) ;GET STATUS
3914 011544 013737 001412 005352  MOV      T5000,TEMP1
3915 011552 004737 044242          JSR      PC,DLY          ;DO DELAY TO CATCH MDS
3916 011556 013737 001400 005352  MOV      T10,TEMP1
3917 011564 004737 043466          JSR      PC,FRDY          ;FIND RDY
3918 011570 104117          ERROR    117             ;NO RDY AFTER SELECT DRIVE CMD.
3919 011572 032737 100000 005314  BIT      #CERR,HCS1
3920 011600 001053          BNE     2$
3921 011602 013737 005342 005352  MOV      HMR2,TEMP1
3922 011610 042737 177770 005352  BIC      #C<DRVMSK>,TEMP1
3923 011616 020037 005352          CMP      RO,TEMP1      ;S/B SAME
3924 011622 001020          BNE     3$
3925 011624 005700          TST     RO
3926 011626 001003          BNE     4$
3927 011630 005737 005426          TST     DDPCH          ;SEE IF XXDP CHAIN MODE
3928 011634 001016          BNE     5$
3929 011636 005237 005434          4$: INC     DRVS          ;INC DRIVE COUNT.
3930 011642 005211          INC     (R1)           ;SET DRIVE PRESENT FLAG
3931 011644 053711 001170          BIS     $TMP4,(R1)     ;ADD CDT IF RK07.
3932 011650 104401 001205          TYPE    $CRLF
3933 011654 010046          MOV     RO,-(SP)      ;SAVE RO FOR TYPEOUT
3934
3935 011656 104403          TYPOS
3936 011660 001          .BYTE  1
3937 011661 000          .BYTE  0
3938 011662 000403          BR      5$            ;GO TYPE--OCTAL ASCII
3939
3940 011664 004737 044260          3$: JSR     PC,BYP        ;TYPE BYPASS DR #
3941 011670 104001          ERROR   1             ;WRITTEN DR # DOES NOT MATCH RKMR2 DR #
3942
3943 011672 022121          5$: CMP     (R1)+,(R1)+ ;ADV PTR TO NEXT EVEN #
3944 011674 062700          ADD     #2,RO          ;ADV DRV ADDR TO NEXT EVEN #
3945 011700 005037 001170          CLR     $TMP4         ;CLEAR FOR NEXT TRY

```

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3946 011704 022700 000010          CMP      #8.,RO          ;TESTED ALL 4 DRIVES?
3947 011710 001266                BNE      1$              ;BR IF NO
3948 011712 005737 005434          TST      DRVS
3949 011716 001065                BNE      10$
3950 011720 104076                ERROR    76              ;NO DRIVES FOUND ON BUSS
3951 011722 000000                HALT
3952 011724 000137 010646          JMP      STS              ;SETUP CORRECTLY
3953                                     ;AND PRESS 'CONTINUE'
3954 011730 032737 000040 005330 2$:  BIT      #DTYE,HER
3955 011736 001405                BEQ      13$
3956 011740 012737 002000 001170    MOV      #CDT,$TMP4      ;ADD CDT
3957 011746 000137 011466                JMP      1$              ;TRY AGAIN
3958 011752 032737 001000 005316 13$: BIT      #MDS,HCS2
3959 011760 001015                BNE      6$
3960 011762 032737 000400 005316    BIT      #UFE,HCS2
3961 011770 001015                BNE      7$
3962 011772 032737 000001 005326    BIT      #DRA,HDS
3963 012000 001015                BNE      8$
3964 012002 032737 010000 005316    BIT      #NED,HCS2
3965 012010 001424                BEQ      9$
3966 012012 000727                BR       5$
3967
3968 012014 004737 044260          6$:  JSR      PC,BYP          ;TYPE BYP DR #
3969 012020 104002                ERROR    2$              ;MDS DETECTED
3970 012022 000723                BR       5$
3971
3972 012024 004737 044260          7$:  JSR      PC,BYP
3973 012030 104003                ERROR    3$              ;UFE DETECTED
3974 012032 000717                BR       5$
3975
3976 012034 032737 010000 005316 8$:  BIT      #NED,HCS2
3977 012042 001675                BEQ      4$
3978 012044 104401 056462          TYPE    MSG15            ;DRV#
3979 012050 010046                MOV      RO,-(SP)        ;SAVE RO FOR TYPEOUT
3980                                     ;TYPE DR#
3981 012052 104403                TYPOS
3982 012054 001                .BYTE 1                ;GO TYPE--OCTAL ASCII
3983 012055 000                .BYTE 0                ;TYPE 1 DIGIT(S)
3984 012056 104010                ERROR    10             ;SUPPRESS LEADING ZEROS
3985 012060 000704                BR       5$              ;DRA & NED BOTH SET
3986
3987 012062 004737 044260          9$:  JSR      PC,BYP
3988 012066 104004                ERROR    4$              ;NO DRA & NO NED = OTHER PORT SELECTED
3989 012070 000700                BR       5$
3990 012072 000137 012472          10$: JMP      NUDRV
3991
3992 012076          VERIFY:
3993
3994
3995
3996
3997
3998
3999
4000
4001

```

```

*****
*TEST 3          VERIFY OPERATOR DRIVE SELECTIONS
*
* THIS TEST IS ENTERED ONLY IF DRIVE SELECTION IS NOT
* DEFAULTED. EVERY EVEN NUMBERED DRIVE (0,2,4,6) IS ADDRESSED &
* CONTROLLER ERROR (CERR) IS EXAMINED. IF NOT SET, THE
* PROGRAM WILL ASSUME THE DRIVE IS PRESENT AS AN RK06.
*

```



```

4002
4003
4004
4005
4006
4007
4008
4009
4010 012076 000004
4011 012100 012737 000001 001174
4012 012106 012706 001100
4013 012112 005000
4014 012114 012701 005436
4015 012120
4016 012120 104415
4017 012122 012706 001100
4018
4019 012126 012765 000040 000010
4020 012134 013737 001400 005352
4021 012142 004737 043466
4022 012146 104120
4023 012150 010065 000010
4024 012154 012737 000001 005314
4025 012162 053737 001170 005314
4026 012170 013765 005314 000000
4027 012176 013737 001412 005352
4028 012204 004737 044242
4029 012210 013737 001400 005352
4030 012216 004737 043466
4031 012222 104117
4032 012224 032737 100000 005314
4033 012232 001037
4034 012234 013737 005342 005352
4035 012242 042737 177770 005352
4036 012250 020037 005352
4037 012254 001015
4038 012256 005711
4039 012260 001402
4040 012262 053711 001170
4041 012266 022121
4042 012270 062700 000002
4043 012274 005037 001170
4044 012300 022700 000010
4045 012304 001305
4046 012306 000475
4047
4048 012310 004737 044260
4049 012314 104001
4050 012316 005711
4051 012320 001762
4052 012322 005337 005434
4053 012326 005011
4054 012330 000756
4055
4056 012332 032737 000040 005330
4057 012340 001405

```

```

*****
IF CERR WAS SET, THAT DRIVE WILL BE BYPASSED
ONLY IF THE ERROR WAS A RESULT OF MDS OR UFE SET OR BOTH
NED & DRA RESET (WRONG PORT). IF CERR IS A RESULT OF
NED ONLY, IT IS CHECKED AGAINST THE INPUTTED INFORMATION TO
VERIFY IT WAS NOT SPECIFIED.
IF CERR DUE TO DTYE, DRIVE WILL BE TESTED AS AN RK07.
*****
TST3: SCOPE
MOV #1,STIMES ;DO 1 ITERATION
MOV #STACK,SP ;RESTORE STK PTR
CLR RO ;DRIVE ADDR
MOV #DRIVO,R1 ;DRIVE FLAG
1$: SCOPI
MOV #STACK,SP ;RESTORE STK PTR
MOV #SCLR,RKCS2(R5) ;SUBSYSTEM CLEAR
MOV T10,TEMP1 ;SET TIME OUT
JSR PC,FRDY ;FIND RDY
ERROR 120 ;NO RDY AFTER SCLR
MOV RO,RKCS2(R5) ;DRV ADDR
MOV #SELDRV,HCS1
BIS STMP4,HCS1 ;ADD CDT IF RK07
MOV HCS1,RKCS1(R5) ;GET STATUS
MOV T50000,TEMP1
JSR PC,DLY ;DO DELAY TO CATCH MDS
MOV T10,TEMP1
JSR PC,FRDY ;FIND RDY
ERROR 117 ;NO RDY AFTER SELECT DRIVE CMD.
BIT #CERR,HCS1
BNE 2$
MOV HMR2,TEMP1
BIC #C<DRVMSK>,TEMP1
CMP RO,TEMP1 ;S/B SAME
BNE 3$
11$: TST (R1)
BEQ 4$
BIS STMP4,(R1) ;SET RK07 FLAG
CMP (R1)+,(R1)+ ;ADV PTR TO NEXT EVEN #
ADD #2,RO ;ADV DRV ADDR TO NEXT EVEN #
CLR STMP4 ;CLEAR FOR NEXT DRIVE
CMP #8.,RO
BNE 1$ ;MORE LEFT
BR TST4 ;GO TO NEXT TEST
3$: JSR PC,BYP ;TRY BYPASS DRIVE#
ERROR 1 ;WRITTEN DR# DOES NOT MATCH RKMR2 DR#
TST (R1)
BEQ 4$ ;BRANCH IF NOT SPEC BY INPUT
4$: DEC DRIVS ;DECREMENT TOTAL DRIVS
CLR (R1) ;CLEAR DRIVE FLAG
BR 4$
2$: BIT #DTYE,HER
BEQ 13$

```

```

4058 012342 012737 002000 001170      MOV      #CDT,STMP4      ;ADD CDT
4059 012350 000137 012120      JMP      1$              ;TRY AGAIN
4060
4061 012354 032737 001000 005316 13$:    BIT      #MDS,HCS2
4062 012362 001027      BNE     6$
4063 012364 032737 000400 005316      BIT      #UFE,HCS2
4064 012372 001027      BNE     7$
4065 012374 032737 000001 005326      BIT      #DRA,HDS
4066 012402 001005      BNE     8$
4067 012404 032737 010000 005316      BIT      #NED,HCS2
4068 012412 001423      BEQ     9$
4069 012414 000404      BR      10$
4070 012416 032737 010000 005316 8$:    BIT      #NED,HCS2
4071 012424 001714      BEQ     11$
4072 012426 005711      TST     (R1)
4073 012430 001716      BEQ     4$
4074
4075 012432 004737 044260      JSR     PC,BYP          ;TYPE BYPASS DRIVE#
4076 012436 104006      ERROR  6
4077 012440 000730      BR      12$
4078
4079 012442 004737 044260      JSR     PC,BYP          ;TYPE BYPASS DRIVE#
4080 012446 104002      ERROR  2                ;MDS DETECTED
4081 012450 000724      BR      12$
4082
4083 012452 004737 044260      JSR     PC,BYP
4084 012456 104003      ERROR  3                ;UFE DETECTED
4085 012460 000720      BR      12$
4086
4087 012462 004737 044260      JSR     PC,BYP
4088 012466 104004      ERROR  4                ;DRA & NED RESET - OTHER PORT SELECTED
4089 012470 000714      BR      12$
4090
4091
4092
4093
4094
4095
4096
4097
4098
4099
4100 012472 005037 001456      NUDRV: CLR      BYPCERR  ;ENTER HERE FROM LAST TEST
4101
4102 012476 005037 001170      CLR      STMP4          ;ALLOW CHECKING CERR IN 'FRDY'
4103
4104
4105
4106
4107
4108
4109
4110
4111
4112
4113 012502 000004      TST4:  SCOPE

```

; THIS PART OF THE PROGRAM WILL BE REPEATED FOR EACH
; DRIVE PRESENT
; 'SUNIT' CONTAINS THE ADDRESS OF THE DRIVE CURRENTLY
; UNDER TEST
; *****
; *TEST 4 FIND NEXT DRIVE TO BE TESTED
; *
; * THIS TEST FINDS THE NEXT DRIVE PRESENT & PUTS THAT
; * ADDRESS IN 'SUNIT' & STMP4 IS SET TO CDT IF RK07.
; * THROUGHOUT THE FOLLOWING TESTS, THE DRIVE TESTED IS
; * THE DRIVE WHOSE ADDRESS IS IN 'SUNIT'.
; *
; *****

```

4114 012504 012737 000001 001174      MOV      #1,$TIMES      ;;DO 1 ITERATION
4115 012512 012706 001100      MOV      #STACK,SP    ;RESTORE STK PTR
4116 012516 012737 000004 001214      MOV      #STN-1,$STSTN
4117 012524 012737 000004 001102      MOV      #STN-1,$STSTNM
4118
4119 012532 005737 005434      TST      DRIVS        ;ANY DRIVES PRESENT?
4120 012536 001004      BNE      4$          ;YES BRANCH
4121 012540 104401 056242      TYPE    MSG8        ;ALL DRIVES TESTED
4122 012544 000137 042704      JMP      $EOP1      ;NO, GO TO END
4123
4124 012550 005037 005464      CLR      UNITB      ;SETUP PORT A
4125 012554 112737 000101 056560 4$:  MOV      #A,MSG19A
4126 012562 013701 001342      MOV      DRVPTR,R1  ;ADDR OF NEXT DRIVE FLAG
4127 012566 005737 001220      TST      $DEVCT     ;IS FIRST DRIVE BEING CHECKED
4128 012572 001403      BEQ      2$          ;YES BRANCH
4129 012574 062737 000002 001222 1$:  ADD      #2,$UNIT    ;INCR TO NEXT EVEN DRIVE
4130 012602 005711 000004      TST      (R1)       ;IS DRIVE PRESENT?
4131 012604 001003      BNE      5$          ;BR IF YES
4132 012606 062701 000004      ADD      #4,R1      ;ELSE INCR PTR TO NEXT EVEN DRIVE
4133 012612 000770      BR       1$          ;& TRY AGAIN
4134
4135 012614 005737 005426      5$:  TST      DDPCH      ;DDP CHAIN MODE?
4136 012620 001406      BEQ      3$          ;BR IF NO
4137 012622 005737 001222      TST      $UNIT      ;ELSE SEE IF DRV 0
4138 012626 001003      BNE      3$          ;BR IF NO
4139 012630 062701 000004      ADD      #4,R1      ;ELSE FIND NEXT EVEN DRIVE PRESENT
4140 012634 000757      BR       1$
4141
4142 012636 032711 002000      3$:  BIT      #CDT,(R1)  ;SEE IF DRIVE UNDER TEST IS RK07
4143 012642 001403      BEQ      6$          ;BR IF NO
4144 012644 012737 002000 001170      MOV      #CDT,$TMP4 ;ELSE SET RK07 FLAG
4145 012652 062701 000004      ADD      #4,R1
4146 012656 010137 001342      MOV      R1,DRVPTR  ;STORE PTR OF NEXT EVEN DRIVE FLAG
4147 012662 104401 056462      TYPE    MSG15      ;"DRIVE"
4148 012666 013700 001222      MOV      $UNIT,RO
4149 012672 010046      MOV      RO,-($P)   ;;SAVE RO FOR TYPEOUT
4150
4151 012674 104403      TYPOS
4152 012676 001      .BYTE 1           ;;DRIVE #
4153 012677 000      .BYTE 0           ;;GO TYPE--OCTAL ASCII
4154
4155 ; TYPE , $CRLF ;29-SEP-77
4156
4157
4158 012700 005737 001170      TST      $TMP4      ;SEE IF RK07 UNDER TEST
4159 012704 001014      BNE      7$          ;BR IF YES
4160 012706 012737 000632 012770      MOV      #632,LC    ;ELSE LOAD RK06 PARAMERERS
4161 012714 005037 012776      CLR      E.DDT
4162 012720 012737 000777 012772      MOV      #777,MASK
4163 012726 012737 160017 012774      MOV      #160017,MASK1
4164 012734 000423      BR       TST5      ;;GOTO NEXT TEST
4165
4166 012736 012737 001456 012770 7$:  MOV      #1456,LC    ;LOAD RK07 PARAMETERS
4167 012744 012737 000400 012776      MOV      #0,DDT,E.DDT
4168 012752 012737 001777 012772      MOV      #1777,MASK
4169 012760 012737 140017 012774      MOV      #140017,MASK1

```

4170	012766	000406	BR	TST5	::GOTO NEXT TEST
4171					
4172	012770	000000	LC:	0	::LAST CYL
4173	012772	000000	MASK:	0	
4174	012774	000000	MASK1:	0	
4175	012776	000000	E.DDT:	0	::EXPECTED DRIVE TYPE TO E.A0
4176	013000	000000	FC:	0	::FIRST CYL
4177	013002	000001	FCP1:	1	::FC+1
4178	013004		PFSRT:		::ENTER HERE FOR POWER FAIL RESTART

.SBTTL DUAL PORT TESTS

```

*****
*TEST 5          TEST PORT 'A' SEIZE & TIMEOUT
*****
*
*   VERIFY THAT THE DRIVE CAN BE SEIZED & THAT THE PORT
*   TIMEOUT RELEASES THE DRIVE.
*
*   A.  SET VOLUME VALID FOR BOTH PORTS & DO RECAL COMMAND
*
*   B.  A SELECT DRIVE COMMAND IS ISSUED THRU PORT 'A'.
*       THE PROGRAM VERIFIES THE DRIVE HAS BEEN SEIZED BY 'DRIVE
*       AVAILABLE' SET.
*
*   C.  A SELECT DRIVE COMMAND IS ISSUED THRU PORT 'B' THE
*       PROGRAM VERIFIES THAT 'DRIVE AVAILABLE' IS NOT SET
*       FOR PORT 'B' & THAT CERR IS SET
*
*   D.  VERIFY THAT FOR ALL MESSAGES REQUESTED THRU PORT 'B' (MSG
*       AD-A3, B0-B3) THAT MESSAGE 0 ALWAYS RETURNS FROM PORT B
*       WHILE PORT 'A' IS SEIZED.
*
*   E.  WAIT FOR THE PORT TIMEOUT TO OCCUR ON PORT 'A' BY
*       CONTINUOUSLY CHECKING FOR ATTN ON PORT 'B'.
*       AFTER ATTN-B IS REC'D, A DRIVE SELECT COMMAND IS
*       ISSUED THRU PORT 'B' TO VERIFY THAT DRIVE AVAILABLE IS SET
*       IN MSG AD.
*
*       MEASURE THE DURATION OF THE TIMEOUT & TYPE THE VALUE
*       FOR THE FIRST PASS ONLY.
*
*   F.  VERIFY THAT ONLY PORT 'B' GETS 'DSC' & 'ATTN'.
*
*   G.  VERIFY THE DRIVE CLEAR COMMAND CLEARS 'DSC' & 'ATTN'
*       ON PORT 'B' BUT DOES NOT RELEASE THE DRIVE FROM PORT 'B'.

```

4216										
4217	013004	000004								
4218	013006	012737	000001	001174						
4219	013014	012706	001100							
4220	013020	012737	000000	005464						
4221	013026	112737	000101	056560						
4222	013034	013737	005466	001366						
4223	013042	004737	047204							
4224										
4225	013046	004737	045534							

```

*****
*ST5:  SCOPE
*      MOV #1,STIMES          ;;DO 1 ITERATION
*      MOV #STACK,SP
*      MOV #0,UNITB          ;SETUP PORT A
*      MOVB #'A,MSG19A
*      MOV TIMER,COUNT
*      JSR PC,TMO            ;DO TIMEOUT
*
*      JSR PC,SUBCLR

```

F07

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 83
TS TEST PORT 'A' SEIZE & TIMEOUT

SEQ 0083

4226	013052	104024			ERROR	24		;CERR AFTER SCLR
4227								
4228								
4229	013054	004737	044172		JSR	PC, DRAB		;SEE IF DRIVE AVAIL
4230	013060	104045			ERROR	45		;PORT A NOT AVAIL AFTER TMO
4231	013062						7\$:	
4232	013062	012765	100000	000000	MOV	#CCLR, RKCS1(R5)		
4233	013070	013765	001222	000010	MOV	\$UNIT, RKCS2(R5)		
4234	013076	063765	005464	000010	ADD	UNITB, RKCS2(R5)		
4235	013104	012737	000003	005314	MOV	#PACK, HCS1		
4236	013112	004737	043372		JSR	PC, DOCMD		;DO PACK CMD & GET CONTR RDY
4237	013116	104116			ERROR	116		;CONTR NOT RDY AFT PACK CMD
4238								
4239	013120	032737	000100	005342	BIT	#D.VV, HMR2		
4240	013126	001001			BNE	64\$		
4241	013130	104027			ERROR	27		;VV NOT SET AFTER PACK CMD
4242	013132						64\$:	
4243	013132	012737	042644	001176	MOV	#SEOP, \$ESCAPE		
4244	013140	005737	005464		TST	UNITB		
4245	013144	001022			BNE	2\$		
4246	013146	012737	000001	005464	MOV	#1, UNITB		;SETUP PORT B
4247	013154	112737	000102	056560	MOVB	#'B, MSG19A		
4248	013162	013737	005466	001366	MOV	TIMER, COUNT		
4249	013170	004737	047204		JSR	PC, TMO		;DO TIMEOUT
4250								
4251	013174	004737	045534		JSR	PC, SUBCLR		
4252	013200	104223			ERROR	223		;CERR AFTER SCLR
4253								
4254	013202	004737	044172		JSR	PC, DRAB		;SEE IF DROVE AVAIL
4255	013206	104045			ERROR	45		;PORT B NOT AVAIL AFTER TMO
4256	013210	000724			BR	7\$		
4257	013212						2\$:	
4258								
4259	013212	012765	100000	000000	MOV	#CCLR, RKCS1(R5)		
4260	013220	013765	001222	000010	MOV	\$UNIT, RKCS2(R5)		
4261	013226	063765	005464	000010	ADD	UNITB, RKCS2(R5)		;ADD 1 IF ON PORT B
4262	013234	012737	000013	005314	MOV	#RECAL, HCS1		
4263	013242	004737	043372		JSR	PC, DOCMD		;DO RECAL CMD & GET CONTR RDY
4264	013246	104124			ERROR	124		;RDY NOT SET AFTER RECAL CMD
4265								
4266	013250	012765	000001	000026	MOV	#1, RKMR1(R5)		;SELECT WORD 1
4267	013256	004737	045146		JSR	PC, GSTAT		
4268	013262	032737	020000	005342	BIT	#D.RTZ, HMR2		
4269	013270	001001			BNE	65\$		
4270	013272	104070			ERROR	70		;RTZ NOT SET DURING RECAL CMD
4271	013274	013737	001400	005354	MOV	T10, TEMP2		;SETUP TIMEOUT
4272	013302	004737	044006		JSR	PC, FATT1		;FIND ATTN
4273	013306	104055			ERROR	55		;NO ATTN AFTER RECAL CMD
4274								
4275	013310	012737	050340	005404	MOV	#<D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>, E.A0		;EXPECTED MSG A0
4276	013316	005037	005406		CLR	E.B0		;EXPECTED MSG B0
4277	013322	012737	001720	005410	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>, E.A1		;EXPECTED A1
4278	013330	012737	000001	005412	MOV	#1, E.B1		;MSG ID FOR EXPECTED MSG B1
4279	013336	005037	005414		CLR	E.A2		;EXPECTED MSG A2
4280	013342	012737	000002	005416	MOV	#2, E.B2		;MSG ID FOR EXPECTED MSG B2
4281	013350	012737	000003	005422	MOV	#3, E.B3		;MSG ID FOR EXPECTED MSG B3

G07

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 84
TS TEST PORT 'A' SEIZE & TIMEOUT

SEQ 0084

4282										
4283	013356	004737	044274			JSR	PC,CHKMSG		;CHECK MSGS A0, B0, A1, B1	
4284	013362	000007				.WORD	T.A2!T.B2!T.B3		& MSGS SPECIFIED HERE	
4285	013364	104221				ERROR	221		;MSG A0 ERROR AFTER RECAL CMD	
4286	013366	104066				ERROR	66		;MSH B0 ERROR	
4287	013370	104222				ERROR	222		;MSG A1 ERROR	
4288	013372	104067				ERROR	67		;MSG B1 ERROR	
4289	013374	012765	000002	000026		MOV	#2,RKMR1(R5)			
4290	013402	004737	045146			JSR	PC,GSTAT			
4291	013406	005737	001356			TST	CYLDIF		;SEE IF MSG A2=0	
4292	013412	001401				BEQ	66\$;BR IF YES	
4293	013414	104047				ERROR	47		;MSG A2 NOT CLEARED AFTER RECAL CMD	
4294	013416	005737	001360		66\$:	TST	CYLDIF		;SEE IF MSG B2=0	
4295	013422	001401				BEQ	67\$;BR IF YES	
4296	013424	104050				ERROR	50		;MSG B2 NOT CLEARED AFTER RECAL CMD	
4297	013426				67\$:					
4298										
4299	013426	012765	100000	000000		MOV	#CCLR,RKCS1(R5)			
4300	013434	013765	001222	000010		MOV	\$UNIT,RKCS2(R5)		;DRIVE#	
4301	013442	063765	005464	000010		ADD	UNITB,RKCS2(R5)		;ADD 1 IF ON PORT B	
4302	013450	012737	000005	005314		MOV	#CLEAR,HCS1			
4303	013456	004737	043372			JSR	PC,DOCMD		;DO DRIVE CLEAR CMD & GET CONTR RDY	
4304	013462	104151				ERROR	151		;NO RDY AFTER DRIVE CLEAR CMD	
4305	013464	004737	043750			JSR	PC,TSTATN		;TEST FOR ATTN	
4306	013470	000401				BR	68\$			
4307	013472	104154				ERROR	154		;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD	
4308	013474				68\$:					
4309										
4310	013474	012737	010340	005404		MOV	#<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0		;EXPECTED MSG A0	
4311	013502	005037	005406			CLR	E.B0		;EXPECTED MSG B0	
4312	013506	012737	001720	005410		MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1		;EXPECTED A1	
4313	013514	012737	000001	005412		MOV	#1,E.B1		;MSG ID FOR EXPECTED MSG B1	
4314	013522	005037	005414			CLR	E.A2		;EXPECTED MSG A2	
4315	013526	012737	000002	005416		MOV	#2,E.B2		;MSG ID FOR EXPECTED MSG B2	
4316	013534	012737	000003	005422		MOV	#3,E.B3		;MSG ID FOR EXPECTED MSG B3	
4317										
4318	013542	004737	044274			JSR	PC,CHKMSG		;CHECK MSGS A0, B0, A1, B1	
4319	013546	000003				.WORD	T.A2!T.B2!0		& MSGS SPECIFIED HERE	
4320	013550	104033				ERROR	33		;MSG A0 ERROR AFTER DRV CLEAR CMD	
4321	013552	104034				ERROR	34		;MSH B0 ERROR	
4322	013554	104035				ERROR	35		;MSG A1 ERROR	
4323	013556	104036				ERROR	36		;MSG B1 ERROR	
4324										
4325										
4326	013560	012737	000000	005464		MOV	#0,UNITB		;SETUP PORT A	
4327	013566	112737	000101	056560		MOV	#'A,MSG19A			
4328	013574	013737	005466	001366		MOV	TIMER,COUNT			
4329	013602	004737	047204			JSR	PC,TMO		;DO TIMEOUT	
4330										
4331	013606	004737	045534			JSR	PC,SUBCLR			
4332	013612	104024				ERROR	24		;CERR AFTER SCLR	
4333										
4334										
4335	013614	004737	044172			JSR	PC,DRAW		;SEE IF DRIVE AVAIL	
4336	013620	104045				ERROR	45		;PORT A NOT AVAIL AFTER TMO	
4337	013622	012737	014544	001176		MOV	#5\$, \$ESCAPE			

H07

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 85
T5 TEST PORT 'A' SEIZE & TIMEOUT

SEQ 0085

4338											
4339	013630	012737	010340	005404		MOV	#<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0	;EXPECTED MSG A0			
4340	013636	005037	005406			CLR	E.B0	;EXPECTED MSG B0			
4341	013642	012737	001720	005410		MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1			
4342	013650	012737	000001	005412		MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1			
4343	013656	005037	005414			CLR	E.A2	;EXPECTED MSG A2			
4344	013662	012737	000002	005416		MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2			
4345	013670	012737	000003	005422		MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3			
4346											
4347	013676	004737	044274			JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1			
4348	013702	000000				.WORD	0!0!0	; & MSGS SPECIFIED HERE			
4349	013704	104077				ERROR	77	;MSG A0 ERROR AFTER TIMEOUT			
4350	013706	104100				ERROR	100	;MSG B0 ERROR			
4351	013710	104101				ERROR	101	;MSG A1 ERROR			
4352	013712	104102				ERROR	102	;MSG B1 ERROR			
4353	013714	012737	000001	005464		MOV	#1,UNITB	;SELECT PORT B BEFORE TIMEOUT OR RELEASE			
4354	013722	112737	000102	056560		MOVB	#'B,MSG19A	;SETUP ERROR MSG FOR PORT B			
4355	013730	005037	001176			CLR	\$ESCAPE				
4356	013734	004737	044172			JSR	PC,DRAV	;SEE IF DRIVE AVAIL			
4357	013740	000401				BR	1\$;BR IF NOT AVAIL			
4358	013742	104103				ERROR	103	;PORT B AVAIL BEFORE TMO OR RELEASE			
4359											
4360	013744	032737	100000	005314	1\$:	BIT	#CERR,HCS1				
4361	013752	001001				BNE	6\$				
4362	013754	104130				ERROR	130	;CERR NOT SET AFTER SEL DRIVE & DRIVE NOT AVAIL			
4363	013756	012737	010100	005404	6\$:	MOV	#<D.SPIN!D.VV>,E.A0				
4364	013764	013737	005404	005410		MOV	E.A0,E.A1	;MSG 0 & 1 SHOULD ALWAYS RETURN SAME			
4365	013772	053737	012776	005410		BIS	E.DDT,E.A1				
4366	014000	005037	005406			CLR	E.B0	;WORD 0 FOR PORT B			
4367	014004	005037	005412			CLR	E.B1				
4368	014010	012737	014544	001176		MOV	#5\$, \$ESCAPE				
4369											
4370	014016	004737	044274			JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1			
4371	014022	000000				.WORD	0!0!0	; & MSGS SPECIFIED HERE			
4372	014024	104104				ERROR	104	;MSG A0 ERROR WHILE PORT B UNAVAILABLE			
4373	014026	104105				ERROR	105	;MSG B0 ERROR			
4374	014030	104106				ERROR	106	;MSG A1 ERROR			
4375	014032	104107				ERROR	107	;MSG B1 ERROR			
4376											
4377	014034	005737	001216			TST	\$PASS				
4378	014040	001402				BEQ	8\$;BR IF FIRST PASS			
4379	014042	000137	014544			JMP	5\$;ELSE EXIT TEST			
4380	014046	005037	001176		8\$:	CLR	\$ESCAPE				
4381	014052	012765	100000	000000		MOV	#CCLR,RKCS1(R5)				
4382	014060	012737	000000	005464		MOV	#0,UNITB	;SETUP FOR PORT A AGAIN			
4383	014066	112737	000101	056560		MOVB	#'A,MSG19A				
4384	014074	012737	000360	001366		MOV	#360,COUNT	;SETUP 4 SEC TIMEOUT			
4385	014102	004737	047114			JSR	PC,CLKON	;TURN ON CLOCK			
4386											
4387	014106	004737	044172			JSR	PC,DRAV				
4388	014112	104045				ERROR	45	;PORT A NOT AVAIL AFTER TIMEOUT			
4389											
4390	014114	012737	000001	005464		MOV	#1,UNITB	;SELECT PORT B BEFORE TIMEOUT OR RELEASE			
4391	014122	112737	000102	056560		MOVB	#'B,MSG19A				
4392	014130	004737	044172			JSR	PC,DRAV	;SEE IF PORT B DRIVE AVAIL			
4393	014134	000401				BR	3\$;BR IF NOT AVAIL			

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4394 014136 104103          ERROR 103          ;PORT B AVAIL BEFORE TMO OR RELEASE
4395
4396 014140 012765 100000 000000 3$: MOV #CCLR,RKCS1(R5)
4397 014146 013704 001222      MOV $UNIT,R4
4398 014152 063704 005464      ADD UNITB,R4
4399 014156 004737 044214      JSR PC,FATT3
4400 014162 104110          ERROR 110          ;NO ATTN ON PORT B TO ALLOW SEIZE
4401
4402 014164 004737 047162      JSR PC,CLKOF
4403 014170 004737 044172      JSR PC,DRAV
4404 014174 104045          ERROR 45          ;SEE IF PORT B DRIVE AVAIL
4405                                     ;PORT B NOT AVAIL
4406 014176 012737 014544 001176      MOV #5$, $ESCAPE
4407
4408 014204 012737 050340 005404      MOV #<D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
4409 014212 005037 005406      CLR E.B0          ;EXPECTED MSG B0
4410 014216 012737 001720 005410      MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
4411 014224 012737 000001 005412      MOV #1,E.B1      ;MSG ID FOR EXPECTED MSG B1
4412 014232 005037 005414      CLR E.A2          ;EXPECTED MSG A2
4413 014236 012737 000002 005416      MOV #2,E.B2      ;MSG ID FOR EXPECTED MSG B2
4414 014244 012737 000003 005422      MOV #3,E.B3      ;MSG ID FOR EXPECTED MSG B3
4415
4416 014252 004737 044274      JSR PC,CHKMSG
4417 014256 000000          .WORD 0!0!0
4418 014260 104077          ERROR 77          ;CHECK MSGS A0, B0, A1, B1
4419 014262 104100          ERROR 100         ;& MSGS SPECIFIED HERE
4420 014264 104101          ERROR 101         ;MSG A0 ERROR AFTER TIMEOUT
4421 014266 104102          ERROR 102         ;MSH B0 ERROR
4422 014270 005037 001176      CLR $ESCAPE
4423 014274 012737 000000 005464      MOV #0,UNITB
4424 014302 112737 000101 056560      MOV #A,MSG19A
4425 014310 004737 043750      JSR PC,TSTATN
4426 014314 000401          BR 4$
4427 014316 104111          ERROR 111         ;MSG A1 ERROR
4428                                     ;MSG B1 ERROR
4429 014320 012737 014544 001176 4$: MOV #5$, $ESCAPE
4430 014326 012737 000001 005464      MOV #1,UNITB
4431 014334 112737 000102 056560      MOV #B,MSG19A
4432                                     ;SETUP FOR PORT A
4433 014342 012765 100000 000000      MOV #CCLR,RKCS1(R5)
4434 014350 013765 001222 000010      MOV $UNIT,RKCS2(R5) ;DRIVE#
4435 014356 063765 005464 000010      ADD UNITB,RKCS2(R5) ;ADD 1 IF ON PORT B
4436 014364 012737 000005 005314      MOV #CLEAR,HCS1
4437 014372 004737 043372      JSR PC,DOCMD
4438 014376 104151          ERROR 151         ;DO DRIVE CLEAR CMD & GET CONTR RDY
4439 014400 004737 043750      JSR PC,TSTATN
4440 014404 000401          BR 69$
4441 014406 104154          ERROR 154         ;NO RDY AFTER DRIVE CLEAR CMD
4442                                     ;TEST FOR ATTN
4443                                     ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
4444 014410 012737 010340 005404      MOV #<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
4445 014416 005037 005406      CLR E.B0          ;EXPECTED MSG B0
4446 014422 012737 001720 005410      MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
4447 014430 012737 000001 005412      MOV #1,E.B1      ;MSG ID FOR EXPECTED MSG B1
4448 014436 005037 005414      CLR E.A2          ;EXPECTED MSG A2
4449 014442 012737 000002 005416      MOV #2,E.B2      ;MSG ID FOR EXPECTED MSG B2

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4450 014450 012737 000003 005422 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
4451
4452 014456 004737 044274 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
4453 014462 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
4454 014464 104033 ERROR 33 ;MSG A0 ERROR AFTER DRV CLEAR CMD
4455 014466 104034 ERROR 34 ;MSH B0 ERROR
4456 014470 104035 ERROR 35 ;MSG A1 ERROR
4457 014472 104036 ERROR 36 ;MSG B1 ERROR
4458
4459
4460 014474 012701 000360 MOV #360,R1
4461 014500 163701 001366 SUB COUNT,R1 ;R1-COUNT=R1
4462 014504 012746 000021 MOV #17,-(SP) ;:PUT THE MULTIPLIER ON THE STACK
4463 014510 010146 MOV R1,-(SP) ;:PUT THE MULTIPLICAND ON THE STACK
4464 014512 004737 054270 JSR PC,@#MULT ;:CALL THE MULTIPLY ROUTINE
4465 014516 012616 MOV (SP)+,(SP) ;:DISREGARD THE MSB'S
4466 014520 012601 MOV (SP)+,R1 ;:GET THE LSB'S OF THE PRODUCT
4467 014522 104401 056564 TYPE MSG20 ;PORT TIMEOUT
4468 014526 010146 MOV R1,-(SP) ;PUSH BINARY ONTO STACK
4469 014530 004737 054174 JSR PC,$$SB2D ;CONVERT TO ASCII
4470 014534 004737 054230 JSR PC,$$SUPRS ;TYPE IT
4471 014540 104401 056645 TYPE ,MSG22 ;MS
4472
4473 014544 005037 001176 S$: CLR $ESCAPE
4474 014550 004737 047162 JSR PC,CLKOF
4475
4476
4477
4478
4479
4480
4481
4482 014554 000004
4483 014556 012737 000001 001174
4484 014564 012706 001100
4485 014570 012737 000001 005464
4486 014576 112737 000102 056560
4487 014604 013737 005466 001366
4488 014612 004737 047204
4489
4490 014616 004737 045534 JSR PC,SUBCLR
4491 014622 104024 ERROR 24 ;CERR AFTER SCLR
4492
4493
4494 014624 004737 044172 JSR PC,DRAV ;SEE IF DRIVE AVAIL
4495 014630 104045 ERROR 45 ;PORT B NOT AVAIL AFTER TMO
4496 014632 012737 015554 001176 MOV #5,$ESCAPE
4497
4498 014640 012737 010340 005404 MOV #<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
4499 014646 005037 005406 CLR E.B0 ;EXPECTED MSG B0
4500 014652 012737 001720 005410 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
4501 014660 012737 000001 005412 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
4502 014666 005037 005414 CLR E.A2 ;EXPECTED MSG A2
4503 014672 012737 000002 005416 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
4504 014700 012737 000003 005422 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
4505

```

```

*****
*TEST 6 TEST PORT 'B' SEIZE & TIMEOUT
*
* THE PREVIOUS TEST IS REPEATED FOR PORT 'B'.
*
*****
TST6: SCOPE

```

4506	014706	004737	044274			JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1
4507	014712	000000				.WORD	0!0!0	; & MSGS SPECIFIED HERE
4508	014714	104077				ERROR	77	;MSG A0 ERROR AFTER TIMEOUT
4509	014716	104100				ERROR	100	;MSH B0 ERROR
4510	014720	104101				ERROR	101	;MSG A1 ERROR
4511	014722	104102				ERROR	102	;MSG B1 ERROR
4512	014724	012737	000000	005464		MOV	#0,UNITB	;SELECT PORT A BEFORE TIMEOUT OR RELEASE
4513	014732	112737	000101	056560		MOVB	#'A,MSG19A	;SETUP ERROR MSG FOR PORT A
4514	014740	005037	001176			CLR	\$ESCAPE	
4515	014744	004737	044172			JSR	PC,DRAV	;SEE IF DRIVE AVAIL
4516	014750	000401				BR	1\$;BR IF NOT AVAIL
4517	014752	104103				ERROR	103	;PORT A AVAIL BEFORE TMO OR RELEASE
4518								
4519	014754	032737	100000	005314	1\$:	BIT	#CERR,HCS1	
4520	014762	001001				BNE	6\$	
4521	014764	104130				ERROR	130	;CERR NOT SET AFTER SEL DRIVE & DRIVE NOT AVAIL
4522	014766	012737	010100	005404	6\$:	MOV	#<D.SPIN!D.VV>,E.A0	
4523	014774	013737	005404	005410		MOV	E.A0,E.A1	;MSG 1 & 0 SHOULD ALWAYS RETURN SAME
4524	015002	053737	012776	005410		BIS	E.DD†,E.A1	
4525	015010	005037	005406			CLR	E.B0	;WORD 0 FOR PORT A
4526	015014	005037	005412			CLR	E.B1	
4527	015020	012737	015554	001176		MOV	#5\$, \$ESCAPE	
4528								
4529	015026	004737	044274			JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1
4530	015032	000000				.WORD	0!0!0	; & MSGS SPECIFIED HERE
4531	015034	104104				ERROR	104	;MSG A0 ERROR WHILE PORT A UNAVAILABLE
4532	015036	104105				ERROR	105	;MSH B0 ERROR
4533	015040	104106				ERROR	106	;MSG A1 ERROR
4534	015042	104107				ERROR	107	;MSG B1 ERROR
4535								
4536	015044	005737	001216			TST	\$PASS	
4537	015050	001402				BEQ	8\$;BR IF FIRST PASS
4538	015052	000137	015554			JMP	5\$;ELSE EXIT TEST
4539	015056	005037	001176		8\$:	CLR	\$ESCAPE	
4540	015062	012765	100000	000000		MOV	#CCLR,RKCS1(R5)	;SETUP FOR PORT B AGAIN
4541	015070	012737	000001	005464		MOV	#1,UNITB	
4542	015076	112737	000102	056560		MOVB	#'B,MSG19A	
4543	015104	012737	000360	001366		MOV	#360,COUNT	;SETUP 4 SEC TIMEOUT
4544	015112	004737	047114			JSR	PC,CLKON	;TURN ON CLOCK
4545								
4546	015116	004737	044172			JSR	PC,DRAV	
4547	015122	104045				ERROR	45	;PORT B NOT AVAIL AFTER TIMEOUT
4548								
4549	015124	012737	000000	005464		MOV	#0,UNITB	;SELECT PORT A BEFORE TIMEOUT OR RELEASE
4550	015132	112737	000101	056560		MOVB	#'A,MSG19A	
4551	015140	004737	044172			JSR	PC,DRAV	;SEE IF PORT A DRIVE AVAIL
4552	015144	000401				BR	3\$;BR IF NOT AVAIL
4553	015146	104103				ERROR	103	;PORT A AVAIL BEFORE TMO OR RELEASE
4554								
4555	015150	012765	100000	000000	3\$:	MOV	#CCLR,RKCS1(R5)	
4556	015156	013704	001222			MOV	\$UNIT,R4	
4557	015162	063704	005464			ADD	UNITB,R4	
4558	015166	004737	044214			JSR	PC,FATT3	
4559	015172	104110				ERROR	110	;NO ATTN ON PORT A TO ALLOW SEIZE
4560								
4561	015174	004737	047162			JSR	PC,CLKOF	

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4562 015200 004737 044172 JSR PC,DRAV ;SEE IF PORT A DRIVE AVAIL
4563 015204 104045 ERROR 45 ;PORT A NOT AVAIL
4564
4565 015206 012737 015554 001176 MOV #5$, $ESCAPE
4566
4567 015214 012737 050340 005404 MOV #<D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
4568 015222 005037 005406 CLR E.B0 ;EXPECTED MSG B0
4569 015226 012737 001720 005410 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
4570 015234 012737 000001 005412 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
4571 015242 005037 005414 CLR E.A2 ;EXPECTED MSG A2
4572 015246 012737 000002 005416 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
4573 015254 012737 000003 005422 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
4574
4575 015262 004737 044274 JSR PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
4576 015266 000000 .WORD 0!0!0 ;& MSGS SPECIFIED HERE
4577 015270 104077 ERROR 77 ;MSG A0 ERROR AFTER TIMEOUT
4578 015272 104100 ERROR 100 ;MSH B0 ERROR
4579 015274 104101 ERROR 101 ;MSG A1 ERROR
4580 015276 104102 ERROR 102 ;MSG B1 ERROR
4581 015300 005037 001176 CLR $ESCAPE
4582 015304 012737 000001 005464 MOV #1,UNITB ;SETUP FOR PORT B
4583 015312 112737 000102 056560 MOVB #'B,MSG19A
4584 015320 004737 043750 JSR PC,TSTATN ;TEST FOR ATTN ON PORT B
4585 015324 000401 BR 4$
4586 015326 104111 ERROR 111 ;PORT B ATTN SET W/O REQUEST PENDING
4587
4588 015330 012737 015554 001176 4$: MOV #5$, $ESCAPE
4589 015336 012737 000000 005464 MOV #0,UNITB ;SETUP FOR PORT A
4590 015344 112737 000101 056560 MOVB #'A,MSG19A
4591
4592 015352 012765 100000 000000 MOV #CLR,RKCS1(R5)
4593 015360 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;DRIVE#
4594 015366 063765 005464 000010 ADD UNITB,RKCS2(R5) ;ADD 1 IF ON PORT B
4595 015374 012737 000005 005314 MOV #CLEAR,HCS1
4596 015402 004737 043372 JSR PC,DOCMD ;DO DRIVE CLEAR CMD & GET CONTR RDY
4597 015406 104151 ERROR 151 ;NO RDY AFTER DRIVE CLEAR CMD
4598 015410 004737 043750 JSR PC,TSTATN ;TEST FOR ATTN
4599 015414 000401 BR 64$
4600 015416 104154 ERROR 154 ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
4601 015420
4602 64$:
4603 015420 012737 010340 005404 MOV #<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
4604 015426 005037 005406 CLR E.B0 ;EXPECTED MSG B0
4605 015432 012737 001720 005410 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
4606 015440 012737 000001 005412 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
4607 015446 005037 005414 CLR E.A2 ;EXPECTED MSG A2
4608 015452 012737 000002 005416 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
4609 015460 012737 000003 005422 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
4610
4611 015466 004737 044274 JSR PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
4612 015472 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
4613 015474 104033 ERROR 33 ;MSG A0 ERROR AFTER DRV CLEAR CMD
4614 015476 104034 ERROR 34 ;MSH B0 ERROR
4615 015500 104035 ERROR 35 ;MSG A1 ERROR
4616 015502 104036 ERROR 36 ;MSG B1 ERROR
4617

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M07

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 90
T6 TEST PORT 'B' SEIZE & TIMEOUT

SEQ 0090

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4618
4619 015504 012701 000360      MOV      #360,R1
4620 015510 163701 001366      SUB      COUNT,R1          ;R1-COUNT=R1
4621 015514 012746 000021      MOV      #17,-(SP)        ;:PUT THE MULTIPLIER ON THE STACK
4622 015520 010146          MOV      R1,-(SP)         ;:PUT THE MULTIPLICAND ON THE STACK
4623 015522 004737 054270      JSR      PC,@#MULT        ;:CALL THE MULTIPLY ROUTINE
4624 015526 012616          MOV      (SP)+,(SP)       ;:DISREGARD THE MSB'S
4625 015530 012601          MOV      (SP)+,R1        ;:GET THE LSB'S OF THE PRODUCT
4626 015532 104401 056651      TYPE     MSG23           ;PORT TIMEOUT
4627 015536 010146          MOV      R1,-(SP)        ;PUSH BINARY ONTO STACK
4628 015540 004737 054174      JSR      PC,$$SB2D       ;CONVERT TO ASCII
4629 015544 004737 054230      JSR      PC,$$SUPRS      ;TYPE IT
4630 015550 104401 056645      TYPE     ,MSG22         ;MS
4631
4632 015554 005037 001176      5$:     CLR      $ESCAPE
4633 015560 004737 047162      JSR      PC,CLKOF
4634
4635      ;*****
4636      ;*TEST 7          PRINT DRIVE SERIAL NUMBER
4637      ;*
4638      ;*          THIS TEST READS & PRINTS THE DRIVE SERIAL # FROM MSG A3
4639      ;*          IN BCD ON THE 1'ST PASS ONLY.
4640      ;*          IT ALSO TESTS THAT THE SERIAL # READ THRU BOTH PORTS
4641      ;*          ARE THE SAME.
4642      ;*
4643      ;*****
4644 015564 000004          TST7:   SCOPE
4645 015566 012737 000001 001174      MOV      #1,$TIMES      ;;DO 1 ITERATION
4646 015574 012706 001100          MOV      #STACK,SP
4647
4648 015600 005737 001216          TST      $PASS
4649 015604 001042          BNE     TST10           ;;GO TO NEXT TST IF NOT 1ST PASS
4650
4651 015606 004737 045534          JSR      PC,SUBCLR
4652 015612 104024          ERROR   24             ;CERR AFTER SCLR
4653
4654 015614 104401 056474          TYPE     MSG16         ;DRIVE SERIAL NO.
4655 015620 012765 000003 000026      MOV      #3,RKMR1(R5)   ;SELECT BYTE 3
4656 015626 004737 045146          JSR      PC,GSTAT      ;GET STATUS
4657 015632 013701 005342          MOV      HMR2,R1        ;GET SERIAL #
4658 015636 012704 053762          MOV      #SOCTVL,R4     ;GET ADDR CHAR BUFF
4659 015642 010446          MOV      R4,-(SP)       ;STORE ON STACK FOR $$SUPRS
4660 015644 012703 000003          MOV      #3,R3         ;SETUP CHAR COUNT
4661 015650 006101          ROL     R1             ;INITIALIZE BIT POSITIONS
4662 015652 006101          ROL     R1
4663 015654 006101          1$:     ROL     R1         ;GET NEXT 4 BITS
4664 015656 006101          ROL     R1
4665 015660 006101          ROL     R1
4666 015662 006101          ROL     R1
4667 015664 010100          MOV      R1,RO         ;GET WORKING COPY
4668 015666 042700 177760          BIC     #177760,RO      ;CLEAR ALL BUT LOW 4 BITS
4669 015672 052700 000060          BIS     #60,RO         ;CONVERT TO ASCII DIGIT
4670 015676 110024          MOVB   RO,(R4)+        ;PUT ASCII DIGIT INTO CHAR BUFF
4671 015700 005303          DEC     R3
4672 015702 001364          BNE     1$
4673 015704 105014          CLRB   (R4)           ;BR IF ALL 3 CHARS NOT DONE
;ELSE INSERT NULL TERMINATOR

```

4674
4675 015706 004737 054230

JSR PC,SSUPRS ;TYPE
TYPE ,SCLF ;29-SEP-77
TYPE ,SCLF ;29-SEP-77

*TEST 10 TEST PORT 'A' COMMAND SEIZE & ATTENTION

* VERIFY THE OPERATION OF 'DSC' & 'ATTN' BITS AFTER A COMMAND.

* A. ISSUE A SEEK COMMAND TO CYLINDER 10 THRU PORT 'A'.

* B. VERIFY SEIZURE & THAT 'DSC' & 'ATTN' SETS FOR PORT 'A'
* ONLY AFTER SEEK COMPLETION

* C. VERIFY 'ATTN' REMAINS SET BEYOND TIMEOUT

* D. VERIFY A DRIVE CLEAR COMMAND RESETS 'DSC' & 'ATTN'
* & DOES NOT RELEASE THE DRIVE FROM PORT 'A'.

*TST10: SCOPE

4695 015712 000004
4696 015714 012737 000001 001174

MOV #1,STIMES ;;DO 1 ITERATION

4697 015722 012706 001100

MOV #STACK,SP ;SETUP PORT A

4698 015726 012737 000000 005464

MOV #0,UNITB ;SETUP PORT A

4699 015734 112737 000101 056560

MOV #A,MSG19A

4700 015742 013737 005466 001366

MOV TIMER,COUNT

4701 015750 004737 047204

JSR PC,TMO ;DO TIMEOUT

4702

4703 015754 004737 045534

JSR PC,SUBCLR

4704 015760 104024

ERROR 24 ;CERR AFTER SCLR

4705

4706

4707 015762 004737 044172

JSR PC,DRAV ;SEE IF DRIVE AVAIL

4708 015766 104045

ERROR 45 ;PORT A NOT AVAIL AFTER TMO

4709 015770 012765 000012 000020

MOV #10.,RKDC(R5) ;SEEK TO CYL 10.

4710

4711 015776 012737 000017 005314

MOV #SEEK,HCS1

4712 016004 004737 043372

JSR PC,DOCMD ;DO SEEK CMD & GET CONTR READY

4713 016010 104131

ERROR 131 ;NO RDY AFTER SEEK CMD

4714

4715 016012 013737 001412 005352

MOV T50000,TEMP1 ;SETUP TIMEOUT

4716 016020 004737 044106

JSR PC,FATT2 ;FIND ATTN

4717 016024 104132

ERROR 132 ;NO ATTN AFTER SEEK CMD

4718

4719 016026 032737 100000 005314

BIT #CERR,HCS1

4720 016034 001401

BEQ 65\$

4721 016036 104210

ERROR 210 ;CERR AFTER SEEK CMD

4722

4723 016040

65\$:

4724

4725 016040 012737 050340 005404

MOV #<D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0

4726 016046 005037 005406

CLR E.B0 ;EXPECTED MSG B0

4727 016052 012737 001720 005410

MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1

4728 016060 012737 000001 005412

MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1

4729 016066 005037 005414

CLR E.A2 ;EXPECTED MSG A2

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4730 016072 012737 000002 005416      MOV      #2,E.B2      ;MSG ID FOR EXPECTED MSG B2
4731 016100 012737 000003 005422      MOV      #3,E.B3      ;MSG ID FOR EXPECTED MSG B3
4732
4733 016106 004737 044274      JSR      PC,CHKMSG    ;CHECK MSGS AD, B0, A1, B1
4734 016112 000000      .WORD    0!0!0        ;& MSGS SPECIFIED HERE
4735 016114 104161      ERROR    161          ;MSG AD ERROR AFTER SEEK CMD
4736 016116 104162      ERROR    162          ;MSH B0 ERROR
4737 016120 104163      ERROR    163          ;MSG A1 ERROR
4738 016122 104164      ERROR    164          ;MSG B1 ERROR
4739
4740 016124 004737 045756      JSR      PC,RDCYLA    ;READ CYL ADDR
4741 016130 023727 001360 000012      CMP      CYLADD,#10.  ;SEE IF CYL 10.
4742 016136 001415      BEQ      64$          ;BR IF YES
4743 016140 012737 000000 001344      MOV      #0,FRCYL
4744 016146 012737 000012 001346      MOV      #10.,TOCYL
4745 016154 012737 000012 001354      MOV      #10.,CALDIF
4746 016162 012765 000012 000020      MOV      #10.,RKDC(R5) ;REFRESH RKDC
4747 016170 104224      ERROR    224          ;DID NOT SEEK TO CYL 10.
4748
4749 016172 012737 000226 001366 64$:      MOV      #150. COUNT  ;DO 2.5 SEC TIMEOUT
4750 016200 004737 047204      JSR      PC,TMO
4751 016204 004737 044106      JSR      PC,FATT2
4752 016210 104112      ERROR    112          ;ATTN CLEARED AFTER TMO
4753
4754 016212 012765 100000 000000      MOV      #CCLR,RKCS1(R5) ;DRIVE#
4755 016220 013765 001222 000010      MOV      $UNIT,RKCS2(R5) ;ADD 1 IF ON PORT B
4756 016226 063765 005464 000010      ADD      UNITB,RKCS2(R5)
4757 016234 012737 000005 005314      MOV      #CLEAR,HCS1
4758 016242 004737 043372      JSR      PC,DOCMD
4759 016246 104151      ERROR    151          ;DO DRIVE CLEAR CMD & GET CONTR RDY
4760 016250 004737 043750      JSR      PC,TSTATN    ;NO RDY AFTER DRIVE CLEAR CMD
4761 016254 000401      BR
4762 016256 104154      ERROR    154          ;TEST FOR ATTN
4763 016260      66$:
4764
4765 016260 012737 010340 005404      MOV      #<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
4766 016266 005037 005406      CLR      E.B0          ;EXPECTED MSG B0
4767 016272 012737 001720 005410      MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
4768 016300 012737 000001 005412      MOV      #1,E.B1      ;MSG ID FOR EXPECTED MSG B1
4769 016306 005037 005414      CLR      E.A2          ;EXPECTED MSG A2
4770 016312 012737 000002 005416      MOV      #2,E.B2      ;MSG ID FOR EXPECTED MSG B2
4771 016320 012737 000003 005422      MOV      #3,E.B3      ;MSG ID FOR EXPECTED MSG B3
4772
4773 016326 004737 044274      JSR      PC,CHKMSG    ;CHECK MSGS AD, B0, A1, B1
4774 016332 000003      .WORD    T.A2!T.B2!0  ;& MSGS SPECIFIED HERE
4775 016334 104033      ERROR    33          ;MSG AD ERROR AFTER DRV CLEAR CMD
4776 016336 104034      ERROR    34          ;MSH B0 ERROR
4777 016340 104035      ERROR    35          ;MSG A1 ERROR
4778 016342 104036      ERROR    36          ;MSG B1 ERROR
4779
4780 016344 004737 044172      JSR      PC,DRAV
4781 016350 104113      ERROR    113          ;PORT A NOT AVAIL AFTER DRIVE CLEAR CMD
4782
4783 *****
4784 *TEST 11 TEST PORT 'B' COMMAND SEIZE & ATTENTION
4785 *

```

```

4786                                     ;* THE PREVIOUS TEST IS REPEATED FOR PORT 'B',
4787                                     ;* BUT THE SEEK IS TO CYLINDER 0
4788                                     ;*
4789                                     ;* *****
4790 016352 000004 TST11: SCOPE
4791 016354 012737 000001 001174 MOV #1,STIMES ;;DO 1 ITERATION
4792 016362 012706 001100 MOV #STACK,SP
4793 016366 012737 000001 005464 MOV #1,UNITB ;SETUP PORT B
4794 016374 112737 000102 056560 MOVB #'B,MSG19A
4795 016402 013737 005466 001366 MOV TIMER,COUNT
4796 016410 004737 047204 JSR PC,TMO ;DO TIMEOUT
4797
4798 016414 004737 045534 JSR PC,SUBCLR
4799 016420 104024 ERROR 24 ;CERR AFTER SCLR
4800
4801
4802 016422 004737 044172 JSR PC,DRAV ;SEE IF DRIVE AVAIL
4803 016426 104045 ERROR 45 ;PORT B NOT AVAIL AFTER TMO
4804 016430 012765 000000 000020 MOV #0,RKDC(R5) ;SEEK TO CYL 0
4805
4806 016436 012737 000017 005314 MOV #SEEK,HCS1
4807 016444 004737 043372 JSR PC,DOCMD ;DO SEEK CMD & GET CONTR READY
4808 016450 104131 ERROR 131 ;NO RDY AFTER SEEK CMD
4809
4810 016452 013737 001412 005352 MOV T50000,TEMP1 ;SETUP TIMEOUT
4811 016460 004737 044106 JSR PC,FATT2 ;FIND ATTN
4812 016464 104132 ERROR 132 ;NO ATTN AFTER SEEK CMD
4813
4814 016466 032737 100000 005314 BIT #CERR,HCS1
4815 016474 001401 BEQ 65$
4816 016476 104210 ERROR 210 ;CERR AFTER SEEK CMD
4817
4818 016500 65$:
4819
4820 016500 012737 050340 005404 MOV #<D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
4821 016506 005037 005406 CLR E.B0 ;EXPECTED MSG B0
4822 016512 012737 001720 005410 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
4823 016520 012737 000001 005412 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
4824 016526 005037 005414 CLR E.A2 ;EXPECTED MSG A2
4825 016532 012737 000002 005416 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
4826 016540 012737 000003 005422 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
4827
4828 016546 004737 044274 JSR PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
4829 016552 000000 .WORD 0!0!0 ;& MSGS SPECIFIED HERE
4830 016554 104161 ERROR 161 ;MSG A0 ERROR AFTER SEEK CMD
4831 016556 104162 ERROR 162 ;MSG B0 ERROR
4832 016560 104163 ERROR 163 ;MSG A1 ERROR
4833 016562 104164 ERROR 164 ;MSG B1 ERROR
4834
4835 016564 004737 045756 JSR PC,RDCYLA ;READ CYL ADDR
4836 016570 023727 001360 000000 CMP CYLADD,#0 ;SEE IF CYL 0
4837 016576 001415 BEQ 64$ ;BR IF YES
4838 016600 012737 000012 001344 MOV #10,FRCYL
4839 016606 012737 000000 001346 MOV #0,T0CYL
4840 016614 012737 000000 001354 MOV #0,CALDIF
4841 016622 012765 000000 000020 MOV #0,RKDC(R5) ;REFRESH RKDC

```

```

4842 016630 104224          ERROR 224          ;DID NOT SEEK TO CYL 0
4843
4844 016632 012737 000226 001366 64$: MOV #150, COUNT
4845 016640 004737 047204          JSR PC, TMO          ;DO 2.5 SEC TIMEOUT
4846 016644 004737 044106          JSR PC, FATT2
4847 016650 104112          ERROR 112          ;ATTN CLEARED AFTER TMO
4848
4849 016652 012765 100000 000000 MOV #CLR, RKCS1(R5)
4850 016660 013765 001222 000010 MOV #UNIT, RKCS2(R5) ;DRIVE#
4851 016666 063765 005464 000010 ADD UNITB, RKCS2(R5) ;ADD 1 IF ON PORT B
4852 016674 012737 000005 005314 MOV #CLR, HCS1
4853 016702 004737 043372          JSR PC, DOCMD        ;DO DRIVE CLEAR CMD & GET CONTR RDY
4854 016706 104151          ERROR 151          ;NO RDY AFTER DRIVE CLEAR CMD
4855 016710 004737 043750          JSR PC, TSTATN       ;TEST FOR ATTN
4856 016714 000401          BR 66$
4857 016716 104154          ERROR 154          ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
4858 016720
4859
4860 016720 012737 010340 005404 MOV #<D.DRA!D.SPIN!D.DRDY!D.VV>, E.A0 ;EXPECTED MSG A0
4861 016726 005037 005406          CLR E.B0             ;EXPECTED MSG B0
4862 016732 012737 001720 005410 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>, E.A1 ;EXPECTED A1
4863 016740 012737 000001 005412 MOV #1, E.B1          ;MSG ID FOR EXPECTED MSG B1
4864 016746 005037 005414          CLR E.A2             ;EXPECTED MSG A2
4865 016752 012737 000002 005416 MOV #2, E.B2          ;MSG ID FOR EXPECTED MSG B2
4866 016760 012737 000003 005422 MOV #3, E.B3          ;MSG ID FOR EXPECTED MSG B3
4867
4868 016766 004737 044274          JSR PC, CHKMSG       ;CHECK MSGS A0, B0, A1, B1
4869 016772 000003          .WORD T.A2!T.B2!0   ; & MSGS SPECIFIED HERE
4870 016774 104033          ERROR 33             ;MSG A0 ERROR AFTER DRV CLEAR CMD
4871 016776 104034          ERROR 34             ;MSG B0 ERROR
4872 017000 104035          ERROR 35             ;MSG A1 ERROR
4873 017002 104036          ERROR 36             ;MSG B1 ERROR
4874
4875 017004 004737 044172          JSR PC, DRAW
4876 017010 104113          ERROR 113           ;PORT B NOT AVAIL AFTER DRIVE CLEAR CMD
4877

```

```

*****
*TEST 12      TEST RESET PORT 'A' ATTENTION BY DRIVE CLEAR COMMAND
*
*   VERIFY THAT A DRIVE CLEAR COMMAND CLEARS ONLY THE ATTENTION BIT OF
*   THE SEIZING PORT
*
*   A.  SET EACH PORT'S ATTENTION BIT BY PERFORMING SEEK
*        COMMANDS TO CYLINDER 0 & ALLOWING TIMEOUTS.
*
*   B.  SEIZE THE DRIVE THRU PORT 'A' & ISSUE A DRIVE CLEAR COMMAND
*        VERIFY THAT 'DSC' & 'ATTN' FOR PORT 'A' HAVE BEEN CLEARED
*
*   C.  SEIZE THE DRIVE THRU PORT 'B' & VERIFY 'DSC' & 'ATTN'
*        HAVE NOT CLEARED
*****

```

```

4892
4893 017012 000004          TST12: SCOPE
4894 017014 012737 000001 001174 MOV #1, STIMES      ;;DO 1 ITERATION
4895 017022 012706 001100          MOV #STACK, SP
4896 017026 012737 000000 005464 MOV #0, UNITB       ;SETUP PORT A
4897 017034 112737 000101 056560 MOVB #'A, MSG19A

```


E08

CZR6GB0 RK611 DU PORT LGC TST MACY11 30A(1052) 03-JAN-78 08:56 PAGE 95
 CZR6GB.P11 03-JAN-78 08:46 T12 TEST RESET PORT 'A' ATTENTION BY DRIVE CLEAR COMMAND

SEQ 0095

```

4898 017042 013737 005466 001366      MOV      TIMER,COUNT
4899 017050 004737 047204      JSR      PC,TM0          ;DO TIMEOUT
4900
4901 017054 004737 045534      JSR      PC,SUBCLR
4902 017060 104024      ERROR    24            ;CERR AFTER SCLR
4903
4904
4905 017062 004737 044172      JSR      PC,DRAW
4906 017066 104045      ERROR    45            ;SEE IF DRIVE AVAIL
4907                                ;PORT A NOT AVAIL AFTER TMO
4908 017070 012737 000017 005314      MOV      #SEEK,HCS1
4909 017076 004737 043372      JSR      PC,DOCMD
4910 017102 104131      ERROR    131           ;DO SEEK CMD & GET CONTR READY
4911                                ;NO RDY AFTER SEEK CMD
4912 017104 013737 001412 005352      MOV      T50000,TEMP1
4913 017112 004737 044106      JSR      PC,FATT2
4914 017116 104132      ERROR    132           ;SETUP TIMEOUT
4915                                ;FIND ATTN
4916 017120 032737 100000 005314      BIT      #CERR,HCS1
4917 017126 001401      BEQ
4918 017130 104210      ERROR    210           ;CERR AFTER SEEK CMD
4919
4920 017132                                64$:
4921
4922 017132 012737 050340 005404      MOV      #<D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
4923 017140 005037 005406      CLR      E.B0          ;EXPECTED MSG B0
4924 017144 012737 001720 005410      MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
4925 017152 012737 000001 005412      MOV      #1,E.B1       ;MSG ID FOR EXPECTED MSG B1
4926 017160 005037 005414      CLR      E.A2          ;EXPECTED MSG A2
4927 017164 012737 000002 005416      MOV      #2,E.B2       ;MSG ID FOR EXPECTED MSG B2
4928 017172 012737 000003 005422      MOV      #3,E.B3       ;MSG ID FOR EXPECTED MSG B3
4929
4930 017200 004737 044274      JSR      PC,CHKMSG
4931 017204 000000      .WORD   0!0!0         ;CHECK MSGS A0, B0, A1, B1
4932 017206 104161      ERROR    161           ;& MSGS SPECIFIED HERE
4933 017210 104162      ERROR    162           ;MSG A0 ERROR AFTER SEEK CMD
4934 017212 104163      ERROR    163           ;MSG B0 ERROR
4935 017214 104164      ERROR    164           ;MSG A1 ERROR
4936                                ;MSG B1 ERROR
4937 017216 013737 005466 001366      MOV      TIMER,COUNT
4938 017224 004737 047204      JSR      PC,TM0          ;DO 1.5 SEC TIMEOUT
4939 017230 012737 000001 005464      MOV      #1,UNITB
4940 017236 112737 000102 056560      MOV      #B,MSG19A
4941 017244 004737 044172      JSR      PC,DRAW
4942 017250 104045      ERROR    45            ;PORT B NOT AVAIL AFTER TMO
4943
4944
4945 017252 012737 010340 005404      MOV      #<D.DRA!0!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
4946 017260 005037 005406      CLR      E.B0          ;EXPECTED MSG B0
4947 017264 012737 001720 005410      MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
4948 017272 012737 000001 005412      MOV      #1,E.B1       ;MSG ID FOR EXPECTED MSG B1
4949 017300 005037 005414      CLR      E.A2          ;EXPECTED MSG A2
4950 017304 012737 000002 005416      MOV      #2,E.B2       ;MSG ID FOR EXPECTED MSG B2
4951 017312 012737 000003 005422      MOV      #3,E.B3       ;MSG ID FOR EXPECTED MSG B3
4952
4953 017320 004737 044274      JSR      PC,CHKMSG
;CHECK MSGS A0, B0, A1, B1

```

F08

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 96
T12 TEST RESET PORT 'A' ATTENTION BY DRIVE CLEAR COMMAND

SEQ 0096

```

4954 017324 000000 .WORD 0!0!0 ;& MSGS SPECIFIED HERE
4955 017326 104165 ERROR 165 ;MSG A0 ERROR AFTER TIMEOUT
4956 017330 104166 ERROR 166 ;MSH B0 ERROR
4957 017332 104167 ERROR 167 ;MSG A1 ERROR
4958 017334 104170 ERROR 170 ;MSG B1 ERROR
4959
4960 017336 012737 000017 005314 MOV #SEEK,HCS1
4961 017344 004737 043372 JSR PC,DOCMD ;DO SEEK CMD & GET CONTR READY
4962 017350 104131 ERROR 131 ;NO RDY AFTER SEEK CMD
4963
4964 017352 013737 001412 005352 MOV T50000,TEMP1 ;SETUP TIMEOUT
4965 017360 004737 044106 JSR PC,FATT2 ;FIND ATTN
4966 017364 104132 ERROR 132 ;NO ATTN AFTER SEEK CMD
4967
4968 017366 032737 100000 005314 BIT #CERR,HCS1
4969 017374 001401 BEQ 65$
4970 017376 104210 ERROR 210 ;CERR AFTER SEEK CMD
4971
4972 017400 65$:
4973
4974 017400 012737 050340 005404 MOV #<D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
4975 017406 005037 005406 CLR E.B0 ;EXPECTED MSG B0
4976 017412 012737 001720 005410 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
4977 017420 012737 000001 005412 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
4978 017426 005037 005414 CLR E.A2 ;EXPECTED MSG A2
4979 017432 012737 000002 005416 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
4980 017440 012737 000003 005422 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
4981
4982 017446 004737 044274 JSR PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
4983 017452 000000 .WORD 0!0!0 ;& MSGS SPECIFIED HERE
4984 017454 104161 ERROR 161 ;MSG A0 ERROR AFTER SEEK CMD
4985 017456 104162 ERROR 162 ;MSH B0 ERROR
4986 017460 104163 ERROR 163 ;MSG A1 ERROR
4987 017462 104164 ERROR 164 ;MSG B1 ERROR
4988
4989 017464 013737 005466 001366 MOV TIMER,COUNT
4990 017472 004737 047204 JSR PC,TMO
4991 017476 012737 000000 005464 MOV #0,UNITB ;SETUP PORTC
4992 017504 112737 000101 056560 MOVB #'A,MSG19A
4993 017512 004737 044172 JSR PC,DRAV
4994 017516 104045 ERROR 45 ;PORT A NOT AVAIL AFTER TMO
4995
4996
4997 017520 012737 050340 005404 MOV #<D.DRA!D.DSC!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
4998 017526 005037 005406 CLR E.B0 ;EXPECTED MSG B0
4999 017532 012737 001720 005410 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5000 017540 012737 000001 005412 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5001 017546 005037 005414 CLR E.A2 ;EXPECTED MSG A2
5002 017552 012737 000002 005416 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5003 017560 012737 000003 005422 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5004
5005 017566 004737 044274 JSR PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
5006 017572 000000 .WORD 0!0!0 ;& MSGS SPECIFIED HERE
5007 017574 104165 ERROR 165 ;MSG A0 ERROR AFTER TIMEOUT
5008 017576 104166 ERROR 166 ;MSH B0 ERROR
5009 017600 104167 ERROR 167 ;MSG A1 ERROR

```

G08

CZR6G80 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 97
T12 TEST RESET PORT 'A' ATTENTION BY DRIVE CLEAR COMMAND

SEQ 0097

```

5010 017602 104170          ERROR 170          ;MSG B1 ERROR
5011
5012 017604 012765 100000 000000  MOV #CLR,RKCS1(R5)
5013 017612 013765 001222 000010  MOV $UNIT,RKCS2(R5) ;DRIVE#
5014 017620 063765 005464 000010  ADD UNITB,RKCS2(R5) ;ADD 1 IF ON PORT B
5015 017626 012737 000005 005314  MOV #CLR,HCS1
5016 017634 004737 043372          JSR PC,DOCMD          ;DO DRIVE CLEAR CMD & GET CONTR RDY
5017 017640 104151          ERROR 151          ;NO RDY AFTER DRIVE CLEAR CMD
5018 017642 004737 043750          JSR PC,TSTATN        ;TEST FOR ATTN
5019 017646 000401          BR 66$
5020 017650 104154          ERROR 154          ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
5021 017652
5022
5023 017652 012737 010340 005404  MOV #<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
5024 017660 005037 005406          CLR E.B0             ;EXPECTED MSG B0
5025 017664 012737 001720 005410  MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5026 017672 012737 000001 005412  MOV #1,E.B1          ;MSG ID FOR EXPECTED MSG B1
5027 017700 005037 005414          CLR E.A2             ;EXPECTED MSG A2
5028 017704 012737 000002 005416  MOV #2,E.B2          ;MSG ID FOR EXPECTED MSG B2
5029 017712 012737 000003 005422  MOV #3,E.B3          ;MSG ID FOR EXPECTED MSG B3
5030
5031 017720 004737 044274          JSR PC,CHKMSG        ;CHECK MSGS A0, B0, A1, B1
5032 017724 000003          .WORD T.A2!T.B2!0    ;& MSGS SPECIFIED HERE
5033 017726 104033          ERROR 33             ;MSG A0 ERROR AFTER DRV CLEAR CMD
5034 017730 104034          ERROR 34             ;MSG B0 ERROR
5035 017732 104035          ERROR 35             ;MSG A1 ERROR
5036 017734 104036          ERROR 36             ;MSG B1 ERROR
5037
5038 017736 013737 005466 001366  MOV TIMER,COUNT
5039 017744 004737 047204          JSR PC,TMO           ;DO 1.5 SEC TIMEOUT
5040 017750 012737 000001 005464  MOV #1,UNITB         ;SETUP PORT B
5041 017756 112737 000102 056560  MOV #B,MSG19A
5042 017764 004737 043750          JSR PC,TSTATN
5043 017770 104114          ERROR 114           ;ATTN RESET ON PORT B AFTER DR CLR CMD
5044
5045
5046
5047 017772 012737 050340 005404  MOV #<D.DRA!D.DSC!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
5048 020000 005037 005406          CLR E.B0             ;EXPECTED MSG B0
5049 020004 012737 001720 005410  MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5050 020012 012737 000001 005412  MOV #1,E.B1          ;MSG ID FOR EXPECTED MSG B1
5051 020020 005037 005414          CLR E.A2             ;EXPECTED MSG A2
5052 020024 012737 000002 005416  MOV #2,E.B2          ;MSG ID FOR EXPECTED MSG B2
5053 020032 012737 000003 005422  MOV #3,E.B3          ;MSG ID FOR EXPECTED MSG B3
5054
5055 020040 004737 044274          JSR PC,CHKMSG        ;CHECK MSGS A0, B0, A1, B1
5056 020044 000000          .WORD 0!0!0         ;& MSGS SPECIFIED HERE
5057 020046 104165          ERROR 165           ;MSG A0 ERROR AFTER TIMEOUT
5058 020050 104166          ERROR 166           ;MSG B0 ERROR
5059 020052 104167          ERROR 167           ;MSG A1 ERROR
5060 020054 104170          ERROR 170           ;MSG B1 ERROR

```

```

*****
*TEST 13          TEST RESET PORT 'B' ATTENTION BY DRIVE CLEAR COMMAND
*
*          THE PREVIOUS TEST IS REPEATED FOR PORT 'B'.
*

```

5061
5062
5063
5064
5065

H08

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 98
T13 TEST RESET PORT 'B' ATTENTION BY DRIVE CLEAR COMMAND

SEQ 0098

```

5066
5067
5068 020056 000004
5069 020060 012737 000001 001174
5070 020066 012706 001100
5071 020072 012737 000001 005464
5072 020100 112737 000102 056560
5073 020106 013737 005466 001366
5074 020114 004737 047204
5075
5076 020120 004737 045534
5077 020124 104024
5078
5079
5080 020126 004737 044172
5081 020132 104045
5082
5083 020134 012737 000017 005314
5084 020142 004737 043372
5085 020146 104131
5086
5087 020150 013737 001412 005352
5088 020156 004737 044106
5089 020162 104132
5090
5091 020164 032737 100000 005314
5092 020172 001401
5093 020174 104210
5094
5095 020176
5096
5097 020176 012737 050340 005404
5098 020204 005037 005406
5099 020210 012737 001720 005410
5100 020216 012737 000001 005412
5101 020224 005037 005414
5102 020230 012737 000002 005416
5103 020236 012737 000003 005422
5104
5105 020244 004737 044274
5106 020250 000000
5107 020252 104161
5108 020254 104162
5109 020256 104163
5110 020260 104164
5111
5112 020262 013737 005466 001366
5113 020270 004737 047204
5114 020274 012737 000000 005464
5115 020302 112737 000101 056560
5116 020310 004737 044172
5117 020314 104045
5118
5119
5120 020316 012737 010340 005404
5121 020324 005037 005406

```

```

*****
TST13: SCOPE
MOV #1,STIMES ;;DO 1 ITERATION
MOV #STACK,SP
MOV #1,UNITB ;SETUP PORT B
MOV #B,MSG19A
MOV TIMER,COUNT
JSR PC,TMO ;DO TIMEOUT
JSR PC,SUBCLR
ERROR 24 ;CERR AFTER SCLR
JSR PC,DRAW ;SEE IF DRIVE AVAIL
ERROR 45 ;PORT B NOT AVAIL AFTER TMO
MOV #SEEK,HCS1
JSR PC,DOCMD ;DO SEEK CMD & GET CONTR READY
ERROR 131 ;NO RDY AFTER SEEK CMD
MOV T50000,TEMP1 ;SETUP TIMEOUT
JSR PC,FATT2 ;FIND ATTN
ERROR 132 ;NO ATTN AFTER SEEK CMD
BIT #CERR,HCS1
BEQ 64$
ERROR 210 ;CERR AFTER SEEK CMD
64$:
MOV #<D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
CLR E.B0 ;EXPECTED MSG B0
MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
CLR E.A2 ;EXPECTED MSG A2
MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
JSR PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
WORD 0!0!0 ;& MSGS SPECIFIED HERE
ERROR 161 ;MSG A0 ERROR AFTER SEEK CMD
ERROR 162 ;MSG B0 ERROR
ERROR 163 ;MSG A1 ERROR
ERROR 164 ;MSG B1 ERROR
MOV TIMER,COUNT
JSR PC,TMO ;DO 1.5 SEC TIMEOUT
MOV #0,UNITB ;SETUP PORT A
MOV #A,MSG19A
JSR PC,DRAW
ERROR 45 ;PORT A NOT AVAIL AFTER TMO
MOV #<D.DRA!0!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
CLR E.B0 ;EXPECTED MSG B0

```

```

5122 020330 012737 001720 005410      MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5123 020336 012737 000001 005412      MOV      #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5124 020344 005037 005414      CLR      E.A2 ;EXPECTED MSG A2
5125 020350 012737 000002 005416      MOV      #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5126 020356 012737 000003 005422      MOV      #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5127
5128 020364 004737 044274      JSR      PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
5129 020370 000000      .WORD   0!0!0 ;& MSGS SPECIFIED HERE
5130 020372 104165      ERROR   165 ;MSG A0 ERROR AFTER TIMEOUT
5131 020374 104166      ERROR   166 ;MSH B0 ERROR
5132 020376 104167      ERROR   167 ;MSG A1 ERROR
5133 020400 104170      ERROR   170 ;MSG B1 ERROR
5134
5135 020402 012737 000017 005314      MOV      #SEEK,HCS1
5136 020410 004737 043372      JSR      PC,DOCMD ;DO SEEK CMD & GET CONTR READY
5137 020414 104131      ERROR   131 ;NO RDY AFTER SEEK CMD
5138
5139 020416 013737 001412 005352      MOV      T50000,TEMP1 ;SETUP TIMEOUT
5140 020424 004737 044106      JSR      PC,FATT2 ;FIND ATTN
5141 020430 104132      ERROR   132 ;NO ATTN AFTER SEEK CMD
5142
5143 020432 032737 100000 005314      BIT      #CERR,HCS1
5144 020440 001401      BEQ     65$
5145 020442 104210      ERROR   210 ;CERR AFTER SEEK CMD
5146
5147 020444      65$:
5148
5149 020444 012737 050340 005404      MOV      #<D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
5150 020452 005037 005406      CLR      E.B0 ;EXPECTED MSG B0
5151 020456 012737 001720 005410      MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5152 020464 012737 000001 005412      MOV      #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5153 020472 005037 005414      CLR      E.A2 ;EXPECTED MSG A2
5154 020476 012737 000002 005416      MOV      #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5155 020504 012737 000003 005422      MOV      #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5156
5157 020512 004737 044274      JSR      PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
5158 020516 000000      .WORD   0!0!0 ;& MSGS SPECIFIED HERE
5159 020520 104161      ERROR   161 ;MSG A0 ERROR AFTER SEEK CMD
5160 020522 104162      ERROR   162 ;MSH B0 ERROR
5161 020524 104163      ERROR   163 ;MSG A1 ERROR
5162 020526 104164      ERROR   164 ;MSG B1 ERROR
5163
5164 020530 013737 005466 001366      MOV      TIMER,COUNT
5165 020536 004737 047204      JSR      PC,TMO
5166 020542 012737 000001 005464      MOV      #1,UNITB ;SETUP PORTC
5167 020550 112737 000102 056560      MOV      #B,MSG19A
5168 020556 004737 044172      JSR      PC,DRAV
5169 020562 104045      ERROR   45 ;PORT B NOT AVAIL AFTER TMO
5170
5171
5172 020564 012737 050340 005404      MOV      #<D.DRA!D.DSC!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
5173 020572 005037 005406      CLR      E.B0 ;EXPECTED MSG B0
5174 020576 012737 001720 005410      MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5175 020604 012737 000001 005412      MOV      #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5176 020612 005037 005414      CLR      E.A2 ;EXPECTED MSG A2
5177 020616 012737 000002 005416      MOV      #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
    
```

JOB

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 100
T13 TEST RESET PORT 'B' ATTENTION BY DRIVE CLEAR COMMAND

SEQ 0100

```

5178 020624 012737 000003 005422      MOV      #3,E.B3      ;MSG ID FOR EXPECTED MSG B3
5179
5180 020632 004737 044274      JSR      PC_CHKMSG   ;CHECK MSGS A0, B0, A1, B1
5181 020636 000000                .WORD   0!0!0       ;& MSGS SPECIFIED HERE
5182 020640 104165      ERROR   165         ;MSG A0 ERROR AFTER TIMEOUT
5183 020642 104166      ERROR   166         ;MSH B0 ERROR
5184 020644 104167      ERROR   167         ;MSG A1 ERROR
5185 020646 104170      ERROR   170         ;MSG B1 ERROR
5186
5187 020650 012765 100000 000000      MOV      #CCLR,RKCS1(R5)
5188 020656 013765 001222 000010      MOV      $UNIT,RKCS2(R5) ;DRIVE#
5189 020664 063765 005464 000010      ADD      UNITB,RKCS2(R5) ;ADD 1 IF ON PORT B
5190 020672 012737 000005 005314      MOV      #CLEAR,HCS1
5191 020700 004737 043372      JSR      PC_DOCMD    ;DO DRIVE CLEAR CMD & GET CONTR RDY
5192 020704 104151      ERROR   151         ;NO RDY AFTER DRIVE CLEAR CMD
5193 020706 004737 043750      JSR      PC_TSTATN   ;TEST FOR ATTN
5194 020712 000401      BR      66$
5195 020714 104154      ERROR   154         ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
5196 020716
5197
5198 020716 012737 010340 005404      MOV      #<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
5199 020724 005037 005406      CLR      E.B0       ;EXPECTED MSG B0
5200 020730 012737 001720 005410      MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5201 020736 012737 000001 005412      MOV      #1,E.B1    ;MSG ID FOR EXPECTED MSG B1
5202 020744 005037 005414      CLR      E.A2       ;EXPECTED MSG A2
5203 020750 012737 000002 005416      MOV      #2,E.B2    ;MSG ID FOR EXPECTED MSG B2
5204 020756 012737 000003 005422      MOV      #3,E.B3    ;MSG ID FOR EXPECTED MSG B3
5205
5206 020764 004737 044274      JSR      PC_CHKMSG   ;CHECK MSGS A0, B0, A1, B1
5207 020770 000003                .WORD   T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
5208 020772 104033      ERROR   33         ;MSG A0 ERROR AFTER DRV CLEAR CMD
5209 020774 104034      ERROR   34         ;MSH B0 ERROR
5210 020776 104035      ERROR   35         ;MSG A1 ERROR
5211 021000 104036      ERROR   36         ;MSG B1 ERROR
5212
5213 021002 013737 005466 001366      MOV      TIMER_COUNT
5214 021010 004737 047204      JSR      PC_TM0     ;DO 1.5 SEC TIMEOUT
5215 021014 012737 000000 005464      MOV      #0,UNITB   ;SETUP PORT A
5216 021022 112737 000101 056560      MOV      #A,MSG19A
5217 021030 004737 043750      JSR      PC_TSTATN
5218 021034 104114      ERROR   114        ;ATTN RESET ON PORT A AFTER DR CLR CMD
5219
5220
5221
5222 021036 012737 050340 005404      MOV      #<D.DRA!D.DSC!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
5223 021044 005037 005406      CLR      E.B0       ;EXPECTED MSG B0
5224 021050 012737 001720 005410      MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5225 021056 012737 000001 005412      MOV      #1,E.B1    ;MSG ID FOR EXPECTED MSG B1
5226 021064 005037 005414      CLR      E.A2       ;EXPECTED MSG A2
5227 021070 012737 000002 005416      MOV      #2,E.B2    ;MSG ID FOR EXPECTED MSG B2
5228 021076 012737 000003 005422      MOV      #3,E.B3    ;MSG ID FOR EXPECTED MSG B3
5229
5230 021104 004737 044274      JSR      PC_CHKMSG   ;CHECK MSGS A0, B0, A1, B1
5231 021110 000000                .WORD   0!0!0       ;& MSGS SPECIFIED HERE
5232 021112 104165      ERROR   165         ;MSG A0 ERROR AFTER TIMEOUT
5233 021114 104166      ERROR   166         ;MSH B0 ERROR

```

66\$:

K08

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 101
T13 TEST RESET PORT 'B' ATTENTION BY DRIVE CLEAR COMMAND

SEQ 0101

5234 021116 104167
5235 021120 104170

ERROR 167 ;MSG A1 ERROR
ERROR 170 ;MSG B1 ERROR

```
*****
:TEST 14 TEST RELEASE, DRIVE SEIZED BY PORT 'A'
:
:A. SEIZE THE DRIVE THRU PORT 'A'
:
:B. ISSUE A RELEASE USING RKCS2 THRU PORT 'A'
:
:C. VERIFY PORT 'B' CAN ACCESS THE DRIVE IMMEDIATELY &
: THAT NEITHER PORT SEES 'DSC' OR 'ATTN'
*****
```

5248 021122 000004
5249 021124 012737 000001 001174
5250 021132 012706 001100
5251 021136 012737 000000 005464
5252 021144 112737 000101 056560
5253 021152 013737 005466 001366
5254 021160 004737 047204
5255
5256 021164 004737 045534
5257 021170 104024
5258
5259
5260 021172 004737 044172
5261 021176 104045
5262 021200 013765 001222 000010
5263 021206 012737 000000 005464
5264 021214 063765 005464 000010
5265 021222 112737 000101 056560
5266 021230 062765 000010 000010
5267 021236 012737 000001 005314
5268 021244 004737 043372
5269 021250 104117
5270
5271 021252 013765 001222 000010
5272 021260 012737 000001 005464
5273 021266 063765 005464 000010
5274 021274 112737 000102 056560
5275 021302 012737 000001 005314
5276 021310 004737 043372
5277 021314 104117
5278
5279 021316 032737 000040 005342
5280 021324 001001
5281 021326 104071
5282 021330
5283 021330 004737 043750
5284 021334 000401
5285 021336 104115
5286 021340
5287
5288 021340 012737 010340 005404
5289 021346 005037 005406

```
*****
TST14: SCOPE
MOV #1,STIMES ;DO 1 ITERATION
MOV #STACK,SP
MOV #0,UNITB ;SETUP PORT A
MOV #A,MSG19A
MOV TIMER,COUNT
JSR PC,TMO ;DO TIMEOUT
JSR PC,SUBCLR
ERROR 24 ;CERR AFTER SCLR
JSR PC,DRAV ;SEE IF DRIVE AVAIL
ERROR 45 ;PORT A NOT AVAIL AFTER TMO
MOV #UNITB, RKCS2(R5) ;SETUP FOR PORT A
MOV #0,UNITB
ADD UNITB, RKCS2(R5)
MOV #A,MSG19A
ADD #RLS, RKCS2(R5) ;RELEASE PORT A
MOV #SELDRV, HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 117 ;NO RDY AFTER SEL DRV CMD
MOV #UNITB, RKCS2(R5) ;SETUP FOR PORT B
MOV #1,UNITB
ADD UNITB, RKCS2(R5)
MOV #B,MSG19A
MOV #SELDRV, HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 117 ;NO RKY AFTER SEL DRV CMD
BIT #D.DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT B
BNE 64$ ;BR IF YES
ERROR 71 ;PORT B NOT AVAIL AFTER PORT A RLS
64$: JSR PC,TSTATN
BR 1$
ERROR 115 ;ATTN SET IN PORT B AFTER RLS OF PORT A
1$: MOV #<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
CLR E.B0 ;EXPECTED MSG B0
```

L08

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 102
T14 TEST RELEASE, DRIVE SEIZED BY PORT 'A'

SEQ 0102

```

5290 021352 012737 001720 005410      MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5291 021360 012737 000001 005412      MOV      #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5292 021366 005037 005414      CLR      E.A2 ;EXPECTED MSG A2
5293 021372 012737 000002 005416      MOV      #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5294 021400 012737 000003 005422      MOV      #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5295
5296 021406 004737 044274      JSR      PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
5297 021412 000000      .WORD   0!0!0 ;& MSGS SPECIFIED HERE
5298 021414 104133      ERROR   133 ;MSG A0 ERROR AFTER RELEASED TO PORT B
5299 021416 104134      ERROR   134 ;MSH B0 ERROR
5300 021420 104135      ERROR   135 ;MSG A1 ERROR
5301 021422 104136      ERROR   136 ;MSG B1 ERROR
5302
5303 021424 012737 000000 005464      MOV      #0,UNITB ;ADDRESS PORT A
5304 021432 112737 000101 056560      MOV      #A,MSG19A
5305 021440 004737 043750      JSR      PC,STATN
5306 021444 000433      BR      TST15 ;GOTO NEXT TST
5307 021446 104115      ERROR   115 ;ATTN SET AFTER RLS ISSUED
5308
5309 021450 012737 010340 005404      MOV      #<D.DRA!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
5310 021456 005037 005406      CLR      E.B0 ;EXPECTED MSG B0
5311 021462 012737 001720 005410      MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5312 021470 012737 000001 005412      MOV      #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5313 021476 005037 005414      CLR      E.A2 ;EXPECTED MSG A2
5314 021502 012737 000002 005416      MOV      #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5315 021510 012737 000003 005422      MOV      #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5316
5317 021516 004737 044274      JSR      PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
5318 021522 000000      .WORD   0!0!0 ;& MSGS SPECIFIED HERE
5319 021524 104211      ERROR   211 ;MSG A0 ERROR AFTER RELEASE ISSUED
5320 021526 104212      ERROR   212 ;MSH B0 ERROR
5321 021530 104213      ERROR   213 ;MSG A1 ERROR
5322 021532 104214      ERROR   214 ;MSG B1 ERROR
5323
5324
5325
5326
5327
5328
5329

```

```

*****
*TEST 15 TEST RELEASE, DRIVE SEIZED BY PORT 'B'
*
* THE PREVIOUS TEST IS REPEATED FOR PORT 'B'.
*
*****

```

```

5330 021534 000004      TST15: SCOPE
5331 021536 012737 000001 001174      MOV      #1,STIMES ;DO 1 ITERATION
5332 021544 012706 001100      MOV      #STACK,SP
5333 021550 012737 000001 005464      MOV      #1,UNITB ;SETUP PORT B
5334 021556 112737 000102 056560      MOV      #B,MSG19A
5335 021564 013737 005466 001366      MOV      TIMER,COUNT
5336 021572 004737 047204      JSR      PC,TMO ;DO TIME'OUT
5337
5338 021576 004737 045534      JSR      PC,SUBCLR
5339 021602 104024      ERROR   24 ;CERR AFTER SCLR
5340
5341
5342 021604 004737 044172      JSR      PC,DRAV ;SEE IF DRIVE AVAIL
5343 021610 104045      ERROR   45 ;PORT B NOT AVAIL AFTER TMO
5344 021612 013765 001222 000010      MOV      $UNIT,RKCS2(R5) ;SETUP FOR PORT B
5345 021620 012737 000001 005464      MOV      #1,UNITB

```


M08

CZR6GBD RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 103
T15 TEST RELEASE, DRIVE SEIZED BY PORT 'B'

SEQ 0103

5346	021626	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
5347	021634	112737	000102	056560	MOV	#'B,MSG19A	
5348	021642	062765	000010	000010	ADD	#RLS,RKCS2(R5)	;RELEASE PORT B
5349	021650	012737	000001	005314	MOV	#SELDRV,HCS1	
5350	021656	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
5351	021662	104117			ERROR	117	;NO RDY AFTER SEL DRV CMD
5352							
5353	021664	013765	001222	000010	MOV	SUNIT,RKCS2(R5)	;SETUP FOR PORT A
5354	021672	012737	000000	005464	MOV	#D,UNITB	
5355	021700	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
5356	021706	112737	000101	056560	MOV	#'A,MSG19A	
5357	021714	012737	000001	005314	MOV	#SELDRV,HCS1	
5358	021722	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
5359	021726	104117			ERROR	117	;NO RKY AFTER SEL DRV CMD
5360							
5361	021730	032737	000040	005342	BIT	#D.DRA,HMR2	;SEE IF DRIVE AVAIL ON PORT A
5362	021736	001001			BNE	64\$;BR IF YES
5363	021740	104071			ERROR	71	;PORT A NOT AVAIL AFTER PORT B RLS
5364	021742						
5365	021742	004737	043750		JSR	PC,TSTATN	
5366	021746	000401			BR	1\$	
5367	021750	104115			ERROR	115	;ATTN SET IN PORT A AFTER RLS OF PORT B
5368	021752						
5369							
5370	021752	012737	010340	005404	MOV	#<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0	;EXPECTED MSG A0
5371	021760	005037	005406		CLR	E.B0	;EXPECTED MSG B0
5372	021764	012737	001720	005410	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1
5373	021772	012737	000001	005412	MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1
5374	022000	005037	005414		CLR	E.A2	;EXPECTED MSG A2
5375	022004	012737	000002	005416	MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2
5376	022012	012737	000003	005422	MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
5377							
5378	022020	004737	044274		JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1
5379	022024	000000			.WORD	0!0!0	; & MSGS SPECIFIED HERE
5380	022026	104133			ERROR	133	;MSG A0 ERROR AFTER RELEASED TO PORT A
5381	022030	104134			ERROR	134	;MSG B0 ERROR
5382	022032	104135			ERROR	135	;MSG A1 ERROR
5383	022034	104136			ERROR	136	;MSG B1 ERROR
5384							
5385	022036	012737	000001	005464	MOV	#1,UNITB	;ADDRESS PORT B
5386	022044	112737	000102	056560	MOV	#'B,MSG19A	
5387	022052	004737	043750		JSR	PC,TSTATN	
5388	022056	000433			BR	TST16	;GOTO NEXT TST
5389	022060	104115			ERROR	115	;ATTN SET AFTER RLS ISSUED
5390							
5391	022062	012737	010340	005404	MOV	#<D.DRA!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0	;EXPECTED MSG A0
5392	022070	005037	005406		CLR	E.B0	;EXPECTED MSG B0
5393	022074	012737	001720	005410	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1
5394	022102	012737	000001	005412	MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1
5395	022110	005037	005414		CLR	E.A2	;EXPECTED MSG A2
5396	022114	012737	000002	005416	MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2
5397	022122	012737	000003	005422	MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
5398							
5399	022130	004737	044274		JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1
5400	022134	000000			.WORD	0!0!0	; & MSGS SPECIFIED HERE
5401	022136	104211			ERROR	211	;MSG A0 ERROR AFTER RELEASE ISSUED

N08

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 104
T15 TEST RELEASE, DRIVE SEIZED BY PORT 'B'

SEQ 0104

5402 022140 104212
5403 022142 104213
5404 022144 104214

ERROR 212 ;MSH B0 ERROR
ERROR 213 ;MSG A1 ERROR
ERROR 214 ;MSG B1 ERROR

```

*****
*TEST 16 TEST RELEASE FROM PORT 'A' WITH PORT 'B' REQUESTING
*
* A. PORT 'A' SEIZES THE DRIVE & DOES A SEEK TO SELF COMMAND.
* THE PROGRAM VERIFIES 'DSC' & 'ATTN' ON PORT 'A' ONLY
* ON COMPLETION
*
* B. PORT 'B' TRIES TO ACCESS THE DRIVE. THE PROGRAM VERIFIES
* DRIVE NOT AVAILABLE
*
* C. A RELEASE BY PORT 'A' IS ISSUED. VERIFY PORT 'B' CAN
* ACCESS THE DRIVE IMMEDIATELY & THAT 'DSC' & 'ATTN'
* ARE SEEN ON PORT 'B'
*
* D. VERIFY PORT 'A' 'DSC' & 'ATTN' REMAINS SET AFTER RELEASE
*
* E. THE PROGRAM ISSUES A DRIVE CLEAR COMMAND TO PORT 'B'
* & VERIFIES 'DSC' & 'ATTN' RESETS.
*
* F. THE PROGRAM THEN VERIFIES THAT PORT 'B' DOES NOT SEE
* FURTHER (MULTIPLE) ATTENTIONS FROM WHAT WOULD HAVE BEEN
* NORMAL TIMEOUT FROM PORT 'A'.
*****

```

5430 022146 000004
5431 022150 012737 000001 001174
5432 022156 012706 001100
5433 022162 013765 001222 000010
5434 022170 012737 000001 005464
5435 022176 063765 005464 000010
5436 022204 112737 000102 056560
5437 022212 062765 000010 000010
5438 022220 012737 000001 005314
5439 022226 004737 043372
5440 022232 104117
5441
5442 022234 013765 001222 000010
5443 022242 012737 000000 005464
5444 022250 063765 005464 000010
5445 022256 112737 000101 056560
5446 022264 012737 000001 005314
5447 022272 004737 043372
5448 022276 104117
5449
5450 022300 032737 000040 005342
5451 022306 001001
5452 022310 104071
5453 022312
5454
5455 022312 012737 010340 005404
5456 022320 005037 005406
5457 022324 012737 001720 005410

```

*****
*ST16: SCOPE
MOV #1,STIMES ;;DO 1 ITERATION
MOV #STACK,SP
MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
MOV #1,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'B,MSG19A
ADD #RLS,RKCS2(R5) ;RELEASE PORT B
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 117 ;NO RDY AFTER SEL DRV CMD
MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
MOV #0,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'A,MSG19A
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 117 ;NO RKY AFTER SEL DRV CMD
BIT #D.DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT A
BNE 64$ ;BR IF YES
ERROR 71 ;PORT A NOT AVAIL AFTER PORT B RLS
64$:
MOV #<D.DRA!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
CLR E.B0 ;EXPECTED MSG B0
MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1

```

```

5458 022332 012737 000001 005412      MOV      #1,E.B1      ;MSG ID FOR EXPECTED MSG B1
5459 022340 005037 005414      CLR      E.A2        ;EXPECTED MSG A2
5460 022344 012737 000002 005416      MOV      #2,E.B2      ;MSG ID FOR EXPECTED MSG B2
5461 022352 012737 000003 005422      MOV      #3,E.B3      ;MSG ID FOR EXPECTED MSG B3
5462
5463 022360 004737 044274      JSR      PC,CHKMSG    ;CHECK MSGS A0, B0, A1, B1
5464 022364 000000      .WORD   0!0!0        ;& MSGS SPECIFIED HERE
5465 022366 104211      ERROR   211          ;MSG A0 ERROR AFTER RELEASE ISSUED
5466 022370 104212      ERROR   212          ;MSG B0 ERROR
5467 022372 104213      ERROR   213          ;MSG A1 ERROR
5468 022374 104214      ERROR   214          ;MSG B1 ERROR
5469 022376 004737 045534      JSR      PC,SUBCLR    ;CERR AFTER SCLR
5470 022402 104024      ERROR   24
5471
5472 022404 012737 000017 005314      MOV      #SEEK,HCS1   ;DO SEEK CMD & GET CONTR READY
5473 022412 004737 043372      JSR      PC,DOCMD     ;NO RDY AFTER SEEK CMD
5474 022416 104131      ERROR   131
5475
5476 022420 013737 001412 005352      MOV      T5000,TEMP1 ;SETUP TIMEOUT
5477 022426 004737 044106      JSR      PC,FATT2     ;FIND ATTN
5478 022432 104132      ERROR   132          ;NO ATTN AFTER SEEK CMD
5479
5480 022434 032737 100000 005314      BIT      #CERR,HCS1   ;CERR AFTER SEEK CMD
5481 022442 001401      BEQ     65$
5482 022444 104210      ERROR   210
5483
5484 022446      65$:
5485
5486 022446 012737 050340 005404      MOV      #<D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
5487 022454 005037 005406      CLR      E.B0        ;EXPECTED MSG B0
5488 022460 012737 001720 005410      MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5489 022466 012737 000001 005412      MOV      #1,E.B1      ;MSG ID FOR EXPECTED MSG B1
5490 022474 005037 005414      CLR      E.A2        ;EXPECTED MSG A2
5491 022500 012737 000002 005416      MOV      #2,E.B2      ;MSG ID FOR EXPECTED MSG B2
5492 022506 012737 000003 005422      MOV      #3,E.B3      ;MSG ID FOR EXPECTED MSG B3
5493
5494 022514 004737 044274      JSR      PC,CHKMSG    ;CHECK MSGS A0, B0, A1, B1
5495 022520 000000      .WORD   0!0!0        ;& MSGS SPECIFIED HERE
5496 022522 104161      ERROR   161          ;MSG A0 ERROR AFTER SEEK CMD
5497 022524 104162      ERROR   162          ;MSG B0 ERROR
5498 022526 104163      ERROR   163          ;MSG A1 ERROR
5499 022530 104164      ERROR   164          ;MSG B1 ERROR
5500
5501
5502 022532 012737 000001 005464      MOV      #1,UNITB     ;SETUP PORT B
5503 022540 112737 000102 056560      MOV     #'B,MSG19A
5504 022546 004737 044172      JSR      PC,DRAV      ;SEE IF DRIVE AVAIL
5505 022552 000401      BR      1$
5506 022554 104103      ERROR   103          ;PORT B AVAIL BEFORE TMO OR RELEASE
5507
5508 022556 032737 100000 005314 1$:      BIT      #CERR,HCS1
5509 022564 001001      BNE     2$
5510 022566 104130      ERROR   130          ;CERR NOT SET AFTER SEL DRIVE CMD
5511
5512 022570 004737 043750 2$:      JSR      PC,TSTATN
5513 022574 000401      BR      3$

```

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 106
T16 TEST RELEASE FROM PORT 'A' WITH PORT 'B' REQUESTING

SEQ 0106

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5514 022576 104115          ERROR 115          ;ATTN SET IN PORT B AFTER RLS FROM PORT A
5515
5516 022600 012765 100000 000000 3$:  MOV #CCLR,RKCS1(R5)
5517 022606 013765 001222 000010  MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
5518 022614 012737 000000 005464  MOV #0,UNITB
5519 022622 063765 005464 000010  ADD UNITB,RKCS2(R5)
5520 022630 112737 000101 056560  MOVB #'A,MSG19A
5521 022636 062765 000010 000010  ADD #RLS,RKCS2(R5) ;RELEASE PORT A
5522 022644 012737 000001 005314  MOV #SELDRV,HCS1
5523 022652 004737 043372  JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
5524 022656 104117  ERROR 117 ;NO RDY AFTER SEL DRV CMD
5525
5526 022660 013765 001222 000010  MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
5527 022666 012737 000001 005464  MOV #1,UNITB
5528 022674 063765 005464 000010  ADD UNITB,RKCS2(R5)
5529 022702 112737 000102 056560  MOVB #'B,MSG19A
5530 022710 012737 000001 005314  MOV #SELDRV,HCS1
5531 022716 004737 043372  JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
5532 022722 104117  ERROR 117 ;NO RKY AFTER SEL DRV CMD
5533
5534 022724 032737 000040 005342  BIT #D.DRA,HMR2 ;SEE IF L3IVE AVAIL ON PORT B
5535 022732 001001  BNE 66$ ;BR IF YES
5536 022734 104071  ERROR 71 ;PORT B NOT AVAIL AFTER PORT A RLS
5537 022736
5538 022736 004737 043750 66$: JSR PC,TSTATN
5539 022742 104122  ERROR 122 ;NO ATTN IN PORT B AFTER RLS FROM PORT A
5540
5541
5542 022744 012737 050340 005404  MOV #<D.DRA!D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
5543 022752 005037 005406  CLR E.B0 ;EXPECTED MSG B0
5544 022756 012737 001720 005410  MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5545 022764 012737 000001 005412  MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5546 022772 005037 005414  CLR E.A2 ;EXPECTED MSG A2
5547 022776 012737 000002 005416  MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5548 023004 012737 000003 005422  MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5549
5550 023012 004737 044274  JSR PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
5551 023016 000000  .WORD 0!0!0 ;& MSGS SPECIFIED HERE
5552 023020 104211  ERROR 211 ;MSG A0 ERROR AFTER RELEASE ISSUED
5553 023022 104212  ERROR 212 ;MSG B0 ERROR
5554 023024 104213  ERROR 213 ;MSG A1 ERROR
5555 023026 104214  ERROR 214 ;MSG B1 ERROR
5556 023030 013765 001222 000010  MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
5557 023036 012737 000001 005464  MOV #1,UNITB
5558 023044 063765 005464 000010  ADD UNITB,RKCS2(R5)
5559 023052 112737 000102 056560  MOVB #'B,MSG19A
5560 023060 062765 000010 000010  ADD #RLS,RKCS2(R5) ;RELEASE PORT B
5561 023066 012737 000001 005314  MOV #SELDRV,HCS1
5562 023074 004737 043372  JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
5563 023100 104117  ERROR 117 ;NO RDY AFTER SEL DRV CMD
5564
5565 023102 013765 001222 000010  MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
5566 023110 012737 000000 005464  MOV #0,UNITB
5567 023116 063765 005464 000010  ADD UNITB,RKCS2(R5)
5568 023124 112737 000101 056560  MOVB #'A,MSG19A
5569 023132 012737 000001 005314  MOV #SELDRV,HCS1

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5570 023140 004737 043372 JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
5571 023144 104117 ERROR 117 ;NO RKY AFTER SEL DRV CMD
5572
5573 023146 032737 000040 005342 BIT #D,DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT A
5574 023154 001001 BNE 67$ ;BR IF YES
5575 023156 104071 ERROR 71 ;PORT A NOT AVAIL AFTER PORT B RLS
5576 023160 67$: JSR PC,TSTATN
5577 023160 004737 043750 ERROR 123 ;ATTN CLEARED IN PORT A AFT RLS FROM PORT B
5578 023164 104123
5579
5580
5581 023166 012737 050340 005404 MOV #<D,DRA!D.DSC!D.DRA!D.SPIN!D.DRY!D.VV>,E.A0 ;EXPECTED MSG A0
5582 023174 005037 005406 CLR E.B0 ;EXPECTED MSG B0
5583 023200 012737 001720 005410 MOV #<D,SPOK!D.CART!D.DOOR!D.BRM!D.SSP>,E.A1 ;EXPECTED A1
5584 023206 012737 000001 005412 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5585 023214 005037 005414 CLR E.A2 ;EXPECTED MSG A2
5586 023220 012737 000002 005416 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5587 023226 012737 000003 005422 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5588
5589 023234 004737 044274 JSR PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
5590 023240 000000 .WORD 0!0!0 ;& MSGS SPECIFIED HERE
5591 023242 104211 ERROR 211 ;MSG A0 ERROR AFTER RELEASE ISSUED
5592 023244 104212 ERROR 212 ;MSH B0 ERROR
5593 023246 104213 ERROR 213 ;MSG A1 ERROR
5594 023250 104214 ERROR 214 ;MSG B1 ERROR
5595 023252 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
5596 023260 012737 000000 005464 MOV #0,UNITB
5597 023266 063765 005464 000010 ADD UNITB,RKCS2(R5)
5598 023274 112737 000101 056560 MOVB #'A,MSG19A
5599 023302 062765 000010 000010 ADD #RLS,RKCS2(R5) ;RELEASE PORT A
5600 023310 012737 000001 005314 MOV #SELDV,HCS1
5601 023316 004737 043372 JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
5602 023322 104117 ERROR 117 ;NO RDY AFTER SEL DRV CMD
5603
5604 023324 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
5605 023332 012737 000001 005464 MOV #1,UNITB
5606 023340 063765 005464 000010 ADD UNITB,RKCS2(R5)
5607 023346 112737 000102 056560 MOVB #'B,MSG19A
5608 023354 012737 000001 005314 MOV #SELDV,HCS1
5609 023362 004737 043372 JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
5610 023366 104117 ERROR 117 ;NO RKY AFTER SEL DRV CMD
5611
5612 023370 032737 000040 005342 BIT #D,DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT B
5613 023376 001001 BNE 68$ ;BR IF YES
5614 023400 104071 ERROR 71 ;PORT B NOT AVAIL AFTER PORT A RLS
5615 023402 68$:
5616
5617 023402 012765 100000 000000 MOV #CLR,RKCS1(R5) ;DRIVE#
5618 023410 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;ADD 1 IF ON PORT B
5619 023416 063765 005464 000010 ADD UNITB,RKCS2(R5)
5620 023424 012737 000005 005314 MOV #CLEAR,HCS1
5621 023432 004737 043372 JSR PC,DOCMD ;DO DRIVE CLEAR CMD & GET CONTR RDY
5622 023436 104151 ERROR 151 ;NO RDY AFTER DRIVE CLEAR CMD
5623 023440 004737 043750 JSR PC,TSTATN ;TEST FOR ATTN
5624 023444 000401 BR 69$
5625 023446 104154 ERROR 154 ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD

```

```

5626 023450
5627
5628 023450 012737 010340 005404
5629 023456 005037 005406
5630 023462 012737 001720 005410
5631 023470 012737 000001 005412
5632 023476 005037 005414
5633 023502 012737 000002 005416
5634 023510 012737 000003 005422
5635
5636 023516 004737 044274
5637 023522 000003
5638 023524 104033
5639 023526 104034
5640 023530 104035
5641 023532 104036
5642
5643 023534 013737 005466 001366
5644 023542 004737 047204
5645 023546 004737 043750
5646 023552 000401
5647 023554 104144
5648
5649
5650
5651
5652
5653
5654
5655 023556 000004
5656 023560 012737 000001 001174
5657 023566 012706 001100
5658 023572 013765 001222 000010
5659 023600 012737 000000 005464
5660 023606 063765 005464 000010
5661 023614 112737 000101 056560
5662 023622 062765 000010 000010
5663 023630 012737 000001 005314
5664 023636 004737 043372
5665 023642 104117
5666
5667 023644 013765 001222 000010
5668 023652 012737 000001 005464
5669 023660 063765 005464 000010
5670 023666 112737 000102 056560
5671 023674 012737 000001 005314
5672 023702 004737 043372
5673 023706 104117
5674
5675 023710 032737 000040 005342
5676 023716 001001
5677 023720 104071
5678 023722
5679
5680 023722 012737 010340 005404
5681 023730 005037 005406

```

69\$:

```

MOV #<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
CLR E.B0 ;EXPECTED MSG B0
MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
CLR E.A2 ;EXPECTED MSG A2
MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3

```

```

JSR PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
; & MSGS SPECIFIED HERE
WORD T.A2!T.B2!0
ERROR 33 ;MSG A0 ERROR AFTER DRV CLEAR CMD
ERROR 34 ;MSG B0 ERROR
ERROR 35 ;MSG A1 ERROR
ERROR 36 ;MSG B1 ERROR

```

```

MOV TIMER,COUNT
JSR PC,TMO ;DO 1.5 SEC TIMEOUT ON PORT B
JSR PC,TSTATN
BR TST17 ;GOTO NEXT TST
ERROR 144 ;MULT ATTN ON PORT B

```

```

*****
:TEST 17 TEST RELEASE FROM PORT 'B' WITH PORT 'A' REQUESTING
:
: THE PREVIOUS TEST IS REPEATED FOR PORT 'B'.
:
*****

```

```

TST17: SCOPE
MOV #1,STIMES ;DO 1 ITERATION
MOV #STACK,SP
MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
MOV #0,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'A,MSG19A
ADD #RLS,RKCS2(R5) ;RELEASE PORT A
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 117 ;NO RDY AFTER SEL DRV CMD

MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
MOV #1,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'B,MSG19A
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 117 ;NO RKY AFTER SEL DRV CMD

BIT #D.DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT B
BNE 64$ ;BR IF YES
ERROR 71 ;PORT B NOT AVAIL AFTER PORT A RLS

```

64\$:

```

MOV #<D.DRA!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
CLR E.B0 ;EXPECTED MSG B0

```



```

5738 024204 000401 BR 3$
5739 024206 104115 ERROR 115 ;ATTN SET IN PORT A AFTER RLS FROM PORT B
5740
5741 024210 012765 100000 000000 3$: MOV #CCLR,RKCS1(R5)
5742 024216 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
5743 024224 012737 000001 005464 MOV #1,UNITB
5744 024232 063765 005464 000010 ADD UNITB,RKCS2(R5)
5745 024240 112737 000102 056560 MOVB #'B,MSG19A
5746 024246 062765 000010 000010 ADD #RLS,RKCS2(R5) ;RELEASE PORT B
5747 024254 012737 000001 005314 MOV #SELDRV,HCS1
5748 024262 004737 043372 JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
5749 024266 104117 ERROR 117 ;NO RDY AFTER SEL DRV CMD
5750
5751 024270 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
5752 024276 012737 000000 005464 MOV #0,UNITB
5753 024304 063765 005464 000010 ADD UNITB,RKCS2(R5)
5754 024312 112737 000101 056560 MOVB #'A,MSG19A
5755 024320 012737 000001 005314 MOV #SELDRV,HCS1
5756 024326 004737 043372 JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
5757 024332 104117 ERROR 117 ;NO RKY AFTER SEL DRV CMD
5758
5759 024334 032737 000040 005342 BIT #D.DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT A
5760 024342 001001 BNE 66$ ;BR IF YES
5761 024344 104071 ERROR 71 ;PORT A NOT AVAIL AFTER PORT B RLS
5762 024346
5763 024346 004737 043750 66$: JSR PC,TSTATN
5764 024352 104122 ERROR 122 ;NO ATTN IN PORT A AFTER RLS FROM PORT B
5765
5766
5767 024354 012737 050340 005404 MOV #<D.DRA!D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
5768 024362 005037 005406 CLR E.B0 ;EXPECTED MSG B0
5769 024366 012737 001720 005410 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5770 024374 012737 000001 005412 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5771 024402 005037 005414 CLR E.A2 ;EXPECTED MSG A2
5772 024406 012737 000002 005416 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5773 024414 012737 000003 005422 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5774
5775 024422 004737 044274 JSR PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
5776 024426 000000 .WORD 0!0!0 ;& MSGS SPECIFIED HERE
5777 024430 104211 ERROR 211 ;MSG A0 ERROR AFTER RELEASE ISSUED
5778 024432 104212 ERROR 212 ;MSG B0 ERROR
5779 024434 104213 ERROR 213 ;MSG A1 ERROR
5780 024436 104214 ERROR 214 ;MSG B1 ERROR
5781 024440 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
5782 024446 012737 000000 005464 MOV #0,UNITB
5783 024454 063765 005464 000010 ADD UNITB,RKCS2(R5)
5784 024462 112737 000101 056560 MOVB #'A,MSG19A
5785 024470 062765 000010 000010 ADD #RLS,RKCS2(R5) ;RELEASE PORT A
5786 024476 012737 000001 005314 MOV #SELDRV,HCS1
5787 024504 004737 043372 JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
5788 024510 104117 ERROR 117 ;NO RDY AFTER SEL DRV CMD
5789
5790 024512 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
5791 024520 012737 000001 005464 MOV #1,UNITB
5792 024526 063765 005464 000010 ADD UNITB,RKCS2(R5)
5793 024534 112737 000102 056560 MOVB #'B,MSG19A

```


5794	024542	012737	000001	005314	MOV	#SELDRV,HCS1	
5795	024550	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
5796	024554	104117			ERROR	117	;NO RKY AFTER SEL DRV CMD
5797							
5798	024556	032737	000040	005342	BIT	#D.DRA,HMR2	;SEE IF DRIVE AVAIL ON PORT B
5799	024564	001001			BNE	67\$;BR IF YES
5800	024566	104071			ERROR	71	;PORT B NOT AVAIL AFTER PORT A RLS
5801	024570						
5802	024570	004737	043750		JSR	PC,TSTATN	
5803	024574	104123			ERROR	123	;ATTN CLEARED IN PORT B AFT RLS FROM PORT A
5804							
5805							
5806	024576	012737	050340	005404	MOV	#<D.DRA!D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0	;EXPECTED MSG A0
5807	024604	005037	005406		CLR	E.B0	;EXPECTED MSG B0
5808	024610	012737	001720	005410	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1
5809	024616	012737	000001	005412	MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1
5810	024624	005037	005414		CLR	E.A2	;EXPECTED MSG A2
5811	024630	012737	000002	005416	MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2
5812	024636	012737	000003	005422	MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
5813							
5814	024644	004737	044274		JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1
5815	024650	000000			WORD	0!0!0	& MSGS SPECIFIED HERE
5816	024652	104211			ERROR	211	;MSG A0 ERROR AFTER RELEASE ISSUED
5817	024654	104212			ERROR	212	;MSH B0 ERROR
5818	024656	104213			ERROR	213	;MSG A1 ERROR
5819	024660	104214			ERROR	214	;MSG B1 ERROR
5820	024662	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT B
5821	024670	012737	000001	005464	MOV	#1,UNITB	
5822	024676	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
5823	024704	112737	000102	056560	MOV	#'B,MSG19A	
5824	024712	062765	000010	000010	ADD	#RLS,RKCS2(R5)	;RELEASE PORT B
5825	024720	012737	000001	005314	MOV	#SELDRV,HCS1	
5826	024726	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
5827	024732	104117			ERROR	117	;NO RDY AFTER SEL DRV CMD
5828							
5829	024734	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT A
5830	024742	012737	000000	005464	MOV	#0,UNITB	
5831	024750	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
5832	024756	112737	000101	056560	MOV	#'A,MSG19A	
5833	024764	012737	000001	005314	MOV	#SELDRV,HCS1	
5834	024772	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
5835	024776	104117			ERROR	117	;NO RKY AFTER SEL DRV CMD
5836							
5837	025000	032737	000040	005342	BIT	#D.DRA,HMR2	;SEE IF DRIVE AVAIL ON PORT A
5838	025006	001001			BNE	68\$;BR IF YES
5839	025010	104071			ERROR	71	;PORT A NOT AVAIL AFTER PORT B RLS
5840	025012						
5841							
5842	025012	012765	100000	000000	MOV	#CLR,RKCS1(R5)	
5843	025020	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;DRIVE#
5844	025026	063765	005464	000010	ADD	UNITB,RKCS2(R5)	;ADD 1 IF ON PORT B
5845	025034	012737	000005	005314	MOV	#CLEAR,HCS1	
5846	025042	004737	043372		JSR	PC,DOCMD	;DO DRIVE CLEAR CMD & GET CONTR RDY
5847	025046	104151			ERROR	151	;NO RDY AFTER DRIVE CLEAR CMD
5848	025050	004737	043750		JSR	PC,TSTATN	;TEST FOR ATTN
5849	025054	000401			BR	69\$	

```

5850 025056 104154          ERROR 154          ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
5851 025060          69$:
5852
5853 025060 012737 010340 005404  MOV    #<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
5854 025066 005037 005406  CLR    E.B0 ;EXPECTED MSG B0
5855 025072 012737 001720 005410  MOV    #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5856 025100 012737 000001 005412  MOV    #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5857 025106 005037 005414  CLR    E.A2 ;EXPECTED MSG A2
5858 025112 012737 000002 005416  MOV    #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5859 025120 012737 000003 005422  MOV    #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5860
5861 025126 004737 044274  JSR    PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
5862 025132 000003          .WORD  T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
5863 025134 104033          ERROR 33 ;MSG A0 ERROR AFTER DRV CLEAR CMD
5864 025136 104034          ERROR 34 ;MSH B0 ERROR
5865 025140 104035          ERROR 35 ;MSG A1 ERROR
5866 025142 104036          ERROR 36 ;MSG B1 ERROR
5867
5868 025144 013737 005466 001366  MOV    TIMER,COUNT
5869 025152 004737 047204  JSR    PC,TMO ;DO 1.5 SEC TIMEOUT ON PORT A
5870 025156 004737 043750  JSR    PC,TSTATN
5871 025162 000401          BR     TST20 ;GOTO NEXT TST
5872 025164 104144          ERROR 144 ;MULT ATTN ON PORT A
5873
5874
5875
5876
5877
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5879
5880
5881
5882
5883
5884
5885 025166 000004          *****
5886 025170 012737 000001 001174  *TEST 20 TEST RELEASE FROM REQUESTING PORT 'B' INHIBITS 'ATTN'
5887 025176 012706 001100          *
5888 025202 013765 001222 000010          * A. PORT 'A' SEIZES THE DRIVE
5889 025210 012737 000001 005464          * B. PORT 'B' ATTEMPTS TO SEIZE THE DRIVE
5890 025216 063765 005464 000010          * C. PORT 'B' & PORT 'A' RELEASE THE DRIVE, IN THAT ORDER
5891 025224 112737 000102 056560          * D. THE PROGRAM VERIFIES THAT NEITHER PORT 'A' OR 'B' ATTENTION
5892 025232 062765 000010 000010          * BITS SET
5893 025240 012737 000001 005314          *
5894 025246 004737 043372          *****
5895 025252 104117          †TST20: SCOPE
5896
5897 025254 013765 001222 000010          MOV    $UNIT,RKCS2(R5) ;SETUP FOR PORT A
5898 025262 012737 000000 005464          MOV    #0,UNITB
5899 025270 063765 005464 000010          ADD    UNITB,RKCS2(R5)
5900 025276 112737 000101 056560          MOV    #1,UNITB
5901 025304 012737 000001 005314          MOV    #1,UNITB ;RELEASE PORT B
5902 025312 004737 043372          ADD    #B,MSG19A
5903 025316 104117          MOV    #RLS,RKCS2(R5) ;RELEASE PORT B
5904
5905 025320 032737 000040 005342          MOV    #SELDV,HCS1
          JSR    PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
          ERROR 117 ;NO RDY AFTER SEL DRV CMD
          BIT    #D.DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT A

```

```

5906 025326 001001 BNE 64$ ;BR IF YES
5907 025330 104071 ERROR 71 ;PORT A NOT AVAIL AFTER PORT B RLS
5908 025332 64$:
5909
5910 025332 012737 010340 005404 MOV #<D.DRA!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
5911 025340 005037 005406 CLR E.B0 ;EXPECTED MSG B0
5912 025344 012737 001720 005410 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5913 025352 012737 000001 005412 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5914 025360 005037 005414 CLR E.A2 ;EXPECTED MSG A2
5915 025364 012737 000002 005416 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5916 025372 012737 000003 005422 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5917
5918 025400 004737 044274 JSR PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
5919 025404 000000 .WORD 0:0:0 ;& MSGS SPECIFIED HERE
5920 025406 104211 ERROR 211 ;MSG A0 ERROR AFTER RELEASE ISSUED
5921 025410 104212 ERROR 212 ;MSG B0 ERROR
5922 025412 104213 ERROR 213 ;MSG A1 ERROR
5923 025414 104214 ERROR 214 ;MSG B1 ERROR
5924 025416 004737 045534 JSR PC,SUBCLR
5925 025422 104024 ERROR 24 ;CERR AFTER SCLR
5926
5927 025424 012737 000001 005464 MOV #1,UNITB ;SETUP PORT B
5928 025432 112737 000102 056560 MOVB #'B,MSG19A
5929 025440 004737 044172 JSR PC,DRAV ;SEE IF DRIVE AVAIL
5930 025444 000401 BR 1$
5931 025446 104103 ERROR 103 ;PORT B AVAIL BEFORE TMO OR RELEASE
5932
5933 025450 032737 100000 005314 1$: BIT #CERR,HCS1
5934 025456 001001 BNE 2$
5935 025460 104130 ERROR 130 ;CERR NOT SET BY NO DRA
5936
5937 025462 004737 043750 2$: JSR PC,TSTATN
5938 025466 000401 BR 3$
5939 025470 104115 ERROR 115 ;ATTN SET IN PORT B AFTER RLS FROM PORT A
5940
5941 025472 012765 100000 000000 3$: MOV #CCLR,RKCS1(R5)
5942 025500 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
5943 025506 012737 000001 005464 MOV #1,UNITB
5944 025514 063765 005464 000010 ADD UNITB,RKCS2(R5)
5945 025522 112737 000102 056560 MOVB #'B,MSG19A
5946 025530 062765 000010 000010 ADD #RLS,RKCS2(R5) ;RELEASE PORT B
5947 025536 012737 000001 005314 MOV #SELDRV,HCS1
5948 025544 004737 043372 JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
5949 025550 104117 ERROR 117 ;NO RDY AFTER SEL DRV CMD
5950
5951 025552 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
5952 025560 012737 000000 005464 MOV #0,UNITB
5953 025566 063765 005464 000010 ADD UNITB,RKCS2(R5)
5954 025574 112737 000101 056560 MOVB #'A,MSG19A
5955 025602 012737 000001 005314 MOV #SELDRV,HCS1
5956 025610 004737 043372 JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
5957 025614 104117 ERROR 117 ;NO RKY AFTER SEL DRV CMD
5958
5959 025616 032737 000040 005342 BIT #D.DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT A
5960 025624 001001 BNE 65$ ;BR IF YES
5961 025626 104071 ERROR 71 ;PORT A NOT AVAIL AFTER PORT B RLS

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5962 025630          65$:
5963 025630 013765 001222 000010  MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
5964 025636 012737 000000 005464  MOV #0,UNITB
5965 025644 063765 005464 000010  ADD UNITB,RKCS2(R5)
5966 025652 112737 000101 056560  MOVB #'A,MSG19A
5967 025660 062765 000010 000010  ADD #PLS,RKCS2(R5) ;RELEASE PORT A
5968 025666 012737 000001 005314  MOV #SELDRV,HCS1
5969 025674 004737 043372  JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
5970 025700 104117  ERROR 117 ;NO RDY AFTER SEL DRV CMD
5971
5972 025702 013765 001222 000010  MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
5973 025710 012737 000001 005464  MOV #1,UNITB
5974 025716 063765 005464 000010  ADD UNITB,RKCS2(R5)
5975 025724 112737 000102 056560  MOVB #'B,MSG19A
5976 025732 012737 000001 005314  MOV #SELDRV,HCS1
5977 025740 004737 043372  JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
5978 025744 104117  ERROR 117 ;NO RKY AFTER SEL DRV CMD
5979
5980 025746 032737 000040 005342  BIT #D.DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT B
5981 025754 001001  BNE 66$ ;BR IF YES
5982 025756 104071  ERROR 71 ;PORT B NOT AVAIL AFTER PORT A RLS
5983
5984 025760 013737 005466 001366 66$:
5985 025766 004737 047204  MOV TIMER,COUNT
5986 025772 004737 043750  JSR PC,TMO ;DO 1.5 SEC TIMEOUT ON PORT B
5987 025776 000401  JSR PC,TSTATN
5988 026000 104115  BR 4$
5989  ERROR 115 ;ATTN SET ON PORT B AFTER RLS FROM PORT B
5990
5991 026002          4$:
5992 026002 012737 010340 005404  MOV #<D.DRA!0!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
5993 026010 005037 005406  CLR E.B0 ;EXPECTED MSG B0
5994 026014 012737 001720 005410  MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5995 026022 012737 000001 005412  MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5996 026030 005037 005414  CLR E.A2 ;EXPECTED MSG A2
5997 026034 012737 000002 005416  MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5998 026042 012737 000003 005422  MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5999
6000 026050 004737 044274  JSR PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
6001 026054 000000  .WORD 0!0!0 ;& MSGS SPECIFIED HERE
6002 026056 104211  ERROR 211 ;MSG A0 ERROR AFTER RELEASE ISSUED
6003 026060 104212  ERROR 212 ;MSG B0 ERROR
6004 026062 104213  ERROR 213 ;MSG A1 ERROR
6005 026064 104214  ERROR 214 ;MSG B1 ERROR
6006 026066 012737 000000 005464  MOV #0,UNITB ;SETUP PORT A
6007 026074 112737 000101 056560  MOVB #'A,MSG19A
6008 026102 004737 043750  JSR PC,TSTATN
6009 026106 000401  BR TST21 ;GOTO NEXT TST
6010 026110 104115  ERROR 115 ;ATTN SET ON PORT A, AFTER RLS FROM PORT A
6011
6012
6013
6014
6015
6016
6017

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*****
*TEST 21 TEST RELEASE FROM REQUESTING PORT 'A' INHIBITS 'ATTN'
*
* THE PREVIOUS TEST IS REPEATED FOR PORT 'B'.
*
*****

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6018 026112 000004 TST21: SCOPE
6019 026114 012737 000001 001174 MOV #1,STIMES ;;DO 1 ITERATION
6020 026122 012706 001100 MOV #STACK,SP
6021 026126 013765 001222 000010 MOV #UNIT,RKCS2(R5) ;SETUP FOR PORT A
6022 026134 012737 000000 005464 MOV #0,UNITB
6023 026142 063765 005464 000010 ADD UNITB,RKCS2(R5)
6024 026150 112737 000101 056560 MOVB #'A,MSG19A
6025 026156 062765 000010 000010 ADD #RLS,RKCS2(R5) ;RELEASE PORT A
6026 026164 012737 000001 005314 MOV #SELDRV,HCS1
6027 026172 004737 043372 JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
6028 026176 104117 ERROR 117 ;NO RDY AFTER SEL DRV CMD
6029
6030 026200 013765 001222 000010 MOV #UNIT,RKCS2(R5) ;SETUP FOR PORT B
6031 026206 012737 000001 005464 MOV #1,UNITB
6032 026214 063765 005464 000010 ADD UNITB,RKCS2(R5)
6033 026222 112737 000102 056560 MOVB #'B,MSG19A
6034 026230 012737 000001 005314 MOV #SELDRV,HCS1
6035 026236 004737 043372 JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
6036 026242 104117 ERROR 117 ;NO RKY AFTER SEL DRV CMD
6037
6038 026244 032737 000040 005342 BIT #D,DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT B
6039 026252 001001 BNE 64$ ;BR IF YES
6040 026254 104071 ERROR 71 ;PORT B NOT AVAIL AFTER PORT A RLS
6041 026256
6042 64$:
6043 026256 012737 010340 005404 MOV #<D.DRA!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
6044 026264 005037 005406 CLR E.B0 ;EXPECTED MSG B0
6045 026270 012737 001720 005410 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
6046 026276 012737 000001 005412 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
6047 026304 005037 005414 CLR E.A2 ;EXPECTED MSG A2
6048 026310 012737 000002 005416 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
6049 026316 012737 000003 005422 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
6050
6051 026324 004737 044274 JSR PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
6052 026330 000000 .WORD 0!0!0 ;& MSGS SPECIFIED HERE
6053 026332 104211 ERROR 211 ;MSG A0 ERROR AFTER RELEASE ISSUED
6054 026334 104212 ERROR 212 ;MSH B0 ERROR
6055 026336 104213 ERROR 213 ;MSG A1 ERROR
6056 026340 104214 ERROR 214 ;MSG B1 ERROR
6057 026342 004737 045534 JSR PC,SUBCLR
6058 026346 104024 ERROR 24 ;CERR AFTER SCLR
6059
6060 026350 012737 000000 005464 MOV #0,UNITB ;SETUP PORT A
6061 026356 112737 000101 056560 MOVB #'A,MSG19A
6062 026364 004737 044172 JSR PC,DRAV ;SEE IF DRIVE AVAIL
6063 026370 000401 BR 1$
6064 026372 104103 ERROR 103 ;PORT A AVAIL BEFORE TMO OR RELEASE
6065
6066 026374 032737 100000 005314 1$: BIT #CERR,HCS1
6067 026402 001001 BNE 2$
6068 026404 104130 ERROR 130 ;CERR NOT SET BY NO DRA
6069
6070 026406 004737 043750 2$: JSR PC,TSTATN
6071 026412 000401 BR 3$
6072 026414 104115 ERROR 115 ;ATTN SET IN PORT A AFTER RLS FROM PORT B
6073

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M09

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 116
T21 TEST RELEASE FROM REQUESTING PORT 'A' INHIBITS 'ATTN'

SEQ 0116

6074	026416	012765	100000	000000	3\$:	MOV	#CCLR,RKCS1(R5)	
6075	026424	013765	001222	000010		MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT A
6076	026432	012737	000000	005464		MOV	#0,UNITB	
6077	026440	063765	005464	000010		ADD	UNITB,RKCS2(R5)	
6078	026446	112737	000101	056560		MOV	#'A,MSG19A	
6079	026454	062765	000010	000010		ADD	#RLS,RKCS2(R5)	;RELEASE PORT A
6080	026462	012737	000001	005314		MOV	#SELDRV,HCS1	
6081	026470	004737	043372			JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6082	026474	104117				ERROR	117	;NO RDY AFTER SEL DRV CMD
6083								
6084	026476	013765	001222	000010		MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT B
6085	026504	012737	000001	005464		MOV	#1,UNITB	
6086	026512	063765	005464	000010		ADD	UNITB,RKCS2(R5)	
6087	026520	112737	000102	056560		MOV	#'B,MSG19A	
6088	026526	012737	000001	005314		MOV	#SELDRV,HCS1	
6089	026534	004737	043372			JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6090	026540	104117				ERROR	117	;NO RKY AFTER SEL DRV CMD
6091								
6092	026542	032737	000040	005342		BIT	#D.DRA,HMR2	;SEE IF DRIVE AVAIL ON PORT B
6093	026550	001001				BNE	65\$;BR IF YES
6094	026552	104071				ERROR	71	;PORT B NOT AVAIL AFTER PORT A RLS
6095	026554				65\$:			
6096	026554	013765	001222	000010		MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT B
6097	026562	012737	000001	005464		MOV	#1,UNITB	
6098	026570	063765	005464	000010		ADD	UNITB,RKCS2(R5)	
6099	026576	112737	000102	056560		MOV	#'B,MSG19A	
6100	026604	062765	000010	000010		ADD	#RLS,RKCS2(R5)	;RELEASE PORT B
6101	026612	012737	000001	005314		MOV	#SELDRV,HCS1	
6102	026620	004737	043372			JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6103	026624	104117				ERROR	117	;NO RDY AFTER SEL DRV CMD
6104								
6105	026626	013765	001222	000010		MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT A
6106	026634	012737	000000	005464		MOV	#0,UNITB	
6107	026642	063765	005464	000010		ADD	UNITB,RKCS2(R5)	
6108	026650	112737	000101	056560		MOV	#'A,MSG19A	
6109	026656	012737	000001	005314		MOV	#SELDRV,HCS1	
6110	026664	004737	043372			JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6111	026670	104117				ERROR	117	;NO RKY AFTER SEL DRV CMD
6112								
6113	026672	032737	000040	005342		BIT	#D.DRA,HMR2	;SEE IF DRIVE AVAIL ON PORT A
6114	026700	001001				BNE	66\$;BR IF YES
6115	026702	104071				ERROR	71	;PORT A NOT AVAIL AFTER PORT B RLS
6116	026704				66\$:			
6117	026704	013737	005466	001366		MOV	TIMER,COUNT	
6118	026712	004737	047204			JSR	PC,TMO	;DO 1.5 SEC TIMEOUT ON PORT A
6119	026716	004737	043750			JSR	PC,TSTATN	
6120	026722	000401				BR	4\$	
6121	026724	104115				ERROR	115	;ATTN SET ON PORT A AFTER RLS FROM PORT A
6122								
6123	026726				4\$:			
6124								
6125	026726	012737	010340	005404		MOV	#<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0	;EXPECTED MSG A0
6126	026734	005037	005406			CLR	E.B0	;EXPECTED MSG B0
6127	026740	012737	001720	005410		MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1
6128	026746	012737	000001	005412		MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1
6129	026754	005037	005414			CLR	E.A2	;EXPECTED MSG A2

TEST RELEASE FROM REQUESTING PORT 'A' INHIBITS 'ATTN'

6130	026760	012737	000002	005416	MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2
6131	026766	012737	000003	005422	MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
6132							
6133	026774	004737	044274		JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1
6134	027000	000000			.WORD	0!0!0	& MSGS SPECIFIED HERE
6135	027002	104211			ERROR	211	;MSG A0 ERROR AFTER RELEASE ISSUED
6136	027004	104212			ERROR	212	;MSH B0 ERROR
6137	027006	104213			ERROR	213	;MSG A1 ERROR
6138	027010	104214			ERROR	214	;MSG B1 ERROR
6139	027012	012737	000001	005464	MOV	#1,UNITB	;SETUP PORT B
6140	027020	112737	000102	056560	MOVB	#'B,MSG19A	
6141	027026	004737	043750		JSR	PC,TSTATN	
6142	027032	000401			BR	TST22	;GOTO NEXT TST
6143	027034	104115			ERROR	115	;ATTN SET ON PORT B, AFTER RLS FROM PORT B

```

*****
*TEST 22      TEST RELEASE BY PORT 'B' WHEN SEIZED BY PORT 'A'
*
*      VERIFY THAT A RELEASE ISSUED BY ONE PORT IS NOT RECOGNIZED IF
*      THE DRIVE IS SEIZED BY THE OTHER PORT
*
*      A.  SEIZE THE DRIVE THRU PORT 'A'.
*
*      B.  ISSUE A RELEASE THRU PORT 'B' & VERIFY DRIVE STILL SEIZED
*      BY PORT 'A'.
*****

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6157	027036	000004			TST22:	SCOPE	
6158	027040	012737	000001	001174	MOV	#1,\$TIMES	;DO 1 ITERATION
6159	027046	012706	001100		MOV	#STACK,SP	
6160	027052	012737	000000	005464	MOV	#0,UNITB	;SETUP PORT A
6161	027060	112737	000101	056560	MOVB	#'A,MSG19A	
6162	027066	013737	005466	001366	MOV	TIMER,COUNT	
6163	027074	004737	047204		JSR	PC,TMO	;DO TIMEOUT
6164							
6165	027100	004737	045534		JSR	PC,SUBCLR	
6166	027104	104024			ERROR	24	;CERR AFTER SCLR
6167							
6168							
6169	027106	004737	044172		JSR	PC,DRAV	;SEE IF DRIVE AVAIL
6170	027112	104045			ERROR	45	;PORT A NOT AVAIL AFTER TMO
6171	027114	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT B
6172	027122	012737	000001	005464	MOV	#1,UNITB	
6173	027130	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
6174	027136	112737	000102	056560	MOVB	#'B,MSG19A	
6175	027144	062765	000010	000010	ADD	#RLS,RKCS2(R5)	;RELEASE PORT B
6176	027152	012737	000001	005314	MOV	#SELDRV,HCS1	
6177	027160	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6178	027164	104117			ERROR	117	;NO RDY AFTER SEL DRV CMD
6179							
6180	027166	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT A
6181	027174	012737	000000	005464	MOV	#0,UNITB	
6182	027202	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
6183	027210	112737	000101	056560	MOVB	#'A,MSG19A	
6184	027216	012737	000001	005314	MOV	#SELDRV,HCS1	
6185	027224	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY

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6186 027230 104117          ERROR 117          ;NO RKY AFTER SEL DRV CMD
6187
6188 027232 032737 000040 005342  BIT      #D.DRA,HMR2      ;SEE IF DRIVE AVAIL ON PORT A
6189 027240 001001          BNE      64$          ;BR IF YES
6190 027242 104071          ERROR 71            ;PORT A NOT AVAIL AFTER PORT B RLS
6191 027244
6192
6193 027244 012737 010340 005404  MOV      #<D.DRA!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
6194 027252 005037 005406          CLR      E.B0        ;EXPECTED MSG B0
6195 027256 012737 001720 005410  MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
6196 027264 012737 000001 005412  MOV      #1,E.B1      ;MSG ID FOR EXPECTED MSG B1
6197 027272 005037 005414          CLR      E.A2        ;EXPECTED MSG A2
6198 027276 012737 000002 005416  MOV      #2,E.B2      ;MSG ID FOR EXPECTED MSG B2
6199 027304 012737 000003 005422  MOV      #3,E.B3      ;MSG ID FOR EXPECTED MSG B3
6200
6201 027312 004737 044274          JSR      PC,CHKMSG    ;CHECK MSGS A0, B0, A1, B1
6202 027316 000000          .WORD   0!0!0        ;& MSGS SPECIFIED HERE
6203 027320 104211          ERROR 211          ;MSG A0 ERROR AFTER RELEASE ISSUED
6204 027322 104212          ERROR 212          ;MSH B0 ERROR
6205 027324 104213          ERROR 213          ;MSG A1 ERROR
6206 027326 104214          ERROR 214          ;MSG B1 ERROR
6207
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6209
6210
6211
6212
6213
6214 027330 000004          *ST23: SCOPE
6215 027332 012737 000001 001174  MOV      #1,$TIMES    ;;DO 1 ITERATION
6216 027340 012706 001100          MOV      #STACK,SP
6217 027344 012737 000001 005464  MOV      #1,UNITB     ;SETUP PORT B
6218 027352 112737 000102 056560  MOVB     #'B,MSG19A
6219 027360 013737 005466 001366  MOV      TIMER,COUNT
6220 027366 004737 047204          JSR      PC,TMO      ;DO TIMEOUT
6221
6222 027372 004737 045534          JSR      PC,SUBCLR   ;CERR AFTER SCLR
6223 027376 104024          ERROR 24
6224
6225
6226 027400 004737 044172          JSR      PC,DRAV     ;SEE IF DRIVE AVAIL
6227 027404 104045          ERROR 45           ;PORT B NOT AVAIL AFTER TMO
6228 027406 013765 001222 000010  MOV      $UNIT,RKCS2(R5) ;SETUP FOR PORT A
6229 027414 012737 000000 005464  MOV      #0,UNITB
6230 027422 063765 005464 000010  ADD      UNITB,RKCS2(R5)
6231 027430 112737 000101 056560  MOVB     #'A,MSG19A
6232 027436 062765 000010 000010  ADD      #RLS,RKCS2(R5) ;RELEASE PORT A
6233 027444 012737 000001 005314  MOV      #SELD, HCS1
6234 027452 004737 043372          JSR      PC,DOCMO    ;DO SELDRV (STATUS) CMD & GET CONTR RDY
6235 027456 104117          ERROR 117          ;NO RDY AFTER SEL DRV CMD
6236
6237 027460 013765 001222 000010  MOV      $UNIT,RKCS2(R5) ;SETUP FOR PORT B
6238 027466 012737 000001 005464  MOV      #1,UNITB
6239 027474 063765 005464 000010  ADD      UNITB,RKCS2(R5)
6240 027502 112737 000102 056560  MOVB     #'B,MSG19A
6241 027510 012737 000001 005314  MOV      #SELD, HCS1

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*****
*TEST 23          TEST RELEASE BY PORT 'A' WHEN SEIZED BY PORT 'B'
*
*          THE PREVIOUS TEST IS REPEATED FOR PORT 'B'.
*
*****

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C10

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 119
T23 TEST RELEASE BY PORT 'A' WHEN SEIZED BY PORT 'B'

SEQ 0119

6242	027516	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6243	027522	104117			ERROR	117	;NO RKY AFTER SEL DRV CMD
6244							
6245	027524	032737	000040	005342	BIT	#D.DRA,HMR2	;SEE IF DRIVE AVAIL ON PORT B
6246	027532	001001			BNE	64\$;BR IF YES
6247	027534	104071			ERROR	71	;PORT B NOT AVAIL AFTER PORT A RLS
6248	027536						
6249							
6250	027536	012737	010340	005404	MOV	#<D.DRA!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0	;EXPECTED MSG A0
6251	027544	005037	005406		CLR	E.B0	;EXPECTED MSG B0
6252	027550	012737	001720	005410	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1
6253	027556	012737	000001	005412	MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1
6254	027564	005037	005414		CLR	E.A2	;EXPECTED MSG A2
6255	027570	012737	000002	005416	MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2
6256	027576	012737	000003	005422	MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
6257							
6258	027604	004737	044274		JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1
6259	027610	000000			.WORD	0!0!0	; & MSGS SPECIFIED HERE
6260	027612	104211			ERROR	211	;MSG A0 ERROR AFTER RELEASE ISSUED
6261	027614	104212			ERROR	212	;MSH B0 ERROR
6262	027616	104213			ERROR	213	;MSG A1 ERROR
6263	027620	104214			ERROR	214	;MSG B1 ERROR
6264							

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6277 027622 000004
6278 027624 012737 000001 001174
6279 027632 012706 001100
6280 027636 013765 001222 000010
6281 027644 012737 000001 005464
6282 027652 063765 005464 000010
6283 027660 112737 000102 056560
6284 027666 062765 000010 000010
6285 027674 012737 000001 005314
6286 027702 004737 043372
6287 027706 104117
6288
6289 027710 013765 001222 000010
6290 027716 012737 000000 005464
6291 027724 063765 005464 000010
6292 027732 112737 000101 056560
6293 027740 012737 000001 005314
6294 027746 004737 043372
6295 027752 104117
6296
6297 027754 032737 000040 005342
6298 027762 001001
6299 027764 104071
6300 027766
6301 027766 004737 045534
6302 027772 104024
6303
6304 027774 012765 000012 000020
6305 030002 012737 000017 005314
6306 030010 004737 043372
6307 030014 104131
6308 030016 013765 001222 000010
6309 030024 012737 000000 005464
6310 030032 063765 005464 000010
6311 030040 112737 000101 056560
6312 030046 062765 000010 000010
6313 030054 012737 000001 005314
6314 030062 004737 043372
6315 030066 104117
6316
6317 030070 013765 001222 000010
6318 030076 012737 000001 005464
6319 030104 063765 005464 000010
6320 030112 112737 000102 056560

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*****
*TEST 24 TEST COMMAND FOLLOWED BY IMMEDIATE RELEASE: PORT A
*
* A. ISSUE A SEEK COMMAND TO CYL 10 FROM PORT 'A' & AN
* IMMEDIATE RELEASE TO PORT 'A'
*
* B. VERIFY THE DRIVE IS AVAILABLE TO PORT 'B' & PORT B SEES ATTN
*
* C. VERIFY PORT A DOES NOT RAISE ATTN WHEN SEEK COMPLETED.
*
*****
†ST24: SCOPE
MOV #1,STIMES ;DO 1 ITERATION
MOV #STACK,SP
MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
MOV #1,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'B,MSG19A
ADD #RLS,RKCS2(R5) ;RELEASE PORT B
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 11? ;NO RDY AFTER SEL DRV CMD

MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
MOV #0,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'A,MSG19A
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 11? ;NO RKY AFTER SEL DRV CMD

BIT #D,DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT A
BNE 64$ ;BR IF YES
ERROR 71 ;PORT A NOT AVAIL AFTER PORT B RLS

64$: JSR PC,SUBCLR
ERROR 24 ;CERR AFTER SCLR

MOV #10,RKDC(R5)
MOV #SEEK,HCS1
JSR PC,DOCMD ;DO SEEK CMD & GET CONTR READY
ERROR 13? ;NO RDY AFTER SEEK CMD

MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
MOV #0,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'A,MSG19A
ADD #RLS,RKCS2(R5) ;RELEASE PORT A
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 11? ;NO RDY AFTER SEL DRV CMD

MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
MOV #1,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'B,MSG19A

```

E10

CZR6GBD RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 121
T24 TEST COMMAND FOLLOWED BY IMMEDIATE RELEASE: PORT A

SEQ 0121

6321	030120	012737	000001	005314	MOV	#SELDV HCS1	
6322	030126	004737	043372		JSR	PC, DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6323	030132	104117			ERROR	117	;NO RKY AFTER SEL DRV CMD
6324							
6325	030134	032737	000040	005342	BIT	#D.DRA,HMR2	;SEE IF DRIVE AVAIL ON PORT B
6326	030142	001001			BNE	65\$;BR IF YES
6327	030144	104071			ERROR	71	;PORT B NOT AVAIL AFTER PORT A RLS
6328	030146						
6329	030146	013737	001412	005352	MOV	T50000,TEMP1	
6330	030154	004737	044106		JSR	PC,FAT12	
6331	030160	104152			ERROR	152	;NO ATTN ON PORT B AFTER SEEK & RLS FROM PORT A
6332							
6333							
6334	030162	012737	050340	005404	MOV	#<D.DRA!D.DSC!D.SPIN!D.DRDY!D.VV>,E.A0	;EXPECTED MSG A0
6335	030170	005037	005406		CLR	E.B0	;EXPECTED MSG B0
6336	030174	012737	001720	005410	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1
6337	030202	012737	000001	005412	MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1
6338	030210	005037	005414		CLR	E.A2	;EXPECTED MSG A2
6339	030214	012737	000002	005416	MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2
6340	030222	012737	000003	005422	MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
6341							
6342	030230	004737	044274		JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1
6343	030234	000000			.WORD	0!0!0	; & MSGS SPECIFIED HERE
6344	030236	104145			ERROR	145	;MSG A0 ERROR AFTER SEEK & RLS FROM PORT A
6345	030240	104146			ERROR	146	;MSG B0 ERROR
6346	030242	104147			ERROR	147	;MSG A1 ERROR
6347	030244	104150			ERROR	150	;MSG B1 ERROR
6348	030246	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT B
6349	030254	012737	000001	005464	MOV	#1,UNITB	
6350	030262	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
6351	030270	112737	000102	056560	MOVB	#'B,MSG19A	
6352	030276	062765	000010	000010	ADD	#RLS,RKCS2(R5)	;RELEASE PORT B
6353	030304	012737	000001	005314	MOV	#SELDV HCS1	
6354	030312	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6355	030316	104117			ERROR	117	;NO RDY AFTER SEL DRV CMD
6356							
6357	030320	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT A
6358	030326	012737	000000	005464	MOV	#0,UNITB	
6359	030334	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
6360	030342	112737	000101	056560	MOVB	#'A,MSG19A	
6361	030350	012737	000001	005314	MOV	#SELDV HCS1	
6362	030356	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6363	030362	104117			ERROR	117	;NO RKY AFTER SEL DRV CMD
6364							
6365	030364	032737	000040	005342	BIT	#D.DRA,HMR2	;SEE IF DRIVE AVAIL ON PORT A
6366	030372	001001			BNE	66\$;BR IF YES
6367	030374	104071			ERROR	71	;PORT A NOT AVAIL AFTER PORT B RLS
6368	030376						
6369	030376	004737	043750		JSR	PC,TSTATN	
6370	030402	000401			BR	1\$	
6371	030404	104160			ERROR	160	;ATTN ON PORT A AFTER SEEK & RLS FROM PORT A
6372							
6373	030406						
6374							
6375	030406	012737	010340	005404	MOV	#<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0	;EXPECTED MSG A0
6376	030414	005037	005406		CLR	E.B0	;EXPECTED MSG B0

F10

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 122
T24 TEST COMMAND FOLLOWED BY IMMEDIATE RELEASE: PORT A

SEQ 0122

6377	030420	012737	001720	005410	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1
6378	030426	012737	000001	005412	MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1
6379	030434	005037	005414		CLR	E.A2	;EXPECTED MSG A2
6380	030440	012737	000002	005416	MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2
6381	030446	012737	000003	005422	MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
6382							
6383	030454	004737	044274		JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1
6384	030460	000000			.WORD	0!0!0	; & MSGS SPECIFIED HERE
6385	030462	104145			ERROR	145	;MSG A0 ERROR AFTER SEEK & RLS FROM PORT A
6386	030464	104146			ERROR	146	;MSG B0 ERROR
6387	030466	104147			ERROR	147	;MSG A1 ERROR
6388	030470	104150			ERROR	150	;MSG B1 ERROR
6389							

```

*****
*TEST 25 TEST COMMAND FOLLOWED BY IMMEDIATE RELEASE: PORT B
*
* THE PREVIOUS TEST IS REPEATED FOR PORT 'B',
* BUT THE SEEK IS TO CYLINDER 0
*
*****

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6397	030472	000004			†ST25: SCOPE		
6398	030474	012737	000001	001174	MOV	#1,\$TIMES	;DO 1 ITERATION
6399	030502	012706	001100		MOV	#STACK,SP	
6400	030506	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT A
6401	030514	012737	000000	005464	MOV	#0,UNITB	
6402	030522	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
6403	030530	112737	000101	056560	MOVB	#'A,MSG19A	
6404	030536	062765	000010	000010	ADD	#RLS,RKCS2(R5)	;RELEASE PORT A
6405	030544	012737	000001	005314	MOV	#SELDRV,HCS1	
6406	030552	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6407	030556	104117			ERROR	117	;NO RDY AFTER SEL DRV CMD
6408							
6409	030560	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT B
6410	030566	012737	000001	005464	MOV	#1,UNITB	
6411	030574	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
6412	030602	112737	000102	056560	MOVB	#'B,MSG19A	
6413	030610	012737	000001	005314	MOV	#SELDRV,HCS1	
6414	030616	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6415	030622	104117			ERROR	117	;NO RKY AFTER SEL DRV CMD
6416							
6417	030624	032737	000040	005342	BIT	#D,DRA,HMR2	;SEE IF DRIVE AVAIL ON PORT B
6418	030632	001001			BNE	64\$;BR IF YES
6419	030634	104071			ERROR	71	;PORT B NOT AVAIL AFTER PORT A RLS
6420	030636						
6421	030636	004737	045534		JSR	PC,SUBCLR	
6422	030642	104024			ERROR	24	;CERR AFTER SCLR
6423							
6424	030644	012765	000000	000020	MOV	#0,RKDC(R5)	
6425	030652	012737	000017	005314	MOV	#SEEK,HCS1	
6426	030660	004737	043372		JSR	PC,DOCMD	;DO SEEK CMD & GET CONTR READY
6427	030664	104131			ERROR	131	;NO RDY AFTER SEEK CMD
6428	030666	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT B
6429	030674	012737	000001	005464	MOV	#1,UNITB	
6430	030702	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
6431	030710	112737	000102	056560	MOVB	#'B,MSG19A	
6432	030716	062765	000010	000010	ADD	#RLS,RKCS2(R5)	;RELEASE PORT B

G10

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 123
T25 TEST COMMAND FOLLOWED BY IMMEDIATE RELEASE: PORT B

SEQ 0123

6433	030724	012737	000001	005314	MOV	#SELDRV,HCS1	
6434	030732	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6435	030736	104117			ERROR	117	;NO RDY AFTER SEL DRV CMD
6436							
6437	030740	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT A
6438	030746	012737	000000	005464	MOV	#0,UNITB	
6439	030754	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
6440	030762	112737	000101	056560	MOVB	#'A,MSG19A	
6441	030770	012737	000001	005314	MOV	#SELDRV,HCS1	
6442	030776	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6443	031002	104117			ERROR	117	;NO RKY AFTER SEL DRV CMD
6444							
6445	031004	032737	000040	005342	BIT	#D.DRA,HMR2	;SEE IF DRIVE AVAIL ON PORT A
6446	031012	001001			BNE	65\$;BR IF YES
6447	031014	104071			ERROR	71	;PORT A NOT AVAIL AFTER PORT B RLS
6448	031016						
6449	031016	013737	001412	005352	MOV	T50000,TEMP1	
6450	031024	004737	044106		JSR	PC,FATT2	
6451	031030	104152			ERROR	152	;NO ATTN ON PORT A AFTER SEEK & RLS FROM PORT B
6452							
6453							
6454	031032	012737	050340	005404	MOV	#<D.DRA!D.DSC!D.SPIN!D.DRDY!D.VV>,E.A0	;EXPECTED MSG A0
6455	031040	005037	005406		CLR	E.B0	;EXPECTED MSG B0
6456	031044	012737	001720	005410	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRM!D.SSP>,E.A1	;EXPECTED A1
6457	031052	012737	000001	005412	MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1
6458	031060	005037	005414		CLR	E.A2	;EXPECTED MSG A2
6459	031064	012737	000002	005416	MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2
6460	031072	012737	000003	005422	MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
6461							
6462	031100	004737	044274		JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1
6463	031104	000000			WORD	0!0!0	& MSGS SPECIFIED HERE
6464	031106	104145			ERROR	145	;MSG A0 ERROR AFTER SEEK & RLS FROM PORT B
6465	031110	104146			ERROR	146	;MSG B0 ERROR
6466	031112	104147			ERROR	147	;MSG A1 ERROR
6467	031114	104150			ERROR	150	;MSG B1 ERROR
6468	031116	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT A
6469	031124	012737	000000	005464	MOV	#0,UNITB	
6470	031132	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
6471	031140	112737	000101	056560	MOVB	#'A,MSG19A	
6472	031146	062765	000010	000010	ADD	#RLS,RKCS2(R5)	;RELEASE PORT A
6473	031154	012737	000001	005314	MOV	#SELDRV,HCS1	
6474	031162	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6475	031166	104117			ERROR	117	;NO RDY AFTER SEL DRV CMD
6476							
6477	031170	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT B
6478	031176	012737	000001	005464	MOV	#1,UNITB	
6479	031204	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
6480	031212	112737	000102	056560	MOVB	#'B,MSG19A	
6481	031220	012737	000001	005314	MOV	#SELDRV,HCS1	
6482	031226	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6483	031232	104117			ERROR	117	;NO RKY AFTER SEL DRV CMD
6484							
6485	031234	032737	000040	005342	BIT	#D.DRA,HMR2	;SEE IF DRIVE AVAIL ON PORT B
6486	031242	001001			BNE	66\$;BR IF YES
6487	031244	104071			ERROR	71	;PORT B NOT AVAIL AFTER PORT A RLS
6488	031246						

66\$:

H10

CZR6GBD RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 124
T25 TEST COMMAND FOLLOWED BY IMMEDIATE RELEASE: PORT B

SEQ 0124

```
6489 031246 004737 043750 JSR PC,TSTATN
6490 031252 000401 BR 15
6491 031254 104160 ERROR 160 ;ATTN ON PORT B AFTER SEEK & RLS FROM PORT B
6492
6493 031256 15:
6494
6495 031256 012737 010340 005404 MOV #<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
6496 031264 005037 005406 CLR E.B0 ;EXPECTED MSG B0
6497 031270 012737 001720 005410 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
6498 031276 012737 000001 005412 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
6499 031304 005037 005414 CLR E.A2 ;EXPECTED MSG A2
6500 031310 012737 000002 005416 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
6501 031316 012737 000003 005422 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
6502
6503 031324 004737 044274 JSR PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
6504 031330 000000 .WORD 0!0!0 ;& MSGS SPECIFIED HERE
6505 031332 104145 ERROR 145 ;MSG A0 ERROR AFTER SEEK & RLS FROM PORT B
6506 031334 104146 ERROR 146 ;MSG B0 ERROR
6507 031336 104147 ERROR 147 ;MSG A1 ERROR
6508 031340 104150 ERROR 150 ;MSG B1 ERROR
6509
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6521
6522
6523
6524 031342 000004
6525 031344 012737 000001 001174
6526 031352 012706 001100
6527 031356 004737 045534
6528 031362 104024
6529 031364 013765 001222 000010
6530 031372 012737 000001 005464
6531 031400 063765 005464 000010
6532 031406 112737 000102 056560
6533 031414 062765 000010 000010
6534 031422 012737 000001 005314
6535 031430 004737 043372
6536 031434 104117
6537
6538 031436 013765 001222 000010
6539 031444 012737 000000 005464
6540 031452 063765 005464 000010
6541 031460 112737 000101 056560
6542 031466 012737 000001 005314
6543 031474 004737 043372
6544 031500 104117

*****
*TEST 26 TEST TIMEOUT RETRIGGER THRU PORT 'A'
*
* VERIFY THAT THE PORT TIMEOUT ONE-SHOT CAN BE RETRIGGERED.
*
* A. PORT 'A' SEIZES THE DRIVE
*
* B. THE PROGRAM WAITS 500MS & RE-SEIZES THE DRIVE THRU PORT 'A'
*
* C. PORT 'B' ATTEMPTS TO SEIZE THE DRIVE & THE PROGRAM
* VERIFIES THAT FULL TIMEOUT TOOK PLACE FROM STEP B ABOVE.
*
*****
†ST26: SCOPE
MOV #1,$TIMES ;;DO 1 ITERATION
MOV #STACK,SP
JSR PC,SUBCLR
ERROR 24 ;CERR AFTER SCLR
MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
MOV #1,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'B,MSG19A
ADD #RLS,RKCS2(R5) ;RELEASE PORT B
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 117 ;NO RDY AFTER SEL DRV CMD

MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
MOV #0,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'A,MSG19A
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 117 ;NO RKY AFTER SEL DRV CMD
```

```

6545
6546 031502 032737 000040 005342      BIT      #D.DRA,HMR2      ;SEE IF DRIVE AVAIL ON PORT A
6547 031510 001001                BNE      64$           ;BR IF YES
6548 031512 104071                ERROR    71           ;PORT A NOT AVAIL AFTER PORT B RLS
6549 031514                64$:
6550 031514 004737 045534      JSR      PC,SUBCLR
6551 031520 104024                ERROR    24           ;CERR AFTER SCLR
6552
6553 031522 012737 000036 001366      MOV      #30,COUNT
6554 031530 004737 047204      JSR      PC,TMO       ;DO 500MS TIMEOUT
6555 031534 004737 044172      JSR      PC,DRAV      ;RE-SEIZE DRIVE THRU PORT A
6556 031540 104045                ERROR    45           ;PORT A NOT AVAIL AFTER TIMECUT
6557
6558 031542 012737 010340 005404      MOV      #<D.DRA!D!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
6559 031550 005037 005406      CLR      E.B0         ;EXPECTED MSG B0
6560 031554 012737 001720 005410      MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
6561 031562 012737 000001 005412      MOV      #1,E.B1      ;MSG ID FOR EXPECTED MSG B1
6562 031570 005037 005414      CLR      E.A2         ;EXPECTED MSG A2
6563 031574 012737 000002 005416      MOV      #2,E.B2      ;MSG ID FOR EXPECTED MSG B2
6564 031602 012737 000003 005422      MOV      #3,E.B3      ;MSG ID FOR EXPECTED MSG B3
6565
6566 031610 004737 044274      JSR      PC,CHKMSG    ;CHECK MSGS A0, B0, A1, B1
6567 031614 000000                .WORD    0!0!0        ;& MSGS SPECIFIED HERE
6568 031616 104165                ERROR    165         ;MSG A0 ERROR AFTER TIMEOUT
6569 031620 104166                ERROR    166         ;MSG B0 ERROR
6570 031622 104167                ERROR    167         ;MSG A1 ERROR
6571 031624 104170                ERROR    170         ;MSG B1 ERROR
6572 031626 012737 177777 001366      MOV      #-1,COUNT
6573 031634 004737 047114      JSR      PC,CLKON     ;TURN ON CLOCK
6574 031640 012737 000001 005464      MOV      #1,UNITB    ;SETUP PORT B
6575 031646 004737 044172      JSR      PC,DRAV      ;SEE IF DRV AVAIL
6576 031652 000401                BR       1$           ;BR IF NO
6577 031654 104103                ERROR    103         ;PORT A AVAIL BEFORE TMO OR RLS
6578 031656 112737 000102 056560 1$:
6579 031664 013704 001222      MOV      #B,MSG19A
6580 031670 063704 005464      ADD     UNITB,R4
6581 031674 004737 044214      JSR      PC,FATT3
6582 031700 104110                ERROR    110         ;NO ATTN ON PORT B TO ALLOW SEIZE
6583
6584 031702 004737 047162      JSR      PC,CLKOF     ;TURN CLOCK OFF
6585 031706 005137 001366      COM      COUNT
6586 031712 023727 001366 000043      CMP      COUNT,#35.  ;GET ACTUAL COUNT OF TIMEOUT
6587 031720 002001                BGE     TST27        ;COMPARE COUNT AGAINST APPROX 1 SEC
6588 031722 104153                ERROR    153         ;GO TO NEXT TEST
6589
6590
6591
6592
6593
6594
6595
6596 031724 000004                *****
6597 031726 012737 000001 001174      TST27: SCOPE
6598 031734 012706 001100      MOV      #1,$TIMES   ;DO 1 ITERATION
6599 031740 004737 045534      MOV      #STACK,SP
6600 031744 104024                JSR      PC,SUBCLR
                                ERROR    24           ;CERR AFTER SCLR

```

6601	031746	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT A
6602	031754	012737	000000	005464	MOV	#0,UNITB	
6603	031762	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
6604	031770	112737	000101	056560	MOVB	#'A,MSG19A	
6605	031776	062765	000010	000010	ADD	#RLS,RKCS2(R5)	;RELEASE PORT A
6606	032004	012737	000001	005314	MOV	#SELDRV,HCS1	
6607	032012	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6608	032016	104117			ERROR	117	;NO RDY AFTER SEL DRV CMD
6609							
6610	032020	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT B
6611	032026	012737	000001	005464	MOV	#1,UNITB	
6612	032034	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
6613	032042	112737	000102	056560	MOVB	#'B,MSG19A	
6614	032050	012737	000001	005314	MOV	#SELDRV,HCS1	
6615	032056	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6616	032062	104117			ERROR	117	;NO RKY AFTER SEL DRV CMD
6617							
6618	032064	032737	000040	005342	BIT	#D.DRA,HMR2	;SEE IF DRIVE AVAIL ON PORT B
6619	032072	001001			BNE	64\$;BR IF YES
6620	032074	104071			ERROR	71	;PORT B NOT AVAIL AFTER PORT A RLS
6621	032076						
6622	032076	004737	045534		JSR	PC,SUBCLR	
6623	032102	104024			ERROR	24	;CERR AFTER SCLR
6624							
6625	032104	012737	000036	001366	MOV	#30,COUNT	
6626	032112	004737	047204		JSR	PC,TMO	;DO 500MS TIMEOUT
6627	032116	004737	044172		JSR	PC,DRAV	;RE-SEIZE DRIVE THRU PORT B
6628	032122	104045			ERROR	45	;PORT B NOT AVAIL AFTER TIMEOUT
6629							
6630	032124	012737	010340	005404	MOV	#<D.DRA!D!D.SPIN!D.DRDY!D.VV>,E.A0	;EXPECTED MSG A0
6631	032132	005037	005406		CLR	E.B0	;EXPECTED MSG B0
6632	032136	012737	001720	005410	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1
6633	032144	012737	000001	005412	MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1
6634	032152	005037	005414		CLR	E.A2	;EXPECTED MSG A2
6635	032156	012737	000002	005416	MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2
6636	032164	012737	000003	005422	MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
6637							
6638	032172	004737	044274		JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1
6639	032176	000000			.WORD	0!0!0	; & MSGS SPECIFIED HERE
6640	032200	104165			ERROR	165	;MSG A0 ERROR AFTER TIMEOUT
6641	032202	104166			ERROR	166	;MSG B0 ERROR
6642	032204	104167			ERROR	167	;MSG A1 ERROR
6643	032206	104170			ERROR	170	;MSG B1 ERROR
6644	032210	012737	177777	001366	MOV	#-1,COUNT	
6645	032216	004737	047114		JSR	PC,CLKON	;TURN ON CLOCK
6646	032222	012737	000000	005464	MOV	#0,UNITB	;SETUP PORT A
6647	032230	004737	044172		JSR	PC,DRAV	;SEE IF DRV AVAIL
6648	032234	000401			BR	1\$;BR IF NO
6649	032236	104103			ERROR	103	;PORT B AVAIL BEFORE TMO OR RLS
6650	032240	112737	000101	056560	MOVB	#'A,MSG19A	
6651	032246	013704	001222		MOV	\$UNIT,R4	
6652	032252	063704	005464		ADD	UNITB,R4	
6653	032256	004737	044214		JSR	PC,FATT3	
6654	032262	104110			ERROR	110	;NO ATTN ON PORT A TO ALLOW SEIZE
6655							
6656	032264	004737	047162		JSR	PC,CLKOF	;TURN CLOCK OFF

64\$:

1\$:

K10

CZR6GB0 RK611 DU PORT LGC TST MACY11 30A(1052) 03-JAN-78 08:56 PAGE 127
CZR6GB.P11 03-JAN-78 08:46 T27 TEST TIMEOUT RETRIGGER THRU PORT 'B'

SEQ 0127

6657	032270	005137	001366	
6658	032274	023727	001366	000043
6659	032302	002001		
6660	032304	104153		
6661				

COM	COUNT
CMP	COUNT, #35.
BGE	TST30
ERROR	153

;GET ACTUAL COUNT OF TIMEOUT
;COMPARE COUNT AGAINST APPROX 1 SEC
;GO TO NEXT TEST
;TIMEOUT DID NOT RE-TRIGGER

6662
6663
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6680
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6682
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6684
6685
6686
6687
6688
6689
6690
6691
6692
6693
6694
6695
6696
6697
6698
6699
6700
6701
6702
6703
6704
6705
6706
6707
6708
6709
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6711
6712
6713
6714
6715
6716
6717

032306 000004
032310 012737 000001 001174
032316 012706 001100
032322 004737 045534
032326 104024
032330 013765 001222 000010
032336 012737 000001 005464
032344 063765 005464 000010
032352 112737 000102 056560
032360 062765 000010 000010
032366 012737 000001 005314
032374 004737 043372
032400 104117
032402 013765 001222 000010
032410 012737 000000 005464
032416 063765 005464 000010
032424 112737 000101 056560
032432 012737 000001 005314
032440 004737 043372
032444 104117
032446 032737 000040 005342
032454 001001
032456 104071
032460
032460 004737 045534
032464 104024
032466 012737 000001 005464
032474 112737 000102 056560
032502 004737 044172
032506 000401
032510 104103
032512 012765 100000 000000
032520 013765 001222 000010
032526 012737 000000 005464
032534 063765 005464 000010
032542 112737 000101 056560
032550 062765 000010 000010
032556 012737 000001 005314
032564 004737 043372

```
*****
*TEST 30 TEST PORT 'A' TIMER INHIBIT
*
* A. PORT 'A' SEIZES THE DRIVE
* B. PORT 'B' ATTEMPTS TO SEIZE THE DRIVE
* C. PORT 'A' RELEASES THE DRIVE
* D. PORT 'A' ATTEMPTS TO GET THE DRIVE BACK.
*
* THE PROGRAM VERIFIES THAT PORT 'A' CANNOT ACCESS
* THE DRIVE FOR APPROX 1 SEC
*****
```

```
*****
†TST30: SCOPE
MOV #1,STIMES ;DO 1 ITERATION
MOV #STACK,SP
JSR PC,SUBCLR
ERROR 24 ;CERR AFTER SCLR
MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
MOV #1,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'B,MSG19A
ADD #RLS,RKCS2(R5) ;RELEASE PORT B
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 117 ;NO RDY AFTER SEL DRV CMD
MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
MOV #0,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'A,MSG19A
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 117 ;NO RKY AFTER SEL DRV CMD
BIT #D,DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT A
BNE 64$ ;BR IF YES
ERROR 71 ;PORT A NOT AVAIL AFTER PORT B RLS
64$: JSR PC,SUBCLR
ERROR 24 ;CERR AFTER SCLR
MOV #1,UNITB ;SETUP FOR PORT B
MOVB #'B,MSG19A
JSR PC,DRAV ;PORT B TRIES TO SEIZE THE DRIVE
BR 1$ ;BR IF NOT AVAIL
ERROR 103 ;PORT B AVAIL BEFORE TMO OR RELEASE
1$: MOV #CCLR,RKCS1(R5)
MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
MOV #0,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'A,MSG19A
ADD #RLS,RKCS2(R5) ;RELEASE PORT A
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
```

M10

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 129
T30 TEST PORT 'A' TIMER INHIBIT

SEQ 0129

```

6718 032570 104117          ERROR 117          ;NO RDY AFTER SEL DRV CMD
6719
6720 032572 013765 001222 000010  MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
6721 032600 012737 000001 005464  MOV #1,UNITB
6722 032606 063765 005464 000010  ADD UNITB,RKCS2(R5)
6723 032614 112737 000102 056560  MOVB #'B,MSG19A
6724 032622 012737 000001 005314  MOV #SELDV,HCS1
6725 032630 004737 043372  JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
6726 032634 104117          ERROR 117          ;NO RKY AFTER SEL DRV CMD
6727
6728 032636 032737 000040 005342  BIT #D,DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT B
6729 032644 001001          BNE 65$ ;BR IF YES
6730 032646 104071          ERROR 71          ;PORT B NOT AVAIL AFTER PORT A RLS
6731 032650          65$:
6732 032650 012737 000000 005464  MOV #0,UNITB ;SETUP FOR PORT A
6733 032656 112737 000101 056560  MOVB #'A,MSG19A
6734 032664 004737 044172  JSR PC,DRAV
6735 032670 000401          BR 2$
6736 032672 104103          ERROR 103         ;PORT A AVAIL BEFORE TMO OR RELEASE
6737
6738 032674 012765 100000 000000 2$: MOV #CCLR,RKCS1(R5)
6739 032702 012737 177777 001366  MOV #-1,COUNT
6740 032710 004737 047114  JSR PC,CLKON
6741 032714 013704 001222  MOV $UNIT,R4
6742 032720 063704 005464  ADD UNITB,R4
6743 032724 004737 044214  JSR PC,FATT3
6744 032730 104110          ERROR 110         ;NO ATTN ON PORT A TO ALLOW SEIZE
6745
6746 032732 004737 047162  JSR PC,CLKOF ;TURN CLOCK OFF
6747 032736 005137 001366  COM COUNT ;GET ACTUAL COUNT OF TIMEOUT
6748 032742 023727 001366 000043  CMP COUNT,#35. ;COMPARE AGAINST APPROX 1 SEC
6749 032750 002001          BGE TST31 ;GO TO NEXT TST
6750 032752 104153          ERROR 153         ;TIMEOUT DID NOT RE-TRIGGER
6751
6752          ;*****
6753          ;*TEST 31 TEST PORT 'B' TIMER INHIBIT
6754          ;*
6755          ;* THE PREVIOUS TEST IS REPEATED FOR PORT 'B'.
6756          ;*
6757          ;*****
6758 032754 000004          †TST31: SCOPE
6759 032756 012737 000001 001174  MOV #1,$TIMES ;DO 1 ITERATION
6760 032764 012706 001100  MOV #STACK,SP
6761 032770 004737 045534  JSR PC,SUBCLR
6762 032774 104024          ERROR 24          ;CERR AFTER SCLR
6763 032776 013765 001222 000010  MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
6764 033004 012737 000000 005464  MOV #0,UNITB
6765 033012 063765 005464 000010  ADD UNITB,RKCS2(R5)
6766 033020 112737 000101 056560  MOVB #'A,MSG19A
6767 033026 062765 000010 000010  ADD #RLS,RKCS2(R5) ;RELEASE PORT A
6768 033034 012737 000001 005314  MOV #SELDV,HCS1
6769 033042 004737 043372  JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
6770 033046 104117          ERROR 117          ;NO RDY AFTER SEL DRV CMD
6771
6772 033050 013765 001222 000010  MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
6773 033056 012737 000001 005464  MOV #1,UNITB

```

N10

CZR6GB0 RK611 DU PORT LGC TST MACY11 30A(1052) 03-JAN-78 08:56 PAGE 130
 CZR6GB.P11 03-JAN-78 08:46 T31 TEST PORT 'B' TIMER INHIBIT

SEQ 0130

6774	033064	063765	005464	000010		ADD	UNITB,RKCS2(R5)	
6775	033072	112737	000102	056560		MOVB	#'B,MSG19A	
6776	033100	012737	000001	005314		MOV	#SELD, HCS1	
6777	033106	004737	043372			JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6778	033112	104117				ERROR	117	;NO RKY AFTER SEL DRV CMD
6779								
6780	033114	032737	000040	005342		BIT	#D.DRA,HMR2	;SEE IF DRIVE AVAIL ON PORT B
6781	033122	001001				BNE	64\$;BR IF YES
6782	033124	104071				ERROR	71	;PORT B NOT AVAIL AFTER PORT A RLS
6783	033126				64\$:			
6784	033126	004737	045534			JSR	PC,SUBCLR	
6785	033132	104024				ERROR	24	;CERR AFTER SCLR
6786								
6787	033134	012737	000000	005464		MOV	#0,UNITB	;SETUP FOR PORT A
6788	033142	112737	000101	056560		MOVB	#'A,MSG19A	
6789	033150	004737	044172			JSR	PC,DRAV	;PORT A TRIES TO SEIZE THE DRIVE
6790	033154	000401				BR	1\$;BR IF NOT AVAIL
6791	033156	104103				ERROR	103	;PORT A AVAIL BEFORE TMO OR RELEASE
6792								
6793	033160	012765	100000	000000	1\$:	MOV	#CCLR,RKCS1(R5)	
6794	033166	013765	001222	000010		MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT B
6795	033174	012737	000001	005464		MOV	#1,UNITB	
6796	033202	063765	005464	000010		ADD	UNITB,RKCS2(R5)	
6797	033210	112737	000102	056560		MOVB	#'B,MSG19A	
6798	033216	062765	000010	000010		ADD	#RLS,RKCS2(R5)	;RELEASE PORT B
6799	033224	012737	000001	005314		MOV	#SELD, HCS1	
6800	033232	004737	043372			JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6801	033236	104117				ERROR	117	;NO RDY AFTER SEL DRV CMD
6802								
6803	033240	013765	001222	000010		MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT A
6804	033246	012737	000000	005464		MOV	#0,UNITB	
6805	033254	063765	005464	000010		ADD	UNITB,RKCS2(R5)	
6806	033262	112737	000101	056560		MOVB	#'A,MSG19A	
6807	033270	012737	000001	005314		MOV	#SELD, HCS1	
6808	033276	004737	043372			JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
6809	033302	104117				ERROR	117	;NO RKY AFTER SEL DRV CMD
6810								
6811	033304	032737	000040	005342		BIT	#D.DRA,HMR2	;SEE IF DRIVE AVAIL ON PORT A
6812	033312	001001				BNE	65\$;BR IF YES
6813	033314	104071				ERROR	71	;PORT A NOT AVAIL AFTER PORT B RLS
6814	033316				65\$:			
6815	033316	012737	000001	005464		MOV	#1,UNITB	;SETUP FOR PORT B
6816	033324	112737	000102	056560		MOVB	#'B,MSG19A	
6817	033332	004737	044172			JSR	PC,DRAV	
6818	033336	000401				BR	2\$	
6819	033340	104103				ERROR	103	;PORT B AVAIL BEFORE TMO OR RELEASE
6820								
6821	033342	012765	100000	000000	2\$:	MOV	#CCLR,RKCS1(R5)	
6822	033350	012737	177777	001366		MOV	#-1,COUNT	
6823	033356	004737	047114			JSR	PC,CLKON	
6824	033362	013704	001222			MOV	\$UNIT,R4	
6825	033366	063704	005464			ADD	UNITB,R4	
6826	033372	004737	044214			JSR	PC,FATT3	
6827	033376	104110				ERROR	110	;NO ATTN ON PORT B TO ALLOW SEIZE
6828								
6829	033400	004737	047162			JSR	PC,CLKOF	;TURN CLOCK OFF

B11

CZR6GB0 RK611 DU PORT LGC TST MACY11 30A(1052) 03-JAN-78 08:56 PAGE 131
CZR6GB.P11 03-JAN-78 08:46 T31 TEST PORT 'B' TIMER INHIBIT

SEQ 0131

6830	033404	005137	001366	
6831	033410	023727	001366	000043
6832	033416	002001		
6833	033420	104153		
6834				

COM	COUNT
CMP	COUNT, #35.
BGE	TST32
ERROR	153

;GET ACTUAL COUNT OF TIMEOUT
;COMPARE AGAINST APPROX 1 SEC
;GO TO NEXT TST
;TIMEOUT DID NOT RE-TRIGGER

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6856 033422 000004
6857 033424 012737 000001 001174
6858 033432 004737 045534
6859 033436 104024
6860 033440 012706 001100
6861 033444 013765 001222 000010
6862 033452 012737 000001 005464
6863 033460 063765 005464 000010
6864 033466 112737 000102 056560
6865 033474 062765 000010 000010
6866 033502 012737 000001 005314
6867 033510 004737 043372
6868 033514 104117
6869
6870 033516 013765 001222 000010
6871 033524 012737 000000 005464
6872 033532 063765 005464 000010
6873 033540 112737 000101 056560
6874 033546 012737 000001 005314
6875 033554 004737 043372
6876 033560 104117
6877
6878 033562 032737 000040 005342
6879 033570 001001
6880 033572 104071
6881 033574
6882 033574 004737 045534
6883 033600 104024
6884
6885 033602 005237 005276
6886 033606 012737 034046 001176
6887 033614 012737 000007 005314
6888 033622 053737 001170 005314
6889 033630 013765 005314 000000
6890 033636 013737 001400 005352

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*****
:TEST 32 TEST UNLOAD COMMAND TIMER INHIBIT THRU PORT 'A'
:
: VERIFY THAT THE UNLOAD COMMAND THRU A PORT, SEIZES THAT
: PORT FOR AS LONG HAS HEADS ARE UNLOADED & RELEASE IS NOT
:
: ISSUED.
:
: A. ISSUE AN UNLOAD COMMAND THRU PORT 'A'.
: VERIFY DRIVE UNLOADS & ATTENTION IS SET.
:
: B. DELAY FOR 5 SECONDS & VERIFY DRIVE NOT AVAILABLE
: TO PORT 'B' TO INSURE TIMERS INHIBITED
:
:
: C. ISSUE A RELEASE FROM PORT 'A'. VERIFY DRIVE BECOMES
: AVAILABLE TO PORT 'B'
:
: D. LOAD HEADS FROM PORT 'B' & VERIFY 'ATTN-B' AT COMPLETION
:
*****
TST32: SCOPE
MOV #1,$TIMES ;DO 1 ITERATION
JSR PC,SUBCLR ;SUB SYSTEM CLEAR 29-SEP-77
ERROR 24
MOV #STACK,SP
MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
MOV #1,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'B,MSG19A
ADD #RLS,RKCS2(R5) ;RELEASE PORT B
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 117 ;NO RDY AFTER SEL DRV CMD
MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
MOV #0,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'A,MSG19A
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 117 ;NO RKY AFTER SEL DRV CMD
BIT #0,DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT A
BNE 64$ ;BR IF YES
ERROR 71 ;PORT A NOT AVAIL AFTER PORT B RLS
64$: JSR PC,SUBCLR ;CERR AFTER SCLR
ERROR 24
INC UNLD ;USED FOR VALID HALT
MOV #2,$$ESCAPE
MOV #UNLOAD,HCS1 ;UNLOAD CMD
BIS $TMP4,HCS1
MOV HCS1,RKCS1(R5)
MOV T10,TEMP1 ;SETUP TIMEOUT

```

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6891 033644 004737 044106 JSR PC,FATT2 ;FIND ATTN
6892 033650 104073 ERROR 73 ;NO ATTN AFTER UNLD CMD
6893
6894 033652 012737 000454 001366 MOV #300,COUNT
6895 033660 004737 047204 JSR PC,TMO ;DO 5 SEC DELAY
6896 033664 012737 000001 005464 MOV #1,UNITB ;SETUP FOR PORT B
6897 033672 112737 000102 056560 MOVB #'B,MSG19A
6898 033700 004737 044172 JSR PC,DRAV
6899 033704 000401 BR 1$
6900 033706 104155 ERROR 155 ;PORT B AVAIL BEFORE RLS WHEN UNLOADED
6901 ;UNLOAD DID NOT INHIBIT TIMERS
6902
6903 033710 012765 100000 000000 1$: MOV #CCLR,RKCS1(R5)
6904 033716 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
6905 033724 012737 000000 005464 MOV #0,UNITB
6906 033732 063765 005464 000010 ADD UNITB,RKCS2(R5)
6907 033740 112737 000101 056560 MOVB #'A,MSG19A
6908 033746 062765 000010 000010 ADD #RLS,RKCS2(R5) ;RELEASE PORT A
6909 033754 012737 000001 005314 MOV #SELDV,HCS1
6910 033762 004737 043372 JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
6911 033766 104117 ERROR 117 ;NO RDY AFTER SEL DRV CMD
6912
6913 033770 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
6914 033776 012737 000001 005464 MOV #1,UNITB
6915 034004 063765 005464 000010 ADD UNITB,RKCS2(R5)
6916 034012 112737 000102 056560 MOVB #'B,MSG19A
6917 034020 012737 000001 005314 MOV #SELDV,HCS1
6918 034026 004737 043372 JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
6919 034032 104117 ERROR 117 ;NO RKY AFTER SEL DRV CMD
6920
6921 034034 032737 000040 005342 BIT #D,DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT B
6922 034042 001001 BNE 65$ ;BR IF YES
6923 034044 104071 ERROR 71 ;PORT B NOT AVAIL AFTER PORT A RLS
6924
6925 034046 005037 001176 65$: CLR $ESCAPE
6926 2$:
6927 034052 004737 045534 JSR PC,SUBCLR
6928 034056 104024 ERROR 24 ;CERR AFTER SCLR
6929
6930 034060 012737 000011 005314 MOV #SRTSPL,HCS1
6931 034066 004737 043372 JSR PC,DOCMD ;DO START SPINDLE CMD & GET CONTR RDY
6932 034072 104121 ERROR 121 ;RDY NOT SET AFTER ST SPIN CMD.
6933
6934 034074 013737 001406 005354 MOV T100,TEMP2 ;SETUP TIMEOUT
6935 034102 004737 044006 JSR PC,FATT1 ;FIND ATTN
6936 034106 104074 ERROR 74 ;NO ATTN AFTER ST SPIN CMD.
6937
6938 034110 005037 005276 CLR UNLD
6939
6940 034114 005037 005276 CLR UNLD

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6941
6942 *****
6943 *TEST 33 TEST UNLOAD COMMAND TIMER INHIBIT THRU PORT 'B'
6944 *
6945 * THE PREVIOUS TEST IS REPEATED FOR PORT 'B'.
6946 *

```

```

6947
6948 034120 000004
6949 034122 012737 000001 001174
6950 034130 004737 045534
6951 034134 104024
6952 034136 012706 001100
6953 034142 013765 001222 000010
6954 034150 012737 000000 005464
6955 034156 063765 005464 000010
6956 034164 112737 000101 056560
6957 034172 062765 000010 000010
6958 034200 012737 000001 005314
6959 034206 004737 043372
6960 034212 104117
6961
6962 034214 013765 001222 000010
6963 034222 012737 000001 005464
6964 034230 063765 005464 000010
6965 034236 112737 000102 056560
6966 034244 012737 000001 005314
6967 034252 004737 043372
6968 034256 104117
6969
6970 034260 032737 000040 005342
6971 034266 001001
6972 034270 104071
6973
6974 034272 004737 045534
6975 034276 104024
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6977 034300 005237 005276
6978 034304 012737 034544 001176
6979 034312 012737 000007 005314
6980 034320 053737 001170 005314
6981 034326 013765 005314 000000
6982 034334 013737 001400 005352
6983 034342 004737 044106
6984 034346 104073
6985
6986 034350 012737 000454 001366
6987 034356 004737 047204
6988 034362 012737 000000 005464
6989 034370 112737 000101 056560
6990 034376 004737 044172
6991 034402 000401
6992 034404 104155
6993
6994
6995 034406 012765 100000 000000
6996 034414 013765 001222 000010
6997 034422 012737 000001 005464
6998 034430 063765 005464 000010
6999 034436 112737 000102 056560
7000 034444 062765 000010 000010
7001 034452 012737 000001 005314
7002 034460 004737 043372

```

†ST33: SCOPE
MOV #1,STIMES ;DO 1 ITERATION
JSR PC,SUBCLR ;SUB SYSTEM CLEAR 29-SEP-77
ERROR 24
MOV #STACK,SP
MOV \$UNIT,RKCS2(R5) ;SETUP FOR PORT A
MOV #0,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'A,MSG19A
ADD #RLS,RKCS2(R5) ;RELEASE PORT A
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 117 ;NO RDY AFTER SEL DRV CMD
MOV \$UNIT,RKCS2(R5) ;SETUP FOR PORT B
MOV #1,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'B,MSG19A
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 117 ;NO RKY AFTER SEL DRV CMD
BIT #D,DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT B
BNE 64\$;BR IF YES
ERROR 71 ;PORT B NOT AVAIL AFTER PORT A RLS
64\$: JSR PC,SUBCLR
ERROR 24 ;CERR AFTER SCLR
INC UNLD ;USED FOR VALID HALT
MOV #2\$,SESCAPE
MOV #UNLOAD,HCS1 ;UNLOAD CMD
BIS \$TMP4,HCS1
MOV HCS1,RKCS1(R5)
MOV T10,TEMP1 ;SETUP TIMEOUT
JSR PC,FATT2 ;FIND ATTN
ERROR 73 ;NO ATTN AFTER UNLD CMD
MOV #300,COUNT
JSR PC,TMO ;DO 5 SEC DELAY
MOV #0,UNITB ;SETUP FOR PORT A
MOVB #'A,MSG19A
JSR PC,DRAV
BR 1\$
ERROR 155 ;PORT A AVAIL BEFORE RLS WHEN UNLOADED
;UNLOAD DID NOT INHIBIT TIMERS
1\$: MOV #CCLR,RKCS1(R5)
MOV \$UNIT,RKCS2(R5) ;SETUP FOR PORT B
MOV #1,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'B,MSG19A
ADD #RLS,RKCS2(R5) ;RELEASE PORT B
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY


```

7003 034464 104117          ERROR 117          ;NO RDY AFTER SEL DRV CMD
7004
7005 034466 013765 001222 000010  MOV      $UNIT,RKCS2(R5) ;SETUP FOR PORT A
7006 034474 012737 000000 005464  MOV      #0,UNITB
7007 034502 063765 005464 000010  ADD      UNITB,RKCS2(R5)
7008 034510 112737 000101 056560  MOV      #'A,MSG19A
7009 034516 012737 000001 005314  MOV      #SELDRV,HCS1
7010 034524 004737 043372  JSR      PC,DOCMD      ;DO SELDRV (STATUS) CMD & GET CONTR RDY
7011 034530 104117          ERROR 117          ;NO RKY AFTER SEL DRV CMD
7012
7013 034532 032737 000040 005342  BIT      #D.DRA,HMR2    ;SEE IF DRIVE AVAIL ON PORT A
7014 034540 001001          BNE      65$         ;BR IF YES
7015 034542 104071          ERROR 71          ;PORT A NOT AVAIL AFTER PORT B RLS
7016 034544
7017 034544 005037 001176 65$: CLR      $ESCAPE
7018
7019 034550 004737 045534 2$: JSR      PC,SUBCLR
7020 034554 104024          ERROR 24          ;CERR AFTER SCLR
7021
7022 034556 012737 000011 005314  MOV      #SRTSPL,HCS1
7023 034564 004737 043372  JSR      PC,DOCMD      ;DO START SPINDLE CMD & GET CONTR RDY
7024 034570 104121          ERROR 121         ;RDY NOT SET AFTER ST SPIN CMD.
7025
7026 034572 013737 001406 005354  MOV      T100,TEMP2    ;SETUP TIMEOUT
7027 034600 004737 044006  JSR      PC,FATT1      ;FIND ATTN
7028 034604 104074          ERROR 74          ;NO ATTN AFTER ST SPIN CMD.
7029
7030 034606 005037 005276  CLR      UNLD
7031
7032 034612 005037 005276  CLR      UNLD
7033
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```

*****
*TEST 34      TEST RECAL COMMAND TIMER INHIBIT THRU PORT 'A'
*
*   VERIFY THAT THE RECAL COMMAND THRU A PORT SEIZES THAT PORT
*   FOR AS LONG AS THE RECAL IS IN PROGRESS & RELEASE IS NOT ISSUED.
*
*   A.   ISSUE A RECAL COMMAND FROM CYL 410 THRU PORT 'A'
*
*   B.   VERIFY PORT 'B' CANNOT SEIZE THE DRIVE UNTIL PORT 'A'
*   RECEIVES ATTN AND TIMES OUT.THIS INSURES THAT
*   THE TIMERS ARE INHIBITED.
*
*****

```

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7048 034616 000004          TST34: SCOPE
7049 034620 012737 000001 001174  MOV      #1,$TIMES      ;;DO 1 ITERATION
7050 034626 004737 045534  JSR      PC,SUBCLR      ;SUB SYSTEM CLEAR 29-SEP-77
7051 034632 104024          ERROR 24          ;29-SEP-77
7052 034634 012706 001100  MOV      #STACK,SP
7053 034640 013765 001222 000010  MOV      $UNIT,RKCS2(R5) ;SETUP FOR PORT B
7054 034646 012737 000001 005464  MOV      #1,UNITB
7055 034654 063765 005464 000010  ADD      UNITB,RKCS2(R5)
7056 034662 112737 000102 056560  MOV      #'B,MSG19A
7057 034670 062765 000010 000010  ADD      #RLS,RKCS2(R5) ;RELEASE PORT B
7058 034676 012737 000001 005314  MOV      #SELDRV,HCS1

```

G11

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 136
T34 TEST RECAL COMMAND TIMER INHIBIT THRU PORT 'A'

SEQ 0136

7059	034704	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
7060	034710	104117			ERROR	117	;NO RDY AFTER SEL DRV CMD
7061							
7062	034712	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT A
7063	034720	012737	000000	005464	MOV	#D,UNITB	
7064	034726	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
7065	034734	112737	000101	056560	MOVB	#'A,MSG19A	
7066	034742	012737	000001	005314	MOV	#SELDLV,HCS1	
7067	034750	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
7068	034754	104117			ERROR	117	;NO RKY AFTER SEL DRV CMD
7069							
7070	034756	032737	000040	005342	BIT	#D,DRA,HMR2	;SEE IF DRIVE AVAIL ON PORT A
7071	034764	001001			BNE	64\$;BR IF YES
7072	034766	104071			ERROR	71	;PORT A NOT AVAIL AFTER PORT B RLS
7073	034770						
7074	034770	004737	045534		JSR	PC,SUBCLR	
7075	034774	104024			ERROR	24	;CERR AFTER SCLR
7076							
7077	034776	012737	035464	001176	MOV	#5\$, \$ESCAPE	
7078	035004	013765	012770	000020	MOV	LC,RKDC(R5)	;SEEK TO LAST CYL
7079							
7080	035012	012737	000017	005314	MOV	#SEEK,HCS1	
7081	035020	004737	043372		JSR	PC,DOCMD	;DO SEEK CMD & GET CONTR READY
7082	035024	104131			ERROR	131	;NO RDY AFTER SEEK CMD
7083							
7084	035026	013737	001412	005352	MOV	T50000,TEMP1	;SETUP TIMEOUT
7085	035034	004737	044106		JSR	PC,FATT2	;FIND ATTN
7086	035040	104132			ERROR	132	;NO ATTN AFTER SEEK CMD
7087							
7088	035042	032737	100000	005314	BIT	#CERR,HCS1	
7089	035050	001401			BEQ	65\$	
7090	035052	104210			ERROR	210	;CERR AFTER SEEK CMD
7091							
7092	035054						
7093							
7094	035054	012737	050340	005404	MOV	#<D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0	;EXPECTED MSG A0
7095	035062	005037	005406		CLR	E.B0	;EXPECTED MSG B0
7096	035066	012737	001720	005410	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1
7097	035074	012737	000001	005412	MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1
7098	035102	005037	005414		CLR	E.A2	;EXPECTED MSG A2
7099	035106	012737	000002	005416	MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2
7100	035114	012737	000003	005422	MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
7101							
7102	035122	004737	044274		JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1
7103	035126	000000			.WORD	0!0!0	; & MSGS SPECIFIED HERE
7104	035130	104161			ERROR	161	;MSG A0 ERROR AFTER SEEK CMD
7105	035132	104162			ERROR	162	;MSG B0 ERROR
7106	035134	104163			ERROR	163	;MSG A1 ERROR
7107	035136	104164			ERROR	164	;MSG B1 ERROR
7108							
7109							
7110	035140	012765	100000	000000	MOV	#CLR,RKCS1(R5)	
7111	035146	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;DRIVE#
7112	035154	063765	005464	000010	ADD	UNITB,RKCS2(R5)	;ADD 1 IF ON PORT B
7113	035162	012737	000005	005314	MOV	#CLEAR,HCS1	
7114	035170	004737	043372		JSR	PC,DOCMD	;DO DRIVE CLEAR CMD & GET CONTR RDY

64\$:

65\$:

H11

CZR6GBD RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 137
T34 TEST RECAL COMMAND TIMER INHIBIT THRU PORT 'A'

SEQ 0137

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7115 035174 104151          ERROR 151          ;NO RDY AFTER DRIVE CLEAR CMD
7116 035176 004737 043750   JSR    PC,TSTATN   ;TEST FOR ATTN
7117 035202 000401          BR     66$         ;
7118 035204 104154          ERROR 154          ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
7119 035206          66$:
7120
7121 035206 012737 010340 005404   MOV    #<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
7122 035214 005037 005406          CLR    E.B0        ;EXPECTED MSG B0
7123 035220 012737 001720 005410   MOV    #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
7124 035226 012737 000001 005412   MOV    #1,E.B1     ;MSG ID FOR EXPECTED MSG B1
7125 035234 005037 005414          CLR    E.A2        ;EXPECTED MSG A2
7126 035240 012737 000002 005416   MOV    #2,E.B2     ;MSG ID FOR EXPECTED MSG B2
7127 035246 012737 000003 005422   MOV    #3,E.B3     ;MSG ID FOR EXPECTED MSG B3
7128
7129 035254 004737 044274          JSR    PC,CHKMSG   ;CHECK MSGS A0, B0, A1, B1
7130 035260 000003          WORD  T,A2!T.B2!0 ;& MSGS SPECIFIED HERE
7131 035262 104033          ERROR 33          ;MSG A0 ERROR AFTER DRV CLEAR CMD
7132 035264 104034          ERROR 34          ;MSG B0 ERROR
7133 035266 104035          ERROR 35          ;MSG A1 ERROR
7134 035270 104036          ERROR 36          ;MSG B1 ERROR
7135
7136 035272 012737 000013 005314   MOV    #RECAL,HCS1 ;RECAL COMMAND
7137 035300 053737 001170 005314   BIS    $TMP4,HCS1
7138 035306 013765 005314 000000   MOV    HCS1,RKCS1(R5)
7139 035314 013704 001222          MOV    $UNIT,R4
7140 035320 063704 005464          ADD    UNITB,R4
7141 035324 136465 005304 000017 1$:   BITB  ATTN(R4),RKASOF+1(R5) ;SEE IF ATTN SET
7142 035332 001027          BNE   3$          ;BR IF YES
7143
7144 035334 012737 000001 005464   MOV    #1,UNITB    ;SETUP FOR PORT B
7145 035342 112737 000102 056560   MOVB  #'B,MSG19A
7146 035350 004737 044172          JSR    PC,DRAV     ;SEE IF DRV AVAIL
7147 035354 000401          BR     2$         ;RETURN HERE IF NO
7148 035356 104177          ERROR 177         ;PORT B AVAIL
7149          ;RECAL DID NOT INHIBIT TIMERS
7150 035360 012765 100000 000000 2$:   MOV    #CLR,RKCS1(R5)
7151 035366 012737 000000 005464   MOV    #0,UNITB
7152 035374 112737 000101 056560   MOVB  #'A,MSG19A
7153 035402 004737 044172          JSR    PC,DRAV     ;SEE IF DRV AVAIL
7154 035406 104203          ERROR 203        ;PORT A NOT REMAIN AVAIL DURING RECAL
7155 035410 000745          BR     1$
7156
7157 035412 004737 045146          3$:   JSR    PC,GSTAT
7158 035416 032737 020000 005342   BIT    #D.PIP,HMR2 ;SEE IF ANY MOTION
7159 035424 001401          BEQ   4$          ;BR IF NO
7160 035426 104072          ERROR 72         ;PIP SET AFTER ATTN RECB FROM RECAL
7161 035430 012737 000170 001366 4$:   MOV    #120,COUNT
7162 035436 004737 047204          JSR    PC,TMO
7163 035442 012737 000001 005464   MOV    #1,UNITB   ;DO 2 SEC DLY
7164 035450 112737 000102 056560   MOVB  #'B,MSG19A  ;SETUP FOR PORT B
7165 035456 004737 044172          JSR    PC,DRAV     ;SEE IF DRV NOW AVAIL
7166 035462 104045          ERROR 45         ;PORT B NOT AVAIL AFTER TMO
7167
7168 035464 005037 001176          5$:   CLR    $ESCAPE
7169
7170          ;:*****

```

```

7171 ;*TEST 35 TEST RECAL COMMAND TIMER INHIBIT THRU PORT 'B'
7172 ;*
7173 ;* THE PREVIOUS TEST IS REPEATED FOR PORT 'B'.
7174 ;*
7175 ;*****
7176 035470 000004 TST35: SCOPE
7177 035472 012737 000001 001174 MOV #1,$TIMES ;DO 1 ITERATION
7178 035500 004737 045534 JSR PC,SUBCLR ;SUBSYSTEM CLEAR 29-SEP-77
7179 035504 104024 ERROR 24 ;29-SEP-77
7180 035506 012706 001100 MOV #STACK,SP
7181 035512 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
7182 035520 012737 000000 005464 MOV #0,UNITB
7183 035526 063765 005464 000010 ADD UNITB,RKCS2(R5)
7184 035534 112737 000101 056560 MOVB #'A,MSG19A
7185 035542 062765 000010 000010 ADD #RLS,RKCS2(R5) ;RELEASE PORT A
7186 035550 012737 000001 005314 MOV #SELD, HCS1
7187 035556 004737 043372 JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
7188 035562 104117 ERROR 117 ;NO RDY AFTER SEL DRV CMD
7189
7190 035564 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
7191 035572 012737 000001 005464 MOV #1,UNITB
7192 035600 063765 005464 000010 ADD UNITB,RKCS2(R5)
7193 035606 112737 000102 056560 MOVB #'B,MSG19A
7194 035614 012737 000001 005314 MOV #SELD, HCS1
7195 035622 004737 043372 JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
7196 035626 104117 ERROR 117 ;NO RKY AFTER SEL DRV CMD
7197
7198 035630 032737 000040 005342 BIT #D.DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT B
7199 035636 001001 BNE 64$ ;BR IF YES
7200 035640 104071 ERROR 71 ;PORT B NOT AVAIL AFTER PORT A RLS
7201 035642 64$:
7202 035642 004737 045534 JSR PC,SUBCLR
7203 035646 104024 ERROR 24 ;CERR AFTER SCLR
7204
7205 035650 012737 036336 001176 MOV #5$, $ESCAPE
7206 035656 013765 012770 000020 MOV LC,RKDC(R5) ;SEEK TO LAST CYL
7207
7208 035664 012737 000017 005314 MOV #SEEK,HCS1
7209 035672 004737 043372 JSR PC,DOCMD ;DO SEEK CMD & GET CONTR READY
7210 035676 104131 ERROR 131 ;NO RDY AFTER SEEK CMD
7211
7212 035700 013737 001412 005352 MOV T50000,TEMP1 ;SETUP TIMEOUT
7213 035706 004737 044106 JSR PC,FATT2 ;FIND ATTN
7214 035712 104132 ERROR 132 ;NO ATTN AFTER SEEK CMD
7215
7216 035714 032737 100000 005314 BIT #CERR,HCS1
7217 035722 001401 BEQ 65$
7218 035724 104210 ERROR 210 ;CERR AFTER SEEK CMD
7219
7220 035726 65$:
7221
7222 035726 012737 050340 005404 MOV #<D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
7223 035734 005037 005406 CLR E.B0 ;EXPECTED MSG B0
7224 035740 012737 001720 005410 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
7225 035746 012737 000001 005412 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
7226 035754 005037 005414 CLR E.A2 ;EXPECTED MSG A2

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```

7227 035760 012737 000002 005416 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
7228 035766 012737 000003 005422 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
7229
7230 035774 004737 044274 JSR PC,CHKMSG ;CHECK MSGS AD, BO, A1, B1
7231 036000 000000 .WORD 0!0!0 ;& MSGS SPECIFIED HERE
7232 036002 104161 ERROR 161 ;MSG AD ERROR AFTER SEEK CMD
7233 036004 104162 ERROR 162 ;MSH BO ERROR
7234 036006 104163 ERROR 163 ;MSG A1 ERROR
7235 036010 104164 ERROR 164 ;MSG B1 ERROR
7236
7237
7238 036012 012765 100000 000000 MOV #CCLR,RKCS1(R5)
7239 036020 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;DRIVE#
7240 036026 063765 005464 000010 ADD UNITB,RKCS2(R5) ;ADD 1 IF ON PORT B
7241 036034 012737 000005 005314 MOV #CLEAR,HCS1
7242 036042 004737 043372 JSR PC,DOCMD ;DO DRIVE CLEAR CMD & GET CONTR RDY
7243 036046 104151 ERROR 151 ;NO RDY AFTER DRIVE CLEAR CMD
7244 036050 004737 043750 JSR PC,TSTATN ;TEST FOR ATTN
7245 036054 000401 BR 66$
7246 036056 104154 ERROR 154 ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
7247 036060
7248
7249 036060 012737 010340 005404 MOV #<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG AD
7250 036066 005037 005406 CLR E.B0 ;EXPECTED MSG B0
7251 036072 012737 001720 005410 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
7252 036100 012737 000001 005412 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
7253 036106 005037 005414 CLR E.A2 ;EXPECTED MSG A2
7254 036112 012737 000002 005416 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
7255 036120 012737 000003 005422 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
7256
7257 036126 004737 044274 JSR PC,CHKMSG ;CHECK MSGS AD, BO, A1, B1
7258 036132 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
7259 036134 104033 ERROR 33 ;MSG AD ERROR AFTER DRV CLEAR CMD
7260 036136 104034 ERROR 34 ;MSH BO ERROR
7261 036140 104035 ERROR 35 ;MSG A1 ERROR
7262 036142 104036 ERROR 36 ;MSG B1 ERROR
7263
7264 036144 012737 000013 005314 MOV #RECAL,HCS1 ;RECAL COMMAND
7265 036152 053737 001170 005314 BIS $TMP4,HCS1
7266 036160 013765 005314 000000 MOV HCS1,RKCS1(R5)
7267 036166 013704 001222 MOV $UNIT,R4
7268 036172 063704 005464 ADD UNITB,R4
7269 036176 136465 005304 000017 1$: BITB ATTN(R4),RKASOF+1(R5) ;SEE IF ATTN SET
7270 036204 001027 BNE 3$ ;BR IF YES
7271
7272 036206 012737 000000 005464 MOV #0,UNITB ;SETUP FOR PORT A
7273 036214 112737 000101 056560 MOVB #'A,MSG19A
7274 036222 004737 044172 JSR PC,DRAV ;SEE IF DRV AVAIL
7275 036226 000401 BR 2$ ;RETURN HERE IF NO
7276 036230 104177 ERROR 177 ;PORT A AVAIL
7277 ;RECAL DID NOT INHIBIT TIMERS
7278 036232 012765 100000 000000 2$: MOV #CCLR,RKCS1(R5)
7279 036240 012737 000001 005464 MOV #1,UNITB
7280 036246 112737 000102 056560 MOVB #'B,MSG19A ;SETUP PORT B
7281 036254 004737 044172 JSR PC,DRAV ;SEE IF DRV AVAIL
7282 036260 104203 ERROR 203 ;PORT B NOT REMAIN AVAIL DURING RECAL
    
```

K11

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 140
T35 TEST RECAL COMMAND TIMER INHIBIT THRU PORT 'B'

SEQ 0140

```

7283 036262 000745 BR 1$
7284
7285 036264 004737 045146 005342 3$: JSR PC,GSTAT
7286 036270 032737 020000 BIT #D,PIP,HMR2 ;SEE IF ANY MOTION
7287 036276 001401 BEQ 4$ ;BR IF NO
7288 036300 104072 ERROR 72 ;PIP SET AFTER ATTN RECA FROM RECAL
7289 036302 012737 000170 001366 4$: MOV #120,COUNT
7290 036310 004737 047204 JSR PC,TMO ;DO 2 SEC DLY
7291 036314 012737 000000 005464 MOV #0,UNITB ;SETUP FOR PORT A
7292 036322 112737 000101 056560 MOVB #'A,MSG19A
7293 036330 004737 044172 JSR PC,DRAV ;SEE IF DRV NOW AVAIL
7294 036334 104045 ERROR 45 ;PORT A NOT AVAIL AFTER TMO
7295
7296 036336 005037 001176 5$: CLR $ESCAPE
7297
7298
7299

```

```

*****
:TEST 36 READ & SAVE BAD SECTOR INFO & TYPE PACK SERIAL #

```

```

* THIS TEST VERIFIES THAT CYL 632 (1456 FOR RK07), TRACK 2 CAN BE READ.
* THIS AREA CONTAINS BAD SECTOR INFO WHICH IS WRITTEN BY THE
* FACTORY DURING MANF. ALL BAD SECTOR INFO (BSE) WILL BE STORED
* AT THIS TIME TO MASK FUTURE READ HEADER OR DATA ERROR PRINTOUTS.

```

```

* SECTORS 0,2,4,6,8 CONTAIN IDENTICAL INFO FOR 22 SECTOR HARDWARE DETECTED BAD SEC
* SECTORS 10,12,14,16,18,20 CONTAIN IDENTICAL INFO FOR 22 SECTOR SOFTWARE DETECTED

```

```

* IF BSE INFO CANNOT BE READ, OR IF AFTER READING THE BSE INFO
* IT IS DETERMINED THAT AN ALIGNMENT CARTRIDGE IS USED,
* A MESSAGE WILL BE TYPED INDICATING THAT ALL
* FUTURE FORMAT AND READ-WRITE TESTS WILL BE BYPASSED.
* THIS IS DONE SO AS NOT TO DESTROY BSE INFO OR AN ALIGNMENT PACK BY WRITING

```

```

* THE PACK SERIAL # IS TYPED IN OCTAL & FOR THE FIRST PASS ONLY.

```

```

*****
:ST36: SCOPE

```

```

7319 036342 000004 000001 001174 MOV #1,$TIMES ;DO 1 ITERATION
7320 036344 012737 000001 MOV #STACK,SP ;RESTORE STK PTR
7321 036352 012706 001100
7322
7323 036356 004737 045534 JSR PC,SUBCLR
7324 036362 104024 ERROR 24 ;CERR AFTER SCLR
7325
7326 036364 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
7327 036372 012737 000001 005464 MOV #1,UNITB
7328 036400 063765 005464 000010 ADD UNITB,RKCS2(R5)
7329 036406 112737 000102 056560 MOVB #'9,MSG19A
7330 036414 062765 000010 000010 ADD #RLS,RKCS2(R5) ;RELEASE PORT B
7331 036422 012737 000001 005314 MOV #SELDRV,HCS1
7332 036430 004737 043372 JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
7333 036434 104117 ERROR 117 ;NO RDY AFTER SEL DRV CMD
7334
7335 036436 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
7336 036444 012737 000000 005464 MOV #0,UNITB
7337 036452 063765 005464 000010 ADD UNITB,RKCS2(R5)
7338 036460 112737 000101 056560 MOVB #'A,MSG19A

```

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 141
T36 READ & SAVE BAD SECTOR INFO & TYPE PACK SERIAL #

SEQ 0141

7339	036466	012737	000001	005314	MOV	#SELDV,HCS1	
7340	036474	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
7341	036500	104117			ERROR	117	;NO RKY AFTER SEL DRV CMD
7342							
7343	036502	032737	000040	005342	BIT	#D.DRA,HMR2	;SEE IF DRIVE AVAIL ON PORT A
7344	036510	001001			BNE	64\$;BR IF YES
7345	036512	104071			ERROR	71	;PORT A NOT AVAIL AFTER PORT B RLS
7346	036514						
7347							
7348	036514	004737	045534		JSR	PC,SUBCLR	
7349	036520	104024			ERROR	24	;CERR AFTER SCLR
7350							
7351	036522	005037	005354		CLR	TEMP2	;SECTOR CTR
7352	036526	005037	005356		CLR	TEMP3	;0=22 SECTOR HARDWARE DETECTED TABLE
7353							;1=22 SECTOR SOFTWARE DETECTED TABLE
7354							;2=DONE
7355	036532	012737	002276	005360	MOV	#BSE22H,TEMP4	;STORE 22 SECTOR HARDWARE BSE ADDR.
7356	036540	013765	005360	000004	MOV	TEMP4,RKBA(R5)	
7357	036546	012737	001000	005362	MOV	#1000,TEMP5	;TRACK 2, SECTOR 0
7358	036554	013765	005362	000006	MOV	TEMP5,RKDA(R5)	
7359							
7360	036562	013765	012770	000020	MOV	LC,RKDC(R5)	;LAST CYL
7361	036570	012765	177400	000002	MOV	#-256,RKWC(R5)	;LOAD WORD CT
7362	036576	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	
7363	036604	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
7364	036612	012737	000021	005314	MOV	#RDATA,HCS1	
7365	036620	004737	043430		JSR	PC,DATCMD	;DO READ DATA CMD & GET CONTR READY
7366	036624	104226			ERROR	226	;NO RDY AFTER READ DATA CMD
7367	036626	004737	045146		JSR	PC,GSTAT	;GET FRESH DATA
7368	036632	032737	100000	005314	BIT	#CERR,HCS1	
7369	036640	001470			BEQ	8\$	
7370	036642	104227			ERROR	227	;CERR AFTER READ DATA CMD
7371							
7372	036644	012737	010340	005404	MOV	#<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0	;EXPECTED MSG A0
7373	036652	005037	005406		CLR	E.B0	;EXPECTED MSG B0
7374	036656	012737	001720	005410	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1
7375	036664	012737	000001	005412	MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1
7376	036672	005037	005414		CLR	E.A2	;EXPECTED MSG A2
7377	036676	012737	000002	005416	MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2
7378	036704	012737	000003	005422	MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
7379							
7380	036712	004737	044274		JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1
7381	036716	000000			.WORD	0!0!0	; & MSGS SPECIFIED HERE
7382	036720	104054			ERROR	54	;MSG A0 ERROR AFTER READ DATA CMD
7383	036722	104026			ERROR	26	;MSG B0 ERROR
7384	036724	104056			ERROR	56	;MSG A1 ERROR
7385	036726	104030			ERROR	30	;MSG B1 ERROR
7386							
7387	036730	004737	045534		JSR	PC,SUBCLR	
7388	036734	104024			ERROR	24	;CERR AFTER SUBCLR
7389							
7390	036736	005237	005354		INC	TEMP2	
7391	036742	023727	005354	000005	CMP	TEMP2,#5	;READ ALL 5 SECTORS?
7392	036750	001007			BNE	5\$	
7393	036752	005737	005356		TST	TEMP3	
7394	036756	001002			BNE	2\$	

```

7395 036760 104233          ERROR 233          ;CANT READ SECTORS 0,2,4,6,8
7396 036762 000414          BR      3$
7397 036764 104230          2$:  ERROR 230          ;CANT READ SECTORS 10,12,14,16,18,20
7398 036766 000412          BR      3$
7399
7400 036770 013765 005360 000004 5$:  MOV    TEMP4,RKBA(R5) ;RESTORE TABLE ADDR
7401 036776 062737 000002 005362  ADD    #2,TEMP5       ;SETUP TO READ 2 SECTORS FROM LAST
7402 037004 013765 005362 000006  MOV    TEMP5,RKDA(R5)
7403 037012 000663          BR      1$
7404
7405 037014 005237 001452 3$:  INC    BSERR          ;SET BSE FLAG
7406 037020 000454          BR      TST37         ;GO TO NEXT TEST
7407 037022 005737 002304 8$:  TST    BSE22H+6      ;TEST CARTRIDGE TYPE
7408 037026 001404          BEQ    9$             ;BRANCH IF DATA CARTRIDGE
7409 037030 104235          ERROR 235          ;ALIGNMENT CARTRIDGE USED
7410 037032 005237 001452  INC    BSERR          ;SET BSE ERROR FLAG
7411 037036 000426          BR      10$
7412
7413 037040 005237 005356 9$:  INC    TEMP3         ;
7414 037044 023727 005356 000001  CMP    TEMP3,#1      ;
7415 037052 001020          BNE    10$           ;
7416 037054 005037 005354  CLR    TEMP2         ;
7417 037060 012737 003276 005360  MOV    #BSE22S,TEMP4 ;STORE 22 SECTOR SOFTWARE BSE ADDR
7418 037066 013765 005360 000004  MOV    TEMP4,RKBA(R5) ;
7419 037074 012737 001012 005362  MOV    #1012,TEMP5   ;TRACK 2, SECTOR 12
7420 037102 013765 005362 000006  MOV    TEMP5,RKDA(R5) ;
7421 037110 000137 036562          JMP    1$            ;REPEAT
7422
7423 037114 005737 001216 10$:  TST    $PASS        ;
7424 037120 001014          BNE    TST37         ;GO TO NEXT TST IF NOT 1'ST PASS
7425 037122 104401 056521  TYPE   MSG17         ;CART SERIAL #
7426 037126 012746 002276  MOV    #BSE22H,-(SP) ;
7427 037132 004737 053660  JSR    PC,$DB20      ;CONVERT DBL BINARY WORD TO OCTAL
7428 037136 004737 054230  JSR    PC,$SUPRS     ;TYPE SERIAL #
7429 037142 104401 001205  TYPE   $CRLF
7430 037146 104401 001205  TYPE   $CRLF
7431
7432
7433
7434
7435
7436
7437
7438
7439
7440
7441
7442
7443
7444
7445
7446
7447
7448
7449 037152 000004          TST37: SCOPE
7450 037154 012737 000001 001174  MOV    #1,$TIMES    ;;DO 1 ITERATION

```

```

*****
*TEST 37      DATA TESTS
*****
*
*   VERIFY UNIQUE DATA CAN BE WRITTEN THRU EITHER PORT & READ
*   BACK CORRECTLY THRU BOTH PORTS.
*
*   A.  ALL 0'S ARE WRITTEN THRU PORT 'A' ON CYL 0, SECTOR 0,
*        TRACK 0 & VERIFIED BY READING BACK THRU BOTH PORTS.
*
*   B.  ALL 1'S ARE WRITTEN THRU PORT 'B' ON CYL 10, SECTOR 0,
*        TRACK 0 & VERIFIED BY READING BACK THRU BOTH PORTS.
*
*   C.  THE PROGRAM CHECKS THAT CYL 0 WAS NOT OVERWRITTEN
*        BY READING & VERIFYING ALL 0'S THRU PORT 'B'.
*****

```


7451	037162	012706	001100		MOV	#STACK, SP	
7452							
7453	037166	013765	001222	000010	MOV	\$UNIT, RKCS2(R5)	; SETUP FOR PORT B
7454	037174	012737	000001	005464	MOV	#1, UNITB	
7455	037202	063765	005464	000010	ADD	UNITB, RKCS2(R5)	
7456	037210	112737	000102	056560	MOV	#'B, MSG19A	
7457	037216	062765	000010	000010	ADD	#RLS, RKCS2(R5)	; RELEASE PORT B
7458	037224	012737	000001	005314	MOV	#SELDRV, HCS1	
7459	037232	004737	043372		JSR	PC, DOCMD	; DO SELDRV (STATUS) CMD & GET CONTR RDY
7460	037236	104117			ERROR	117	; NO RDY AFTER SEL DRV CMD
7461							
7462	037240	013765	001222	000010	MOV	\$UNIT, RKCS2(R5)	; SETUP FOR PORT A
7463	037246	012737	000000	005464	MOV	#0, UNITB	
7464	037254	063765	005464	000010	ADD	UNITB, RKCS2(R5)	
7465	037262	112737	000101	056560	MOV	#'A, MSG19A	
7466	037270	012737	000001	005314	MOV	#SELDRV, HCS1	
7467	037276	004737	043372		JSR	PC, DOCMD	; DO SELDRV (STATUS) CMD & GET CONTR RDY
7468	037302	104117			ERROR	117	; NO RKY AFTER SEL DRV CMD
7469							
7470	037304	032737	000040	005342	BIT	#D, DRA, HMR2	; SEE IF DRIVE AVAIL ON PORT A
7471	037312	001001			BNE	64\$; BR IF YES
7472	037314	104071			ERROR	71	; PORT A NOT AVAIL AFTER PORT B RLS
7473	037316						
7474	037316	004737	045534		JSR	PC, SUBCLR	
7475	037322	104024			ERROR	24	; CERR AFTER SCLR
7476							
7477	037324	005037	001374		CLR	SECTOR	
7478	037330	005037	001360		CLR	CYLADD	
7479	037334	012737	001434	001436	MOV	#DATA0, DATA01	; HOLD DATA
7480	037342	013765	001374	000006	MOV	SECTOR, RKDA(R5)	; SETUP TO WRITE
7481	037350	013765	001436	000004	MOV	DATA01, RKBA(R5)	; SETUP DATA
7482	037356	013765	001360	000020	MOV	CYLADD, RKDC(R5)	; SETUP CYLINDER
7483	037364	052765	000020	000010	BIS	#BAI, RKCS2(R5)	
7484	037372	012765	177400	000002	MOV	#-256., RKWC(R5)	
7485							
7486	037400	012737	000023	005314	MOV	#<WRDATA>, HCS1	
7487	037406	004737	043430		JSR	PC, DATCMD	; DO DATA X FOR CMD & GET CONTR RDY
7488	037412	104011			ERROR	11	; NO RDY AFTER WRITE DATA CMD
7489	037414	004737	045146		JSR	PC, GSTAT	; GET FRESH STATUS
7490	037420	032737	100000	005314	BIT	#CERR, HCS1	
7491	037426	001465			BEQ	68\$; BR IF NO ERRORS
7492							
7493	037430	032737	000200	005330	BIT	#BSE, HER	; SEE IF BAD SECTOR FLAG
7494	037436	001421			BEQ	66\$; BR IF NO
7495	037440	004737	047002		JSR	PC, TRUERR	; ELSE SEE IF SECTOR LISTED IN BSE TABLE
7496	037444	000455			BR	67\$; RETURN HERE IF NO
7497							
7498	037446	005237	001374		INC	SECTOR	; RETURN HERE IF YES
7499	037452	023727	001374	000012	CMP	SECTOR, #10.	; ARE 10 CONSEC. SECTORS BAD
7500	037460	001003			BNE	65\$; BR IF NO
7501	037462	104046			ERROR	46	; ABORTING TEST DETECTED 10 BAD SECTORS
7502	037464	000137	040246		JMP	7\$; BYPASS TEST
7503	037470	012765	100000	000000	MOV	#CCLR, RKCS1(R5)	; TRY ANOTHER SECTOR
7504	037476	000137	037342		JMP	1\$	
7505	037502	104012			ERROR	12	; CERR WITH WRITE DATA CMD
7506							

7507	037504	012737	010340	005404	MOV	#<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0	;EXPECTED MSG A0
7508	037512	005037	005406		CLR	E.B0	;EXPECTED MSG B0
7509	037516	012737	001720	005410	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1
7510	037524	012737	000001	005412	MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1
7511	037532	005037	005414		CLR	E.A2	;EXPECTED MSG A2
7512	037536	012737	000002	005416	MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2
7513	037544	012737	000003	005422	MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
7514							
7515	037552	004737	044274		JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1
7516	037556	000003			.WORD	T.A2!T.B2!0	; & MSGS SPECIFIED HERE
7517	037560	104052			ERROR	52	;MSG A0 ERROR AFTER WRITE DATA CMD
7518	037562	104023			ERROR	23	;MSH B0 ERROR
7519	037564	104053			ERROR	53	;MSG A1 ERROR
7520	037566	104025			ERROR	25	;MSG B1 ERROR
7521	037570	104401	056607		TYPE	MSG21	;ABORTING BALANCE OF TESTS
7522	037574	000137	042644		JMP	\$EOP	
7523	037600	104043			ERROR	43	;BAD SECTOR NOT LISTED IN TABLE
7524	037602						
7525							
7526	037602	012737	010340	005404	MOV	#<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0	;EXPECTED MSG A0
7527	037610	005037	005406		CLR	E.B0	;EXPECTED MSG B0
7528	037614	012737	001720	005410	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1
7529	037622	012737	000001	005412	MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1
7530	037630	005037	005414		CLR	E.A2	;EXPECTED MSG A2
7531	037634	012737	000002	005416	MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2
7532	037642	012737	000003	005422	MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
7533							
7534	037650	004737	044274		JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1
7535	037654	000003			.WORD	T.A2!T.B2!0	; & MSGS SPECIFIED HERE
7536	037656	104052			ERROR	52	;MSG A0 ERROR AFTER WRITE DATA CMD
7537	037660	104023			ERROR	23	;MSH B0 ERROR
7538	037662	104053			ERROR	53	;MSG A1 ERROR
7539	037664	104025			ERROR	25	;MSG B1 ERROR
7540	037666	005000			CLR	R0	;CLEAR TO DO PORT A FIRST
7541							
7542	037670	005700			TST	R0	;SEE IF DOING PORT 'A'
7543	037672	001056			BNE	3\$;BR IF NO
7544	037674	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT B
7545	037702	012737	000001	005464	MOV	#1,UNITB	
7546	037710	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
7547	037716	112737	000102	056560	MOVB	#'B,MSG19A	
7548	037724	062765	000010	000010	ADD	#RLS,RKCS2(R5)	;RELEASE PORT B
7549	037732	012737	000001	005314	MOV	#SELDRV,HCS1	
7550	037740	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
7551	037744	104117			ERROR	117	;NO RDY AFTER SEL DRV CMD
7552							
7553	037746	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;SETUP FOR PORT A
7554	037754	012737	000000	005464	MOV	#0,UNITB	
7555	037762	063765	005464	000010	ADD	UNITB,RKCS2(R5)	
7556	037770	112737	000101	056560	MOVB	#'A,MSG19A	
7557	037776	012737	000001	005314	MOV	#SELDRV,HCS1	
7558	040004	004737	043372		JSR	PC,DOCMD	;DO SELDRV (STATUS) CMD & GET CONTR RDY
7559	040010	104117			ERROR	117	;NO RKY AFTER SEL DRV CMD
7560							
7561	040012	032737	000040	005342	BIT	#D.DRA,HMR2	;SEE IF DRIVE AVAIL ON PORT A
7562	040020	001001			BNE	69\$;BR IF YES

7563	040022	104071				ERROR	71		;PORT A NOT AVAIL AFTER PORT B RLS
7564	040024				69\$:				
7565	040024	000137	040246			JMP	7\$;GO & WRITE CHECK THRU PORT 'A'
7566					3\$:				
7567	040030	020027	000001			CMP	RO,#1		;SEE IF DOING PORT 'B'
7568	040034	001056				BNE	4\$;BR IF NO
7569	040036	013765	001222	000010		MOV	\$UNIT,RKCS2(R5)		;SETUP FOR PORT A
7570	040044	012737	000000	005464		MOV	#0,UNITB		
7571	040052	063765	005464	000010		ADD	UNITB,RKCS2(R5)		
7572	040060	112737	000101	056560		MOVB	#'A,MSG19A		
7573	040066	062765	000010	000010		ADD	\$RLS,RKCS2(R5)		;RELEASE PORT A
7574	040074	012737	000001	005314		MOV	\$SELD, HCS1		
7575	040102	004737	043372			JSR	PC,DOCMD		;DO SELDRV (STATUS) CMD & GET CONTR RDY
7576	040106	104117				ERROR	117		;NO RDY AFTER SEL DRV CMD
7577									
7578	040110	013765	001222	000010		MOV	\$UNIT,RKCS2(R5)		;SETUP FOR PORT B
7579	040116	012737	000001	005464		MOV	#1,UNITB		
7580	040124	063765	005464	000010		ADD	UNITB,RKCS2(R5)		
7581	040132	112737	000102	056560		MOVB	#'B,MSG19A		
7582	040140	012737	000001	005314		MOV	\$SELD, HCS1		
7583	040146	004737	043372			JSR	PC,DOCMD		;DO SELDRV (STATUS) CMD & GET CONTR RDY
7584	040152	104117				ERROR	117		;NO RKY AFTER SEL DRV CMD
7585									
7586	040154	032737	000040	005342		BIT	#D.DRA,HMR2		;SEE IF DRIVE AVAIL ON PORT B
7587	040162	001001				BNE	70\$;BR IF YES
7588	040164	104071				ERROR	71		;PORT B NOT AVAIL AFTER PORT A RLS
7589	040166				70\$:				
7590	040166	000137	040246			JMP	7\$;GO WRITE CHECK THRU PORT 'B'
7591									
7592	040172	020027	000002		4\$:	CMP	RO,#2		;SEE IF DID WRT CHK THRU PORT A & B
7593	040176	001402				BEQ	5\$;BR IF YES
7594	040200	000137	040562			JMP	8\$;ELSE RO=3, EXIT TEST
7595									
7596	040204	023727	001436	001434	5\$:	CMP	DATA01,\$DATA0		;SEE IF JUST WROTE 0'S ON CYL 0
7597	040212	001010				BNE	6\$;BR IF NO
7598	040214	012737	000012	001360		MOV	#10,CYLADD		;ELSE WRITE 1'S ON CYL 10
7599	040222	012737	001440	001436		MOV	\$DATA1,DATA01		
7600	040230	000137	037342			JMP	1\$;GO DO IT
7601									
7602	040234	005037	001360		6\$:	CLR	CYLADD		;RECHK CYL 0 FOR 0'S
7603	040240	012737	001434	001436		MOV	\$DATA0,DATA01		
7604									
7605	040246	052765	000020	000010	7\$:	BIS	\$BAI,RKCS2(R5)		;SETUP TO WRITE CHECK
7606	040254	013765	001436	000004		MOV	DATA01,RKBA(R5)		
7607	040262	013765	001360	000020		MOV	CYLADD,RKDC(R5)		
7608	040270	012765	177400	000002		MOV	#-256.,RKWC(R5)		
7609	040276	013765	001374	000006		MOV	SECTOR,RKDA(R5)		
7610	040304	013737	001374	001370		MOV	SECTOR,SEC		;COPY FOR FUTURE USE
7611									
7612	040312	012737	000031	005314		MOV	#<WRTCHK>,HCS1		
7613	040320	004737	043430			JSR	PC,DATCMD		;DO DATA X FOR CMD & GET CONTR RDY
7614	040324	104015				ERROR	15		;NO RDY AFTER WRITE CHECK CMD
7615	040326	004737	045146			JSR	PC,GSTAT		;GET FRESH STATUS
7616	040332	032737	100000	005314		BIT	\$CERR,HCS1		
7617	040340	001453				BEQ	72\$		
7618	040342	032737	040000	005316		BIT	\$WCE,HCS2		;SEE IF WRITE CHECK ERROR

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7619 040350 001410          BEQ      71$
7620 040352 016537 000024 001414  MOV     RKDB(R5),WD1 ;ACTUAL WORD FOR PRINTOUT
7621 040360 013737 001434 001416  MOV     DATA0,WD2  ;EXPECTED WORD FOR TYPEOUT
7622 040366 104016          ERROR   16          ;WCE AFTER WRITE CMD
7623 040370 000437          BR      72$
7624
7625 040372 104022          71$:  ERROR   22          ;CERR AFTER WRITE CHECK CMD
7626
7627 040374 012737 010340 005404  MOV     #<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
7628 040402 005037 005406          CLR     E.B0        ;EXPECTED MSG B0
7629 040406 012737 001720 005410  MOV     #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
7630 040414 012737 000001 005412  MOV     #1,E.B1      ;MSG ID FOR EXPECTED MSG B1
7631 040422 005037 005414          CLR     E.A2        ;EXPECTED MSG A2
7632 040426 012737 000002 005416  MOV     #2,E.B2      ;MSG ID FOR EXPECTED MSG B2
7633 040434 012737 000003 005422  MOV     #3,E.B3      ;MSG ID FOR EXPECTED MSG B3
7634
7635 040442 004737 044274          JSR     PC,CHKMSG   ;CHECK MSGS A0 B0 A1 B1
7636 040446 000003          .WORD  T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
7637 040450 104057          ERROR   57          ;MSG A0 ERROR AFTER WRITE CHECK CMD
7638 040452 104031          ERROR   31          ;MSG B0 ERROR
7639 040454 104060          ERROR   60          ;MSG A1 ERROR
7640 040456 104032          ERROR   32          ;MSG B1 ERROR
7641 040460 104401 056607          TYPE   MSG21       ;ABORTING BALANCE OF TESTS
7642 040464 000137 042644          JMP     $EOP
7643
7644 040470          72$:
7645
7646 040470 012737 010340 005404  MOV     #<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
7647 040476 005037 005406          CLR     E.B0        ;EXPECTED MSG B0
7648 040502 012737 001720 005410  MOV     #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
7649 040510 012737 000001 005412  MOV     #1,E.B1      ;MSG ID FOR EXPECTED MSG B1
7650 040516 005037 005414          CLR     E.A2        ;EXPECTED MSG A2
7651 040522 012737 000002 005416  MOV     #2,E.B2      ;MSG ID FOR EXPECTED MSG B2
7652 040530 012737 000003 005422  MOV     #3,E.B3      ;MSG ID FOR EXPECTED MSG B3
7653
7654 040536 004737 044274          JSR     PC,CHKMSG   ;CHECK MSGS A0 B0 A1 B1
7655 040542 000003          .WORD  T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
7656 040544 104057          ERROR   57          ;MSG A0 ERROR AFTER WRITE CHECK CMD
7657 040546 104031          ERROR   31          ;MSG B0 ERROR
7658 040550 104060          ERROR   60          ;MSG A1 ERROR
7659 040552 104032          ERROR   32          ;MSG B1 ERROR
7660
7661 040554 005200          INC     R0
7662 040556 000137 037670          JMP     2$
7663
7664
7665
7666
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7671
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7674

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8\$:

TEST 40 ALTERNATING SEEK INTERACTION TEST

THIS TEST VERIFIES THAT THERE ARE NO TIMING INTERACTION PROBLEMS BETWEEN SEEKS FROM BOTH PORTS.

A. PORT 'A' SEIZES THE DRIVE & SEEKS TO CYLINDER 0 & RELEASES THE DRIVE AFTER 'ATTN' IS RECEIVED.

THE PROGRAM VERIFIES THAT UNTIL ATTN IS RECEIVED,

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7690 040562 000004
7691 040564 012737 000001 001174
7692 040572 012706 001100
7693
7694 040576 004737 045534
7695 040602 104024
7696
7697 040604 013765 001222 000010
7698 040612 012737 000001 005464
7699 040620 063765 005464 000010
7700 040626 112737 000102 056560
7701 040634 062765 000010 000010
7702 040642 012737 000001 005314
7703 040650 004737 043372
7704 040654 104117
7705
7706 040656 013765 001222 000010
7707 040664 012737 000000 005464
7708 040672 063765 005464 000010
7709 040700 112737 000101 056560
7710 040706 012737 000001 005314
7711 040714 004737 043372
7712 040720 104117
7713
7714 040722 032737 000040 005342
7715 040730 001001
7716 040732 104071
7717 040734
7718
7719 040734 005037 001414
7720 040740 013737 012770 001416
7721
7722 040746 012765 100000 000000
7723 040754 013765 001222 000010
7724 040762 063765 005464 000010
7725 040770 013765 001414 000020
7726 040776 012737 000017 005314
7727 041004 004737 043372
7728 041010 104131
7729
7730 041012 013737 001412 005352

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*****
* PORT 'B' SEES CONTROLLER ERROR & DRIVE NOT AVAILABLE.
*
* B. PORT 'B' SEIZES THE DRIVE & SEEKS TO THE LAST CYL
* & RELEASES THE DRIVE AFTER 'ATTN' IS RECEIVED
*
* THE PROGRAM VERIFIES THAT UNTIL ATTN IS RECEIVED,
* PORT 'A' SEES CONTROLLER ERROR & DRIVE NOT AVAILABLE.
*
* C. THE ABOVE IS REPEATED FOR A PATTERN OF CONVERGING SEEKS
* TOWARD THE CENTER OF THE CARTRIDGE.
*
* D. THE PROGRAM VERIFIES MULTIPLE ATTENTIONS OR ERRORS
* DO NOT OCCUR AS A RESULT OF TIMING PROBLEMS.
*
*****
TST40: SCOPE
MOV #1,$TIMES ;;DO 1 ITERATION
MOV #STACK,SP
JSR PC,SUBCLR
ERROR 24 ;CERR AFTER SCLR
MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
MOV #1,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'B,MSG19A
ADD #RLS,RKCS2(R5) ;RELEASE PORT B
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 117 ;NO RDY AFTER SEL DRV CMD
MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
MOV #0,UNITB
ADD UNITB,RKCS2(R5)
MOVB #'A,MSG19A
MOV #SELDRV,HCS1
JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 117 ;NO RKY AFTER SEL DRV CMD
BIT #D,DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT A
BNE 64$ ;BR IF YES
ERROR 71 ;PORT A NOT AVAIL AFTER PORT B RLS
64$:
CLR WD1 ;SETUP CONVERGING LIMITS
MOV LC,WD2
10$: MOV #CCLR,RKCS1(R5)
MOV $UNIT,RKCS2(R5)
ADD UNITB,RKCS2(R5)
MOV WD1,RKDC(R5)
MOV #SEEK,HCS1
JSR PC,DOCMD ;DO SEEK CMD & GET CONTR READY
ERROR 131 ;NO RDY AFTER SEEK CMD-PORT A
MOV T50000,TEMP1

```

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7731 041020 013704 001222 1$: MOV $UNIT,R4
7732 041024 136465 005304 000017 BITB ATTN(R4),RKASOF+1(R5) ;TEST FOR ATTN ON PORT A
7733 041032 001032 BNE 3$ ;BR IF THERE
7734 041034 012737 000001 005464 MOV #1,UNITB ;ELSE VERIFY PORT B NOT AVAIL
7735 041042 112737 000102 056560 MOVB #B,MSG19A
7736 041050 004737 044172 JSR PC,DRAV
7737 041054 000403 BR 2$
7738 041056 104103 ERROR 103 ;PORT B AVAIL BEFORE TMO OR RELEASE
7739 041060 000137 042644 JMP 11$
7740
7741 041064 012765 100000 000000 2$: MOV #CCLR,RKCS1(R5)
7742 041072 005337 005352 DEC TEMP1
7743 041076 001350 BNE 1$ ;SEE IF PORT A HAS ATTN
7744 041100 005037 005464 CLR UNITB
7745 041104 112737 000101 056560 MOVB #A,MSG19A
7746 041112 104156 ERROR 156 ;NO ATTN ON PORT A AFTER SEEK
7747 041114 000137 042644 JMP 11$ ;EXIT TEST
7748
7749 041120 005037 005464 3$: CLR UNITB ;SETUP FOR PORT A
7750 041124 112737 000101 056560 MOVB #A,MSG19A
7751 041132 004737 044172 JSR PC,DRAV
7752 041136 104157 ERROR 157 ;PORT A NOT AVAIL AFTER SEEK
7753
7754 041140 032737 100000 005314 BIT #CERR,HCS1
7755 041146 001401 BEQ 4$
7756 041150 104210 ERROR 210 ;CERR AFTER SEEK CMD ON PORT A
7757
7758 041152 4$:
7759
7760 041152 012737 050340 005404 MOV #<D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
7761 041160 005037 005406 CLR E.B0 ;EXPECTED MSG B0
7762 041164 012737 001720 005410 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
7763 041172 012737 000001 005412 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
7764 041200 005037 005414 CLR E.A2 ;EXPECTED MSG A2
7765 041204 012737 000002 005416 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
7766 041212 012737 000003 005422 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
7767
7768 041220 004737 044274 JSR PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
7769 041224 000000 .WORD 0!0!0 ;& MSGS SPECIFIED HERE
7770 041226 104161 ERROR 161 ;MSG A0 ERROR AFTER SEEK CMD
7771 041230 104162 ERROR 162 ;MSG B0 ERROR
7772 041232 104163 ERROR 163 ;MSG A1 ERROR
7773 041234 104164 ERROR 164 ;MSG B1 ERROR
7774 041236 013737 001414 001346 MOV WD1,TOCYL ;SETUP TO SEE IF ON CORRECT CYL
7775 041244 013765 001414 000020 MOV WD1,RKDC(R5)
7776 041252 013765 001222 000010 MOV $UNIT,RKCS2(R5)
7777 041260 063765 005464 000010 ADD UNITB,RKCS2(R5)
7778
7779
7780 041266 012700 001666 MOV #RHTAB,R0
7781 041272 012737 000025 005314 MOV #<RDHEAD>,HCS1
7782 041300 004737 043430 JSR PC,DATCMD ;DO DATA X FOR CMD & GET CONTR RDY
7783 041304 104171 ERROR 171 ;NO RDY AFTER READ HEADER CMD
7784 041306 032737 100000 005314 BIT #CERR,HCS1
7785 041314 001401 BEQ 66$
7786 041316 104174 ERROR 174 ;CERR AFTER READ HEADER CMD
    
```

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7787
7788 041320 016520 000024      66$:  MOV      RKDB(R5),(R0)+ ;1'ST WORD FROM SILO TO RHTAB
7789 041324 016520 000024      MOV      RKDB(R5),(R0)+ ;2'ND WORD
7790 041330 016520 000024      MOV      RKDB(R5),(R0)+ ;3'RD WORD
7791
7792
7793 041334 032765 100000 000010      BIT      #DLT,RKCS2(R5)
7794 041342 001403 000000      BEQ      67$
7795 041344 004737 045146      JSR      PC,GSTAT
7796 041350 104173 000000      ERROR   173 ;DLT AFTER READ HEADER CMD
7797 041352
7798
7799 041352 012737 010340 005404      MOV      #<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
7800 041360 005037 005406      CLR      E.B0 ;EXPECTED MSG B0
7801 041364 012737 001720 005410      MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
7802 041372 012737 000001 005412      MOV      #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
7803 041400 005037 005414      CLR      E.A2 ;EXPECTED MSG A2
7804 041404 012737 000002 005416      MOV      #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
7805 041412 012737 000003 005422      MOV      #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
7806
7807 041420 004737 044274      JSR      PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
7808 041424 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
7809 041426 104301 ERROR 301 ;MSG A0 ERROR AFTER READ HEADER CMD
7810 041430 104271 ERROR 271 ;MSG B0 ERROR
7811 041432 104302 ERROR 302 ;MSG A1 ERROR
7812 041434 104272 ERROR 272 ;MSG B1 ERROR
7813
7814 041436 023737 001666 001346      CMP      RHTAB,TOCYL ;CHECK WORD 0 ONLY, CYL#
7815 041444 001401 BEQ      65$ ;BR IF SAME
7816 041446 104051 ERROR   51 ;WRONG CYL# ON HEADER
7817 041450
7818
7819 041450 004737 045534      JSR      PC,SUBCLR
7820 041454 104024 ERROR   24 ;CERR AFTER SCLR
7821
7822 041456 013765 001222 000010      MOV      $UNIT,RKCS2(R5) ;SETUP FOR PORT A
7823 041464 012737 000000 005464      MOV      #0,UNITB
7824 041472 063765 005464 000010      ADD      UNITB,RKCS2(R5)
7825 041500 112737 000101 056560      MOVB    #'A,MSG19A
7826 041506 062765 000010 000010      ADD      #RLS,RKCS2(R5) ;RELEASE PORT A
7827 041514 012737 000001 005314      MOV      #SELDRV,HCS1
7828 041522 004737 043372      JSR      PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
7829 041526 104117 ERROR   117 ;NO RDY AFTER SEL DRV CMD
7830
7831 041530 013765 001222 000010      MOV      $UNIT,RKCS2(R5) ;SETUP FOR PORT B
7832 041536 012737 000001 005464      MOV      #1,UNITB
7833 041544 063765 005464 000010      ADD      UNITB,RKCS2(R5)
7834 041552 112737 000102 056560      MOVB    #'B,MSG19A
7835 041560 012737 000001 005314      MOV      #SELDRV,HCS1
7836 041566 004737 043372      JSR      PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
7837 041572 104117 ERROR   117 ;NO RKY AFTER SEL DRV CMD
7838
7839 041574 032737 000040 005342      BIT      #D.DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT B
7840 041602 001001 BNE     68$ ;BR IF YES
7841 041604 104071 ERROR   71 ;PORT B NOT AVAIL AFTER PORT A RLS
7842 041606

```

7843	041606	012765	100000	000000		MOV	#CCLR,RKCS1(R5)	
7844	041614	013765	001222	000010		MOV	\$UNIT,RKCS2(R5)	
7845	041622	063765	005464	000010		ADD	UNITB,RKCS2(R5)	
7846	041630	013765	001416	000020		MOV	WD2,RKDC(R5)	
7847	041636	012737	000017	005314		MOV	#SEEK,HCS1	
7848	041644	004737	043372			JSR	PC,DOCMD	;DO SEEK CMD & GET CONTR READY
7849	041650	104131				ERROR	131	;NO READY AFTER SEEK CMD-PORT B
7850								
7851	041652	013737	001412	005352		MOV	T50000,TEMP1	
7852	041660	013704	001222		5\$:	MOV	\$UNIT,R4	
7853	041664	005204				INC	R4	
7854	041666	136465	005304	000017		BITB	ATTN(R4),RKASOF+1(R5)	;TEST FOR ATTN ON PORT B
7855	041674	001032				BNE	7\$;BR IF THERE
7856	041676	005037	005464			CLR	UNITB	;ELSE VERIFY PORT A NOT AVAIL
7857	041702	112737	000101	056560		MOV	#'A,MSG19A	
7858	041710	004737	044172			JSR	PC,DRAV	
7859	041714	000403				BR	6\$	
7860	041716	104103				ERROR	103	;PORT A AVAIL BEFORE TMO OR RELEASE
7861	041720	000137	042644			JMP	11\$	
7862								
7863	041724	012765	100000	000000	6\$:	MOV	#CCLR,RKCS1(R5)	
7864	041732	005337	005352			DEC	TEMP1	
7865	041736	001350				BNE	5\$;SEE IF PORT B HAS ATTN
7866	041740	012737	000001	005464		MOV	#1,UNITB	
7867	041746	112737	000102	056560		MOV	#'B,MSG19A	
7868	041754	104156				ERROR	156	;NO ATTN ON PORT B AFTER SEEK
7869	041756	000137	042644			JMP	11\$;EXIT
7870								
7871	041762	012737	000001	005464	7\$:	MOV	#1,UNITB	;SETUP FOR PORT B
7872	041770	112737	000102	056560		MOV	#'B,MSG19A	
7873	041776	004737	044172			JSR	PC,DRAV	
7874	042002	104157				ERROR	157	;PORT B NOT AVAIL AFTER SEEK
7875	042004	032737	100000	005314		BIT	#CERR,HCS1	
7876	042012	001401				BEQ	8\$	
7877	042014	104210				ERROR	210	;CERR AFTER SEEK CMD ON PORT B
7878								
7879	042016				8\$:			
7880								
7881	042016	012737	050340	005404		MOV	#<D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0	;EXPECTED MSG A0
7882	042024	005037	005406			CLR	E.B0	;EXPECTED MSG B0
7883	042030	012737	001720	005410		MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1
7884	042036	012737	000001	005412		MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1
7885	042044	005037	005414			CLR	E.A2	;EXPECTED MSG A2
7886	042050	012737	000002	005416		MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2
7887	042056	012737	000003	005422		MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
7888								
7889	042064	004737	044274			JSR	PC,CHKMSG	;CHECK MSGS A0, B0, A1, B1
7890	042070	000000				.WORD	C!0!0	& MSGS SPECIFIED HERE
7891	042072	104161				ERROR	161	;MSG A0 ERROR AFTER SEEK CMD
7892	042074	104162				ERROR	162	;MSG B0 ERROR
7893	042076	104163				ERROR	163	;MSG A1 ERROR
7894	042100	104164				ERROR	164	;MSG B1 ERROR
7895	042102	013737	001416	001346		MOV	WD2,TOCYL	;SETUP TO SEE IF ON CORRECT CYL
7896	042110	013765	001416	000020		MOV	WD2,RKDC(R5)	
7897	042116	013765	001222	000010		MOV	\$UNIT,RKCS2(R5)	
7898	042124	063765	005464	000010		ADD	UNITB,RKCS2(R5)	

7899								
7900								
7901	042132	012700	001666		MOV	#RHTAB,RO		
7902	042136	012737	000025	005314	MOV	#<RDHEAD>,HCS1		
7903	042144	004737	043430		JSR	PC, DATCMD		;DO DATA X FOR CMD & GET CONTR RDY
7904	042150	104171			ERROR	171		;NO RDY AFTER READ HEADER CMD
7905	042152	032737	100000	005314	BIT	#CERR,HCS1		
7906	042160	001401			BEG	70\$		
7907	042162	104174			ERROR	174		;CERR AFTER READ HEADER CMD
7908								
7909	042164	016520	000024		70\$:	MOV	RKDB(R5),(RO)+	;1'ST WORD FROM SILO TO RHTAB
7910	042170	016520	000024			MOV	RKDB(R5),(RO)+	;2'ND WORD
7911	042174	016520	000024			MOV	RKDB(R5),(RO)+	;3'RD WORD
7912								
7913								
7914	042200	032765	100000	000010	BIT	#DLT,RKCS2(R5)		
7915	042206	001403			BEG	71\$		
7916	042210	004737	045146		JSR	PC,GSTAT		
7917	042214	104173			ERROR	173		;DLT AFTER READ HEADER CMD
7918	042216				71\$:			
7919								
7920	042216	012737	010340	005404	MOV	#<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0		;EXPECTED MSG A0
7921	042224	005037	005406		CLR	E.B0		;EXPECTED MSG B0
7922	042230	012737	001720	005410	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1		;EXPECTED A1
7923	042236	012737	000001	005412	MOV	#1,E.B1		;MSG ID FOR EXPECTED MSG B1
7924	042244	005037	005414		CLR	E.A2		;EXPECTED MSG A2
7925	042250	012737	000002	005416	MOV	#2,E.B2		;MSG ID FOR EXPECTED MSG B2
7926	042256	012737	000003	005422	MOV	#3,E.B3		;MSG ID FOR EXPECTED MSG B3
7927								
7928	042264	004737	044274		JSR	PC,CHKMSG		;CHECK MSGS A0, B0, A1, B1
7929	042270	000003			.WORD	T.A2!T.B2!0		& MSGS SPECIFIED HERE
7930	042272	104301			ERROR	301		;MSG A0 ERROR AFTER READ HEADER CMD
7931	042274	104271			ERROR	271		;MSG B0 ERROR
7932	042276	104302			ERROR	302		;MSG A1 ERROR
7933	042300	104272			ERROR	272		;MSG B1 ERROR
7934								
7935	042302	023737	001666	001346	CMP	RHTAB,TOCYL		;CHECK WORD 0 ONLY, CYL#
7936	042310	001401			BEG	69\$;BR IF SAME
7937	042312	104051			ERROR	51		;WRONG CYL# ON HEADER
7938	042314				69\$:			
7939								
7940	042314	004737	045534		JSR	PC,SUBCLR		
7941	042320	104024			ERROR	24		;CERR AFTER SCLR
7942								
7943	042322	005237	001414		INC	WD1		;CONVERGE THE CYLINDERS
7944	042326	005337	001416		DEC	WD2		
7945	042332	023737	001414	001416	CMP	WD1,WD2		;SEE IF CONVERGED TOGETHER
7946	042340	001063			BNE	9\$;BR IF NO & REPEAT
7947	042342	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)		
7948	042350	063765	005464	000010	ADD	UNITB,RKCS2(R5)		
7949								
7950	042356	012737	000017	005314	MOV	#SEEK,HCS1		
7951	042364	004737	043372		JSR	PC,DOCMD		;DO SEEK CMD & GET CONTR READY
7952	042370	104131			ERROR	131		;NO RDY AFTER SEEK CMD
7953								
7954	042372	013737	001412	005352	MOV	T50000,TEMP1		;SETUP TIMEOUT

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7955 042400 004737 044106 JSR PC,FATT2 ;FIND ATTN
7956 042404 104132 ERROR 132 ;NO ATTN AFTER SEEK CMD
7957
7958 042406 032737 100000 005314 BIT #CERR,HCS1
7959 042414 001401 BEQ 72$
7960 042416 104210 ERROR 210 ;CERR AFTER SEEK CMD
7961
7962 042420 72$:
7963
7964 042420 012737 050340 005404 MOV #<D.DSC!D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
7965 042426 005037 005406 CLR E.B0 ;EXPECTED MSG B0
7966 042432 012737 001720 005410 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
7967 042440 012737 000001 005412 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
7968 042446 005037 005414 CLR E.A2 ;EXPECTED MSG A2
7969 042452 012737 000002 005416 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
7970 042460 012737 000003 005422 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
7971
7972 042466 004737 044274 JSR PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
7973 042472 000000 .WORD 0!0!0 ;& MSGS SPECIFIED HERE
7974 042474 104161 ERROR 161 ;MSG A0 ERROR AFTER SEEK CMD
7975 042476 104162 ERROR 162 ;MSG B0 ERROR
7976 042500 104163 ERROR 163 ;MSG A1 ERROR
7977 042502 104164 ERROR 164 ;MSG B1 ERROR
7978
7979 042504 000137 042644 JMP 11$ ;EXIT TEST
7980 042510 9$:
7981 042510 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT B
7982 042516 012737 000001 005464 MOV #1,UNITB
7983 042524 063765 005464 000010 ADD UNITB,RKCS2(R5)
7984 042532 112737 000102 056560 MOVB #'B,MSG19A
7985 042540 062765 000010 000010 ADD #RLS,RKCS2(R5) ;RELEASE PORT B
7986 042546 012737 000001 005314 MOV #SELDRV,HCS1
7987 042554 004737 043372 JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
7988 042560 104117 ERROR 117 ;NO RDY AFTER SEL DRV CMD
7989
7990 042562 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;SETUP FOR PORT A
7991 042570 012737 000000 005464 MOV #0,UNITB
7992 042576 063765 005464 000010 ADD UNITB,RKCS2(R5)
7993 042604 112737 000101 056560 MOVB #'A,MSG19A
7994 042612 012737 000001 005314 MOV #SELDRV,HCS1
7995 042620 004737 043372 JSR PC,DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
7996 042624 104117 ERROR 117 ;NO RKY AFTER SEL DRV CMD
7997
7998 042626 032737 000040 005342 BIT #D.DRA,HMR2 ;SEE IF DRIVE AVAIL ON PORT A
7999 042634 001001 BNE 73$ ;BR IF YES
8000 042636 104071 ERROR 71 ;PORT A NOT AVAIL AFTER PORT B RLS
8001 042640 73$:
8002 042640 000137 040746 JMP 10$
8003
8004 042644 11$:
8005
8006
8007
8008
8009

```

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8010 .SBTTL END OF PASS ROUTINE
8011
8012 ;*****
8013 ;*INCREMENT THE PASS NUMBER ($PASS)
8014 ;*TYPE "END PASS #XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)
8015 ;*IF THERES A MONITOR GO TO IT
8016 ;*IF THERE ISN'T JUMP TO STS
8017
8018 $EOP:
8019
8020 042644 000004 SCOPE
8021 042646 005037 001176 CLR $ESCAPE
8022 042652 012737 000001 001174 MOV #1,$TIMES
8023 042660 012706 001100 MOV #STACK,SP
8024 042664 005237 001220 INC $DEVCT ; INCR COUNT FOR # DRIVES CHECKED
8025 042670 023737 005434 001220 CMP DRIVS,$DEVCT ; ARE ALL DRIVES PRESENT TESTED?
8026 042676 001403 BEQ $EOP1+2 ; BR IF YES
8027 042700 000137 012472 JMP NUDRV ; ELSE TEST NEXT DRIVE PRESENT
8028 042704 000004 $EOP1: SCOPE
8029 042706 005037 001102 CLR $STNM ; ZERO THE TEST NUMBER
8030 042712 005037 001174 CLR $TIMES ; ZERO THE NUMBER OF ITERATIONS
8031 042716 005237 001216 INC $PASS ; INCREMENT THE PASS NUMBER
8032 042722 042737 100000 001216 BIC #100000,$PASS ; DON'T ALLOW A NEG. NUMBER
8033 042730 005327 DEC (PC)+ ; LOOP?
8034 042732 000001 $EOPCT: .WORD 1
8035 042734 003022 BGT $DOAGN ; YES
8036 042736 012737 MOV (PC)+,a(PC)+ ; RESTORE COUNTER
8037 042740 000001 $ENDCT: .WORD 1
8038 042742 042732 $EOPCT
8039 042744 104401 043011 TYPE $SENDMG ; TYPE "END PASS #"
8040 042750 013746 001216 MOV $PASS,-(SP) ; SAVE $PASS FOR TYPEOUT
8041 042754 104405 TYPDS ; GO TYPE--DECIMAL ASCII WITH SIGN
8042 042756 104401 043006 TYPE $ENULL ; TYPE A NULL CHARACTER
8043 042762 013700 000042 $GET42: MOV a#42,RO ; GET MONITOR ADDRESS
8044 042766 001405 BEQ $DOAGN ; BRANCH IF NO MONITOR
8045 042770 000005 RESET ; CLEAR THE WORLD
8046 042772 004710 $ENDAD: JSR PC,(RO) ; GO TO MONITOR
8047 042774 000240 NOP ; SAVE ROOM
8048 042776 000240 NOP ; FOR
8049 043000 000240 NOP ; ACT11
8050 043002 $DOAGN:
8051 043002 000137 JMP a(PC)+ ; RETURN
8052 043004 010646 $RTNAD: .WORD STS
8053 043006 377 377 000 $ENULL: .BYTE -1,-1,0 ; NULL CHARACTER STRING
8054 043011 015 042412 042116 $ENDMG: .ASCIZ <15><12>/END PASS #/
8055 043016 050040 051501 020123
8056 043024 000043

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SUBROUTINES
.SBTTL SUBROUTINES
;SUBROUTINE TO CLEAR ALL FLAGS FROM DDUMP THRU UNITB
;
8057 CLRFLG: MOV #DDUMP,R0
8058 MOV #-17.,R1
8059 1$: CLR (R0)+
8060 INC R1
8061 BNE 1$
8062 RTS PC
8063
8064 ;TYPE PROGRAM ID IF FTITLE=0
8065 ;
8066 TITLE: TST FTITLE
8067 BNE 1$
8068 INC FTITLE
8069 TYPE MSG1 ;PROGRAM ID
8070 .SBTTL GET VALUE FOR SOFTWARE SWITCH REGISTER
8071 TST 0#42 ;ARE WE RUNNING UNDER XXDP/ACT?
8072 BNE 64$ ;BRANCH IF YES
8073 CMPB $ENV,#1 ;ARE WE RUNNING UNDER APT?
8074 BEQ 64$ ;BRANCH IF YES
8075 CMP SWR,#SWREG ;SOFTWARE SWITCH REG SELECTED?
8076 BNE 65$ ;BRANCH IF NO
8077 GTSWR ;GET SOFT-SWR SETTINGS
8078 BR 65$
8079 64$: MOV #1,$AUTOB ;SET AUTO-MODE INDICATOR
8080 65$:
8081 1$: RTS PC
8082
8083 ;ROUTINE TO INPUT DRIVE NOS. TYPED IN & SET
8084 ;DRIVS, DRIVO-DRIV7 REGISTERS APPROPRIATELY
8085 ;ONLY EVEN NUMBERS ALLOWED (0,2,4,6)
8086 ;
8087 GDRVS: RDLIN
8088 MOV (SP)+,R0 ;GET STARTING ADDR OF ASCII STRING
8089 MOV #-4,R1 ;SET UP COUNT
8090 1$: MOV (R0)+,R2 ;GET ASCII CHAR
8091 BIC #177400,R2 ;MASK HI BYTE
8092 MOV #DRIVO,R3 ;DRIVE FLAG ADDR
8093 MOV #60,R4
8094 2$: CMP R4,R2 ;WAS TYPED CHAR 0 THRU 6?
8095 BEQ 3$ ;BRANCH IF YES
8096 TST (R3)+ ;NO, INCREMENT DR FLAG ADDR
8097 INC R4
8098 CMP R4,#67
8099 BNE 2$ ;S/B 0-6 OR TERMINATOR
8100 TST R2 ;TERMINATOR=0
8101 BNE 4$
8102 CMP R1,#-4
8103
8104 043026 012700 005424
8105 043032 012701 177757
8106 043036 005020
8107 043040 005201
8108 043042 001375
8109 043044 000207
8110
8111 043046 005737 001340
8112 043052 001024
8113 043054 005237 001340
8114 043060 104401 054566
8115 043064 005737 000042
8116 043070 001012
8117 043072 123727 001230 000001
8118 043100 001406
8119 043102 023727 001140 000176
8120 043110 001005
8121 043112 104406
8122 043114 000403
8123 043116 112737 000001 001134
8124 043124
8125 043124 000207

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8113 043202 001431          BEQ      6$          ;DEFAULT ALL DRIVES
8114 043204 005037 005462  7$:     CLR      SIZFLG  ;BYPASS TEST 1 (SIZING)
8115 043210 000207          RTS      PC          ;FOUND TERMINATOR, EXIT
8116
8117 043212 032704 000001  3$:     BIT      #BIT0,R4 ;SEE IF ODD #
8118 043216 001014          BNE      4$          ;
8119 043220 005213          INC      3R3         ;SET UP FLAG FOR THE DRIVE
8120 043222 005237 005434  INC      DRIVS      ;INCREMENT TOTAL # DRIVES TO BE TESTED
8121 043226 112002          MOVA    (R0)+,R2    ;GET NEXT ASCII CHAR.
8122 043230 042702 177400  BIC     #177400,R2  ;MASK
8123 043234 022702 000054  CMP     #54,R2     ;IS IT A COMMA?
8124 043240 001407          BEQ     5$          ;YES, GO TO NEXT WORD.
8125 043242 005702          TST     R2         ;NO, IS IT A TERMINATOR?
8126 043244 001001          BNE     4$          ;IF NOT, SOMETHING WRONG.
8127 043246 000756          BR     7$          ;FOUND TERMINATOR, EXIT
8128
8129 043250 104401 057036  4$:     TYPE    EMI     ;ONLY 0,2,4,6 ALLOWED.
8130 043254 000137 010054  JMP     PRGSRT     ;START ALL OVER
8131
8132 043260 005201          INC     R1         ;S/B NO MORE THAN 4 DIFF
8133 043262 001325          BNE     1$        ;DRIVES TYPED IN.
8134 043264 000771          BR     4$        ;IF NORE, HAVE ERROR.
8135
8136 043266 005237 005462  6$:     INC     SIZFLG  ;DO TEST 1 (SIZING)
8137 043272 000207          RTS     PC        ;EXIT.
8138
8139
8140 ;ROUTINE TO INPUT RKBAS OR DEFAULT.
8141 ;
8142
8143 043274 104412          GBA:    RDOCT
8144 043276 012600          MOV     (SP)+,R0   ;GET LOW ORDER FROM STACK
8145 043300 005700          TST     R0
8146 043302 001403          BEQ     1$        ;BRANCH IF DEFAULT.
8147 043304 010037 001264  MOV     R0,$BASE
8148 043310 000207          RTS     PC
8149 043312 012737 177440 001264 1$:     MOV     #177440,$BASE ;DEFAULT VALUE
8150 043320 000207          RTS     PC
8151
8152
8153 ;ROUTINE TO INPUT RKVEC OR DEFAULT
8154 ;
8155
8156 043322 104412          GINT:   RDOCT
8157 043324 012600          MOV     (SP)+,R0   ;GET LOW ORDER FROM STACK
8158 043326 005700          TST     R0
8159 043330 001405          BEQ     1$        ;BRANCH IF DEFAULT
8160 043332 010037 001314  MOV     R0,RKVEC
8161 043336 004737 043354  2$:     JSR     PC,SETINT
8162 043342 000207          RTS     PC
8163 043344 012737 000210 001314 1$:     MOV     #210,RKVEC  ;DEFAULT VALUE
8164 043352 000771          BR     2$
8165
8166
8167 ;ROUTINE TO SETUP INTERRUPT VECTOR & PRIORITY
8168 ;

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8169
8170 043354 013700 001314
8171 043360 012720 050066
8172 043364 013710 001316
8173 043370 000207
8174
8175
8176
8177
8178
8179 043372 053737 001170 005314
8180 043400 013765 005314 000000
8181 043406 013737 001400 005352
8182 043414 004737 043466
8183 043420 000207
8184 043422 062716 000002
8185 043426 000207
8186
8187
8188
8189
8190 043430 053737 001170 005314
8191 043436 013765 005314 000000
8192 043444 013737 001412 005352
8193 043452 004737 043466
8194 043456 000207
8195 043460 062716 000002
8196 043464 000207
8197
8198
8199
8200
8201
8202
8203
8204
8205 043466 032765 000200 000000
8206 043474 001010
8207 043476 005337 005352
8208 043502 001371
8209 043504 004737 043622
8210 043510 004737 045064
8211 043514 000207
8212 043516 062716 000002
8213 043522 004737 043622
8214 043526 004737 045064
8215 043532 000207
8216
8217
8218
8219 043534 032765 000200 000000
8220 043542 001014
8221 043544 005337 005352
8222 043550 001371
8223 043552 016537 000034 005342
8224 043560 016537 000036 005344

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SETINT: MOV      RKVEC, R0
        MOV      #INTER, (R0)+ ; INTER ADDR TO RKVEC
        MOV      RKPRI, (R0)  ; PRS TO RKVEC+2
        RTS      PC

; THIS ROUTINE SETS CDT IN RKCS1 IF DRIVE UNDER TEST IS AN RK07.
; ENTER WITH COMMAND IN HCS1
DOCMD:  BIS      $TMP4, HCS1 ; ADD CDT IF RK07
        MOV      HCS1, RKCS1(R5) ; DO COMMAND
        MOV      T10, TEMP1
        JSR      PC, FRDY ; FIND CONTR READY
        RTS ; SET HERE IF NOT RDY
        ADD      #2, (SP) ; ELSE SKIP OVER ERROR
        RTS      PC

; THIS ROUTINE IS SIMILAR TO THE ABOVE BUT IS USED FOR DATA TRANSFERS
; & REQUIRES A LONGER TIMEOUT
DATCMD: BIS      $TMP4, HCS1 ; ADD CDT IF RK07
        MOV      HCS1, RKCS1(R5) ; DO CMD
        MOV      T50000, TEMP1
        JSR      PC, FRDY ; FIND CONTR RDY
        RTS
        ADD      #2, (SP)
        RTS      PC

; ROUTINE TO FIND CONTROLLER READY (RDY) DURING A DELAY
; ENTER WITH A COUNT IN TEMP1
; RETURN IF RDY NOT PRESENT (ERROR CONDITION)
; RETURN +2 IF RDY PRESENT (SKIP OVER ERROR)
; STATUS IS OBTAINED BEFORE THE RETURN FOR EITHER CASE
FRDY:   BIT      #RDY, RKCS1(R5)
        BNE      1$
        DEC      TEMP1
        BNE      FRDY
        JSR      PC, HOLD ; STORE ALL RK611 REGS IN HOLDING REGS.
        JSR      PC, CKCERR ; CHECK FOR SPECIAL CERR
        RTS      PC ; NO RDY, EXIT
1$:     ADD      #2, (SP) ; SKIP OVER ERROR
        JSR      PC, HOLD
        JSR      PC, CKCERR ; CHECK FOR SPECIAL CERR
        RTS      PC

; ROUTINE TO FIND CONTROLLER READY AND STORE DRIVE REGS ONLY
FRDY1:  BIT      #RDY, RKCS1(R5)
        BNE      1$
        DEC      TEMP1
        BNE      FRDY1
        MOV      RKMR2(R5), HMR2
        MOV      RKMR3(R5), HMR3

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8225 043566 004737 045064          JSR    PC,CKCERR          ;CHECK FOR SPECIAL CERR CONDITIONS
8226 043572 000207                   RTS    PC                  ;NO RDY, EXIT
8227 043574 062716 000002          1$:   ADD    #2,(SP)       ;SKIP OVER ERROR
8228 043600 016537 000034          005342 MOV    RKMR2(R5),HMR2
8229 043606 016537 000036          005344 MOV    RKMR3(R5),HMR3
8230 043614 004737 045064          JSR    PC,CKCERR          ;CHECK FOR SPECIAL CERR CONDITIONS
8231 043620 000207                   RTS    PC
8232
8233
8234
8235
8236
8237 043622 016537 000000          005314 HOLD:  MOV    RKCS1(R5),HCS1
8238 043630 016537 000010          005316        MOV    RKCS2(R5),HCS2
8239 043636 016537 000002          005320        MOV    RKWC(R5),HWC
8240 043644 016537 000004          005322        MOV    RKBA(R5),HBA
8241 043652 016537 000006          005324        MOV    RKDA(R5),HDA
8242 043660 016537 000012          005326        MOV    RKDS(R5),HDS
8243 043666 016537 000014          005330        MOV    RKER(R5),HER
8244 043674 016537 000016          005332        MOV    RKASOF(R5),HASOF
8245 043702 016537 000020          005334        MOV    RKDC(R5),HDC
8246 043710 016537 000026          005340        MOV    RKMR1(R5),HMR1
8247 043716 016537 000034          005342        MOV    RKMR2(R5),HMR2
8248 043724 016537 000036          005344        MOV    RKMR3(R5),HMR3
8249 043732 016537 000030          005346        MOV    RKECPS(R5),HPOS
8250 043740 016537 000032          005350        MOV    RKECPT(R5),HPAT
8251 043746 000207                   RTS    PC
8252
8253
8254
8255
8256
8257
8258 043750 010446                   ;ROUTINE TO CHECK FOR CORRECT ATTN
8259 043752 013704 001222                   ;RETURN IF ATTN NOT PRESENT (ERROR CONDITION)
8260 043756 063704 005464                   ;RETURN +2 IF ATTN PRESENT (SKIP OVER ERROR)
8261 043762 136437 005304          005333 †STATN: MOV    R4,-(SP)          ;SAV R4
8262 043770 001404                   MOV    $UNIT,R4
8263 043772 012604                   ADD    UNITB,R4          ;ADD 1 IF ON PORT B
8264 043774 062716 000002          BITB   ATTN(R4),HASOF+1 ;BRANCH IF ATTN NOT PRESENT
8265 044000 000207                   BEQ    1$                ;RESTOR R4
8266 044002 012604                   MOV    (SP)+,R4
8267 044004 000207                   ADD    #2,(SP)          ;INCR RET ADDR TO JUMP OVER ERROR.
8268
8269
8270
8271
8272
8273
8274
8275
8276
8277 044006 010446                   1$:   MOV    (SP)+,R4          ;RESTOR R4
8278 044010 012737 177777          005352 FATT1: MOV    R4,-(SP)          ;SAV R4
8279 044016 013704 001222          3$:   MOV    #-1,TEMP1
8280 044022 063704 005464                   MOV    $UNIT,R4
                                           ADD    UNITB,R4          ;ADD 1 IF ON PORT B

```

```

8281 044026 136465 005304 000017 1$: BITB ATTN(R4),RKASOF+1(R5) ;FIND CORRECT ATTN
8282 044034 001014 BNE 2$
8283 044036 005337 005352 DEC TEMP1
8284 044042 001371 BNE 1$
8285 044044 005337 005354 DEC TEMP2
8286 044050 001357 BNE 3$
8287 044052 005065 000026 CLR RKMR1(R5) ;SELECT WORD 0
8288 044056 004737 045146 JSR PC,GSTAT ;GET LATEST STATUS
8289 044062 012604 MOV (SP)+,R4 ;RESTOR R4
8290 044064 000207 RTS PC
8291 044066 005065 000026 2$: CLR RKMR1(R5)
8292 044072 004737 045146 JSR PC,GSTAT ;GET STATUS AFTER ATTN SEEN
8293 044076 012604 MOV (SP)+,R4 ;RESTOR R4
8294 044100 062716 000002 ADD #2,(SP) ;SKIP OVER ERROR
8295 044104 000207 RTS PC

```

```

; ROUTINE TO FIND ATTN WITHIN 1 SEC
; ENTER WITH COUNT IN TEMP1
; RETURN IF NO ATTN (ERROR)
; RETURN +2 IF ATTN FOUND
; STATUS IS OBTAINED BEFORE THE RETURN FOR EITHER CASE

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8305 044106 010446 FATT2: MOV R4, -(SP) ;SAV R4
8306 044110 013704 001222 2$: MOV $UNIT,R4
8307 044114 063704 005464 ADD UNITB,R4 ;ADD 1 IF ON PORT B
8308 044120 136465 005304 000017 BITB ATTN(R4),RKASOF+1(R5) ;FIND CORRECT ATTN
8309 044126 001011 BNE 1$
8310 044130 005337 005352 DEC TEMP1
8311 044134 001365 BNE 2$
8312 044136 005065 000026 CLR RKMR1(R5) ;SELECT WORD 0
8313 044142 004737 045146 JSR PC,GSTAT ;GET LATEST STATUS.
8314 044146 012604 MOV (SP)+,R4 ;RESTOR R4
8315 044150 000207 RTS PC
8316 044152 005065 000026 1$: CLR RKMR1(R5)
8317 044156 004737 045146 JSR PC,GSTAT ;RESTOR R4
8318 044162 012604 MOV (SP)+,R4 ;SKIP OVER ERROR
8319 044164 062716 000002 ADD #2,(SP)
8320 044170 000207 RTS PC

```

```

; THIS ROUTINE CHECKS 'DRIVE AVAILABLE' IN MSG AO AFTER A SELECT COMMAND
; IF NOT SET, IT DOES A NORMAL RETURN
; IF SET, IT DOES A RETURN+2

```

```

8326 044172 004737 045146 005342 DRAY: JSR PC,GSTAT
8327 044176 032737 000040 BIT #D.DRA,HMR2 ;SEE IF DRIVE AVAILABLE SET
8328 044204 001402 BEQ 1$ ;BR IF NO & RETURN
8329 044206 062716 000002 ADD #2,(SP) ;ELSE RETURN+2
8330 044212 000207 1$: RTS PC

```

```

; THIS ROUTINE LOOKS FOR ATTN
; RETURN+2 IF FOUND BEFORE COUNT=0
; RETURN IF COUNT=0 (ERROR CONDITION)

```

8331
8332
8333
8334
8335
8336


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8337 044214 136465 005304 000017 FATT3: BITB ATTN(R4),RKASOF+1(R5) ;TEST FOR ATTN
8338 044222 001004 BNE 1$
8339 044224 005737 001366 TST COUNT ;SEE IF TIME UP
8340 044230 001371 BNE FATT3 ;BR IF NO
8341 044232 000207 RTS PC
8342 044234 062716 000002 1$: ADD #2,(SP) ;JUMP OVER ERROR
8343 044240 000207 RTS PC
8344
8345
8346 ;ENTER WITH A COUNT IN TEMP1
8347 ;THE DELAY IS APPROX 17 US/ITERATION + 12 US TO EXIT
8348 ;WHEN COUNT IS 0. BASED ON AN 11/05
8349
8350 044242 005737 005352 DLY: TST TEMP1 ;5.6 US
8351 044246 001403 BEQ 1$ ;2.5 US
8352 044250 005337 005352 DEC TEMP1 ;6.8 US
8353 044254 000772 BR DLY ;2.5 US
8354 044256 000207 1$: RTS PC ;3.8 US
8355
8356 ;THIS ROUTINE TYPES BYPASSED DRIVE#. ENTER WITH DRIVE# IN RO
8357
8358
8359 044260 104401 056437 BYP: TYPE MSG14 ;BYPASS DRIVE
8360 044264 010046 MOV RO,-(SP) ;SAVE RO FOR TYPEOUT
8361 ;TYPE DR#
8362 044266 104403 TYPOS ;GO TYPE--OCTAL ASCII
8363 044270 001 .BYTE 1 ;TYPE 1 DIGIT(S)
8364 044271 000 .BYTE 0 ;SUPPRESS LEADING ZEROS
8365 044272 000207 RTS PC
8366
8367 ;THIS ROUTINE READS ALL MSG A & B WORDS & CHECKS THEM AS REQ'D.
8368
8369 044274 017437 000000 001460 CHKMSG: MOV @ (SP),CHKFLG ;PASS MSGS TO BE TESTED
8370 044302 062716 000002 ADD #2,(SP) ;BUMP RETURN ADDR TO 1ST ERROR
8371 044306 004737 045210 JSR PC,GSTAT1 ;GET ALL ACTUAL DRIVE & CONTR STATUS
8372
8373 044312 053737 001222 005404 BIS $UNIT,E.A0 ;SET UNIT #
8374 044320 063737 005464 005404 ADD UNITB,E.A0 ;ADD 1 IF ON PORT B
8375 044326 053737 001222 005410 BIS $UNIT,E.A1
8376 044334 063737 005464 005410 ADD UNITB,E.A1
8377 044342 053737 001222 005414 BIS $UNIT,E.A2
8378 044350 063737 005464 005414 ADD UNITB,E.A2
8379 044356 053737 001222 005420 BIS $UNIT,E.A3
8380 044364 063737 005464 005420 ADD UNITB,E.A3
8381 044372 053737 012776 005404 BIS E.DOT,E.A0 ;SET CDT IF RK07
8382
8383 044400 013746 005352 MOV TEMP1,-(SP) ;SAVE TEMP1
8384
8385 044404 013737 005404 005352 MOV E.A0,TEMP1
8386 044412 004737 047224 JSR PC,SBPAR ;GET PARITY FOR MSG A0
8387 044416 013737 005352 005404 MOV TEMP1,E.A0
8388
8389 044424 013737 005410 005352 MOV E.A1,TEMP1
8390 044432 004737 047224 JSR PC,SBPAR ;GET PARITY FOR MSG A1
8391 044436 013737 005352 005410 MOV TEMP1,E.A1
8392

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E13

CZR6G80 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46MACY11 30A(1052) 03-JAN-78 08:56 PAGE 160
GET VALUE FOR SOFTWARE SWITCH REGISTER

SEQ 0160

8393	044444	013737	005414	005352		MOV	E.A2,TEMP1	
8394	044452	004737	047224			JSR	PC,S&PAR	;GET PARITY FOR MSG A2
8395	044456	013737	005352	005414		MOV	TEMP1,E.A2	
8396								
8397	044464	013737	005406	005352		MOV	E.B0,TEMP1	
8398	044472	004737	047224			JSR	PC,S&PAR	;GET PARITY FOR MSG B0
8399	044476	013737	005352	005406		MOV	TEMP1,E.B0	
8400								
8401	044504	013737	005412	005352		MOV	E.B1,TEMP1	
8402	044512	004737	047224			JSR	PC,S&PAR	;GET PARITY FOR MSG B1
8403	044516	013737	005352	005412		MOV	TEMP1,E.B1	
8404								
8405	044524	013737	005416	005352		MOV	E.B2,TEMP1	
8406	044532	004737	047224			JSR	PC,S&PAR	;GET PARITY FOR MSG B2
8407	044536	013737	005352	005416		MOV	TEMP1,E.B2	
8408								
8409	044544	013737	005422	005352		MOV	E.B3,TEMP1	
8410	044552	004737	047224			JSR	PC,S&PAR	;GET PARITY FOR MSG B3
8411	044556	013737	005352	005422		MOV	TEMP1,E.B3	
8412								
8413	044564	012637	005352			MOV	(SP)+,TEMP1	;RESTORE TEMP1
8414	044570	013737	001176	001176		MOV	\$ESCAPE,\$TMP5	;SAVE ESCAPE
8415								
8416	044576	023737	005364	005404		CMP	H.A0,E.A0	;TEST MSG A0
8417	044604	001411				BEQ	2\$;BR IF OK
8418	044606	012737	044620	001176		MOV	#1\$,\$ESCAPE	;ELSE SETUP ESCAPE
8419	044614	011646				MOV	(SP),-(SP)	;COPY RET ADDR.
8420	044616	000207				RTS	PC	; & RETURN TO MAINLINE ERROR
8421								
8422	044620	032777	001000	134312	1\$:	BIT	#SW9,@SWR	;RET HERE FROM MAINLINE ERROR
8423	044626	001107				BNE	20\$; & BR IF LOOP ON ERROR
8424	044630	062716	000002		2\$:	ADD	#2,(SP)	;BUMP RET ADDR TO NEXT ERROR
8425								
8426	044634	023737	005366	005406		CMP	H.B0,E.B0	;TEST MSG B0
8427	044642	001411				BEQ	5\$;BR IF OK
8428	044644	012737	044656	001176		MOV	#4\$,\$ESCAPE	;ELSE SETUP ESCAPE
8429	044652	011646				MOV	(SP),-(SP)	;COPY RET ADDR
8430	044654	000207				RTS	PC	; & RETURN TO MAINLINE ERROR
8431								
8432	044656	032777	001000	134254	4\$:	BIT	#SW9,@SWR	;RETURN HERE FROM MAINLINE ERROR
8433	044664	001070				BNE	20\$; & BR IF LOOP ON ERROR
8434	044666	062716	000002		5\$:	ADD	#2,(SP)	;BUMP RET ADDR TO NEXT ERROR
8435								
8436	044672	023737	005370	005410		CMP	H.A1,E.A1	;TEST MSG A1
8437	044700	001411				BEQ	8\$;BR IF OK
8438	044702	012737	044714	001176		MOV	#7\$,\$ESCAPE	
8439	044710	011646				MOV	(SP),-(SP)	
8440	044712	000207				RTS	PC	
8441								
8442	044714	032777	001000	134216	7\$:	BIT	#SW9,@SWR	
8443	044722	001051				BNE	20\$	
8444	044724	062716	000002		8\$:	ADD	#2,(SP)	
8445								
8446	044730	023737	005372	005412		CMP	H.B1,E.B1	;TEST MSG B1
8447	044736	001411				BEQ	11\$;BR IF OK
8448	044740	012737	044752	001176		MOV	#10\$,\$ESCAPE	

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8449 044746 011646          MOV      (SP),-(SP)
8450 044750 000207          RTS      PC
8451
8452 044752 032777 001000 134160 10$:  BIT      #SW9,@SWR          ;TEST MSG A2?
8453 044760 001032          BNE     20$                ;BR IF NO
8454 044762 062716 000002          ADD     #2,(SP)           ;PUT INFO CYLDIF, DO NOT CHECK
8455
8456 044766 032737 000001 001460 12$:  BIT      #T.A2,CHKFLG      ;TEST MSG A2?
8457 044774 001402          BEQ     13$                ;BR IF NO
8458 044776 004737 046026          JSR     PC,RCYLD          ;PUT INFO CYLDIF, DO NOT CHECK
8459 045002 032737 000002 001460 13$:  BIT      #T.B2,CHKFLG      ;TEST MSG B2?
8460 045010 001402          BEQ     14$                ;BR IF NO
8461 045012 004737 046100          JSR     PC,RCYLA         ;PUT INFO IN CYLADD, DO NOT CHECK
8462
8463 045016 032737 000004 001460 14$:  BIT      #T.B3,CHKFLG      ;TEST MSG B3?
8464 045024 001404          BEQ     15$                ;BR IF NO
8465 045026 004737 046136          JSR     PC,RSEC          ;PUT INFO IN SECTOR, DO NOT CHECK
8466 045032 004737 046174          JSR     PC,RHEAD        ;PUT INFO IN HEADA, DO NOT CHECK
8467
8468 045036 013737 001172 001176 15$:  MOV     $TMP5,$ESCAPE     ;RESTORE
8469 045044 000207          RTS      PC
8470
8471 045046 012706 001100 001176 20$:  MOV     #STACK,SP        ;RESET STACK PTR
8472 045052 013737 001172          MOV     $TMP5,$ESCAPE     ;RESTORE
8473 045060 000177 134024          JMP     @SLPERR
8474
8475          ; THIS ROUTINE CHECKS FOR CERTAIN ERROR CONDITIONS ONLY
8476          ; I.E.: IF NED, CTO OR MDS SET MESSAGE A & B ARE INVALID
8477
8478 045064 005737 001456          CKCERR: TST     BYPCERR
8479 045070 001025          BNE     4$
8480 045072 032737 100000 005314          BIT     #CERR,HCS1
8481 045100 001001          BNE     1$                ;BR IF CERR
8482 045102 000207          RTS      PC
8483
8484 045104 032737 004000 005314 1$:  BIT     #CTO,HCS1
8485 045112 001402          BEQ     2$                ;BR IF NOT CTO
8486 045114 104125          ERROR   125              ;CTO ERROR, MSG A & B INVALID
8487 045116 000207          RTS      PC
8488
8489 045120 032737 010000 005316 2$:  BIT     #NED,HCS2
8490 045126 001401          BEQ     3$                ;BR IF NOT NED
8491 045130 104126          ERROR   126              ;NED ERROR, MSG A & B INVALID
8492
8493 045132 032737 001000 005316 3$:  BIT     #MDS,HCS2
8494 045140 001401          BEQ     4$                ;MDS ERROR, MSG A & B INVALID
8495 045142 104127          ERROR   127
8496
8497 045144 000207          4$:    RTS      PC
8498
8499          ; THIS ROUTINE DOES THE SELECT DRIVE COMMAND TO GET STATUS
8500          ; IT THEN WAITS FOR CONTROLLER READY
8501          ; IF RDY NOT RECEIVED BY THE TIMEOUT, AN ERROR IS FLAGGED
8502
8503
8504

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8505 045146 013746 005352
8506 045152 013765 001222 000010
8507 045160 063765 005464 000010
8508 045166 012737 000001 005314
8509 045174 004737 043372
8510 045200 104117
8511 045202 012637 005352
8512 045206 000207
8513
8514
8515
8516
8517 045210 013746 005352
8518 045214 004737 043622
8519 045220 012765 100000 000000
8520 045226 013765 001222 000010
8521 045234 063765 005464 000010
8522 045242 012765 000003 000026
8523 045250 004737 045470
8524 045254 104117
8525 045256 013737 005342 005400
8526 045264 013737 005344 005402
8527
8528 045272 012765 100000 000000
8529 045300 013765 001222 000010
8530 045306 063765 005464 000010
8531 045314 012765 000002 000026
8532 045322 004737 045470
8533 045326 104117
8534 045330 013737 005342 005374
8535 045336 013737 005344 005376
8536
8537 045344 012765 100000 000000
8538 045352 013765 001222 000010
8539 045360 063765 005464 000010
8540 045366 012765 000001 000026
8541 045374 004737 045470
8542 045400 104117
8543 045402 013737 005342 005370
8544 045410 013737 005344 005372
8545
8546 045416 012765 100000 000000
8547 045424 013765 001222 000010
8548 045432 063765 005464 000010
8549 045440 004737 045470
8550 045444 104117
8551 045446 013737 005342 005364
8552 045454 013737 005344 005366
8553
8554 045462 012637 005352
8555 045466 000207
8556
8557 045470 012737 000001 005314
8558 045476 053737 001170 005314
8559 045504 013765 005314 000000
8560 045512 013737 001400 005352

GSTAT: MOV TEMP1, -(SP) ;SAVE TEMP1
MOV $UNIT, RKCS2(R5) ;CURRENT DRIVE #
ADD UNITB, RKCS2(R5) ;ADD 1 IF ON PORT B
MOV #SELDV, HCS1
JSR PC, DOCMD ;DO SELDRV (STATUS) CMD & GET CONTR RDY
ERROR 117 ;RDY NOT SET BY END OF SELECT DRIVE CMD
MOV (SP)+, TEMP1 ;RESTOR TEMP1.
RTS PC

; THIS ROUTINE GETS STATUS OF ALL DRIVE REGISTERS (MSG A0-A3, B0-B3)
; & ALL CONTROLLER REGISTERS.
GSTAT1: MOV TEMP1, -(SP) ;SAVE TEMP1
JSR PC, HOLD ;GET ALL CONTR REG
MOV #CCLR, RKCS1(R5) ;CLEAR CONTR
MOV $UNIT, RKCS2(R5) ;CURRENT DRIVE #
ADD UNITB, RKCS2(R5) ;ADD 1 IF ON PORT B
MOV #3, RKMR1(R5) ;SELECT WORD 3
JSR PC, GSTAT2
ERROR 117 ;RDY NOT SET BY END OF SELECT DRV CMD
MOV HMR2, H.A3 ;STORE MSG A3
MOV HMR3, H.B3 ;STORE MSG B3

MOV #CCLR, RKCS1(R5)
MOV $UNIT, RKCS2(R5)
ADD UNITB, RKCS2(R5) ;ADD 1 IF ON PORT B
MOV #2, RKMR1(R5) ;SELECT WORD 2
JSR PC, GSTAT2
ERROR 117 ;RDY NOT SET BY END OF SELECT DRV CMD
MOV HMR2, H.A2 ;STORE MSG A2
MOV HMR3, H.B2 ;STORE MSG B2

MOV #CCLR, RKCS1(R5)
MOV $UNIT, RKCS2(R5)
ADD UNITB, RKCS2(R5) ;ADD 1 IF ON PORT B
MOV #1, RKMR1(R5) ;SELECT WORD 1
JSR PC, GSTAT2
ERROR 117 ;RDY NOT SET BY END OF SELECT DRV CMD
MOV HMR2, H.A1 ;STORE MSG A1
MOV HMR3, H.B1 ;STORE MSG B1

MOV #CCLR, RKCS1(R5)
MOV $UNIT, RKCS2(R5)
ADD UNITB, RKCS2(R5) ;ADD 1 IF ON PORT B
JSR PC, GSTAT2
ERROR 117 ;RDY NOT SET BY END OF SEL DRV CMD
MOV HMR2, H.A0 ;STORE MSG A0
MOV HMR3, H.B0 ;STORE MSG B0

MOV (SP)+, TEMP1 ;RESTORE TEMP1
RTS PC

GSTAT2: MOV #SELDV, HCS1
BIS $TMP4, HCS1 ;ADD CDT IF RK07
MOV HCS1, RKCS1(R5) ;GET STATUS
MOV T10, TEMP1

```

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8561 045520 004737 043534      JSR    PC,FRDY1      ;FIND CONTR RDY & STORE DRIVE REGS ONLY
8562 045524 000207              RTS    PC            ;RET HERE IF NOT RDY
8563 045526 062716 000002      ADD    #2,(SP)      ;RET HERE IF OK
8564 045532 000207              RTS    PC
8565
8566
8567
8568
8569
8570
8571
8572
8573 045534 012765 000040 000010  SUBCLR: MOV    #SCLR,RKCS2(R5) ;SUBSYS CLEAR
8574 045542 013737 001400 005352      MOV    T10,TEMP1
8575 045550 004737 043466      JSR    PC,FRDY      ;FIND RDY
8576 045554 104120              ERROR 120          ;RDY NOT SET BY END OF SCLR
8577 045556 013765 001222 000010  MOV    $UNIT,RKCS2(R5) ;CURRENT DRIVE #
8578 045564 063765 005464 000010  ADD    UNITB,RKCS2(R5) ;ADD 1 IF ON PORT B
8579 045572 005065 000026      CLR    RKMR1(R5)    ;SELECT WORD 0
8580 045576 004737 045146      JSR    PC,GSTAT     ;GET STATUS
8581 045602 032737 100000 005314  BIT    #CERR,HCS1   ;CHECK FOR CONT ERROR
8582 045610 001401              BEQ    1$
8583 045612 000207              RTS    PC
8584 045614 062716 000002      1$:  ADD    #2,(SP)   ;SKIP OVER ERROR
8585 045620 000207              RTS    PC
8586
8587
8588
8589
8590 045622 012765 000003 000026  RDSEC: MOV    #3,RKMR1(R5) ;WORD 3
8591 045630 004737 045146      JSR    PC,GSTAT
8592 045634 013737 005344 001374  MOV    HMR3,SECTOR
8593 045642 042737 177017 001374  BIC    #C<M.SECT>,SECTOR
8594 045650 006237 001374      ASR    SECTOR       ;RIGHT JUSTIFY
8595 045654 006237 001374      ASR    SECTOR       ;SECTOR
8596 045660 006237 001374      ASR    SECTOR       ;INFO
8597 045664 006237 001374      ASR    SECTOR
8598 045670 000207              RTS    PC
8599
8600
8601
8602 045672 012765 000002 000026  RDICYLD: MOV   #2,RKMR1(R5) ;WORD 2
8603 045700 004737 045146      JSR    PC,GSTAT
8604 045704 013737 005342 001356  MOV    HMR2,CYLDIF
8605 045712 043737 012774 001356  BIC    MASK1,CYLDIF
8606 045720 006237 001356      ASR    CYLDIF       ;RIGHT JUSTIFY
8607 045724 006237 001356      ASR    CYLDIF       ;CYL DIFF/OFFSET
8608 045730 006237 001356      ASR    CYLDIF       ;INFO
8609 045734 006237 001356      ASR    CYLDIF
8610 045740 023737 001356 012772  CMP    CYLDIF,MASK  ;CHK TO SEE IF RET IN COMPL. FORM
8611 045746 001002              BNE    1$           ;BR IF NOT
8612 045750 005037 001356      CLR    CYLDIF      ;CLR IF YES
8613 045754 000207              1$:  RTS    PC
8614
8615
8616
;READ THE CYL ADDR IN RKMR3, RIGHT JUSTIFY IT & STORE IT IN 'CYLADD'

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```

8617
8618 045756 012765 000002 000026 RDCYLA: MOV #2,RKMR1(R5) ;WORD 2
8619 045764 004737 045146 JSR PC,GSTAT
8620 045770 013737 005344 001360 MOV HMR3,CYLADD
8621 045776 043737 012774 001360 BIC MASK1,CYLADD
8622 046004 006237 001360 ASR CYLADD ;RIGHT JUSTIFY
8623 046010 006237 001360 ASR CYLADD ;CYL ADDR
8624 046014 006237 001360 ASR CYLADD ;INFO
8625 046020 006237 001360 ASR CYLADD
8626 046024 000207 RTS PC
8627
8628 ;READ THE CYL DIFF/OFFSET IN H.A2, RIGHT JUSTIFY IT & STORE IT IN 'CYLDIF'
8629
8630 046026 013737 005374 001356 RCYLD: MOV H.A2,CYLDIF
8631 046034 043737 012774 001356 BIC MASK1,CYLDIF ;CLEAR UNWANTED INFO
8632 046042 006237 001356 ASR CYLDIF ;RIGHT JUSTIFY
8633 046046 006237 001356 ASR CYLDIF
8634 046052 006237 001356 ASR CYLDIF
8635 046056 006237 001356 ASR CYLDIF
8636 046062 023737 001356 012772 CMP CYLDIF,MASK ;CHK TO SEE IF RET IN COMPL. FORM
8637 046070 001002 BNE 1$ ;BR IF NO
8638 046072 005037 001356 CLR CYLDIF ;ELSE CLEAR
8639 046076 000207 1$: RTS PC
8640
8641 ;READ THE CYL ADDR IN H.B2, RIGHT JUSTIFY IT & STORE IT IN 'CYLADD'
8642
8643 046100 013737 005376 001360 RCYLA: MOV H.B2,CYLADD
8644 046106 043737 012774 001360 BIC MASK1,CYLADD ;CLEAR UNWANTED INFO
8645 046114 006237 001360 ASR CYLADD ;RIGHT JUSTIFY
8646 046120 006237 001360 ASR CYLADD
8647 046124 006237 001360 ASR CYLADD
8648 046130 006237 001360 ASR CYLADD
8649 046134 000207 RTS PC
8650
8651 ;READ THE SECTOR COUNT IN H.B3, RIGHT JUSTIFY IT & STORE IT IN 'SECTOR'
8652
8653 046136 013737 005402 001374 RSEC: MOV H.B3,SECTOR
8654 046144 042737 177017 001374 BIC #1C<M.SECT>,SECTOR ;CLEAR UNWANTED INFO
8655 046152 006237 001374 ASR SECTOR ;RIGHT JUSTIFY
8656 046156 006237 001374 ASR SECTOR
8657 046162 006237 001374 ASR SECTOR
8658 046166 006237 001374 ASR SECTOR
8659 046172 000207 RTS PC
8660
8661 ;READ THE HEAD ADDR IN H.B3, RIGHT IT & STORE IT IN 'HEADA'
8662
8663 046174 013737 005402 001422 RHEAD: MOV H.B3,HEADA
8664 046202 042737 170777 001422 BIC #1C<M.HEAD>,HEADA ;CLEAR UNWANTED INFO
8665 046210 006237 001422 ASR HEADA ;RIGHT JUSTIFY IT
8666 046214 000337 001422 SWAB HEADA
8667 046220 000207 RTS PC
8668
8669 ;ROUTINE TO FIND HEADS HOME IN RKMR2 WORD 1 BEFORE SPECIFIED DELAY
8670 ;ENTER WITH TIME IN SECONDS IN TEMP2
8671 ;RETURN IF NOT FOUND
8672 ;RETURN+2 IF FOUND - SKIP OVER ERROR

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8673
8674 046222 012737 177777 005352 FHDHM: MOV #-1,TEMP1 ;ALL 1'S
8675 046230 012765 000001 000026 MOV #1,RKMR1(R5) ;WORD 1
8676 046236 004737 045146 JSR PC,GSTAT
8677 046242 032737 000040 005342 BIT #D.HDHM,HMR2
8678 046250 001007 BNE 2$
8679 046252 005337 005352 DEC TEMP1
8680 046256 001367 BNE 1$
8681 046260 005337 005354 DEC TEMP2
8682 046264 001356 BNE FHDHM
8683 046266 000207 RTS PC
8684 046270 062716 000002 2$: ADD #2,(SP) ;SKIP OVER ERROR
8685 046274 000207 RTS PC
8686
8687 ;ROUTINE TO FIND LOAD HEADS IN RKMR2 WORD 1 BEFORE THE TIMEOUT
8688 ;RETURN IF NOT FOUND
8689 ;RETURN+2 IF FOUND: SKIP OVER ERROR
8690
8691 046276 012737 000372 005352 FLOAD: MOV #250,TEMP1
8692 046304 012765 000001 000026 MOV #1,RKMR1(R5) ;WORD 1
8693 046312 004737 045146 JSR PC,GSTAT
8694 046316 032737 010000 005342 BIT #D.LOAD,HMR2
8695 046324 001004 BNE 2$
8696 046326 005337 005352 DEC TEMP1
8697 046332 001367 BNE 1$
8698 046334 000207 RTS PC
8699 046336 062716 000002 2$: ADD #2,(SP) ;SKIP OVER ERROR
8700 046342 000207 RTS PC
8701
8702 ;FILL HEADER TABLE WITH 66 WORDS OF VALID HEADERS
8703 ;ENTER WITH CYL # IN 'CALADD'
8704 ;ENTER WITH HEAD # IN 'HEAD'
8705 ;ENTER WITH FORMAT IN 'FORMAT'
8706
8707 046344 010046 FHDTAB: MOV RO,-(SP) ;SAV RO
8708 046346 010146 MOV R1,-(SP) ;SAV R1
8709 046350 012700 001462 MOV #HDTAB,RO ;HEADER WORD TABLE ADDR
8710 046354 005001 CLR R1 ;SECTOR COUNTER
8711 046356 013737 001420 001424 MOV HEAD,HD1
8712 046364 006337 001424 ASL HD1
8713 046370 006337 001424 ASL HD1
8714 046374 006337 001424 ASL HD1
8715 046400 006337 001424 ASL HD1
8716 046404 006337 001424 ASL HD1 ;SETUP HEAD # FOR WORD 2 OF HEADER
8717 046410 013737 001426 001430 MOV FORMAT,FMT1
8718 046416 000337 001430 SWAB FMT1
8719 046422 006337 001430 ASL FMT1 ;SETUP FORMAT FOR WORD 2 OF HEADER
8720
8721 046426 013720 001362 1$: MOV CALADD,(RO)+ ;HEADER WORD 1-CYL ADDR
8722 046432 010110 MOV R1,(RO) ;HEADER WORD 2-SECTOR NO
8723 046434 053710 001424 BIS HD1,(RO) ;
8724 046440 053710 001430 BIS FMT1,(RO) ; -HEAD NO
; -FORMAT

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K13

CZR6GBD RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 166
GET VALUE FOR SOFTWARE SWITCH REGISTER

SEQ 0166

8725 046444 004737 046524

JSR PC,SECFLG

L13

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 167
GET VALUE FOR SOFTWARE SWITCH REGISTER

SEQ 0167

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8726
8727 046450 013737 001362 005352      MOV    CALADD,TEMP1
8728 046456 011037 005354              MOV    (RO),TEMP2
8729 046462 043737 001362 005354      BIC    CALADD,TEMP2
8730 046470 042037 005352              BIC    (RO)+,TEMP1
8731 046474 053737 005352 005354      BIS    TEMP1,TEMP2
8732 046502 013720 005354              MOV    TEMP2,(RO)+ ;HEADER WORD 3-HEADER CHECK
8733
8734 046506 005201              INC    R1           ;SECTOR CTR
8735 046510 020127 000026      CMP    R1,#22.     ;ALL 22 SECTORS DONE? (66 WORDS)
8736 046514 001344              BNE    1$          ;BR IF NO
8737
8738 046516 012601              MOV    (SP)+,R1    ;RESTOR R1
8739 046520 012600              MOV    (SP)+,R0    ;RESTOR R0
8740 046522 000207              RTS    PC
8741
8742 ; THIS ROUTINE GETS INFORMATION FROM THE BAD SECTOR TABLE FILLED BY A PREVIOUS
8743 ; TEST & SETS BITS 14 & 15 APPROPRIATLY.
8744 ;

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M13

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 168
GET VALUE FOR SOFTWARE SWITCH REGISTER

SEQ 0168

8745 046524 010246
8746 046526 005737 001426
8747 046532 001016
8748
8749 046534 012702 002306
8750 046540 004737 046574
8751 046544 052710 100000
8752
8753 046550 012702 003306
8754 046554 004737 046574
8755 046560 052710 040000
8756
8757 046564 012602
8758 046566 000207
8759
8760 046570 012602
8761 046572 000207
8762
8763
8764
8765
8766
8767
8768
8769 046574 010346
8770
8771 046576 021227 177777
8772 046602 001002
8773 046604 012603
8774 046606 000207
8775
8776 046610 022237 001362
8777 046614 001403
8778 046616 062702 000002
8779 046622 000765
8780
8781 046624 013703 001420
8782 046630 000303
8783 046632 050103
8784 046634 022203
8785
8786 046636 001401
8787 046640 000756
8788
8789 046642 012603
8790 046644 062716 000004
8791 046650 000207
8792
8793
8794
8795
8796
8797 046652 010046
8798 046654 010146
8799 046656 004737 045622
8800 046662 062737 000001 001374

```

SECFLG: MOV R2, -(SP) ;SAVE R2
          TST FORMAT
          BNE 1$ ;BR IF 20 SECTOR FORMAT
;NOTE: ONLY 22 SECTOR PERFORMED
          MOV #BSE22H+8., R2
          JSR PC, FLGTST ;GET HARDWARE DETECTED FLAG
          BIS #BIT15, (R0) ;RETURN HERE IF GOOD SECTOR
          MOV #BSE22S+8., R2 ;ELSE RETURN HERE
          JSR PC, FLGTST ;GET SOFTWARE DETECTED FLAG
          BIS #BIT14, (R0) ;RETURN HERE IF GOOD SECTOR
          MOV (SP)+, R2 ;ELSE RETURN HERE
          RTS PC
1$: MOV (SP)+, R2 ;RESTORE R2
      RTS PC

; THIS ROUTINE DOES THE ACTUAL SCANNING OF THE BAD SECTOR TABLES
; ENTER WITH THE ADDRESS OF TABLE (BSE22H, BSE22S, ETC.) IN TEMP1
; RETURN IF NO COMPARE
; RETURN+4 IF COMPARE
FLGTST: MOV R3, -(SP) ;SAVE R3
1$: CMP (R2), #-1 ;SEE IF ALL 1'S
     BNE 2$ ;BR IF NO
     MOV (SP)+, R3 ;RESTORE R3
     RTS PC
2$: CMP (R2)+, CALADD ;SEE IF=CYL # & ADR PTR TO TRK/SECTOR WORD
     BEQ 3$
     ADD #2, R2 ;GO TO NEXT CYL WORD IN TABLE
     BR 1$
3$: MOV HEAD, R3 ;GET HEAD # FROM FHDTAB ROUTINE
     SWAB R3
     BIS R1, R3 ;ADD SECTOR # FROM FHDTAB ROUTINE
     CMP (R2)+, R3 ;SEE IF SECTOR/HEAD COMPARE
     ; & INCR PTR TO NEXT CYL WORD
     BEQ 4$ ;BR IF COMPARE
     BR 1$ ;ELSE TRY NEXT CYL
4$: MOV (SP)+, R3 ;RESTORE R3
     ADD #4, (SP) ;INCREMENT RET ADDR
     RTS PC

; THIS ROUTINE SORTS THE RHTAB TABLE FROM WHATEVER SECTOR IT BEGINS
; WITH AND RE-WRITES THE INFO IN SRTTAB TABLE TO BEGIN WITH SECTOR 0
SORT: MOV R0, -(SP) ;SAVE R0
      MOV R1, -(SP) ;SAVE R1
      JSR PC, RDSEC
      ADD #1, SECTOR
    
```

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8801 046670 004737 046760      JSR      PC,MULT6      ;MULT SECTOR BY 6
8802
8803 046674 012700 000204      MOV      #132,R0      ;RO-SECTOR TO RO = INDEX
8804 046700 163700 001374      SUB      SECTOR,R0
8805 046704 010037 001374      MOV      RO,SECTOR
8806 046710 062737 001666 001374  ADD      #RHTAB,SECTOR ;SAVE INDEX
8807
8808 046716 062700 001666      ADD      #RHTAB,R0    ;INDEX TO BOT HALF OF RHTAB
8809 046722 012701 002072      MOV      #SRTTAB,R1  ;INDEX TO TOP HALF OF SRTTAB
8810
8811 046726 012021 002072 1$:      MOV      (R0)+,(R1)+  ;PUT BOTTOM OF RHTAB TO TOP OF SRTTAB
8812 046730 020027 002072      CMP      RO,#RHTAB+132.
8813 046734 001374 1$      BNE
8814
8815 046736 012700 001666      MOV      #RHTAB,R0    ;PUT TOP OF RHTAB TO BOT OF SRTTAB
8816 046742 012021 2$:      MOV      (R0)+,(R1)+
8817 046744 020037 001374      CMP      RO,SECTOR
8818 046750 001374 2$      BNE
8819
8820 046752 012601 001374      MOV      (SP)+,R1    ;RESTOR R1
8821 046754 012600 001374      MOV      (SP)+,R0    ;RESTOR R0
8822 046756 000207 001374      RTS      PC
8823
8824
8825      ;MULT BY 6. ENTER WITH DESIRED # IN 'SECTOR'
8826
8827 046760 006337 001374  MULT6:  ASL      SECTOR      ;2 X SECTOR
8828 046764 013746 001374      MOV      SECTOR,-(SP)
8829 046770 006337 001374      ASL      SECTOR      ;4 X SECTOR
8830 046774 062637 001374      ADD      (SP)+,SECTOR ;(4 X S)+(2 X S) = 6 X SECTOR
8831 047000 000207 001374      RTS      PC
8832
8833
8834      ;THIS ROUTINE IS ENTERED ONLY IF THERE IS A BSE ERROR AFTER A WRITE DATA
8835      ;CMD. IT VERIFIES THAT THE BAD SECTOR IS LISTED IN THE BSE INFORMATION
8836      ;CYLINDER AT CYL 410, TRACK 2.
8837
8838      ;RETURN IF SECTOR NOT LISTED IN BSE TABLE, ERROR CONDITION.
8839      ;RETURN+2 IF LISTED, SKIP OVER ERROR
8840
8841 047002 010446 001374  TRUERR: MOV      R4,-(SP)      ;SAVE R4
8842
8843 047004 032737 010000 005314  BIT      #CFMT,HCS1    ;CHECK FORMAT
8844 047012 001014 001374  BNE      3$           ;BR FOR 20 SECTOR FORMAT
8845                                     ;ONLY 22 SECTOR FMT IN THIS PGM
8846
8847 047014 012704 002306  MOV      #BSE22H+8.,R4
8848 047020 004737 047054  JSR      PC,TERR1     ;SEE IF ON HARDWARE DETECTED TABLE
8849 047024 000407 001374  BR       3$           ;RETURN HERE IF YES
8850
8851 047026 012704 003306  MOV      #BSE22S+8.,R4
8852 047032 004737 047054  JSR      PC,TERR1     ;& SEE IF ON SOFTWARE DETECTED TABLE
8853 047036 000407 001374  BR       3$           ;RETURN HERE IF YES
8854
8855 047040 012604 001374  1$:      MOV      (SP)+,R4    ;RESTORE R4
8856 047042 000207 001374  RTS      PC           ;RETURN WITHOUT JUMPING OVER ERROR

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8857
8858 047044 012604          3$:   MOV    (SP)+,R4      ;RESTORE R4
8859 047046 062716 000002   ADD    #2,(SP)      ;SKIP OVER ERROR ON RETURN
8860 047052 000207          RTS    PC
8861
8862
8863
8864
8865
8866
8867
8868 047054 021427 177777   TERR1: CMP    (R4),#-1    ;SEE IF ALL 1'S
8869 047060 001405          BEQ    1$            ;BR IF YES, NOT ON TABLE
8870 047062 022437 005334   CMP    (R4)+,HDC    ;SEE IF CYL MATCH
8871 047066 001405          BEQ    2$            ;BR IF YES
8872 047070 005724          TST    (R4)+        ;ELSE ADV TO NEXT CYL WORD
8873 047072 000770          BR     TERR1        ;& TRY AGAIN.
8874
8875 047074 062716 000002   1$:   ADD    #2,(SP)
8876 047100 000207          RTS    PC
8877
8878 047102 022437 005324   2$:   CMP    (R4)+,HDA    ;SEE IF SECTOR & TRACK MATCH
8879 047106 001401          BEQ    3$            ;BR IF YES
8880 047110 000761          BR     TERR1        ;OR TRY AGAIN
8881
8882 047112 000207          3$:   RTS    PC
8883
8884
8885
8886
8887
8888
8889 047114 005037 001372   CLKON: CLR    TIMUP
8890 047120 005737 005460   TST    PCLKF
8891 047124 001004          BNE    1$            ;BRANCH IF P-CLOCK PRESENT
8892 047126 012777 000100 132172   MOV    #100,ALKS    ;L-CLOCK, ENABLE INT
8893 047134 000207          RTS    PC
8894 047136 012777 177777 132156   1$:   MOV    #-1,APKSB    ;P-CLOCK, ALL 1'S
8895 047144 012777 000135 132146   MOV    #135,APKS    ;ENABLE INT, CT UP, REP INT
8896 047152 000207          RTS    PC            ;LINE FREQ & RUN
8897
8898
8899
8899 047154 005337 001366   ;KW11-L & KW11-P INTERRUPT HANDLER
8900 047160 000002   CLOCK: DEC    COUNT
8901
8902
8903
8904
8904 047162 005737 005460   CLKOF: TST    PCLKF
8905 047166 001003          BNE    1$            ;BRACH IF P-CLOCK PRESENT
8906 047170 005077 132132   CLR    ALKS        ;L-CLOCK, CLEAR INTERRUPT
8907 047174 000207          RTS    PC
8908 047176 005077 132116   1$:   CLR    APKS        ;P-CLOCK, CLEAR INTERRUPT
8909 047202 000207          RTS    PC
8910
8911
8912
; THIS ROUTINE DOES A TIMEOUT DEPENDING ON THE VALUE IN 'COUNT' WHICH THE

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```

8913 ; 'CLOCK' HANDLER DECREMENTS EVERY 1/60 SEC.
8914 ;
8915 047204 004737 047114 TMO: JSR PC,CLKOM ;TURN CLOCK ON
8916 047210 005737 001366 1$: TST COUNT ;TIME UP?
8917 ; BNE TMO ;BR IF NO S9-SEP-77
8918 047214 001375 ; BNE 1$ ;29-SEP-77
8919 047216 004737 047162 JSR PC,CLKOF ;ELSE, TURN CLOCK OFF
8920 047222 000207 RTS PC
8921 ;
8922 ;
8923 ; THIS ROUTINE GENERATES PARITY FOR EXPECTED MESSAGES
8924 ; ENTER WITH THE EXPECTED WORD IN TEMP1
8925 ; TEMP1 IS ROTATED LEFT 17 TIMES. EACH TIME THE CARRY BIT IS SET,
8926 ; R1 IS INCREMENTED. AT THE END OF 17 ROTATES ( TEMP1 BACK TO ORIG),
8927 ; R1 BIT 0 IS EXAMINED. IF IT IS SET, INDICATING AN ODD # OF 1'S,
8928 ; THE PARITY BIT IS NOT SET IN B
8929 ; IF IT IS NOT SET, INDICATING AN EVEN # OF 1'S ,THE PARITY BIT IS
8930 ; SET IN TEMP1
8931 ;
8932 047224 010046 SBPAR: MOV RO,-(SP) ;SAVE RO
8933 047226 010146 MOV R1,-(SP) ;SAVE R1
8934 047230 012700 000021 MOV #17,R0 ;SHIFT COUNTER
8935 047234 005001 CLR R1 ;COUNT # OF 1'S IN TEMP1
8936 047236 000241 CLC ;CLEAR CARRY
8937 ;
8938 047240 006137 005352 1$: ROL TEMP1
8939 047244 103001 BCC 2$ ;BR IF CARRY CLEAR
8940 047246 005201 INC R1 ;COUNT # OF 1'S
8941 047250 005300 2$: DEC RO ;SHIFT COUNTER
8942 047252 001372 BNE 1$
8943 ;
8944 047254 032701 000001 BIT #BIT0,R1
8945 047260 001003 BNE 3$ ;BR IF ODD # IN RO
8946 047262 052737 100000 005352 BIS #M.PAR,TEMP1 ;SET PARITY BIT
8947 047270 012601 3$: MOV (SP)+,R1 ;RESTORE R1
8948 047272 012600 MOV (SP)+,RO ;RESTORE RO
8949 047274 000207 RTS PC
8950 ;
8951 ;
8952 ; ROUTINE TO ENABLE LOOPING ON INTERMITTANT ERRORS
8953 ; WHEN $LPERR SET BY OTHER THAN SCOPE ROUTINE
8954 ; IE: MY LOOP MACRO
8955 ;
8956 047276 032777 001000 131634 SCOP1$: BIT #SW9,$SWR ;LOOP ON ERROR?
8957 047304 001406 BEQ 1$ ;BR IF NO
8958 047306 105737 001103 TSTB $ERFLG ;HAD ERROR?
8959 047312 001403 BEQ 1$ ;BR IF NO
8960 047314 013716 001110 MOV $LPERR,(SP)
8961 047320 000002 RTI
8962 ;
8963 047322 011637 001110 1$: MOV (SP),$LPERR ;SET LOOP ADDR FOR TIGHT SCOPE LOOP
8964 047326 000002 RTI
8965 ;
8966 ;
8967 ;
8968 ;

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```

8969
8970
8971
8972
8973
8974
8975 047330 022626
8976
8977 047332 004737 045534
8978 047336 104024
8979
8980 047340 005737 005276
8981 047344 001431
8982 047346 005737 000042
8983 047352 001403
8984 047354 104401 056674
8985 047360 000402
8986 047362 104401 056742
8987 047366
8988
8989 047366 004737 045534
8990 047372 104024
8991
8992 047374 012737 000011 005314
8993 047402 004737 043372
8994 047406 104121
8995
8996 047410 013737 001406 005354
8997 047416 004737 044006
8998 047422 104074
8999
9000 047424 005037 005276
9001
9002
9003 047430 005737 005300
9004 047434 001520
9005 047436 005237 005302
9006
9007 047442 012765 100000 000000
9008 047450 013765 001222 000010
9009 047456 063765 005464 000010
9010 047464 012737 000013 005314
9011 047472 004737 043372
9012 047476 104124
9013
9014 047500 012765 000001 000026
9015 047506 004737 045146
9016 047512 032737 020000 005342
9017 047520 001001
9018 047522 104070
9019 047524 013737 001400 005354
9020 047532 004737 044006
9021 047536 104055
9022
9023 047540 012765 100000 000000
9024 047546 013765 001222 000010

```

; THIS ROUTINE IS ENTERED BY TYPING A CONTROL-C.
; IT IS USED TO ALLOW THE OPERATOR TO HALT THE CPU WHILE INSURING
; THAT HEADS ARE LOADED & FORMATTING IS VALID BEFORE ACTUALLY HALTING
; THE CPU.
STOP: CMP (SP)+,(SP)+ ;RESTORE STACK FROM INTERRUPT
JSR PC,SUBCLR
ERROR 24 ;CERR AFTER
TST UNLD ;SEE IF HEADS UNLOADED
BEQ 3\$;BR IF NO
TST 42 ;SEE IF MANUAL OR AUTO MODE
BEQ 1\$;BR IF MANUAL MODE
TYPE ,MSG74 ;PGM ABORT PENDING
BR 2\$
1\$: TYPE ,MSG75 ;HALT PENDING
2\$:
JSR PC,SUBCLR
ERROR 24 ;CERR AFTER SCLR
MOV #SRTSPL,HCS1
JSR PC,DOCMD ;DO START SPINDLE CMD & GET CONTR RDY
ERROR 121 ;RDY NOT SET AFTER ST SPIN CMD.
MOV T100,TEMP2 ;SETUP TIMEOUT
JSR PC,FATT1 ;FIND ATTN
ERROR 74 ;NO ATTN AFTER ST SPIN CMD.
CLR UNLD
3\$: TST BADHDR ;SEE IF HEADERS VALID
BEQ 4\$;BR IF YES
INC HPEND
MOV #CCLR,RKCS1(R5)
MOV \$UNIT,RKCS2(R5)
ADD UNITB,RKCS2(R5) ;ADD 1 IF ON PORT B
MOV #RECAL,HCS1
JSR PC,DOCMD ;DO RECAL CMD & GET CONTR RDY
ERROR 124 ;RDY NOT SET AFTER RECAL CMD
MOV #1,RKMR1(R5) ;SELECT WORD 1
JSR PC,GSTAT
BIT #D.RTZ,HMR2
BNE 64\$
ERROR 70 ;RTZ NOT SET DURING RECAL CMD
MOV T10,TEMP2 ;SETUP TIMEOUT
JSR PC,FATT1 ;FIND ATTN
ERROR 55 ;NO ATTN AFTER RECAL CMD
MOV #CCLR,RKCS1(R5)
MOV \$UNIT,RKCS2(R5) ;DRIVE#

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9025 047554 063765 005464 000010      ADD      UNITB,RKCS2(R5) ;ADD 1 IF ON PORT B
9026 047562 012737 000005 005314      MOV      #CLEAR,HCS1
9027 047570 004737 043372      JSR      PC,DOCMD ;DO DRIVE CLEAR CMD & GET CONTR RDY
9028 047574 104151      ERROR   151 ;NO RDY AFTER DRIVE CLEAR CMD
9029 047576 004737 043750      JSR      PC,TSTATN ;TEST FOR ATTN
9030 047602 000401      BR      65$
9031 047604 104154      ERROR   154 ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
9032 047606
9033
9034 047606 012737 010340 005404      MOV      #<D.DRA!D.SPIN!D.DRDY!D.VV>,E.A0 ;EXPECTED MSG A0
9035 047614 005037 005406      CLR      E.B0 ;EXPECTED MSG B0
9036 047620 012737 001720 005410      MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
9037 047626 012737 000001 005412      MOV      #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
9038 047634 005037 005414      CLR      E.A2 ;EXPECTED MSG A2
9039 047640 012737 000002 005416      MOV      #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
9040 047646 012737 000003 005422      MOV      #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
9041
9042 047654 004737 044274      JSR      PC,CHKMSG ;CHECK MSGS A0, B0, A1, B1
9043 047660 000003      .WORD   T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
9044 047662 104033      ERROR   33 ;MSG A0 ERROR AFTER DRV CLEAR CMD
9045 047664 104034      ERROR   34 ;MSG B0 ERROR
9046 047666 104035      ERROR   35 ;MSG A1 ERROR
9047 047670 104036      ERROR   36 ;MSG B1 ERROR
9048
9049
9050 047672 000137 047736      JMP      FORM ;WRITE VALID FORMATS
9051
9052 047676 005737 000042      4$:    TST      42 ;SEE IF MANUAL OR AUTO MODE
9053 047702 001410      BEQ     5$ ;BR IF MANUAL MODE
9054 047704 104401 056777      TYPE   MSG76 ;PGM ABORTED
9055 047710 005037 042732      CLR     $EOPCT ;SET UP EOP TO EXIT TO MONITOR
9056 047714 005037 001176      CLR     $ESCAPE
9057 047720 000137 042704      JMP     $EOP1 ;ABORT PGM
9058
9059 047724 104401 057021      5$:    TYPE   ,MSG77 ;CPU HALTED
9060 047730 000000      HALT
9061 047732 000137 010646      JMP     ST5 ;START OVER IF CONTINUE PRESSED
9062 047736
9063
9064
9065
9066
9067
9068
9069
9070
9071
9072 047736 011600      BADTMO: MOV     (SP),RO ;SAVE PC WHERE TIMEOUT OCCURRED.
9073 047740 005740      TST     -(RO) ;GET PC BEFORE UPDATE
9074 047742 032777 020000 131170      BIT     #SW13,ASWR ;INHIBIT ERR TYP0UT?
9075 047750 001005      BNE     1$ ;YES, CON'T TYPE
9076 047752 104401 057201      TYPE   EM3 ;ABORT TESTS,UNEXP T.O. @ PC=
9077 047756 010046      MOV     RO,-(SP) ;SAVE RO FOR TYP0UT
9078
9079 047760 104403      TYPOS  ;TYPE PC
9080 047762 006      .BYTE  6 ;GO TYPE--OCTAL ASCII
;TYPE 6 DIGIT(S)

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```

9081 047763 000          .BYTE 0          ;: SUPPRESS LEADING ZEROS
9082 047764 032777 001000 131146 1$: BIT #SW9,ASWR ;: LOOP ON ERROR?
9083 047772 001403          BEQ 2$          ;: NO BRANCH
9084 047774 022626          CMP (SP)+,(SP)+ ;: YES RESTORE STACK
9085 047776 000177 131104          JMP ASLPADR     ;: GO TO STARTING ADDR OF TEST
9086                                     ;: THAT GAVE BAD TIMEOUT
9087 050002 032777 040000 131130 2$: BIT #SW14,ASWR ;: LOOP ON TEST?
9088 050010 001401          BEQ 3$          ;: NO BRANCH
9089 050012 000002          RTI             ;: YES
9090
9091 050014 000000          3$: HALT        ;: UNEXPECTED TIME OUT OCCURRED
9092                                     ;: AS INDICATED. YOU CAN LOOP ON
9093                                     ;: ERROR, LOOP ON TEST OR INHIBIT
9094                                     ;: ERROR TYPEOUT BY SETTING THOSE
9095                                     ;: SWITCHES.
9096
9097 050016 022626          CMP (SP)+,(SP)+ ;: RESTORE STACK
9098 050020 000137 042704          JMP $EOP1      ;: ABORT TESTS
9099
9100                                     .SBTTL MEMORY CHECK ENABLE TRAP
9101
9102 050024 012737 050040 001176 MEMERR: MOV #1$, $ESCAPE ;: STORE PC
9103 050032 011637 001334          MOV (SP), TRAPPC ;: UNEXP MEM PARITY TRAP
9104 050036 104017          ERROR 17
9105 050040 005037 001176 13: CLR $ESCAPE
9106 050044 032777 001000 131066 BIT #SW9,ASWR ;: CHECK IF LOOP ON ERROR
9107 050052 001001          BNE 2$          ;: YES FORCE STACK AND TRY AGAIN
9108 050054 000002          RTI             ;: ELSE RETURN
9109
9110 050056 012706 001100 2$: MOV #STACK, SP ;: INIT STACK
9111 050062 000177 131022          JMP ASLPERR    ;: LOOP ON ERROR
9112
9113                                     .SBTTL RK06 INTERRUPT HANDLER
9114
9115 INTER: NOP
9116 050066 000240          NOP
9117 050070 000240          NOP
9118 050072 000240          NOP
9119 050074 011600          MOV (SP), RO   ;: SAVE PC WHERE INT OCCURRED.
9120 050076 005740          TST -(RO)     ;: GET PC BEFORE UPDATE.
9121 050100 104401 056151          TYPE MSG6     ;: INT AT PC=
9122 050104 010046          MOV RO, -(SP) ;: SAVE RO FOR TYPEOUT
9123                                     ;: TYPE PC
9124 050106 104403          TYPOS        ;: GO TYPE--OCTAL ASCII
9125 050110 006          .BYTE 6      ;: TYPE 6 DIGIT(S)
9126 050111 000          .BYTE 0      ;: SUPPRESS LEADING ZEROS
9127 050112 000000          HALT
9128 050114 000240          NOP
9129 050116 000240          NOP
9130 050120 000002          RTI
9131
9132                                     .SBTTL POWER DOWN AND UP ROUTINES
9133
9134 ;POWER DOWN ROUTINE
9135
9136 050122 012737 050134 000024 $PWRDN: MOV #PWRUP,PWRVEC ;SET UP VECTOR

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9137 050130 000000          HALT
9138 050132 000776          BR      -2          ;HANG UP.
9139
9140          ;POWER UP ROUTINE
9141
9142 050134 005037 050206  $PWRUP: CLR      $PWRCT          ;WAIT LOOP FOR TTY
9143 050140 005237 050206  1$:  INC      $PWRCT          ;WAIT FOR THE INCR
9144 050144 001375          BNE      1$          ;OF WORD
9145 050146 012737 050122 000024  MOV      #SPWRDN,PWRVEC          ;SET POWER DOWN VECTOR
9146 050154 012737 000340 000026  MOV      #PR7,PWRVEC+2          ;PRIORITY 7
9147 050162 012737 000340 000036  MOV      #PR7,TRAPVEC+2          ;LOCKOUT ALL INTERRUPTS FOR TRAPS
9148 050170 012706 001100  MOV      #STACK,SP          ;INITIALIZE STACK
9149 050174 104401 056320  TYPE      ,MSG11          ;REPORT POWER FAIL
9150 050200 000005
9151 050202 000137 013004  RESET
9152          JMP      PFSRT
9153 050206 000000  $PWRCT: 0          ;WAIT COUNT FOR TTY
9154
9155          ;
9156          ;DIVISION UTILITY ROUTINE
9157          ;
9158          ;R0-R1-R2-R3=DIVIDEND
9159          ;R4-R5=DIVISOR
9160          ;R0-R1=REMAINDER AFTER DIVISION
9161          ;R2-R3=QUOTIENT AFTER DIVISION
9162          ;ENTER WITH JSR PC,M.DPID
9163
9164 050210 012746 000040  M.DPID: MOV      #40,-(SP)          ;COUNTER FOR DIVISION CYCLES
9165 050214 010446          MOV      R4,-(SP)          ;HI ORDER
9166 050216 010546          MOV      R5,-(SP)          ;LO ORDER TO THE STACK
9167 050220 005466 000002  NEG      2(SP)          ;FORM NEGATIVE
9168 050224 005416          NEG      @SP          ;VERSION OF DIVISOR
9169 050226 005666 000002  SBC      2(SP)
9170 050232 061601          ADD      @SP,R1
9171 050234 005500          ADC      R0          ;PERFORM INIT SUBT.
9172 050236 066600 000002  ADD      2(SP),R0
9173 050242 103445          BCS      M.DP50          ;IF CARRY THEN OVERFLOW HAS OCCURRED
9174 050244 005046          CLR      -(SP)          ;THIS IS A LONGER LASTING CARRY BIT
9175 050246 006103  M.DP40: ROL      R3
9176 050250 006102          ROL      R2
9177 050252 006101          ROL      R1
9178 050254 006100          ROL      R0
9179 050256 005716          TST      @SP          ;TEST CARRY INDICATOR
9180 050260 001410          BEQ      M.DP41          ;IF TO CARRY THEN ADD, ELSE SUBT.
9181 050262 005016          CLR      @SP          ;CLEAR UP FOR NEXT TIME
9182 050264 066601 000002  ADD      2(SP),R1
9183 050270 005500          ADC      R0          ;ADD -(DIVISOR)
9184 050272 005516          ADC      @SP          ;SET CARRY
9185 050274 066600 000004  ADD      4(SP),R0
9186 050300 000404          BR      M.DP42
9187
9188 050302 060501  M.DP41: ADD      R5,R1          ;ADD +(DIVISOR)
9189 050304 005500          ADC      R0          ;SET CARRY
9190 050306 005516          ADC      @SP
9191 050310 060400          ADD      R4,R0
9192 050312 005516  M.DP42: ADC      @SP          ;SET CARRY

```

9193	050314	005716		TST	@SP		;TEST THE UPDATE INDICATOR
9194	050316	001401		BEQ	.+4		;IF 0, FORGET IT
9195	050320	005203		INC	R3		;NO CARRY POSSIBLE HERE
9196	050322	005366	000006	DEC	6(SP)		;DECREMENT CTR
9197	050326	003347		BGT	M.DP40		;BR IF MORE TO DO
9198	050330	006003		ROR	R3		
9199	050332	103404		BCS	M.DF44		
9200	050334	060501		ADD	R5, R1		
9201	050336	005500		ADC	R0		
9202	050340	060400		ADD	R4, R0		
9203	050342	000241		CLC			
9204							
9205	050344	006103		M.DP44: ROL	R3		
9206	050346	062706	000010	ADD	#10, SP		;ADJUST STACK BY 4 WORDS
9207	050352	000242		CLV			
9208	050354	000207		RTS	PC		
9209							
9210	050356	062706	000006	M.DP50: ADD	#6, SP		
9211	050362	000262		SEV			
9212	050364	000207		RTS	PC		
9213							

```

9214          .SBTTL  SCOPE HANDLER ROUTINE
9215
9216          ;*****
9217          ;*THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
9218          ;*AND LOAD THE TEST NUMBER($STNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
9219          ;*AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
9220          ;*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
9221          ;*SW14=1      LOOP ON TEST
9222          ;*SW11=1      INHIBIT ITERATIONS
9223          ;*SW09=1      LOOP ON ERROR
9224          ;*SW08=1      LOOP ON TEST IN SWR<7:0>
9225          ;*CALL
9226          ;*          SCOPE          ;;SCOPE=IOT
9227
9228          $SCOPE:
9229          050366      104407      040000      130542      1$:      CKSWR          ;; TEST FOR CHANGE IN SOFT-SWR
9230          050370      032777          BIT          #BIT14,$SWR      ;; LOOP ON PRESENT TEST?
9231          050376      001114          BNE          $OVER          ;; YES IF SW14=1
9232          ;*****START OF CODE FOR THE XOR TESTER*****
9233          050400      000416          $XTSTR: BR          6$          ;; IF RUNNING ON THE "XOR" TESTER CHANGE
9234          ;*****          ;; THIS INSTRUCTION TO A "NOP" (NOP=240)
9235          050402      013746      000004          MOV          @#ERRVEC, -(SP)      ;; SAVE THE CONTENTS OF THE ERROR VECTOR
9236          050406      012737      050426      000004          MOV          #5$, @#ERRVEC      ;; SET FOR TIMEOUT
9237          050414      005737      177060          TST          @#177060          ;; TIME OUT ON XOR?
9238          050420      012637      000004          MOV          (SP)+, @#ERRVEC      ;; RESTORE THE ERROR VECTOR
9239          050424      000463          BR          $SVLAD          ;; GO TO THE NEXT TEST
9240          050426      022626          5$:      CMP          (SP)+, (SP)+      ;; CLEAR THE STACK AFTER A TIME OUT
9241          050430      012637      000004          MOV          (SP)+, @#ERRVEC      ;; RESTORE THE ERROR VECTOR
9242          050434      000423          BR          7$          ;; LOOP ON THE PRESENT TEST
9243          050436          6$: ; *****END OF CODE FOR THE XOR TESTER*****
9244          050436      032777      000400      130474          BIT          #BIT08,$SWR      ;; LOOP ON SPEC. TEST?
9245          050444      001404          BEQ          2$          ;; BR IF NO
9246          050446      127737      130466      001102          CMPB         @SWR,$STNM      ;; ON THE RIGHT TEST? SWR<7:0>
9247          050454      001465          BEQ          $OVER          ;; BR IF YES
9248          050456      105737      001103          2$:      TSTB         $ERFLG          ;; HAS AN ERROR OCCURRED?
9249          050462      001421          BEQ          3$          ;; BR IF NO
9250          050464      123737      001115      001103          CMPB         $ERMAX,$ERFLG      ;; MAX. ERRORS FOR THIS TEST OCCURRED?
9251          050472      101015          BHI          3$          ;; BR IF NO
9252          050474      032777      001000      130436          BIT          #BIT09,$SWR      ;; LOOP ON ERROR?
9253          050502      001404          BEQ          4$          ;; BR IF NO
9254          050504      013737      001110      001106          7$:      MOV          $LPERR,$LPADR      ;; SET LOOP ADDRESS TO LAST SCOPE
9255          050512      000446          BR          $OVER
9256          050514      105037      001103          4$:      CLRB         $ERFLG          ;; ZERO THE ERROR FLAG
9257          050520      005037      001174          CLR          $TIMES          ;; CLEAR THE NUMBER OF ITERATIONS TO MAKE
9258          050524      000415          BR          1$          ;; ESCAPE TO THE NEXT TEST
9259          050526      032777      004000      130404          3$:      BIT          #BIT11,$SWR      ;; INHIBIT ITERATIONS?
9260          050534      001011          BNE          1$          ;; BR IF YES
9261          050536      005737      001216          TST          $PASS          ;; IF FIRST PASS OF PROGRAM
9262          050542      001406          BEQ          1$          ;; INHIBIT ITERATIONS
9263          050544      005237      001104          INC          $ICNT          ;; INCREMENT ITERATION COUNT
9264          050550      023737      001174      001104          CMP          $TIMES,$ICNT      ;; CHECK THE NUMBER OF ITERATIONS MADE
9265          050556      002024          BGE          $OVER          ;; BR IF MORE ITERATION REQUIRED
9266          050560      012737      000001      001104          1$:      MOV          #1,$ICNT          ;; REINITIALIZE THE ITERATION COUNTER
9267          050566      013737      050644      001174          MOV          $MXCNT,$TIMES      ;; SET NUMBER OF ITERATIONS TO DO
9268          050574      105237      001102          $SVLAD: INCB         $STNM          ;; COUNT TEST NUMBERS
9269          050600      113737      001102      001214          MOVB        $STNM,$TESTN      ;; SET TEST NUMBER IN APT MAILBOX

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9270 050606 011637 001106          MOV      (SP), $LPADR      ;; SAVE SCOPE LOOP ADDRESS
9271 050612 011637 001110          MOV      (SP), $LPERR     ;; SAVE ERROR LOOP ADDRESS
9272 050616 005037 001176          CLR      $ESCAPE         ;; CLEAR THE ESCAPE FROM ERROR ADDRESS
9273 050622 112737 000001          MOV      #1, $ERMAX      ;; ONLY ALLOW ONE(1) ERROR ON NEXT TEST
9274 050630 013777 001102          $OVER:  MOV      $STNM, @DISPLAY ;; DISPLAY TEST NUMBER
9275 050636 013716 001106          MOV      $LPADR, (SP)    ;; FUDGE RETURN ADDRESS
9276 050642 000002                RTI                      ;; FIXES PS
9277 050644 003720          $MXCNT: 2000.          ;; MAX. NUMBER OF ITERATIONS
9278
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9280
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9292
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9323
9324
9325

```

001115
130304

```

$SBTTL ERROR HANDLER ROUTINE
;*****
;THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT.
;SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
;AND GO TO TYPERR ON ERROR
;THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
;SW15=1 HALT ON ERROR
;SW13=1 INHIBIT ERROR TYPEOUTS
;SW10=1 BELL ON ERROR
;SW09=1 LOOP ON ERROR
;CALL
;* ERROR N ;;ERROR=EMT AND N=ERROR ITEM NUMBER

$ERROR:
7$: CKSWR          ;; TEST FOR CHANGE IN SOFT-SWR
    INCB          ;; SET THE ERROR FLAG
    BEQ $ERFLG   ;; DON'T LET THE FLAG GO TO ZERO
    MOV $STNM, @DISPLAY ;; DISPLAY TEST NUMBER AND ERROR FLAG
    BIT #BIT10, @SWR ;; BELL ON ERROR?
    BEQ 1$       ;; NO - SKIP
    TYPE $BELL   ;; RING BELL
    INC $ERTTL   ;; COUNT THE NUMBER OF ERRORS
    MOV (SP), $ERRPC ;; GET ADDRESS OF ERROR INSTRUCTION
    SUB #2, $ERRPC
    MOVB @ $ERRPC, $ITEMB ;; STRIP AND SAVE THE ERROR ITEM CODE
    BIT #BIT13, @SWR ;; SKIP TYPEOUT IF SET
    BNE 20$     ;; SKIP TYPEOUTS
    JSR PC, TYPERR ;; GO TO USER ERROR ROUTINE
    TYPE , $CRLF

20$: CMPB #APTENV, $ENV ;; RUNNING IN APT MODE
    BNE 2$     ;; NO, SKIP APT ERROR REPORT
    MOVB $ITEMB, 21$ ;; SET ITEM NUMBER AS ERROR NUMBER
    JSR PC, $ATY4 ;; REPORT FATAL ERROR TO APT

21$: .BYTE 0
    .BYTE 0

22$: BR 22$ ;; APT ERROR LOOP
2$: TST @SWR ;; HALT ON ERROR
    BPL 3$   ;; SKIP IF CONTINUE
    HALT    ;; HALT ON ERROR!

3$: CKSWR          ;; TEST FOR CHANGE IN SOFT-SWR
    BIT #BIT09, @SWR ;; LOOP ON ERROR SWITCH SET?
    BEQ 4$     ;; BR IF NO
    MOV $LPERR, (SP) ;; FUDGE RETURN FOR LOOPING
    TST $ESCAPE ;; CHECK FOR AN ESCAPE ADDRESS
    BEQ 5$     ;; BR IF NONE
    MOV $ESCAPE, (SP) ;; FUDGE RETURN ADDRESS FOR ESCAPE

```

```

9326 051032
9327 051032 022737 042772 000042 5$:      CMP      #SENDAD,2#42    ;;ACT-11 AUTO-ACCEPT?
9328 051040 001001                BNE      6$            ;;BRANCH IF NO
9329 051042 000000                HALT                    ;;YES
9330 051044
9331 051044 000002 6$:      RTI                    ;;RETURN
9332
9333 .SBTTL  TYPE ROUTINE
9334
9335 ;*****
9336 ;*ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
9337 ;*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
9338 ;*NOTE1:          $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
9339 ;*NOTE2:          $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
9340 ;*NOTE3:          $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
9341 ;*
9342 ;*CALL:
9343 ;*1) USING A TRAP INSTRUCTION
9344 ;*      TYPE      ,MESADR      ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
9345 ;*OR
9346 ;*      TYPE
9347 ;*      MESADR
9348 ;*
9349 051046 105737 001157 $TYPE:  TSTB      $TPFLG      ;; IS THERE A TERMINAL?
9350 051052 100002          BPL      1$            ;;BR IF YES
9351 051054 000000          HALT                    ;;HALT HERE IF NO TERMINAL
9352 051056 000430          BR      3$            ;;LEAVE
9353 051060 010046          1$:      MOV      RD,-(SP)      ;;SAVE RD
9354 051062 017600 000002  MOV      22(SP),RD      ;;GET ADDRESS OF ASCIZ STRING
9355 051066 122737 000001 001230  CMPB     #APTENV,$ENV    ;;RUNNING IN APT MODE
9356 051074 001011          BNE      62$          ;;NO,GO CHECK FOR APT CONSOLE
9357 051076 132737 000100 001231  BITB     #APTPOOL,$ENVM  ;;SPOOL MESSAGE TO APT
9358 051104 001405          BEQ      62$          ;;NO,GO CHECK FOR CONSOLE
9359 051106 010037 051116  MOV      RD,61$         ;;SETUP MESSAGE ADDRESS FOR APT
9360 051112 004737 051562  JSR      PC,$ATY3      ;;SPOOL MESSAGE TO APT
9361 051116 000000          .WORD     0            ;;MESSAGE ADDRESS
9362 051120 132737 000040 001231  61$:    BITB     #APTCSUP,$ENVM  ;;APT CONSOLE SUPPRESSED
9363 051126 001003          BNE      60$          ;;YES,SKIP TYPE OUT
9364 051130 112046          2$:      MOVB     (RD)+,-(SP)  ;;PUSH CHARACTER TO BE TYPED ONTO STACK
9365 051132 001005          BNE      4$            ;;BR IF IT ISN'T THE TERMINATOR
9366 051134 005726          TST      (SP)+        ;;IF TERMINATOR POP IT OFF THE STACK
9367 051136 012600          60$:    MOV      (SP)+,RD      ;;RESTORE RD
9368 051140 062716 000002  3$:      ADD      #2,(SP)      ;;ADJUST RETURN PC
9369 051144 000002          RTI                    ;;RETURN
9370 051146 122716 000011  4$:      CMPB     #HT,(SP)    ;;BRANCH IF <HT>
9371 051152 001430          BEQ      8$            ;;BRANCH IF NOT <CRLF>
9372 051154 122716 000200  CMPB     #CRLF,(SP)
9373 051160 001006          BNE      5$            ;;POP <CR><LF> EQUIV
9374 051162 005726          TST      (SP)+        ;;TYPE A CR AND LF
9375 051164 104401          TYPE
9376 051166 001205          $CRLF
9377 051170 105037 051324  CLRB     $CHARCNT      ;;CLEAR CHARACTER COUNT
9378 051174 000755          BR      2$            ;;GET NEXT CHARACTER
9379 051176 004737 051260  5$:      JSR      PC,$TYPEC    ;;GO TYPE THIS CHARACTER
9380 051202 123726 001156  6$:      CMPB     $FILLC,(SP)+  ;;IS IT TIME FOR FILLER CHARS.?
9381 051206 001350          BNE      2$            ;;IF NO GO GET NEXT CHAR.

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9382 051210 013746 001154      MOV      $NULL,-(SP)      ;;GET # OF FILLER CHARS. NEEDED
9383                                ;;AND THE NULL CHAR.
9384 051214 105366 000001      7$:  DECB      1(SP)      ;;DOES A NULL NEED TO BE TYPED?
9385 051220 002770                                BLT      6$              ;;BR IF NO--GO POP THE NULL OFF OF STACK
9386 051222 004737 051260      JSR      PC,$TYPEC      ;;GO TYPE A NULL
9387 051226 105337 051324      DECB      $CHARCNT      ;;DO NOT COUNT AS A COUNT
9388 051232 000770                                BR       7$              ;;LOOP
9389
9390                                ;HORIZONTAL TAB PROCESSOR
9391
9392 051234 112716 000040      8$:  MOVB      #' (SP)      ;;REPLACE TAB WITH SPACE
9393 051240 004737 051260      9$:  JSR      PC,$TYPEC      ;;TYPE A SPACE
9394 051244 132737 000007 051324  BITB      #7,$CHARCNT      ;;BRANCH IF NOT AT
9395 051252 001372                                BNE      9$              ;;TAB STOP
9396 051254 005726                                TST      (SP)+          ;;POP SPACE OFF STACK
9397 051256 000724                                BR       2$              ;;GET NEXT CHARACTER
9398 051260 105777 127664      $TYPEC: TSTB     @STPS      ;;WAIT UNTIL PRINTER IS READY
9399 051264 100375                                BPL      $TYPEC
9400 051266 116677 000002 127656  MOVB      2(SP),@STPB      ;;LOAD CHAR TO BE TYPED INTO DATA REG.
9401 051274 122766 000015 000002  CMPB      #CR,2(SP)      ;;IS CHARACTER A CARRIAGE RETURN?
9402 051302 001003                                BNE      1$              ;;BRANCH IF NO
9403 051304 105037 051324      CLRB      $CHARCNT      ;;YES--CLEAR CHARACTER COUNT
9404 051310 000406                                BR       $TYPEX
9405 051312 122766 000012 000002  1$:  CMPB      #LF,2(SP)      ;;IS CHARACTER A LINE FEED?
9406 051320 001402                                BEQ      $TYPEX          ;;BRANCH IF YES
9407 051322 105227                                INCB     (PC)+          ;;COUNT THE CHARACTER
9408 051324 000000      $CHARCNT: .WORD 0      ;;CHARACTER COUNT STORAGE
9409 051326 000207      $TYPEX: RTS      PC
9410
9411                                .SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
9412
9413                                ;*****
9414                                ;*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
9415                                ;*SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
9416                                ;*NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
9417                                ;*BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
9418                                ;*REPLACED WITH SPACES.
9419                                ;*CALL:
9420                                ;*
9421                                ;*      MOV      NUM,-(SP)      ;;PUT THE BINARY NUMBER ON THE STACK
9422                                ;*      TYPDS      ;;GO TO THE ROUTINE
9423
9423 051330                                $TYPDS:
9424 051330 010046      MOV      R0,-(SP)      ;;PUSH R0 ON STACK
9425 051332 010146      MOV      R1,-(SP)      ;;PUSH R1 ON STACK
9426 051334 010246      MOV      R2,-(SP)      ;;PUSH R2 ON STACK
9427 051336 010346      MOV      R3,-(SP)      ;;PUSH R3 ON STACK
9428 051340 010546      MOV      R5,-(SP)      ;;PUSH R5 ON STACK
9429 051342 012746 020200      MOV      #20200,-(SP)    ;;SET BLANK SWITCH AND SIGN
9430 051346 016605 000020      MOV      20(SP),R5      ;;GET THE INPUT NUMBER
9431 051352 100004      BPL      1$              ;;BR IF INPUT IS POS.
9432 051354 005405      NEG      R5              ;;MAKE THE BINARY NUMBER POS.
9433 051356 112766 000055 000001  MOVB      #'-,1(SP)      ;;MAKE THE ASCII NUMBER NEG.
9434 051364 005000      CLR      R0              ;;ZERO THE CONSTANTS INDEX
9435 051366 012703 051544      MOV      #$DBLK,R3      ;;SETUP THE OUTPUT POINTER
9436 051372 112723 000040      MOVB      #' ,(R3)+      ;;SET THE FIRST CHARACTER TO A BLANK
9437 051376 005002      CLR      R2              ;;CLEAR THE BCD NUMBER

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M14

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 181
CONVERT BINARY TO DECIMAL AND TYPE ROUTINE

SEQ 0181

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9438 051400 016001 051534      MOV      $DTBL(R0),R1      ;;GET THE CONSTANT
9439 051404 160105      3$: SUB      R1,R5          ;;FORM THIS BCD DIGIT
9440 051406 002402      BLT      4$              ;;BR IF DONE
9441 051410 005202      INC      R2              ;;INCREASE THE BCD DIGIT BY 1
9442 051412 000774      BR       3$
9443 051414 060105      4$: ADD      R1,R5          ;;ADD BACK THE CONSTANT
9444 051416 005702      TST      R2              ;;CHECK IF BCD DIGIT=0
9445 051420 001002      BNE      5$              ;;FALL THROUGH IF 0
9446 051422 105716      TSTB     (SP)            ;;STILL DOING LEADING 0'S?
9447 051424 100407      BMI      7$              ;;BR IF YES
9448 051426 106316      5$: ASLB     (SP)          ;;MSD?
9449 051430 103003      BCC      6$              ;;BR IF NO
9450 051432 116663 000001 177777  MOVB     1(SP),-1(R3)     ;;YES--SET THE SIGN
9451 051440 052702 000060 6$: BIS      #'0,R2        ;;MAKE THE BCD DIGIT ASCII
9452 051444 052702 000040 7$: BIS      #' ,R2        ;;MAKE IT A SPACE IF NOT ALREADY A DIGIT
9453 051450 110223      MOVB     R2,(R3)+        ;;PUT THIS CHARACTER IN THE OUTPUT BUFFER
9454 051452 005720      TST      (R0)+          ;;JUST INCREMENTING
9455 051454 020027 000010  CMP      R0,#10         ;;CHECK THE TABLE INDEX
9456 051460 002746      BLT      2$              ;;GO DO THE NEXT DIGIT
9457 051462 003002      BGT      8$              ;;GO TO EXIT
9458 051464 010502      MOV      R5,R2          ;;GET THE LSD
9459 051466 000764      BR       6$              ;;GO CHANGE TO ASCII
9460 051470 105726      8$: TSTB     (SP)+        ;;WAS THE LSD THE FIRST NON-ZERO?
9461 051472 100003      BPL      9$              ;;BR IF NO
9462 051474 116663 177777 177776 9$: MOVB     -1(SP),-2(R3) ;;YES--SET THE SIGN FOR TYPING
9463 051502 105013      CLRB     (R3)           ;;SET THE TERMINATOR
9464 051504 012605      MOV      (SP)+,R5       ;;POP STACK INTO R5
9465 051506 012603      MOV      (SP)+,R3       ;;POP STACK INTO R3
9466 051510 012602      MOV      (SP)+,R2       ;;POP STACK INTO R2
9467 051512 012601      MOV      (SP)+,R1       ;;POP STACK INTO R1
9468 051514 012600      MOV      (SP)+,R0       ;;POP STACK INTO R0
9469 051516 104401 051544  TYPE     $DBLK          ;;NOW TYPE THE NUMBER
9470 051522 016666 000002 000004  MOV      2(SP),4(SP)    ;;ADJUST THE STACK
9471 051530 012616      MOV      (SP)+,(SP)
9472 051532 000002      RTI                          ;;RETURN TO USER
9473 051534 023420      $DTBL: 10000.
9474 051536 001750      1000.
9475 051540 000144      100.
9476 051542 000012      10.
9477 051544 000004      $DBLK: .BLKW 4
9478 .SBTTL  APT COMMUNICATIONS ROUTINE
9479
9480 *****
9481 051554 112737 000001 052020 $ATY1: MOVB     #1,$FFLG   ;;TO REPORT FATAL ERROR
9482 051562 112737 000001 052016 $ATY3: MOVB     #1,$MFLG   ;;TO TYPE A MESSAGE
9483 051570 000403      BR       $ATYC
9484 051572 112737 000001 052020 $ATY4: MOVB     #1,$FFLG   ;;TO ONLY REPORT FATAL ERROR
9485 051600      $ATYC:
9486 051600 010046      MOV      R0,-(SP)       ;;PUSH R0 ON STACK
9487 051602 010146      MOV      R1,-(SP)       ;;PUSH R1 ON STACK
9488 051604 105737 052016  TSTB     $MFLG          ;;SHOULD TYPE A MESSAGE?
9489 051610 001450      BEQ      5$              ;;IF NOT: BR
9490 051612 122737 000001 001230  CMPB     #APTENV,$ENV    ;;OPERATING UNDER APT?
9491 051620 001031      BNE      3$              ;;IF NOT: BR
9492 051622 132737 000100 001231  BITB     #APTPOOL,$ENVM  ;;SHOULD SPOOL MESSAGES?
9493 051630 001425      BEQ      3$              ;;IF NOT: BR

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9494 051632 017600 000004          MOV    24(SP),R0      ;; GET MESSAGE ADDR.
9495 051636 062766 000002 000004  ADD    #2,4(SP)      ;; BUMP RETURN ADDR.
9496 051644 005737 001210          TST    $MSGTYPE     ;; SEE IF DONE W/ LAST XMISSION?
9497 051650 001375          BNE    1$           ;; IF NOT: WAIT
9498 051652 010037 001224          MOV    R0,$MSGAD    ;; PUT ADDR IN MAILBOX
9499 051656 105720 2$:          TSTB   (R0)+        ;; FIND END OF MESSAGE
9500 051660 001376          BNE    2$           ;;
9501 051662 163700 001224          SUB    $MSGAD,R0    ;; SUB START OF MESSAGE
9502 051666 006200          ASR    R0           ;; GET MESSAGE LNTH IN WORDS
9503 051670 010037 001226          MOV    R0,$MSGGLT   ;; PUT LENGTH IN MAILBOX
9504 051674 012737 000004 001210  MOV    #4,$MSGTYPE  ;; TELL APT TO TAKE MSG.
9505 051702 000413          BR     5$           ;;
9506 051704 017637 000004 051730 3$:          MOV    24(SP),4$    ;; PUT MSG ADDR IN JSR LINKAGE
9507 051712 062766 000002 000004  ADD    #2,4(SP)      ;; BUMP RETURN ADDRESS
9508 051720 013746 177776          MOV    177776,-(SP) ;; PUSH 177776 ON STACK
9509 051724 004737 051046          JSR    PC,$TYPE     ;; CALL TYPE MACRO
9510 051730 000000          4$:          .WORD 0
9511 051732          5$:
9512 051732 105737 052020 10$:          TSTB   $FFLG        ;; SHOULD REPORT FATAL ERROR?
9513 051736 001416          BEQ    12$         ;; IF NOT: BR
9514 051740 005737 001230          TST    $ENV         ;; RUNNING UNDER APT?
9515 051744 001413          BEQ    12$         ;; IF NOT: BR
9516 051746 005737 001210 11$:          TST    $MSGTYPE     ;; FINISHED LAST MESSAGE?
9517 051752 001375          BNE    11$         ;; IF NOT: WAIT
9518 051754 017637 000004 001212  MOV    24(SP),$FATAL ;; GET ERROR #
9519 051762 062766 000002 000004  ADD    #2,4(SP)      ;; BUMP RETURN ADDR.
9520 051770 005237 001210 12$:          INC    $MSGTYPE     ;; TELL APT TO TAKE ERROR
9521 051774 105037 052020          CLRB   $FFLG        ;; CLEAR FATAL FLAG
9522 052000 105037 052017          CLRB   $LFLG        ;; CLEAR LOG FLAG
9523 052004 105037 052016          CLRB   $MFLG        ;; CLEAR MESSAGE FLAG
9524 052010 012601          MOV    (SP)+,R1     ;; POP STACK INTO R1
9525 052012 012600          MOV    (SP)+,R0     ;; POP STACK INTO R0
9526 052014 000207          RTS    PC           ;; RETURN
9527 052016          $MFLG: .BYTE 0      ;; MESSG. FLAG
9528 052017          $LFLG: .BYTE 0      ;; LOG FLAG
9529 052020          $FFLG: .BYTE 0      ;; FATAL FLAG
9530          .EVEN
9531          APTSIZE=200
9532          APTENV=001
9533          APTSPool=100
9534          APTCSUP=040
9535          .SBTTL BINARY TO OCTAL (ASCII) AND TYPE
9536
9537          ;; *****
9538          ;; *THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
9539          ;; *OCTAL (ASCII) NUMBER AND TYPE IT.
9540          ;; *$TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
9541          ;; *CALL:
9542          ;; *      MOV    NUM,-(SP)      ;; NUMBER TO BE TYPED
9543          ;; *      TYPOS      ;; CALL FOR TYPEOUT
9544          ;; *      .BYTE  N      ;; N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
9545          ;; *      .BYTE  M      ;; M=1 OR 0
9546          ;; *                               ;; 1=TYPE LEADING ZEROS
9547          ;; *                               ;; 0=SUPPRESS LEADING ZEROS
9548          ;; *
9549          ;; *$STYPON----ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST

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9550      ;*$TYPOS OR $TYPOC
9551      ;*CALL:
9552      ;*      MOV      NUM,-(SP)      ;;NUMBER TO BE TYPED
9553      ;*      TYPON      ;;CALL FOR TYPEOUT
9554      ;*
9555      ;*$TYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
9556      ;*CALL:
9557      ;*      MOV      NUM,-(SP)      ;;NUMBER TO BE TYPED
9558      ;*      TYPOC      ;;CALL FOR TYPEOUT
9559
9560 052022 017646 000000      $TYPOS: MOV      2(SP),-(SP)      ;;PICKUP THE MODE
9561 052026 116637 000001 052245      MOVVB 1(SP),$OFILL      ;;LOAD ZERO FILL SWITCH
9562 052034 112637 052247      MOVVB (SP)+,$SOMODE+1      ;;NUMBER OF DIGITS TO TYPE
9563 052040 062716 000002      ADD      #2,(SP)      ;;ADJUST RETURN ADDRESS
9564 052044 000406      BR      $TYPON
9565 052046 112737 000001 052245      $TYPOC: MOVVB #1,$OFILL      ;;SET THE ZERO FILL SWITCH
9566 052054 112737 000006 052247      MOVVB #6,$SOMODE+1      ;;SET FOR SIX(6) DIGITS
9567 052062 112737 000005 052244      $TYPON: MOVVB #5,$SOCNT      ;;SET THE ITERATION COUNT
9568 052070 010346      MOV      R3,-(SP)      ;;SAVE R3
9569 052072 010446      MOV      R4,-(SP)      ;;SAVE R4
9570 052074 010546      MOV      R5,-(SP)      ;;SAVE R5
9571 052076 113704 052247      MOVVB $SOMODE+1,R4      ;;GET THE NUMBER OF DIGITS TO TYPE
9572 052102 005404      NEG      R4
9573 052104 062704 000006      ADD      #6,R4      ;;SUBTRACT IT FOR MAX. ALLOWED
9574 052110 110437 052246      MOVVB  R4,$SOMODE      ;;SAVE IT FOR USE
9575 052114 113704 052245      MOVVB  $OFILL,R4      ;;GET THE ZERO FILL SWITCH
9576 052120 016605 000012      MOV      12(SP),R5      ;;PICKUP THE INPUT NUMBER
9577 052124 005003      CLR      R3      ;;CLEAR THE OUTPUT WORD
9578 052126 006105      1$: ROL      R5      ;;ROTATE MSB INTO "C"
9579 052130 000404      BR      3$      ;;GO DO MSB
9580 052132 006105      2$: ROL      R5      ;;FORM THIS DIGIT
9581 052134 006105      ROL      R5
9582 052136 006105      ROL      R5
9583 052140 010503      MOV      R5,R3
9584 052142 006103      3$: ROL      R3      ;;GET LSB OF THIS DIGIT
9585 052144 105337 052246      DECB  $SOMODE      ;;TYPE THIS DIGIT?
9586 052150 100016      BPL      7$      ;;BR IF NO
9587 052152 042703 177770      BIC      #177770,R3      ;;GET RID OF JUNK
9588 052156 001002      BNE      4$      ;;TEST FOR 0
9589 052160 005704      TST      R4      ;;SUPPRESS THIS 0?
9590 052162 001403      BEQ      5$      ;;BR IF YES
9591 052164 005204      4$: INC      R4      ;;DON'T SUPPRESS ANYMORE 0'S
9592 052166 052703 000060      BIS      #'0,R3      ;;MAKE THIS DIGIT ASCII
9593 052172 052703 000040      5$: BIS      #' ,R3      ;;MAKE ASCII IF NOT ALREADY
9594 052176 110337 052242      MOVVB  R3,8$      ;;SAVE FOR TYPING
9595 052202 104401 052242      TYPE  8$      ;;GO TYPE THIS DIGIT
9596 052206 105337 052244      7$: DECB  $SOCNT      ;;COUNT BY 1
9597 052212 003347      BGT      2$      ;;BR IF MORE TO DO
9598 052214 002402      BLT      6$      ;;BR IF DONE
9599 052216 005204      INC      R4      ;;INSURE LAST DIGIT ISN'T A BLANK
9600 052220 000744      BR      2$      ;;GO DO THE LAST DIGIT
9601 052222 012605      6$: MOV      (SP)+,R5      ;;RESTORE R5
9602 052224 012604      MOV      (SP)+,R4      ;;RESTORE R4
9603 052226 012603      MOV      (SP)+,R3      ;;RESTORE R3
9604 052230 016666 000002 000004      MOV      2(SP),4(SP)      ;;SET THE STACK FOR RETURNING
9605 052236 012616      MOV      (SP)+,(SP)

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9606 052240 000002
9607 052242 000
9608 052243 000
9609 052244 000
9610 052245 000
9611 052246 000000
9612
9613
9614
9615
9616 052250 000000
9617 052252 000000
9618 052254 000000
9619 052256 000001
9620 052257
9621 052260
9622
9623
9624
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9626
9627
9628
9629
9630
9631 052260 005037 052250
9632 052264 012737 052256 052252
9633 052272 013737 052252 052254
9634 052300 012737 052330 000060
9635 052306 012737 000200 000062
9636 052314 005777 126626
9637 052320 012777 000100 126616
9638 052326 000207
9639
9640
9641
9642
9643
9644
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9646
9647 052330 117746 126612
9648 052334 042716 177600
9649 052340 021627 000003
9650 052344 001007
9651 052346 104401 053456
9652 052352 004737 052260
9653 052356 005726
9654 052360 000137 047330
9655 052364 021627 000007
9656 052370 001004
9657 052372 022737 000176 001140
9658 052400 001500
9659
9660 052402
9661 052402 022737 000001 052250

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```

RTI ;; RETURN
BS: .BYTE 0 ;; STORAGE FOR ASCII DIGIT
      .BYTE 0 ;; TERMINATOR FOR TYPE ROUTINE
SOCNT: .BYTE 0 ;; OCTAL DIGIT COUNTER
$OFILL: .BYTE 0 ;; ZERO FILL SWITCH
$OMODE: .WORD 0 ;; NUMBER OF DIGITS TO TYPE
.SBTTL TTY INPUT ROUTINE

;*****
.ENABL LSB
$TKCNT: .WORD 0 ;; NUMBER OF ITEMS IN QUEUE
$TKQIN: .WORD 0 ;; INPUT POINTER
$TKQOUT: .WORD 0 ;; OUTPUT POINTER
$TKQSRT: .BLKB 1 ;; TTY KEYBOARD QUEUE
$TKQEND=.
.EVEN

; *TK INITIALIZE ROUTINE
; *THIS ROUTINE WILL INITIALIZE THE TTY KEYBOARD INPUT QUEUE
; *SETUP THE INTERRUPT VECTOR AND TURN ON THE KEYBOARD INTERRUPT
;
; *CALL:
; * JSR PC, $TKINT
; * RETURN
;
$TKINT: CLR $TKCNT ;; CLEAR COUNT OF ITEMS IN QUEUE
        MOV $TKQSRT, $TKQIN ;; MOVE THE STARTING ADDRESS OF THE
        MOV $TKQIN, $TKQOUT ;; QUEUE INTO THE INPUT & OUTPUT POINTERS.
        MOV $TKSRV, @TKVEC ;; INITIALIZE THE KEYBOARD VECTOR
        MOV #200, @TKVEC+2 ;; "BR" LEVEL 4
        TST @TKKB ;; CLEAR DONE FLAG
        MOV #100, @TKKS ;; ENABLE TTY KEYBOARD INTERRUPT
        RTS PC ;; RETURN TO CALLER

; *TK SERVICE ROUTINE
; *THIS ROUTINE WILL SERVICE THE TTY KEYBOARD INTERRUPT
; *BY READING THE CHARACTER FROM THE INPUT BUFFER AND PUTTING
; *IT IN THE QUEUE.
; *IF THE CHARACTER IS A "CONTROL-C" (↑C) $TKINT IS CALLED AND
; *UPON RETURN EXIT IS MADE TO THE "CONTROL-C" RESTART ADDRESS (STOP)
;
$TKSRV: MOVB @TKKB, -(SP) ;; PICKUP THE CHARACTER
        BIC #↑C17?, (SP) ;; STRIP THE JUNK
        CMP (SP), #3 ;; IS IT A CONTROL C?
        BNE 1$ ;; BRANCH IF NO
        TYPE $CNTLC ;; TYPE A CONTROL-C (↑C)
        JSR PC, $TKINT ;; INIT THE KEYBOARD
        TST (SP)+ ;; CLEAN UP STACK
        JMP STOP ;; CONTROL C RESTART
1$: CMP (SP), #7 ;; IS IT A CONTROL G?
    BNE 2$ ;; BRANCH IF NO
    CMP #SWREG, SWR ;; IS SOFT-SWR SELECTED?
    BEQ 6$ ;; GO TO SWR CHANGE
2$: CMP #1, $TKCNT ;; IS THE QUEUE FULL?

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```

9662 052410 001004          BNE      3$          ;;BRANCH IF NO
9663 052412 104401 001200  TYPE      $BELL      ;;RING THE TTY BELL
9664 052416 005726          TST      (SP)+      ;;CLEAN CHARACTER OFF OF STACK
9665 052420 000451          BR       5$          ;;EXIT
9666 052422 021627 000023  3$:      CMP      (SP),#23  ;;IS IT A CONTROL-S?
9667 052426 001021          BNE      32$        ;;BRANCH IF NO
9668 052430 005077 126510  CLR      @STKS      ;;DISABLE TTY KEYBOARD INTERRUPTS
9669 052434 005726          TST      (SP)+      ;;CLEAN CHAR OFF STACK
9670 052436 105777 126502  31$:      TSTB     @STKS      ;;WAIT FOR A CHAR
9671 052442 100375          BPL      31$        ;;LOOP UNTIL ITS THERE
9672 052444 117746 126476  MOVB     @STKB,-(SP) ;;GET THE CHARACTER
9673 052450 042716 177600  BIC      #1C17,(SP) ;;MAKE IT 7-BIT ASCII
9674 052454 022627 000021  CMP      (SP)+,#21  ;;IS IT A CONTROL-Q?
9675 052460 001366          BNE      31$        ;;BRANCH IF NO
9676 052462 012777 000100 126454  MOV      #100,@STKS ;;REENABLE TTY KEYBOARD INTERRUPTS
9677 052470 000002          RTI                     ;;RETURN
9678 052472 005237 052250  32$:      INC      $TKCNT     ;;COUNT THIS CHARACTER
9679 052476 021627 000140  CMP      (SP),#140  ;;IS IT UPPER CASE?
9680 052502 002405          BLT      4$          ;;BRANCH IF YES
9681 052504 021627 000175  CMP      (SP),#175  ;;IS IT A SPECIAL CHAR?
9682 052510 003002          BGT      4$          ;;BRANCH IF YES
9683 052512 042716 000040  BIC      #40,(SP)   ;;MAKE IT UPPER CASE
9684 052516 112677 177530  4$:      MOVB     (SP)+,@STKQIN ;;AND PUT IT IN QUEUE
9685 052522 005237 052252  INC      $TKQIN     ;;UPDATE THE POINTER
9686 052526 023727 052252 052257  CMP      $TKQIN,#$TKQEND ;;GO OFF THE END?
9687 052534 001003          BNE      5$          ;;BRANCH IF NO
9688 052536 012737 052256 052252  MOV      #$TKQSRT,$TKQIN ;;RESET THE POINTER
9689 052544 000002          RTI                     ;;RETURN

```

```

9690
9691  ;;*****
9692  ;;*SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
9693  ;;*ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
9694  ;;*SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP
9695  ;;*CALL WHEN OPERATING IN TTY INTERRUPT MODE.

```

```

9696 052546 022737 000176 001140 $CKSWR:  CMP      $SWREG,$SWR  ;;IS THE SOFT-SWR SELECTED
9697 052554 001124          BNE      15$        ;;EXIT IF NOT
9698 052556 105777 126362  TSTB     @STKS      ;;IS A CHAR WAITING?
9699 052562 100121          BPL      15$        ;;IF NOT, EXIT
9700 052564 117746 126356  MOVB     @STKB,-(SP) ;;YES
9701 052570 042716 177600  BIC      #1C17,(SP) ;;MAKE IT 7-BIT ASCII
9702 052574 021627 000007  CMP      (SP),#7    ;;IS IT A CONTROL-G?
9703 052600 001300          BNE      2$          ;;IF NOT, PUT IT IN THE TTY QUEUE
9704
9705
9706
9707  ;;*****
9708  ;;*CONTROL IS PASSED TO THIS POINT FROM EITHER THE TTY INTERRUPT SERVICE
9709  ;;*ROUTINE OR FROM THE SOFTWARE SWITCH REGISTER TRAP CALL, AS A RESULT OF A
9710  ;;*CONTROL-G BEING TYPED, AND THE SOFTWARE SWITCH REGISTER BEING SELECTED.

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9710 052602 123727 001134 000001 6$:      CMPB     $AUTOB,#1  ;;ARE WE RUNNING IN AUTO-MODE?
9711 052610 001674          BEQ      2$          ;;BRANCH IF YES
9712 052612 005726          TST      (SP)+      ;;CLEAR CONTROL-G OFF STACK
9713 052614 004737 052260  JSR      PC,$TKINT  ;;FLUSH THE TTY INPUT QUEUE
9714 052620 005077 126320  CLR      @STKS      ;;DISABLE TTY KEYBOARD INTERRUPTS
9715 052624 112737 000001 001135  MOVB     #1,$INTAG  ;;SET INTERRUPT MODE INDICATOR
9716
9717 052632 104401 053470          TYPE      , $CNTLG  ;;ECHO THE CONTROL-G (↑G)

```

9718	052636	104401	053475		\$GTSWR:	TYPE	\$MSWR	::	TYPE CURRENT CONTENTS
9719	052642	013746	000176			MOV	\$WREG,-(SP)	::	SAVE SWREG FOR TYPEOUT
9720	052646	104402				TYPOC		::	GO TYPE--OCTAL ASCII(ALL DIGITS)
9721	052650	104401	053506			TYPE	\$MNEW	::	PROMPT FOR NEW SWR
9722	052654	005046			19\$:	CLR	-(SP)	::	CLEAR COUNTER
9723	052656	005046				CLR	-(SP)	::	THE NEW SWR
9724	052660	105777	126260		7\$:	TSTB	\$STKS	::	CHAR THERE?
9725	052664	100375				BPL	7\$::	IF NOT TRY AGAIN
9726									
9727	052666	117746	126254			MOV	\$STKB,-(SP)	::	PICK UP CHAR
9728	052672	042716	177600			BIC	#1C17,(SP)	::	MAKE IT 7-BIT ASCII
9729									
9730	052676	021627	000003			CMP	(SP),#3	::	IS IT A CONTROL-C?
9731	052702	001015				BNE	9\$::	BRANCH IF NOT
9732	052704	104401	053456			TYPE	\$CNTLC	::	YES, ECHO CONTROL-C (↑C)
9733	052710	062706	000006			ADD	#6,SP	::	CLEAN UP STACK
9734	052714	123727	001135	000001		CMPB	\$INTAG,#1	::	REENABLE TTY KEYBOARD INTERRUPTS?
9735	052722	001003				BNE	8\$::	BRANCH IF NO
9736	052724	012777	000100	126212		MOV	#100,\$STKS	::	ALLOW TTY KEYBOARD INTERRUPTS
9737	052732	000137	047330		8\$:	JMP	STOP	::	CONTROL-C RESTART
9738									
9739									
9740	052736	021627	000025		9\$:	CMP	(SP),#25	::	IS IT A CONTROL-U?
9741	052742	001005				BNE	10\$::	BRANCH IF NOT
9742	052744	104401	053463			TYPE	\$CNTLU	::	YES, ECHO CONTROL-U (↑U)
9743	052750	062706	000006		20\$:	ADD	#6,SP	::	IGNORE PREVIOUS INPUT
9744	052754	000737				BR	19\$::	LET'S TRY IT AGAIN
9745									
9746									
9747	052756	021627	000015		10\$:	CMP	(SP),#15	::	IS IT A <CR>?
9748	052762	001022				BNE	16\$::	BRANCH IF NO
9749	052764	005766	000004			TST	4(SP)	::	YES, IS IT THE FIRST CHAR?
9750	052770	001403				BEQ	11\$::	BRANCH IF YES
9751	052772	016677	000002	126140		MOV	2(SP),\$SWR	::	SAVE NEW SWR
9752	053000	062706	000006		11\$:	ADD	#6,SP	::	CLEAN UP STACK
9753	053004	104401	001205		14\$:	TYPE	\$CRLF	::	ECHO <CR> AND <LF>
9754	053010	123727	001135	000001		CMPB	\$INTAG,#1	::	RE-ENABLE TTY KBD INTERRUPTS?
9755	053016	001003				BNE	15\$::	BRANCH IF NOT
9756	053020	012777	000100	126116		MOV	#100,\$STKS	::	RE-ENABLE TTY KBD INTERRUPTS
9757	053026	000002			15\$:	RTI		::	RETURN
9758	053030	004737	051260		16\$:	JSR	PC,\$TYPEC	::	ECHO CHAR
9759	053034	021627	000060			CMP	(SP),#60	::	CHAR < 0?
9760	053040	002420				BLT	18\$::	BRANCH IF YES
9761	053042	021627	000067			CMP	(SP),#67	::	CHAR > 7?
9762	053046	003015				BGT	18\$::	BRANCH IF YES
9763	053050	042726	000060			BIC	#60,(SP)+	::	STRIP-OFF ASCII
9764	053054	005766	000002			TST	2(SP)	::	IS THIS THE FIRST CHAR
9765	053060	001403				BEQ	17\$::	BRANCH IF YES
9766	053062	006316				ASL	(SP)	::	NO, SHIFT PRESENT
9767	053064	006316				ASL	(SP)	::	CHAR OVER TO MAKE
9768	053066	006316				ASL	(SP)	::	ROOM FOR NEW ONE.
9769	053070	005266	000002		17\$:	INC	2(SP)	::	KEEP COUNT OF CHAR
9770	053074	056616	177776			BIS	-2(SP),(SP)	::	SET IN NEW CHAR
9771	053100	000667				BR	7\$::	GET THE NEXT ONE
9772	053102	104401	001204		18\$:	TYPE	\$QUES	::	TYPE ?<CR><LF>
9773	053106	000720				BR	20\$::	SIMULATE CONTROL-U

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053110 011646
053112 016666 000004 000002
053120 005066 000004
053124 005046
053126 012746 053134
053132 000002
053134 005737 052250
053140 001775
053142 005337 052250
053146 117766 177102 000004
053154 005237 052254
053160 023727 052254 052257
053166 001003
053170 012737 052256 052254
053176 000002

053200 010346
053202 005046
053204 012703 053434
053210 022703 053456
053214 101456
053216 104410
053220 112613
053222 122713 000177
053226 001022
053230 005716
053232 001007
053234 112737 000134 053432
053242 104401 053432
053246 012716 177777
053252 005303
053254 020327 053434
053260 103434
053262 111337 053432
053266 104401 053432
053272 000746
053274 005716
053276 001406

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.DSABL LSB

;*****
;THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
;CALL:
;* RDCHR ;: GET A CHARACTER FROM THE QUEUE
;* RETURN HERE ;: CHARACTER IS ON THE STACK
;* ;: WITH PARITY BIT STRIPPED OFF

SRDCHR: MOV (SP), -(SP) ;: PUSH DOWN THE PC AND
MOV 4(SP), 2(SP) ;: THE PS
CLR 4(SP) ;: GET READY FOR A CHARACTER
CLR -(SP) ;: PUT NEW PS ON STACK
MOV #64$, -(SP) ;: PUT NEW PC ON STACK
RTI ;: POP NEW PC AND PS

64$:
1$: TST $TKCNT ;: WAIT ON A CHARACTER
BEQ 1$
DEC $TKCNT ;: DECREMENT THE COUNTER
MOVB 2$TKQOUT, 4(SP) ;: GET ONE CHARACTER
INC $TKQOUT ;: UPDATE THE POINTER
CMP $TKQOUT, #2$TKQEND ;: DID IT GO OFF OF THE END?
BNE 2$ ;: BRANCH IF NO
MOV #2$TKQSR, $TKQOUT ;: RESET THE POINTER
RTI ;: RETURN

;*****
;THIS ROUTINE WILL INPUT A STRING FROM THE TTY
;CALL:
;* RDLIN ;: INPUT A STRING FROM THE TTY
;* RETURN HERE ;: ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
;* ;: TERMINATOR WILL BE A BYTE OF ALL 0'S

SRDLIN: MOV R3, -(SP) ;: SAVE R3
CLR -(SP) ;: CLEAR THE RUBOUT KEY
1$: MOV #2$TTYIN, R3 ;: GET ADDRESS
2$: CMP #2$TTYIN+22, R3 ;: BUFFER FULL?
BLOS 4$ ;: BR IF YES
RDCHR ;: GO READ ONE CHARACTER FROM THE TTY
MOVB (SP)+, (R3) ;: GET CHARACTER
10$: CMPB #177, (R3) ;: IS IT A RUBOUT
BNE 5$ ;: BR IF NO
TST (SP) ;: IS THIS THE FIRST RUBOUT?
BNE 6$ ;: BR IF NO
MOVB #' \, 9$ ;: TYPE A BACK SLASH
TYPE 9$
MOV #-1, (SP) ;: SET THE RUBOUT KEY
6$: DEC R3 ;: BACKUP BY ONE
CMP R3, #2$TTYIN ;: STACK EMPTY?
BLOS 4$ ;: BR IF YES
MOVB (R3), 9$ ;: SETUP TO TYPEOUT THE DELETED CHAR.
TYPE 9$ ;: GO TYPE
BR 2$ ;: GO READ ANOTHER CHAR.
5$: TST (SP) ;: RUBOUT KEY SET?
BEQ 7$ ;: BR IF NO
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9830 053300 112737 000134 053432      MOVB      #' \,9$          ;;TYPE A BACK SLASH
9831 053306 104401 053432      TYPE      9$
9832 053312 005016          CLR      (SP)          ;;CLEAR THE RUBOUT KEY
9833 053314 122713 000025      7$:      CMPB     #25,(R3)  ;;IS CHARACTER A CTRL U?
9834 053320 001003          BNE      8$           ;;BR IF NO
9835 053322 104401 053463      TYPE     $CNTLU       ;;TYPE A CONTROL "U"
9836 053326 000726          BR       1$           ;;GO START OVER
9837 053330 122713 000022      8$:      CMPB     #22,(R3)  ;;IS CHARACTER A "↑R"?
9838 053334 001011          BNE      3$           ;;BRANCH IF NO
9839 053336 105013          CLRB    (R3)         ;;CLEAR THE CHARACTER
9840 053340 104401 001205      TYPE     $SCRLF      ;;TYPE A "CR" & "LF"
9841 053344 104401 053434      TYPE     $TTYIN      ;;TYPE THE INPUT STRING
9842 053350 000717          BR       2$           ;;GO PICKUP ANOTHER CHACTER
9843 053352 104401 001204      4$:      TYPE     $QUES      ;;TYPE A '?'
9844 053356 000712          BR       1$           ;;CLEAR THE BUFFER AND LOOP
9845 053360 111337 053432      3$:      MOVB     (R3),9$      ;;ECHO THE CHARACTER
9846 053364 104401 053432      TYPE     9$
9847 053370 122723 000015      CMPB     #15,(R3)+   ;;CHECK FOR RETURN
9848 053374 001305          BNE      2$           ;;LOOP IF NOT RETURN
9849 053376 105063 177777      CLRB    -1(R3)      ;;CLEAR RETURN (THE 15)
9850 053402 104401 001206      TYPE     $LF         ;;TYPE A LINE FEED
9851 053406 005726          TST     (SP)+       ;;CLEAN RUBOUT KEY FROM THE STACK
9852 053410 012603          MOV     (SP)+,R3    ;;RESTORE R3
9853 053412 011646          MOV     (SP),-(SP)  ;;ADJUST THE STACK AND PUT ADDRESS OF THE
9854 053414 016666 000004 000002      MOV     4(SP),2(SP)  ;;FIRST ASCII CHARACTER ON IT
9855 053422 012766 053434 000004      MOV     #TTYIN,4(SP)
9856 053430 000002          RTI
9857 053432 000          9$:      .BYTE    0          ;;RETURN
9858 053433 000          .BYTE    0          ;;STORAGE FOR ASCII CHAR. TO TYPE
9859 053434 000022          $TTYIN: .BLKB   22   ;;TERMINATOR
9860 053456 041536 005015 000          $CNTLC: .ASCIZ  /↑C/<15><12>  ;;RESERVE 22 BYTES FOR TTY INPUT
9861 053463 0136 006525 000012          $CNTLU: .ASCIZ  /↑U/<15><12>  ;;CONTROL "C"
9862 053470 043536 005015 000          $CNTLG: .ASCIZ  /↑G/<15><12>  ;;CONTROL "U"
9863 053475 015 051412 051127          $MSWR:  .ASCIZ  <15><12>/SWR = /  ;;CONTROL "G"
9864 053502 036440 000040          $MNEW:  .ASCIZ  / NEW = /
9865 053506 020040 042516 020127
9866 053514 020075 000
9867 053520
9868 .EVEN
9869 .SBTTL READ AN OCTAL NUMBER FROM THE TTY
9870
9871 *****
9872 *THIS ROUTINE WILL READ AN OCTAL (ASCII) NUMBER FROM THE TTY AND
9873 *CHANGE IT TO BINARY.
9874 *THE INPUT CHARACTERS WILL BE CHECKED TO INSURED THEY ARE LEGAL
9875 *OCTAL DIGITS. IF AN ILLEGAL CHARACTER IS READ A "?" WILL BE TYPED
9876 *FOLLOWED BY A CARRIAGE RETURN-LINE FEED. THE COMPLETE NUMBER MUST
9877 *THEN BE RETYPED. THE INPUT IS TERMINATED BY TYPING A CARRIAGE RETURN.
9878 *CALL:
9879 *      RDOCT          ;; READ AN OCTAL NUMBER
9880 *      RETURN HERE   ;; LOW ORDER BITS ARE ON TOP OF THE STACK
9881 *                   ;; HIGH ORDER BITS ARE IN $HIOCT
9882 053520 011646 000004 000002      $RDOCT: MOV     (SP),-(SP)  ;; PROVIDE SPACE FOR THE
9883 053522 016666          MOV     4(SP),2(SP)  ;; INPUT NUMBER
9884 053530 010046          MOV     R0,-(SP)    ;; PUSH R0 ON STACK
9885 053532 010146          MOV     R1,-(SP)    ;; PUSH R1 ON STACK

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H15

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 189
READ AN OCTAL NUMBER FROM THE TTY

SEQ 0189

9886	053534	010246			MOV R2,-(SP) ;: PUSH R2 ON STACK
9887	053536	104411		1\$:	RDLIN ;: READ AN ASCIZ LINE
9888	053540	012600			MOV (SP)+,R0 ;: GET ADDRESS OF 1ST CHARACTER
9889	053542	010037	053646		MOV R0,5\$;: AND SAVE IT
9890	053546	005001			CLR R1 ;: CLEAR DATA WORD
9891	053550	005002			CLR R2
9892	053552	112046		2\$:	MOVB (R0)+,-(SP) ;: PICKUP THIS CHARACTER
9893	053554	001420			BEQ 3\$;: IF ZERO GET OUT
9894	053556	122716	000060		CMPB #'0,(SP) ;: MAKE SURE THIS CHARACTER
9895	053562	003026			BGT 4\$;: IS AN OCTAL DIGIT
9896	053564	122716	000067		CMPB #'7,(SP)
9897	053570	002423			BLT 4\$
9898	053572	006301			ASL R1 ;: *2
9899	053574	006102			ROL R2 ;: *4
9900	053576	006301			ASL R1 ;: *4
9901	053600	006102			ROL R2 ;: *8
9902	053602	006301			ASL R1 ;: *8
9903	053604	006102			ROL R2
9904	053606	042716	177770		BIC #'C7,(SP) ;: STRIP THE ASCII JUNK
9905	053612	062601			ADD (SP)+,R1 ;: ADD IN THIS DIGIT
9906	053614	000756			BR 2\$;: LOOP
9907	053616	005726		3\$:	TST (SP)+ ;: CLEAN TERMINATOR FROM STACK
9908	053620	010166	000012		MOV R1,12(SP) ;: SAVE THE RESULT
9909	053624	010237	053656		MOV R2,\$HIOCT
9910	053630	012602			MOV (SP)+,R2 ;: POP STACK INTO R2
9911	053632	012601			MOV (SP)+,R1 ;: POP STACK INTO R1
9912	053634	012600			MOV (SP)+,R0 ;: POP STACK INTO R0
9913	053636	000002			RTI ;: RETURN
9914	053640	005726		4\$:	TST (SP)+ ;: CLEAN PARTIAL FROM STACK
9915	053642	105010			CLRB (R0) ;: SET A TERMINATOR
9916	053644	104401			TYPE ;: TYPE UP THRU THE BAD CHAR.
9917	053646	000000		5\$:	.WORD 0
9918	053650	104401	001204		TYPE \$QUES ;: "?" "CR" & "LF"
9919	053654	000730			BR 1\$;: TRY AGAIN
9920	053656	000000			\$HIOCT: .WORD 0 ;: HIGH ORDER BITS GO HERE
9921					.SBTTL DOUBLE LENGTH BINARY TO OCTAL ASCII CONVERT ROUTINE
9922					*****
9923					;; THIS ROUTINE WILL CONVERT A 32-BIT UNSIGNED BINARY NUMBER TO AN
9924					;; UNSIGNED OCTAL ASCIZ NUMBER.
9925					;; *CALL
9926					;; *
9927					MOV #PNTR,-(SP) ;: POINTER TO LOW WORD OF BINARY NUMBER
9928					JSR PC,@#\$DB20 ;: CALL THE ROUTINE
9929					;; *
9930					RETURN ;: THE ADDRESS OF THE FIRST ASCIZ CHAR. IS ON THE STACK
9931					;; *
9932	053660	104413		\$DB20:	SAVREG ;: SAVE ALL REGISTERS
9933	053662	016601	000002		MOV 2(SP),R1 ;: PICKUP THE POINTER TO LOW WORD
9934	053666	012705	053777		MOV #\$OCTVL+13.,R5 ;: POINTER TO DATA TABLE
9935	053672	012704	000014		MOV #12.,R4 ;: DO ELEVEN CHARACTERS
9936	053676	012703	177770		MOV #'C7,R3 ;: MASK
9937	053702	012100			MOV (R1)+,R0 ;: LOWER WORD
9938	053704	012101			MOV (R1)+,R1 ;: HIGH WORD
9939	053706	005002			CLR R2 ;: TERMINATOR
9940	053710	110245		1\$:	MOVB R2,-(R5) ;: PUT CHARACTER IN DATA TABLE
9941	053712	010002			MOV R0,R2 ;: GET THIS DIGIT

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9942 053714 005304          DEC      R4          ;; COUNT THIS CHARACTER
9943 053716 003007          BGT     3$          ;; BR IF NOT THE LAST DIGIT
9944 053720 001405          BEQ     2$          ;; BR IF IT IS THE LAST DIGIT
9945 053722 005205          INC     R5          ;; ALL DIGITS DONE-ADJUST POINTER FOR FIRST
9946 053724 010566 000002   MOV     R5,2(SP)   ;; ASCIZ CHAR. & PUT IT ON THE STACK
9947 053730 104414          RESREG          ;; RESTORE ALL REGISTERS
9948 053732 000207          RTS     PC          ;; RETURN TO USER
9949 053734 006203          2$: ASR     R3          ;; POSITION THE MASK FOR THE LAST DIGIT
9950 053736 006001          3$: ROR     R1          ;; POSITION THE BINARY NUMBER FOR
9951 053740 006000          ROR     R0          ;; THE NEXT OCTAL DIGIT
9952 053742 006001          ROR     R1
9953 053744 006000          ROR     R0
9954 053746 006001          ROR     R1
9955 053750 006000          ROR     R0
9956 053752 040302          BIC     R3,R2      ;; MASK OUT ALL JUNK
9957 053754 062702 000060   ADD     #'0,R2     ;; MAKE THIS CHAR. ASCII
9958 053760 000753          BR      1$          ;; GO PUT IT IN THE DATA TABLE
9959 053762 000016          $OCTVL: .BLKB 14.  ;; RESERVE DATA TABLE
9960                                     .SBTTL DOUBLE LENGTH BINARY TO DECIMAL ASCII CONVERT ROUTINE
9961
9962                                     ;*****
9963                                     ;*THIS ROUTINE WILL CONVERT A 32-BIT BINARY NUMBER TO AN UNSIGNED
9964                                     ;*DECIMAL (ASCII) NUMBER. THE SIGN OF THE BINARY NUMBER MUST BE
9965                                     ;*POSITIVE.
9966                                     ;*CALL
9967                                     ;*      MOV     #PNTR, -(SP)  ;; POINTER TO LOW WORD OF BINARY NUMBER
9968                                     ;*      JSR     PC, @#$DB2D
9969                                     ;*      RETURN          ;; THE FIRST ADDRESS OF ASCIZ
9970                                     ;*                                     ;; IS ON THE STACK
9971
9972                                     $DB2D: SAVREG          ;; SAVE REGISTERS
9973 054000 104413          MOV     2(SP),R2   ;; PICKUP THE DATA POINTER
9974 054002 016602 000002   MOV     #$DECVL,R0 ;; GET ADDRESS OF "$DECVL" STRING
9975 054006 012700 054160   MOV     R0,2(SP)  ;; PUT ADDRESS OF ASCIZ STRING ON STACK
9976 054012 010066 000002   MOV     (R2)+,R1  ;; PICKUP THE BINARY NUMBER
9977 054016 012201          MOV     (R2)+,R2
9978 054020 012202          MOV     #10.4$,R3 ;; SET UP TO DO 10 CONVERSIONS
9979 054022 012737 000012 054076   MOV     #$TNPWR,R4 ;; ADDRESS OF TEN POWER
9980 054030 012704 054110   MOV     #$TNPWR+2,R5
9981 054034 012705 054112          1$: CLR     R3          ;; CLEAR PARTIAL
9982 054040 005003          2$: SUB     (R4),R1  ;; SUBTRACT TEN POWER
9983 054042 161401          SBC     R2
9984 054044 005602          SUB     (R5),R2
9985 054046 161502          BLT     3$          ;; BR IF TEN POWER TO LARGE
9986 054050 002402          INC     R3          ;; ADD 1 TO PARTIAL
9987 054052 005203          BR      2$          ;; LOOP
9988 054054 000772          3$: ADD     (R4)+,R1  ;; RESTORE SUBTRACTED VALUE
9989 054056 062401          ADC     R2
9990 054060 005502          ADD     (R4)+,R2
9991 054062 062402          CMP     (R5)+,(R5)+ ;; MOVE TO NEXT TEN POWER
9992 054064 022525          BIS     #'0,R3     ;; CHANGE PARTIAL TO ASCII
9993 054066 052703 000060   MOVB   R3,(R0)+   ;; SAVE IT
9994 054072 110320          DEC     (PC)+     ;; DONE?
9995 054074 005327          .WORD  0
9996 054076 000000          4$: BNE     1$          ;; BR IF NO
9997 054100 001357

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9998 054102 105020          CLRB      (RO)+          ;; TERMINATOR
9999 054104 104414          RESREG          ;; RESTORE REGISTERS
10000 054106 000207          RTS           PC          ;; RETURN
10001 054110 145000          $STNPWR: 145000          ;; 1.0E09
10002 054112 035632          35632
10003 054114 160400          160400          ;; 1.0E08
10004 054116 002765          2765
10005 054120 113200          113200          ;; 1.0E07
10006 054122 000230          230
10007 054124 041100          041100          ;; 1.0E06
10008 054126 000017          17
10009 054130 103240          103240          ;; 1.0E05
10010 054132 000001          1
10011 054134 023420          23420          ;; 1.0E04
10012 054136 000000          0
10013 054140 001750          1750          ;; 1.0E03
10014 054142 000000          0
10015 054144 000144          144          ;; 1.0E02
10016 054146 000000          0
10017 054150 000012          12          ;; 1.0E01
10018 054152 000000          0
10019 054154 000001          1          ;; 1.0E00
10020 054156 000000          0
10021 054160 000014          $DECVL: .BLKB 12.      ;; RESERVE STORAGE FOR ASCIZ STRING
10022                                     .SBTTL SINGLE LENGTH BINARY TO DECIMAL ASCII ROUTINE
10023
10024                                     ;; *****
10025                                     ;; THIS ROUTINE WILL CONVERT A 16-BIT UNSIGNED BINARY NUMBER TO AN
10026                                     ;; UNSIGNED DECIMAL ASCII NUMBER.
10027                                     ;; CALL
10028                                     ;;      MOV      NUMBER, -(SP)          ;; PUT BINARY NUMBER ON THE STACK
10029                                     ;;      JSR      PC, @#$SB2D          ;; CALL
10030                                     ;;      RETURN          ;; ADDRESS OF THE 1ST ASCII CHAR. IS ON THE STACK
10031
10032
10033 054174 016637 000002 054224 $SB2D: MOV      2(SP), 1$          ;; SAVE BINARY NUMBER
10034 054202 012746 054224          MOV      #1$, -(SP)          ;; SET POINTER
10035 054206 004737 054000          JSR      PC, @#$SDB2D          ;; CALL DOUBLE LENGTH CONVERT
10036 054212 062716 000005          ADD      #5, (SP)          ;; ONLY ALLOW FIVE CHARACTERS
10037 054216 012666 000002          MOV      (SP)+, 2(SP)          ;; PICKUP POINTER
10038 054222 000207          RTS      PC          ;; RETURN
10039 054224 000000 000000          1$:      WORD 0,0
10040                                     .SBTTL TYPE NUMERICAL ASCII STRING SUPPRESS LEADING ZEROS
10041
10042                                     ;; *****
10043                                     ;; THIS ROUTINE IS USED TO TYPE AN ASCII NUMBER SUPPRESSING THE
10044                                     ;; LEADING NUMBERS.
10045                                     ;; CALL
10046                                     ;;      MOV      #NUMADR, -(SP)          ;; FIRST ADDRESS OF ASCII STRING
10047                                     ;;      JSR      PC, @#$SUPRS
10048
10049
10050 054230 010046          $SUPRS: MOV      RO, -(SP)          ;; SAVE RO
10051 054232 016600 000004          MOV      4(SP), RO          ;; PICKUP THE POINTER
10052 054236 105710          1$:      TSTB      (RO)          ;; TERMINATE OR?
10053 054240 001403          BEQ      2$          ;; BR IF YES

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```

10054 054242 122720 000060
10055 054246 001773
10056 054250 005300
10057 054252 010037 054260
10058 054256 104401
10059 054260 000000
10060 054262 012600
10061 054264 012616
10062 054266 000207
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10077 054270
10078 054270 010046
10079 054272 010146
10080 054274 010246
10081 054276 005046
10082 054300 016601 000012
10083 054304 100002
10084 054306 005216
10085 054310 005401
10086 054312 016602 000014
10087 054316 100002
10088 054320 005316
10089 054322 005402
10090 054324 012746 000021
10091 054330 005000
10092 054332 103001
10093 054334 060200
10094 054336 006000
10095 054340 006001
10096 054342 005316
10097 054344 001372
10098 054346 022616
10099 054350 001403
10100 054352 005400
10101 054354 005401
10102 054356 005600
10103 054360 005726
10104 054362 010066 000012
10105 054366 010166 000010
10106 054372 012602
10107 054374 012601
10108 054376 012600
10109 054400 000207

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```

          CMPB    #'0,(RO)+      ;; IS THIS AN ASCII "0" ?
          BEQ     1$              ;; BR IF YES
2$:      DEC     RO                ;; BACKUP BY "1"
          MOV     RO,3$           ;; SAVE FOR TYPING
          TYPE                    ;; GO TYPE
3$:      .WORD   0                ;; ASCIZ POINTER GOES HERE
          MOV     (SP)+,RO        ;; RESTORE RO
          MOV     (SP)+,(SP)      ;; RESTORE THE STACK
          RTS     PC              ;; RETURN
.SBTTL   INTEGER MULTIPLY ROUTINE
;*****
;CALL
;      MOV     MULTIPLIER,-(SP)
;      MOV     MULTIPLICAND,-(SP)
;      JSR     PC,@$MULT
;      RETURN  ;;PRODUCT IS ON THE STACK
;
;      STACK  PRODUCT
;      -----
;      TOP    LSB'S
;      +2     MSB'S
;
$MULT:   MOV     RO,-(SP)          ;; PUSH RO ON STACK
          MOV     R1,-(SP)          ;; PUSH R1 ON STACK
          MOV     R2,-(SP)          ;; PUSH R2 ON STACK
          CLR     -(SP)             ;; CLEAR THE SIGN KEY
          MOV     12(SP),R1         ;; GET THE MULTIPLICAND
          BPL     1$                ;; BR IF PLUS
          INC     (SP)              ;; SET THE SIGN KEY
          NEG     R1                 ;; MAKE THE MULTIPLICAND POSTIVE
1$:      MOV     14(SP),R2         ;; GET THE MULTIPLIER
          BPL     2$                ;; BR IF PLUS
          DEC     (SP)              ;; UPDATE THE SIGN KEY
          NEG     R2                 ;; MAKE THE MULTIPLIER POSTIVE
2$:      MOV     #17,-(SP)         ;; SET THE LOOP COUNT
          CLR     RO                ;; SETUP FOR THE MULTIPLY LOOP
3$:      BCC     4$                ;; DON'T ADD IF MULTIPLICAND = 0
          ADD     R2,RO
4$:      ROR     RO                ;; POSITION THE PARITIAL PRODUCT AND
          ROR     R1                ;; THE MULTIPLICAND
          DEC     (SP)              ;; HAS ALL BITS OF THE MULTIPLICAND BEEN DONE?
          BNE     3$                ;; BR IF NO
          CMP     (SP)+,(SP)        ;; SHOULD PRODUCT BE NEGATIVE?
          BEQ     5$                ;; GO TO EXIT IF NO
          NEG     RO                ;; YES--SO MAKE IT SO
          NEG     R1
          SBC     RO
5$:      TST     (SP)+              ;; CLEAR SIGN INFO. OFF OF STACK
          MOV     RO,12(SP)         ;; PUT THE PRODUCT ON THE STACK (MSB'S)
          MOV     R1,10(SP)         ;; LSB'S
          MOV     (SP)+,R2         ;; POP STACK INTO R2
          MOV     (SP)+,R1         ;; POP STACK INTO R1
          MOV     (SP)+,RO         ;; POP STACK INTO RO
          RTS     PC

```

.SBTTL SAVE AND RESTORE RO-R5 ROUTINES

```

*****
*SAVE RO-R5
*CALL:
* SAVREG
*UPON RETURN FROM $$SAVREG THE STACK WILL LOOK LIKE:
*
*TOP---(+16)
* +2---(+18)
* +4---R5
* +6---R4
* +8---R3
*+10---R2
*+12---R1
*+14---R0

```

\$\$SAVREG:

```

MOV RO,-(SP) ;; PUSH RO ON STACK
MOV R1,-(SP) ;; PUSH R1 ON STACK
MOV R2,-(SP) ;; PUSH R2 ON STACK
MOV R3,-(SP) ;; PUSH R3 ON STACK
MOV R4,-(SP) ;; PUSH R4 ON STACK
MOV R5,-(SP) ;; PUSH R5 ON STACK
MOV 22(SP),-(SP) ;; SAVE PS OF MAIN FLOW
MOV 22(SP),-(SP) ;; SAVE PC OF MAIN FLOW
MOV 22(SP),-(SP) ;; SAVE PS OF CALL
MOV 22(SP),-(SP) ;; SAVE PC OF CALL
RTI

```

*RESTORE RO-R5

*CALL:

* RESREG

\$\$RESREG:

```

MOV (SP)+,22(SP) ;; RESTORE PC OF CALL
MOV (SP)+,22(SP) ;; RESTORE PS OF CALL
MOV (SP)+,22(SP) ;; RESTORE PC OF MAIN FLOW
MOV (SP)+,22(SP) ;; RESTORE PS OF MAIN FLOW
MOV (SP)+,R5 ;; POP STACK INTO R5
MOV (SP)+,R4 ;; POP STACK INTO R4
MOV (SP)+,R3 ;; POP STACK INTO R3
MOV (SP)+,R2 ;; POP STACK INTO R2
MOV (SP)+,R1 ;; POP STACK INTO R1
MOV (SP)+,R0 ;; POP STACK INTO R0
RTI

```

.SBTTL TRAP DECODER

```

*****
*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
*GO TO THAT ROUTINE.

```

```

$TRAP: MOV RO,-(SP) ;; SAVE RO
MOV 2(SP),RO ;; GET TRAP ADDRESS
TST -(RO) ;; BACKUP BY 2

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10110
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10127 054402
10128 054402 010046
10129 054404 010146
10130 054406 010246
10131 054410 010346
10132 054412 010446
10133 054414 010546
10134 054416 016646 000022
10135 054422 016646 000022
10136 054426 016646 000022
10137 054432 016646 000022
10138 054436 000002
10139
10140
10141
10142
10143 054440
10144 054440 012666 000022
10145 054444 012666 000022
10146 054450 012666 000022
10147 054454 012666 000022
10148 054460 012605
10149 054462 012604
10150 054464 012603
10151 054466 012602
10152 054470 012601
10153 054472 012600
10154 054474 000002
10155
10156
10157
10158
10159
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10161
10162
10163 054476 010046
10164 054500 016600 000002
10165 054504 005740

```

```

10166 054506 111000          MOVB   (R0),R0          ;;GET RIGHT BYTE OF TRAP
10167 054510 006300          ASL    R0              ;;POSITION FOR INDEXING
10168 054512 016000 054532    MOV    $TRPAD(R0),R0   ;;INDEX TO TABLE
10169 054516 000200          RTS    R0              ;;GO TO ROUTINE
10170
10171
10172          ;;THIS IS USE TO HANDLE THE "GETPRI" MACRO
10173
10174 054520 011646          $TRAP2: MOV   (SP),-(SP) ;;MOVE THE PC DOWN
10175 054522 016666 000004 000002  MOV    4(SP),2(SP)    ;;MOVE THE PSW DOWN
10176 054530 000002          RTI                    ;;RESTORE THE PSW
10177
10178          .SBTTL TRAP TABLE
10179
10180          ;*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
10181          ;*BY THE "TRAP" INSTRUCTION.
10182
10183          ;          ROUTINE
10184          ;          -----
10185 054532 054520          $TRPAD: .WORD  $TRAP2
10186 054534 051046          $TYPE   ;;CALL=TYPE      TRAP+1(104401) TTY TYPEOUT ROUTINE
10187 054536 052046          $TYPOC  ;;CALL=TYPOC     TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS)
10188 054540 052022          $TYPOS  ;;CALL=TYPOS     TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS)
10189 054542 052062          $TYPON  ;;CALL=TYPON     TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL)
10190 054544 051330          $TYPDS  ;;CALL=TYPDS     TRAP+5(104405) TYPE DECIMAL NUMBER (WITH SIGN)
10191
10192 054546 052636          $GTSWR  ;;CALL=GTSWR     TRAP+6(104406) GET SOFT-SWR SETTING
10193
10194 054550 052546          $CKSWR  ;;CALL=CKSWR     TRAP+7(104407) TEST FOR CHANGE IN SOFT-SWR
10195 054552 053110          $RDCHR  ;;CALL=RDCHR     TRAP+10(104410) TTY TYPEIN CHARACTER ROUTINE
10196 054554 053200          $RDLIN  ;;CALL=RDLIN     TRAP+11(104411) TTY TYPEIN STRING ROUTINE
10197 054556 053520          $RDOCT  ;;CALL=RDOCT     TRAP+12(104412) READ AN OCTAL NUMBER FROM TTY
10198 054560 054402          $$SAVREG ;;CALL=SAVREG          TRAP+13(104413) SAVE R0-R5 ROUTINE
10199 054562 054440          $RESREG ;;CALL=RESREG          TRAP+14(104414) RESTORE R0-R5 ROUTINE
10200 054564 047276          $SCOP1$ ;;CALL=SCOP1           TRAP+15(104415) INTERNAL LOOP ON ERROR
10201

```

10202				
10203				
10204				
10205	054566	005015	047125	041111
10206	054574	051525	051040	030113
10207	054602	026466	045522	033460
10208	054610	042040	040525	020114
10209	054616	047520	052122	042040
10210	054624	044522	042526	042040
10211	054632	040511	047107	051517
10212	054640	044524	103	
10213	054643	015	041412	051132
10214	054650	043466	030102	005015
10215	054656	005015	025011	025052
10216	054664	025052	041440	052501
10217	054672	044524	047117	025040
10218	054700	025052	025052	005015
10219	054706	005015	044124	051511
10220	054714	050040	047522	051107
10221	054722	046501	051440	047510
10222	054730	046125	020104	041040
10223	054736	020105	040510	052114
10224	054744	042105	047440	046116
10225	054752	020131	054502	052040
10226	054760	050131	047111	020107
10227	054766	047503	052116	047522
10228	054774	026514	103	
10229	054777	015	047412	044124
10230	055004	051105	044527	042523
10231	055012	020054	040503	052122
10232	055020	044522	043504	020105
10233	055026	047506	046522	052101
10234	055034	044524	043516	040440
10235	055042	042116	020054	051117
10236	055050	052040	042510	042040
10237	055056	044522	042526	
10238	055062	005015	040515	020131
10239	055070	042502	046040	043105
10240	055076	020124	047111	040440
10241	055104	020116	047125	042504
10242	055112	042524	046522	047111
10243	055120	042105	051440	040524
10244	055126	042524		
10245	055130	005015	047111	052111
10246	055136	040511	046114	026131
10247	055144	042040	044522	042526
10248	055152	020123	047524	041040
10249	055160	020105	042524	052123
10250	055166	042105	051440	047510
10251	055174	046125	020104	040510
10252	055202	042526	006472	012
10253	055207	015	040412	020056
10254	055214	053105	047105	047040
10255	055222	046525	042502	042522
10256	055230	020104	047125	052111
10257	055236	051440	046105	041505

.SBTTL SERVICE MESSAGES

MSG1: .ASCII <CR><LF>/UNIBUS RK06-RK07 DUAL PORT DRIVE DIAGNOSTIC/

.ASCII <CR><LF>/CZR6GB0/<CR><LF>

.ASCII <CR><LF>/ ***** CAUTION *****/<CR><LF>

.ASCII <CR><LF>/THIS PROGRAM SHOULD BE HALTED ONLY BY TYPING CONTROL-C/

.ASCII <CR><LF>/OTHERWISE, CARTRIDGE FORMATTING AND, OR THE DRIVE/

.ASCII <CR><LF>/MAY BE LEFT IN AN UNDETERMINED STATE/

.ASCII <CR><LF>/INITIALLY, DRIVES TO BE TESTED SHOULD HAVE://<CR><LF>

.ASCII <CR><LF>/A. EVEN NUMBERED UNIT SELECT PLUGS ONLY/

10258	055244	020124	046120	043525	
10259	055252	020123	047117	054514	
10260	055260	005015	027102	044040	.ASCII <CR><LF>/B. HEADS MANUALLY LOADED/
10261	055266	040505	051504	046440	
10262	055274	047101	040525	046114	
10263	055302	020131	047514	042101	
10264	055310	042105			
10265	055312	005015	027103	041040	.ASCII <CR><LF>/C. BOTH PORTS SELECTED/
10266	055320	052117	020110	047520	
10267	055326	052122	020123	042523	
10268	055334	042514	052103	042105	
10269	055342	005015	027104	042040	.ASCII <CR><LF>/D. DUAL PORT TEST SWITCH ENABLED/
10270	055350	040525	020114	047520	
10271	055356	052122	052040	051505	
10272	055364	020124	053523	052111	
10273	055372	044103	042440	040516	
10274	055400	046102	042105		
10275	055404	005015	027105	053440	.ASCII <CR><LF>/E. WRITE LOCK DISABLED/
10276	055412	044522	042524	046040	
10277	055420	041517	020113	044504	
10278	055426	040523	046102	042105	
10279	055434	005015	027106	042040	.ASCII <CR><LF>/F. DRIVE READY INDICATOR ON/<CR><LF>
10280	055442	044522	042526	051040	
10281	055450	040505	054504	044440	
10282	055456	042116	041511	052101	
10283	055464	051117	047440	006516	
10284	055472	012			
10285	055473	015	042012	044522	.ASCII <CR><LF>/DRIVES NOT TO BE TESTED MUST HAVE/
10286	055500	042526	020123	047516	
10287	055506	020124	047524	041040	
10288	055514	020105	042524	052123	
10289	055522	042105	046440	051525	
10290	055530	020124	040510	042526	
10291	055536	005015	047502	044124	.ASCII <CR><LF>/BOTH PORTS DESELECTED/<CR><LF>
10292	055544	050040	051117	051524	
10293	055552	042040	051505	046105	
10294	055560	041505	042524	006504	
10295	055566	012			
10296	055567	015	042012	051511	.ASCII <CR><LF>/DISABLE DUAL PORT TEST SWITCH ON DUAL PORT MODULE/
10297	055574	041101	042514	042040	
10298	055602	040525	020114	047520	
10299	055610	052122	052040	051505	
10300	055616	020124	053523	052111	
10301	055624	044103	047440	020116	
10302	055632	052504	046101	050040	
10303	055640	051117	020124	047515	
10304	055646	052504	042514		
10305	055652	005015	042502	047506	.ASCIZ <CR><LF>/BEFORE PROCEEDING TO ANY OTHER PROGRAM/<CR><LF>
10306	055660	042522	050040	047522	
10307	055666	042503	042105	047111	
10308	055674	020107	047524	040440	
10309	055702	054516	047440	044124	
10310	055710	051105	050040	047522	
10311	055716	051107	046501	005015	
10312	055724	000			
10313	055725	015	041012	020105	MSG2: .ASCIZ <CR><LF>/BE SURE TO PUT SCRATCH PACK IN DRIVE 0/

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46MACY11 30A(1052) 03-JAN-78 08:56 PAGE 197
SERVICE MESSAGES

SEQ 0197

10314	055732	052523	042522	052040	
10315	055740	020117	052520	020124	
10316	055746	041523	040522	041524	
10317	055754	020110	040520	045503	
10318	055762	044440	020116	051104	
10319	055770	053111	020105	000060	
10320	055776	005015	051104	053111	MSG3: .ASCIZ <CR><LF>/DRIVE(S) TO BE TESTED (EVEN NOS. ONLY): /
10321	056004	024105	024523	052040	
10322	056012	020117	042502	052040	
10323	056020	051505	042524	020104	
10324	056026	042450	042526	020116	
10325	056034	047516	027123	047440	
10326	056042	046116	024531	020072	
10327	056050	000			
10328	056051	015	041012	051525	MSG4: .ASCIZ <CR><LF>/BUSS ADDRESS (177440): /
10329	056056	020123	042101	051104	
10330	056064	051505	020123	030450	
10331	056072	033467	032064	024460	
10332	056100	020072	000		
10333	056103	015	041412	047117	MSG5: .ASCIZ <CR><LF>/CONTROLLER INTERRUPT VECTOR (210): /
10334	056110	051124	046117	042514	
10335	056116	020122	047111	042524	
10336	056124	051122	050125	020124	
10337	056132	042526	052103	051117	
10338	056140	024040	030462	024460	
10339	056146	020072	000		
10340	056151	015	044412	052116	MSG6: .ASCIZ <CR><LF>/INTERRUPT OCCURRED AT PC= /
10341	056156	051105	052522	052120	
10342	056164	047440	041503	051125	
10343	056172	042522	020104	052101	
10344	056200	050040	036503	000	
10345	056205	015	042012	044522	MSG7: .ASCIZ <CR><LF>/DRIVE 0 WILL NOT BE TESTED /
10346	056212	042526	030040	053440	
10347	056220	046111	020114	047516	
10348	056226	020124	042502	052040	
10349	056234	051505	042524	000104	
10350	056242	005015	005015	046101	MSG8: .ASCIZ <CR><LF><CR><LF>/ALL DRIVES TESTED/<CR><LF><LF>
10351	056250	020114	051104	053111	
10352	056256	051505	052040	051505	
10353	056264	042524	006504	005012	
10354	056272	000			
10355	056273	015	005012	044527	MSG10: .ASCIZ <CR><LF><LF>/WILL TEST DRIVES: /
10356	056300	046114	052040	051505	
10357	056306	020124	051104	053111	
10358	056314	051505	000072		
10359	056320	005015	050012	053517	MSG11: .ASCIZ <CR><LF><LF>/POWER UP RESTART TO TEST 1/<CR><LF>
10360	056326	051105	052440	020120	
10361	056334	042522	052123	051101	
10362	056342	020124	047524	052040	
10363	056350	051505	020124	006461	
10364	056356	000012			
10365	056360	005015	047516	046040	MSG13: .ASCII <CR><LF>/NO L OR P CLOCKS PRESENT /
10366	056366	047440	020122	020120	
10367	056374	046103	041517	051513	
10368	056402	050040	042522	042523	
10369	056410	052116			

10370	056412	005015	046101	020114		.ASCIZ <CR><LF>/ALL TESTS BYPASSED/
10371	056420	042524	052123	020123		
10372	056426	054502	040520	051523		
10373	056434	042105	000			
10374	056437	015	041012	050131	MSG14:	.ASCIZ <CR><LF>/BYPASSING DRIVE /
10375	056444	051501	044523	043516		
10376	056452	042040	044522	042526		
10377	056460	000040				
10378	056462	005015	042012	044522	MSG15:	.ASCIZ <CR><LF><LF>/DRIVE /
10379	056470	042526	000040			
10380	056474	005015	042012	044522	MSG16:	.ASCIZ <CR><LF><LF>/DRIVE SERIAL NO. /
10381	056502	042526	051440	051105		
10382	056510	040511	020114	047516		
10383	056516	020056	000			
10384	056521	015	041412	051101	MSG17:	.ASCIZ <CR><LF>/CARTRIDGE SERIAL NO. /
10385	056526	051124	042111	042507		
10386	056534	051440	051105	040511		
10387	056542	020114	047516	020056		
10388	056550	000				
10389	056551	015	050012	051117	MSG19:	.ASCII <CR><LF>/PORT /
10390	056556	020124				
10391	056560	006440	000012		MSG19A:	.ASCIZ / /<CR><LF>
10392	056564	005015	047520	052122	MSG20:	.ASCIZ <CR><LF>/PORT A TIMEOUT: /
10393	056572	040440	052040	046511		
10394	056600	047505	052125	020072		
10395	056606	000				
10396	056607	015	040412	047502	MSG21:	.ASCIZ <CR><LF>/ABORTING BALANCE OF TESTS/<CR><LF>
10397	056614	052122	047111	020107		
10398	056622	040502	040514	041516		
10399	056630	020105	043117	052040		
10400	056636	051505	051524	005015		
10401	056644	000				
10402	056645	040	051515	000	MSG22:	.ASCIZ / MS/
10403	056651	015	050012	051117	MSG23:	.ASCIZ <CR><LF>/PORT B TIMEOUT: /
10404	056656	020124	020102	044524		
10405	056664	042515	052517	035124		
10406	056672	000040				
10407	056674	005015	051120	043517	MSG74:	.ASCIZ <CR><LF>/PROGRAM ABORT PENDING...PLEASE WAIT/
10408	056702	040522	020115	041101		
10409	056710	051117	020124	042520		
10410	056716	042116	047111	027107		
10411	056724	027056	046120	040505		
10412	056732	042523	053440	044501		
10413	056740	000124				
10414	056742	005015	040510	052114	MSG75:	.ASCIZ <CR><LF>/HALT PENDING...PLEASE WAIT/
10415	056750	050040	047105	044504		
10416	056756	043516	027056	050056		
10417	056764	042514	051501	020105		
10418	056772	040527	052111	000		
10419	056777	015	050012	047522	MSG76:	.ASCIZ <CR><LF>/PROGRAM ABORTED/
10420	057004	051107	046501	040440		
10421	057012	047502	052122	042105		
10422	057020	000				
10423	057021	015	041412	052520	MSG77:	.ASCIZ <CR><LF>/CPU HALTED/
10424	057026	044040	046101	042524		
10425	057034	000104				

10426						
10427						
10428						.SBTTL ERROR MESSAGES
10429	057036	005015	047523	051122	EM1:	.ASCIZ <CR><LF>/SORRY, ONLY 0,2,4,6 ALLOWED, TRY AGAIN/<CR><LF>
10430	057044	026131	047440	046116		
10431	057052	020131	026060	026062		
10432	057060	026064	020066	046101		
10433	057066	047514	042527	026104		
10434	057074	052040	054522	040440		
10435	057102	040507	047111	005015		
10436	057110	000				
10437	057111	104	044522	042526	EM2:	.ASCIZ /DRIVE # IN RKCS2 CANNOT BE READ BACK CORRECTLY IN RKMR2/
10438	057116	021440	044440	020116		
10439	057124	045522	051503	020062		
10440	057132	040503	047116	052117		
10441	057140	041040	020105	042522		
10442	057146	042101	041040	041501		
10443	057154	020113	047503	051122		
10444	057162	041505	046124	020131		
10445	057170	047111	051040	046513		
10446	057176	031122	000			
10447	057201	015	040412	047502	EM3:	.ASCIZ <CR><LF>/ABORT TESTS...UNEXPECTED TIME OUT AT PC=/ /
10448	057206	052122	052040	051505		
10449	057214	051524	027056	052456		
10450	057222	042516	050130	041505		
10451	057230	042524	020104	044524		
10452	057236	042515	047440	052125		
10453	057244	040440	020124	041520		
10454	057252	000075				
10455	057254	005015	041101	051117	EM4:	.ASCIZ <CR><LF>/ABORT TESTS...UNEXPECTED INTERRUPT AT PC=/ /
10456	057262	020124	042524	052123		
10457	057270	027123	027056	047125		
10458	057276	054105	042520	052103		
10459	057304	042105	044440	052116		
10460	057312	051105	052522	052120		
10461	057320	040440	020124	041520		
10462	057326	000075				
10463	057330	042115	020123	042523	EM5:	.ASCIZ /MDS SET IN RKCS2/
10464	057336	020124	047111	051040		
10465	057344	041513	031123	000		
10466	057351	125	042506	051440	EM6:	.ASCIZ /UFE SET IN RKCS2/
10467	057356	052105	044440	020116		
10468	057364	045522	051503	000062		
10469	057372	047516	042040	040522	EM7:	.ASCII /NO DRA IN RKDS & NO NED IN RKCS2/
10470	057400	044440	020116	045522		
10471	057406	051504	023040	047040		
10472	057414	020117	042516	020104		
10473	057422	047111	051040	041513		
10474	057430	031123				
10475	057432	005015	044103	041505		.ASCII <CR><LF>/CHECK 1. THE PORT A USING DRIVE FLOP (DP1)/
10476	057440	020113	027061	052040		
10477	057446	042510	050040	051117		
10478	057454	020124	020101	051525		
10479	057462	047111	020107	051104		
10480	057470	053111	020105	046106		
10481	057476	050117	024040	050104		

10482	057504	024461							
10483	057506	005015	027062	042040				.ASCIZ	<CR><LF>/2. DRIVE AVAILABLE TO PORT A STATUS (DP2)/
10484	057514	044522	042526	040440					
10485	057522	040526	046111	041101					
10486	057530	042514	052040	020117					
10487	057536	047520	052122	040440					
10488	057544	051440	040524	052524					
10489	057552	020123	042050	031120					
10490	057560	000051							
10491	057562	051104	053111	020105	EMB:			.ASCIZ	/DRIVE PRESENT BUT NOT SPECIFIED BY OPERATOR/
10492	057570	051120	051505	047105					
10493	057576	020124	052502	020124					
10494	057604	047516	020124	050123					
10495	057612	041505	043111	042511					
10496	057620	020104	054502	047440					
10497	057626	042520	040522	047524					
10498	057634	000122							
10499	057636	051104	053111	020105	EM9:			.ASCIZ	/DRIVE NOT PRESENT BUT SPECIFIED BY OPERATOR/
10500	057644	047516	020124	051120					
10501	057652	051505	047105	020124					
10502	057660	052502	020124	050123					
10503	057666	041505	043111	042511					
10504	057674	020104	054502	047440					
10505	057702	042520	040522	047524					
10506	057710	000122							
10507	057712	041101	051117	020124	EM10:			.ASCIZ	/ABORT TESTS...CANNOT REFERENCE CONTROLLER REGISTER/
10508	057720	042524	052123	027123					
10509	057726	027056	040503	047116					
10510	057734	052117	051040	043105					
10511	057742	051105	047105	042503					
10512	057750	041440	047117	051124					
10513	057756	046117	042514	020122					
10514	057764	042522	044507	052123					
10515	057772	051105	000						
10516	057775	104	040522	044440	EM11:			.ASCIZ	/DRA IN RKDS & NED IN RKCS2 BOTH SET/
10517	060002	020116	045522	051504					
10518	060010	023040	047040	042105					
10519	060016	044440	020116	045522					
10520	060024	051503	020062	047502					
10521	060032	044124	051440	052105					
10522	060040	000							
10523	060041	103	047117	051124	EM12:			.ASCIZ	/CONTROLLER NOT READY IN RKCS1/
10524	060046	046117	042514	020122					
10525	060054	047516	020124	042522					
10526	060062	042101	020131	047111					
10527	060070	051040	041513	030523					
10528	060076	000							
10529	060077	116	020117	052101	EM13:			.ASCIZ	/NO ATTN IN RKASOF/
10530	060104	047124	044440	020116					
10531	060112	045522	051501	043117					
10532	060120	000							
10533	060121	125	042516	050130	EM14:			.ASCIZ	/UNEXPECTED MEMORY PARITY TRAP/
10534	060126	041505	042524	020104					
10535	060134	042515	047515	054522					
10536	060142	050040	051101	052111					
10537	060150	020131	051124	050101					

10538	060156	000				
10539	060157	122	042113	020103	EM15:	.ASCII /RKDC & RKDA INDICATE THAT WCE OCCURRED AT/
10540	060164	020046	045522	040504		
10541	060172	044440	042116	041511		
10542	060200	052101	020105	044124		
10543	060206	052101	053440	042503		
10544	060214	047440	041503	051125		
10545	060222	042522	020104	052101		
10546	060230	005015	054503	020114		.ASCIZ <CR><LF>/CYL 411, TRACK 2, SECTOR 21/
10547	060236	030464	026061	052040		
10548	060244	040522	045503	031040		
10549	060252	020054	042523	052103		
10550	060260	051117	031040	000061		
10551	060266	040503	047116	052117	EM16:	.ASCIZ /CANNOT READ BAD SECTOR INFORMATION/
10552	060274	051040	040505	020104		
10553	060302	040502	020104	042523		
10554	060310	052103	051117	044440		
10555	060316	043116	051117	040515		
10556	060324	044524	047117	000		
10557	060331	115	051505	040523	EM17:	.ASCIZ /MESSAGE AD ERROR/
10558	060336	042507	040440	020060		
10559	060344	051105	047522	000122		
10560	060352	042515	051523	043501	EM18:	.ASCIZ /MESSAGE BD ERROR/
10561	060360	020105	030102	042440		
10562	060366	051122	051117	000		
10563	060373	115	051505	040523	EM19:	.ASCIZ /MESSAGE AI ERROR/
10564	060400	042507	040440	020061		
10565	060406	051105	047522	000122		
10566	060414	042515	051523	043501	EM20:	.ASCIZ /MESSAGE BI ERROR/
10567	060422	020105	030502	042440		
10568	060430	051122	051117	000		
10569	060435	103	051105	020122	EM21:	.ASCIZ /CERR SET IN RKCS1/
10570	060442	042523	020124	047111		
10571	060450	051040	041513	030523		
10572	060456	000				
10573	060457	116	020117	051104	EM22:	.ASCII /NO DRIVES FOUND IN DEVICE MAP (\$DEVN)/<CR><LF>
10574	060464	053111	051505	043040		
10575	060472	052517	042116	044440		
10576	060500	020116	042504	044526		
10577	060506	042503	046440	050101		
10578	060514	024040	042044	053105		
10579	060522	024515	005015			
10580	060526	042523	052524	020120		.ASCIZ /SETUP CORRECTLY AND RESTART/<CR><LF>
10581	060534	047503	051122	041505		
10582	060542	046124	020131	047101		
10583	060550	020104	042522	052123		
10584	060556	051101	006524	000012		
10585	060564	047516	042040	044522	EM23:	.ASCII /NO DRIVES FOUND ON BUSS/<CR><LF>
10586	060572	042526	020123	047506		
10587	060600	047125	020104	047117		
10588	060606	041040	051525	006523		
10589	060614	012				
10590	060615	123	052105	050125		.ASCIZ /SETUP CORRECTLY AND PRESS 'CONTINUE'<CR><LF>
10591	060622	041440	051117	042522		
10592	060630	052103	054514	040440		
10593	060636	042116	050040	042522		

10594	060644	051523	023440	047503	
10595	060652	052116	047111	042525	
10596	060660	006447	000012		
10597	060664	047526	020114	040526	EM24: .ASCIZ /VOL VALID NOT SET IN RKMR2/
10598	060672	044514	020104	047516	
10599	060700	020124	042523	020124	
10600	060706	047111	051040	046513	
10601	060714	031122	000		
10602	060717	015	042012	052105	EM25: .ASCIZ <CR><LF>/DETECTED 10 BAD SECTORS...ABORTING TEST/
10603	060724	041505	042524	020104	
10604	060732	030061	041040	042101	
10605	060740	051440	041505	047524	
10606	060746	051522	027056	040456	
10607	060754	047502	052122	047111	
10608	060762	020107	042524	052123	
10609	060770	000			
10610	060771	104	052105	041505	EM26: .ASCIZ /DETECTED BSE BUT NOT LISTED IN BAD SECTOR FILE/
10611	060776	042524	020104	051502	
10612	061004	020105	052502	020124	
10613	061012	047516	020124	044514	
10614	061020	052123	042105	044440	
10615	061026	020116	040502	020104	
10616	061034	042523	052103	051117	
10617	061042	043040	046111	000105	
10618	061050	042504	042524	052103	EM27: .ASCII /DETECTED BSE IN READ COMMAND/
10619	061056	042105	041040	042523	
10620	061064	044440	020116	042522	
10621	061072	042101	041440	046517	
10622	061100	040515	042116		
10623	061104	005015	052502	020124	.ASCIZ <CR><LF>/BUT NOT IN PREVIOUS WRITE COMMAND TO SAME SECTOR/
10624	061112	047516	020124	047111	
10625	061120	050040	042522	044526	
10626	061126	052517	020123	051127	
10627	061134	052111	020105	047503	
10628	061142	046515	047101	020104	
10629	061150	047524	051440	046501	
10630	061156	020105	042523	052103	
10631	061164	051117	000		
10632	061167	117	046116	020131	EM28: .ASCII /ONLY DRIVES 0,2,4,6 ALLOWED IN \$DEVN/
10633	061174	051104	053111	051505	
10634	061202	030040	031054	032054	
10635	061210	033054	040440	046114	
10636	061216	053517	042105	044440	
10637	061224	020116	042044	053105	
10638	061232	115			
10639	061233	015	051012	046105	.ASCIZ <CR><LF>/RELOAD \$DEVN & PRESS CONTINUE/<CR><LF>
10640	061240	040517	020104	042044	
10641	061246	053105	020115	020046	
10642	061254	051120	051505	020123	
10643	061262	047503	052116	047111	
10644	061270	042525	005015	000	
10645	061275	120	051117	020124	EM29: .ASCIZ /PORT AVAILABLE...TIMERS NOT INHIBITED/
10646	061302	053101	044501	040514	
10647	061310	046102	027105	027056	
10648	061316	044524	042515	051522	
10649	061324	047040	052117	044440	

10650	061332	044116	041111	052111		
10651	061340	042105	000			
10652	061343	120	050111	051440	EM30:	.ASCIZ /PIP SET IN RKMR2/
10653	061350	052105	044440	020116		
10654	061356	045522	051115	000062		
10655	061364	047520	052122	047040	EM31:	.ASCIZ /PORT NOT AVAILABLE/
10656	061372	052117	040440	040526		
10657	061400	046111	041101	042514		
10658	061406	000				
10659	061407	120	051117	020124	EM32:	.ASCIZ /PORT AVAILABLE/
10660	061414	053101	044501	040514		
10661	061422	046102	000105			
10662	061426	052101	047124	051440	EM33:	.ASCIZ /ATTN SET IN RKASOF/
10663	061434	052105	044440	020116		
10664	061442	045522	051501	043117		
10665	061450	000				
10666	061451	101	052124	020116	EM34:	.ASCIZ /ATTN CLEARED IN RKASOF/
10667	061456	046103	040505	042522		
10668	061464	020104	047111	051040		
10669	061472	040513	047523	000106		
10670	061500	042503	051122	047040	EM35:	.ASCIZ /CERR NOT SET IN RKCS1/
10671	061506	052117	051440	052105		
10672	061514	044440	020116	045522		
10673	061522	051503	000061			
10674	061526	054503	020114	042101	EM36:	.ASCIZ /CYL ADDR IN RKMR3 NOT SAME AS RKDC/
10675	061534	051104	044440	020116		
10676	061542	045522	051115	020063		
10677	061550	047516	020124	040523		
10678	061556	042515	040440	020123		
10679	061564	045522	041504	000		
10680	061571	115	046125	044524	EM37:	.ASCIZ /MULTIPLE ATTENTIONS SEEN/
10681	061576	046120	020105	052101		
10682	061604	042524	052116	047511		
10683	061612	051516	051440	042505		
10684	061620	000116				
10685	061622	044524	042515	052517	EM38:	.ASCIZ /TIMEOUT DID NOT RE-TRIGGER FOR FULL SECOND/
10686	061630	020124	044504	020104		
10687	061636	047516	020124	042522		
10688	061644	052055	044522	043507		
10689	061652	051105	043040	051117		
10690	061660	043040	046125	020114		
10691	061666	042523	047503	042116		
10692	061674	000				
10693	061675	103	046131	042040	EM39:	.ASCIZ /CYL DIFF & OFFSET IN RKMR2 NOT CLEARED/
10694	061702	043111	020106	020046		
10695	061710	043117	051506	052105		
10696	061716	044440	020116	045522		
10697	061724	051115	020062	047516		
10698	061732	020124	046103	040505		
10699	061740	042522	000104			
10700	061744	054503	020114	042101	EM40:	.ASCIZ /CYL ADDR IN RKMR3 NOT CLEARED/
10701	061752	051104	044440	020116		
10702	061760	045522	051115	020063		
10703	061766	047516	020124	046103		
10704	061774	040505	042522	000104		
10705	062002	052101	047124	047040	EM55:	.ASCIZ /ATTN NOT CLEARED IN RKASOF/

10706	062010	052117	041440	042514	
10707	062016	051101	042105	044440	
10708	062024	020116	045522	051501	
10709	062032	043117	000		
10710	062035	104	052114	051440	EM63: .ASCIZ /DLT SET IN RKCS2/
10711	062042	052105	044440	020116	
10712	062050	045522	051503	000062	
10713	062056	042522	042101	044040	EM65: .ASCIZ /READ HEADER ERROR/
10714	062064	040505	042504	020122	
10715	062072	051105	047522	000122	
10716	062100	046101	043511	046516	EM69: .ASCIZ /ALIGNMENT CARTRIDGE USED/
10717	062106	047105	020124	040503	
10718	062114	052122	044522	043504	
10719	062122	020105	051525	042105	
10720	062130	000			
10721	062131	103	047524	051440	EM73: .ASCIZ /CTO SET IN RKCS1/
10722	062136	052105	044440	020116	
10723	062144	045522	051503	000061	
10724	062152	052122	020132	047516	EM74: .ASCIZ /RTZ NOT SET IN RKMR2/
10725	062160	020124	042523	020124	
10726	062166	047111	051040	046513	
10727	062174	031122	000		
10728	062177	116	042105	051440	EM79: .ASCIZ /NED SET IN RKCS2/
10729	062204	052105	044440	020116	
10730	062212	045522	051503	000062	
10731	062220	051127	052111	020105	EM80: .ASCIZ /WRITE CHECK ERROR SET IN RKCS2/
10732	062226	044103	041505	020113	
10733	062234	051105	047522	020122	
10734	062242	042523	020124	047111	
10735	062250	051040	041513	031123	
10736	062256	000			
10737	062257	122	040505	020104	EM82: .ASCIZ /READ DATA DID NOT COMPARE WITH WRITE DATA/
10738	062264	040504	040524	042040	
10739	062272	042111	047040	052117	
10740	062300	041440	046517	040520	
10741	062306	042522	053440	052111	
10742	062314	020110	051127	052111	
10743	062322	020105	040504	040524	
10744	062330	000			
10745	062331	104	052101	020101	EM83: .ASCIZ /DATA CHECK ERROR SET IN RKER/
10746	062336	044103	041505	020113	
10747	062344	051105	047522	020122	
10748	062352	042523	020124	047111	
10749	062360	051040	042513	000122	
10750	062366	044127	046111	020105	EM84: .ASCIZ /WHILE WAITING FOR CONTR READY OR AFTER CONTR READY REC'D/
10751	062374	040527	052111	047111	
10752	062402	020107	047506	020122	
10753	062410	047503	052116	020122	
10754	062416	042522	042101	020131	
10755	062424	051117	040440	052106	
10756	062432	051105	041440	047117	
10757	062440	051124	051040	040505	
10758	062446	054504	051040	041505	
10759	062454	042047	000		
10760	062457	122	040505	044504	EM93: .ASCIZ /READING WRONG CYLINDER # IN HEADER...MISPOSITION/
10761	062464	043516	053440	047522	

10762	062472	043516	041440	046131
10763	062500	047111	042504	020122
10764	062506	020043	047111	044040
10765	062514	040505	042504	027122
10766	062522	027056	044515	050123
10767	062530	051517	052111	047511
10768	062536	000116		
10769				
10770				
10771				
10772	062540	042524	052123	047040
10773	062546	027117	020040	041520
10774	062554	000		
10775	062555	122	046513	030522
10776	062562	051011	046513	031122
10777	062570	051011	046513	031522
10778	062576	051011	042513	004522
10779	062604	045522	051504	051011
10780	062612	041513	030523	051011
10781	062620	041513	031123	000
10782	062625	122	053513	004503
10783	062632	045522	040502	051011
10784	062640	042113	004501	045522
10785	062646	051501	043117	051011
10786	062654	042113	004503	045522
10787	062662	041505	051520	051011
10788	062670	042513	050103	000124
10789	062676	043101	042524	020122
10790	062704	052101	047124	051040
10791	062712	041505	042047	043040
10792	062720	047522	020115	042522
10793	062726	040503	020114	047503
10794	062734	046515	047101	000104
10795	062742	043101	042524	020122
10796	062750	047523	052106	040527
10797	062756	042522	052040	046511
10798	062764	047505	052125	000
10799	062771	106	047522	020115
10800	062776	054503	020114	052040
10801	063004	020117	054503	020114
10802	063012	041440	046131	042040
10803	063020	043111	000106	
10804	063024	043101	042524	020122
10805	063032	052523	051502	051531
10806	063040	041440	042514	051101
10807	063046	005015	044103	041505
10808	063054	020113	027061	052040
10809	063062	042510	050040	051117
10810	063070	020124	020102	051525
10811	063076	047111	020107	051104
10812	063104	053111	020105	046106
10813	063112	050117	024040	050104
10814	063120	024461		
10815	063122	005015	027062	042040
10816	063130	044522	042526	040440
10817	063136	040526	046111	041101

.SBTTL DATA HEADERS

DH1: .ASCIZ /TEST NO. PC/
DH2: .ASCIZ /RKMR1 RKMR2 RKMR3 RKER RKDS RKCS1 RKCS2/
DH3: .ASCIZ /RKWC RKBA RKDA RKASOF RKDC RKECPS RKECPT/
DH4: .ASCIZ /AFTER ATTN REC'D FROM RECAL COMMAND/
DH5: .ASCIZ /AFTER SOFTWARE TIMEOUT/
DH6: .ASCIZ /FROM CYL TO CYL CYL DIFF/
DH7: .ASCII /AFTER SUBSYS CLEAR/
.ASCII <CR><LF>/CHECK 1. THE PORT B USING DRIVE FLOP (DP1)/
.ASCII <CR><LF>/2. DRIVE AVAILABLE TO PORT B STATUS (DP2)/

10818	063144	042514	052040	020117			
10819	063152	047520	052122	041040			
10820	063160	051440	040524	052524			
10821	063166	020123	042050	031120			
10822	063174	000051					
10823	063176	042524	052123	047040	DH8:	.ASCIZ /TEST NO.	TRAP PC/
10824	063204	027117	052011	040522			
10825	063212	020120	041520	000			
10826	063217	101	052106	051105	DH9:	.ASCIZ /AFTER START SPINDLE COMMAND REC'D BY DRIVE/	
10827	063224	051440	040524	052122			
10828	063232	051440	044520	042116			
10829	063240	042514	041440	046517			
10830	063246	040515	042116	051040			
10831	063254	041505	042047	041040			
10832	063262	020131	051104	053111			
10833	063270	000105					
10834	063272	052101	042440	042116	DH10:	.ASCIZ /AT END OF HEAD LOADING/	
10835	063300	047440	020106	042510			
10836	063306	042101	046040	040517			
10837	063314	044504	043516	000			
10838	063321	105	050130	041505	DH11:	.ASCIZ /EXPECTED WAS/	
10839	063326	042524	004504	040527			
10840	063334	000123					
10841	063336	043101	042524	020122	DH12:	.ASCII /AFTER SEEK CMD/	
10842	063344	042523	045505	041440			
10843	063352	042115					
10844	063354	005015	044103	041505		.ASCIZ <CR><LF>/CHECK STAT ADD SIGNAL BEFORE & AFTER MUX/	
10845	063362	020113	052123	052101			
10846	063370	040440	042104	051440			
10847	063376	043511	040516	020114			
10848	063404	042502	047506	042522			
10849	063412	023040	040440	052106			
10850	063420	051105	046440	054125			
10851	063426	000					
10852	063427	117	020116	042523	DH13:	.ASCIZ /ON SECTORS 10, 12, 14, 16, 18 OR 20 CYL 410 TRACK 2/	
10853	063434	052103	051117	020123			
10854	063442	030061	020054	031061			
10855	063450	020054	032061	020054			
10856	063456	033061	020054	034061			
10857	063464	047440	020122	030062			
10858	063472	041440	046131	032040			
10859	063500	030061	052040	040522			
10860	063506	045503	031040	000			
10861	063513	102	043105	051117	DH14:	.ASCIZ /BEFORE TIMEOUT OR RELEASE/	
10862	063520	020105	044524	042515			
10863	063526	052517	020124	051117			
10864	063534	051040	046105	040505			
10865	063542	042523	000				
10866	063545	127	044510	042514	DH15:	.ASCIZ /WHILE PORT UNAVAILABLE/	
10867	063552	050040	051117	020124			
10868	063560	047125	053101	044501			
10869	063566	040514	046102	000105			
10870	063574	047524	044440	042116	DH16:	.ASCII /TO INDICATE THAT REQUESTING PORT CAN SEIZE/	
10871	063602	041511	052101	020105			
10872	063610	044124	052101	051040			
10873	063616	050505	042525	052123			

10874	063624	047111	020107	047520	
10875	063632	052122	041440	047101	
10876	063640	051440	044505	042532	
10877	063646	005015	044103	041505	.ASCIZ <CR><LF>/CHECK PORT REQ. FLOPS & 1 SEC SAFETY TIMER/
10878	063654	020113	047520	052122	
10879	063662	051040	050505	020056	
10880	063670	046106	050117	020123	
10881	063676	020046	020061	042523	
10882	063704	020103	040523	042506	
10883	063712	054524	052040	046511	
10884	063720	051105	000		
10885	063723	101	052106	051105	DH17: .ASCIZ /AFTER RECAL COMMAND/
10886	063730	051040	041505	046101	
10887	063736	041440	046517	040515	
10888	063744	042116	000		
10889	063747	101	052106	051105	DH18: .ASCIZ /AFTER UNLOAD COMMAND/
10890	063754	052440	046116	040517	
10891	063762	020104	047503	046515	
10892	063770	047101	000104		
10893	063774	043101	042524	020122	DH19: .ASCIZ /AFTER PACK COMMAND/
10894	064002	040520	045503	041440	
10895	064010	046517	040515	042116	
10896	064016	000			
10897	064017	101	052106	051105	DH20: .ASCIZ /AFTER SELECT DRIVE COMMAND/
10898	064024	051440	046105	041505	
10899	064032	020124	051104	053111	
10900	064040	020105	047503	046515	
10901	064046	047101	000104		
10902	064052	043101	042524	020122	DH21: .ASCIZ /AFTER SUBSYSTEM CLEAR/
10903	064060	052523	051502	051531	
10904	064066	042524	020115	046103	
10905	064074	040505	000122		
10906	064100	043101	042524	020122	DH22: .ASCIZ /AFTER DRIVE CLEAR COMMAND/
10907	064106	051104	053111	020105	
10908	064114	046103	040505	020122	
10909	064122	047503	046515	047101	
10910	064130	000104			
10911	064132	044527	044124	052517	DH23: .ASCIZ /WITHOUT REQUEST PENDING/
10912	064140	020124	042522	052521	
10913	064146	051505	020124	042520	
10914	064154	042116	047111	000107	
10915	064162	054502	042040	044522	DH24: .ASCIZ /BY DRIVE CLEAR COMMAND TO OTHER PORT/
10916	064170	042526	041440	042514	
10917	064176	051101	041440	046517	
10918	064204	040515	042116	052040	
10919	064212	020117	052117	042510	
10920	064220	020122	047520	052122	
10921	064226	000			
10922	064227	101	052106	051105	DH25: .ASCIZ /AFTER SEEK COMMAND/
10923	064234	051440	042505	020113	
10924	064242	047503	046515	047101	
10925	064250	000104			
10926	064252	043101	042524	020122	DH26: .ASCIZ /AFTER READ DATA COMMAND/
10927	064260	042522	042101	042040	
10928	064266	052101	020101	047503	
10929	064274	046515	047101	000104	

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46MACY11 30A(1052) 03-JAN-78 08:56 PAGE 209
DATA HEADERS

SEQ 0209

10986	064753	127	051117	021504	DH40:	.ASCIZ /WORD# HEADER WAS SHOULD BE/
10987	064760	044011	040505	042504		
10988	064766	020122	046527	020123		
10989	064774	051440	047510	046125		
10990	065002	020104	042502	000		
10991	065007	104	051125	047111	DH41:	.ASCII /DURING RECAL COMMAND/
10992	065014	020107	042522	040503		
10993	065022	020114	047503	046515		
10994	065030	047101	104			
10995	065033	015	041412	042510		.ASCIZ <CR><LF>/CHECK BUFF1 SIGNAL BEFORE & AFTER MUX/
10996	065040	045503	041040	043125		
10997	065046	030506	051440	043511		
10998	065054	040516	020114	042502		
10999	065062	047506	042522	023040		
11000	065070	040440	052106	051105		
11001	065076	046440	054125	000		
11002	065103	117	020116	042523	DH42:	.ASCIZ /ON SECTORS 0,2,4,6 OR 8 CYL 410 TRACK 2/
11003	065110	052103	051117	020123		
11004	065116	026060	026062	026064		
11005	065124	020066	051117	034040		
11006	065132	020040	054503	020114		
11007	065140	030464	020060	051124		
11008	065146	041501	020113	000062		
11009	065154	043101	042524	020122	DH43:	.ASCIZ /AFTER RE-SEIZE PORT MID-WAY IN NORMAL TIMEOUT/
11010	065162	042522	051455	044505		
11011	065170	042532	020040	047520		
11012	065176	052122	046440	042111		
11013	065204	053455	054501	044440		
11014	065212	020116	047516	046522		
11015	065220	046101	052040	046511		
11016	065226	047505	052125	000		
11017	065233	106	051117	040515	DH44:	.ASCIZ /FORMAT & ALL READ-WRITE TESTS WILL BE BYPASSED/
11018	065240	020124	020046	046101		
11019	065246	020114	042522	042101		
11020	065254	053455	044522	042524		
11021	065262	052040	051505	051524		
11022	065270	053440	046111	020114		
11023	065276	042502	041040	050131		
11024	065304	051501	042523	000104		
11025	065312	042502	047506	042522	DH45:	.ASCIZ /BEFORE RELEASE WHILE HEADS UNLOADED/
11026	065320	051040	046105	040505		
11027	065326	042523	053440	044510		
11028	065334	042514	044040	040505		
11029	065342	051504	052440	046116		
11030	065350	040517	042504	000104		
11031	065356	051515	020107	020101	DH49:	.ASCIZ /MSG A & B IN RKMR2 & RKMR3 RESP. ARE INVALID/
11032	065364	020046	020102	047111		
11033	065372	051040	046513	031122		
11034	065400	023040	051040	046513		
11035	065406	031522	051040	051505		
11036	065414	027120	040440	042522		
11037	065422	044440	053116	046101		
11038	065430	042111	000			
11039	065433	101	052106	051105	DH51:	.ASCIZ /AFTER SEEK TO SELF COMMAND/
11040	065440	051440	042505	020113		
11041	065446	047524	051440	046105		

11042	065454	020106	047503	046515	
11043	065462	047101	000104		
11044	065466	054503	020114	004443	DH56: .ASCIZ /CYL # HEADER WORD 0/
11045	065474	042510	042101	051105	
11046	065502	053440	051117	020104	
11047	065510	000060			
11048					
11049					.SBTTL ERROR OUTPUT DATA
11050					
11051					.EVEN
11052	065512	001214	001116		DT1: \$TESTN,\$ERRPC
11053	065516	005340	005342	005344	HMR1,HMR2,HMR3,HER,HDS,HCS1,HCS2
11054	065524	005330	005326	005314	
11055	065532	005316			
11056	065534	005320	005322	005324	HWC,HBA,HDA,HASOF,HDC,HPOS,HPAT
11057	065542	005332	005334	005346	
11058	065550	005350			
11059	065552	001214	001334		DT3: \$TESTN,TRAPPC
11060	065556	001214	001116	001344	DT4: \$TESTN,\$ERRPC,FRCYL,TOCYL,CALDIF
11061	065564	001346	001354		
11062	065570	005340	005342	005344	HMR1,HMR2,HMR3,HER,HDS,HCS1,HCS2
11063	065576	005330	005326	005314	
11064	065604	005316			
11065	065606	005320	005322	005324	HWC,HBA,HDA,HASOF,HDC,HPOS,HPAT
11066	065614	005332	005334	005346	
11067	065622	005350			
11068	065624	001214	001116	001416	DT6: \$TESTN,\$ERRPC,WD2,WD1
11069	065632	001414			
11070	065634	005340	005342	005344	HMR1,HMR2,HMR3,HER,HDS,HCS1,HCS2
11071	065642	005330	005326	005314	
11072	065650	005316			
11073	065652	005320	005322	005324	HWC,HBA,HDA,HASOF,HDC,HPOS,HPAT
11074	065660	005332	005334	005346	
11075	065666	005350			
11076	065670	001214	001116	001432	DT7: \$TESTN,\$ERRPC,WD2,WD1
11077	065676	001450	005352		
11078	065702	005340	005342	005344	HMR1,HMR2,HMR3,HER,HDS,HCS1,HCS2
11079	065710	005330	005326	005314	
11080	065716	005316			
11081	065720	005320	005322	005324	HWC,HBA,HDA,HASOF,HDC,HPOS,HPAT
11082	065726	005332	005334	005346	
11083	065734	005350			
11084	065736	001214	001116	001346	DT8: \$TESTN,\$ERRPC,TOCYL,FRCYL,CALDIF
11085	065744	001344	001354		
11086	065750	005340	005342	005344	HMR1,HMR2,HMR3,HER,HDS,HCS1,HCS2
11087	065756	005330	005326	005314	
11088	065764	005316			
11089	065766	005320	005322	005324	HWC,HBA,HDA,HASOF,HDC,HPOS,HPAT
11090	065774	005332	005334	005346	
11091	066002	005350			
11092	066004	001214	001116	001346	DT9: \$TESTN,\$ERRPC,TOCYL,RHTAB
11093	066012	001666			
11094	066014	005340	005342	005344	HMR1,HMR2,HMR3,HER,HDS,HCS1,HCS2
11095	066022	005330	005326	005314	
11096	066030	005316			
11097	066032	005320	005322	005324	HWC,HBA,HDA,HASOF,HDC,HPOS,HPAT

EO1

11098	066040	005332	005334	005346	
11099	066046	005350			
11100	066050	001214	001116	005404	DT13: \$TESTN,\$ERRPC,E.A0,E.B0,E.A1,E.B1,H.A0,H.B0,H.A1,H.B1
11101	066056	005406	005410	005412	
11102	066064	005364	005366	005370	
11103	066072	005372			
11104	066074	005340	005342	005344	HMR1,HMR2,HMR3,HER,HDS,HCS1,HCS2
11105	066102	005330	005326	005314	
11106	066110	005316			
11107	066112	005320	005322	005324	HWC,HBA,HDA,HASOF,HDC,HPOS,HPAT
11108	066120	005332	005334	005346	
11109	066126	005350			
11110	066130	001214	001116	005404	DT14: \$TESTN,\$ERRPC,E.A0,E.B0,E.A1,E.B1,E.A2,E.B2
11111	066136	005406	005410	005412	
11112	066144	005414	005416		
11113	066150	005364	005366	005370	H.A0,H.B0,H.A1,H.B1,H.A2,H.B2
11114	066156	005372	005374	005376	
11115	066164	005340	005342	005344	HMR1,HMR2,HMR3,HER,HDS,HCS1,HCS2
11116	066172	005330	005326	005314	
11117	066200	005316			
11118	066202	005320	005322	005324	HWC,HBA,HDA,HASOF,HDC,HPOS,HPAT
11119	066210	005332	005334	005346	
11120	066216	005350			
11121	066220	001214	001116	005404	DT15: \$TESTN,\$ERRPC,E.A0,E.B0,E.A1,E.B1,E.A2,E.B2,E.B3
11122	066226	005406	005410	005412	
11123	066234	005414	005416	005422	
11124	066242	005364	005366	005370	H.A0,H.B0,H.A1,H.B1,H.A2,H.B2,H.B3
11125	066250	005372	005374	005376	
11126	066256	005402			
11127	066260	005340	005342	005344	HMR1,HMR2,HMR3,HER,HDS,HCS1,HCS2
11128	066266	005330	005326	005314	
11129	066274	005316			
11130	066276	005320	005322	005324	HWC,HBA,HDA,HASOF,HDC,HPOS,HPAT
11131	066304	005332	005334	005346	
11132	066312	005350			
11133					
11134					.SBTTL ERROR DATA FORMATS
11135					
11136	066314	000003			DF1: 3
11137	066316	002	000		.BYTE 2,0
11138	066320	062555			DH2
11139	066322	007	000		.BYTE 7,0
11140	066324	062625			DH3
11141	066326	007	000		.BYTE 7,0
11142					
11143	066330	000001			DF2: 1
11144	066332	002	000		.BYTE 2,0
11145					
11146	066334	000005			DF3: 5
11147	066336	000	000		.BYTE 0,0
11148	066340	062540			DH1
11149	066342	002	000		.BYTE 2,0
11150	066344	063321			DH11
11151	066346	002	000		.BYTE 2,0
11152	066350	062555			DH2
11153	066352	007	000		.BYTE 7,0

11154	066354	062625			DH3
11155	066356	007	000		.BYTE 7,0
11156					
11157	066360	000003		DF4:	3
11158	066362	002	000		.BYTE 2,0
11159	066364	062555			DH2
11160	066366	007	000		.BYTE 7,0
11161	066370	062625			DH3
11162	066372	007	000		.BYTE 7,0
11163					
11164	066374	000005		DF5:	5
11165	066376	000	000		.BYTE 0,0
11166	066400	065356			DH49
11167	066402	000	000		.BYTE 0,0
11168	066404	062540			DH1
11169	066406	002	000		.BYTE 2,0
11170	066410	062555			DH2
11171	066412	007	000		.BYTE 7,0
11172	066414	062625			DH3
11173	066416	007	000		.BYTE 7,0
11174					
11175	066420	000005		DF6:	5
11176	066422	000	000		.BYTE 0,0
11177	066424	062540			DH1
11178	066426	002	000		.BYTE 2,0
11179	066430	062771			DH6
11180	066432	003	000		.BYTE 3,0
11181	066434	062555			DH2
11182	066436	007	000		.BYTE 7,0
11183	066440	062625			DH3
11184	066442	007	000		.BYTE 7,0
11185					
11186					
11187	066444	000004		DF10:	4
11188	066446	000	000		.BYTE 0,0
11189	066450	062540			DH1
11190	066452	002	000		.BYTE 2,0
11191	066454	062555			DH2
11192	066456	007	000		.BYTE 7,0
11193	066460	062625			DH3
11194	066462	007	000		.BYTE 7,0
11195					
11196	066464	000004		DF14:	4
11197	066466	002	000		.BYTE 2,0
11198	066470	064753			DH40
11199	066472	003	000		.BYTE 3,0
11200	066474	062555			DH2
11201	066476	007	000		.BYTE 7,0
11202	066500	062625			DH3
11203	066502	007	000		.BYTE 7,0
11204					
11205					
11206	066504	000004		DF15:	4
11207	066506	000	000		.BYTE 0,0
11208	066510	062540			DH1
11209	066512	002	000		.BYTE 2,0

11210	066514	062555		DH2	
11211	066516	007	000	.BYTE	7,0
11212	066520	062625		DH3	
11213	066522	007	000	.BYTE	7,0
11214					
11215	066524	000005		DF17: 5	
11216	066526	000	000	.BYTE	0,0
11217	066530	065233		DH44	
11218	066532	000	000	.BYTE	0,0
11219	066534	062540		DH1	
11220	066536	002	000	.BYTE	2,0
11221	066540	062555		DH2	
11222	066542	007	000	.BYTE	7,0
11223	066544	062625		DH3	
11224	066546	007	000	.BYTE	7,0
11225	066550	000005		DF20: 5	
11226	066552	000	000	.BYTE	0,0
11227	066554	062540		DH1	
11228	066556	002	000	.BYTE	2,0
11229	066560	065466		DH56	
11230	066562	002	000	.BYTE	2,0
11231	066564	062555		DH2	
11232	066566	007	000	.BYTE	7,0
11233	066570	062625		DH3	
11234	066572	007	000	.BYTE	7,0
11235					
11236	066574	000007		DF21: 7	
11237	066576	000	000	.BYTE	0,0
11238	066600	062540		DH1	
11239	066602	002	000	.BYTE	2,0
11240	066604	064333		DH28	
11241	066606	000	000	.BYTE	0,0
11242	066610	064405		DH31	
11243	066612	004	000	.BYTE	4,0
11244	066614	064343		DH29	
11245	066616	004	000	.BYTE	4,0
11246	066620	062555		DH2	
11247	066622	007	000	.BYTE	7,0
11248	066624	062625		DH3	
11249	066626	007	000	.BYTE	7,0
11250					
11251	066630	000007		DF22: 7	
11252	066632	000	000	.BYTE	0,0
11253	066634	062540		DH1	
11254	066636	002	000	.BYTE	2,0
11255	066640	064333		DH28	
11256	066642	000	000	.BYTE	0,0
11257	066644	064405		DH31	
11258	066646	006	000	.BYTE	6,0
11259	066650	064343		DH29	
11260	066652	006	000	.BYTE	6,0
11261	066654	062555		DH2	
11262	066656	007	000	.BYTE	7,0
11263	066660	062625		DH3	
11264	066662	007	000	.BYTE	7,0
11265					

11266	066664	000007	
11267	066666	000	000
11268	066670	062540	
11269	066672	002	000
11270	066674	064333	
11271	066676	000	000
11272	066700	064405	
11273	066702	007	000
11274	066704	064343	
11275	066706	007	000
11276	066710	062555	
11277	066712	007	000
11278	066714	062625	
11279	066716	007	000
11280			
11281			
11282			
11283			
11284			
11285			
11286			
11287			
11288			
11289			
11290			
11291	066720	104413	
11292	066722	104401	056551
11293	066726	113700	001114
11294	066732	042700	177400
11295	066736	005300	
11296	066740	006300	
11297	066742	006300	
11298	066744	006300	

```
DF23: 7
      .BYTE 0,0
      DH1
      .BYTE 2,0
      DH28
      .BYTE 0,0
      DH31
      .BYTE 7,0
      DH29
      .BYTE 7,0
      DH2
      .BYTE 7,0
      DH3
      .BYTE 7,0
```

```
.EVEN
*****
.SBTL TYPE ERROR ROUTINE
:ENTRY JSR PC,TYP ERR
:RETURN RTS PC
*
*THIS ROUTINE USES THE "ITEM CONTROL BYTE" ($ITEMB) TO DETERMINE WHICH
*ERROR IS TO BE REPORTED. IT THEN USES THE "ERROR TABLE" ($ERRTB)
*ENTRY TO DEFINE WHAT INFORMATION IS TO BE REPORTED CONCERNING
*THE ERROR.
*****
```

```
TYPERR: SAVREG
      TYPE MSG19 ;PORT A OR B
      MOVB $ITEMB,RO ;ENTER ERROR NUMBER
      BIC #177400,RO ;CLEAR SIGN EXTENSION
      DEC RO ;FORM INDEX FOR ERROR TABLE
      ASL RO
      ASL RO
      ASL RO
```


11299	066746	062700	005470	1\$:	ADD	#\$ERRTB,RO	;FORM ADDRESS OF ERROR ENTRY
11300	066752	012037	066766		MOV	(RO)+,2\$;GET EM POINTER
11301	066756	001404			BEQ	3\$;BRANCH IF THERE ISN'T ONE
11302	066760	104401	001205		TYPE	, \$CRLF	;TYPE CARRIAGE RETURN LINE FEED
11303	066764	104401			TYPE		;TYPE ERROR MESSAGE (EM)
11304	066766	000000		2\$:	.WORD	0	;EM POINTER GOES HERE
11305	066770	012037	067004	3\$:	MOV	(RO)+,4\$;GET DH POINTER
11306	066774	001404			BEQ	5\$;BRANCH IF THERE ISN'T ONE
11307	066776	104401	001205		TYPE	, \$CRLF	;TYPE CR-LF
11308	067002	104401			TYPE		;TYPE DATA HEADER
11309	067004	000000		4\$:	.WORD	0	;DH POINTER GOES HERE
11310	067006	012001		5\$:	MOV	(RO)+,R1	;GET DT POINTER
11311	067010	001455			BEQ	20\$;BRANCH IF THERE ARE NONE
11312	067012	005004			CLR	R4	;SET INDENT SWITCH
11313	067014	012000			MOV	(RO)+,R0	;GET DF POINTER
11314	067016	012002			MOV	(RO)+,R2	;STORE NUMBER OF DH'S
11315	067020	001446			BEQ	17\$;DH NUM IS 0-BRANCH
11316	067022	005104			COM	R4	;NO INDENT
11317	067024	104401	001205		TYPE	, \$CRLF	
11318	067030	112003		10\$:	MOVB	(RO)+,R3	;GET & STORE NUMBER OF DATA WORDS
11319	067032	105720			TSTB	(RO)+	;BUMP PAST FORMAT WORD
11320	067034	005703			TST	R3	;TEST IF ANY DATA FOR THIS HEADER
11321	067036	001407			BEQ	14\$;NO - SKIP DATA PRINT
11322	067040	013146		11\$:	MOV	@(R1)+,-(SP)	;PUT FIRST DATA WORD ON STACK
11323	067042	104402			TYPOC		;TYPE IT
11324	067044	005303			DEC	R3	;MORE DATA WORDS
11325	067046	001403			BEQ	14\$;NO-BRANCH

```

11326 067050 104401 067200          TYPE      SPACE2      ;TYPE SEPARATORS
11327 067054 000771                   BR          11$        ;LOOP
11328 067056 005302          14$:      DEC          R2        ;MORE DH'S?
11329 067060 003431                   BLE          20$        ;NO-BRANCH
11330 067062 104401 001205          TYPE      $CRLF
11331 067066 005760 000002          TST          2(R0)      ; ONLY A DH IN THIS REQUEST?
11332 067072 001404                   BEQ          15$        ;YES-BRANCH BYPASS INDENT
11333 067074 005104                   COM          R4        ;INDENT?
11334 067076 001002                   BNE          15$        ;NO-BRANCH
11335 067100 104401 067200          TYPE      SPACE2      ;YES-TYPE SPACES
11336 067104 012037 067112          15$:      MOV          (R0)+,16$ ;GET NEXT DH POINTER
11337 067110 104401                   TYPE
11338 067112 000000          16$:      .WORD      0        ;TYPE DH
11339 067114 105710                   TSTB        (R0)      ;DH POINTER GOES HERE
11340 067116 001003                   BNE          21$        ;TYPE A DT?
11341 067120 062700 000002          ADD          #2,R0     ;YES-BRANCH
11342 067124 000754                   BR           14$        ;INCREMENT DF POINTER
11343 067126 104401 001205          21$:      TYPE      $CRLF      ;SEE IF END OF DF BLOCK
11344 067132 005704                   TST          R4        ;INDENT?
11345 067134 001335                   BNE          10$        ;NO-BRANCH
11346 067136 104401 067200          17$:      TYPE      SPACE2      ;YES-TYPE SPACES
11347 067142 000732                   BR           10$        ;LOOP
11348 067144 104414          20$:      RESREG
11349
11350 067146 032777 010000 111764          BIT          #SW12,@SWR ;SEE IF ABORT DRV AFTER 20 ERRORS
11351 067154 001410                   BEQ          25$        ;BR IF NO
11352 067156 023727 001103 000024          CMP          $ERFLG,#20. ;ELSE SEE IF HAVE 20 ERRORS
11353 067164 001004                   BNE          25$        ;BR IF NO
11354 067166 012706 001100          MOV          #STACK,SP ;ELSE RESTORE STACK PTR
11355 067172 000137 042644          JMP          $EOP      ;AND GO TO NEXT DRV
11356
11357 067176 000207          25$:      RTS          PC
11358 067200 020040          SPACE2:    .ASCIZ/ /    ;2 SPACES
11359
11360
11361          ; ODT-11 -- V005A
11362          ; DEC-11-UODPA-A-LA
11363          ; COPYRIGHT 1969,1970,1972
11364          ; DIGITAL EQUIPMENT CORPORATION
11365          ; MAYNARD, MASSACHUSETTS 01754
11366          .ENABL   ABS,AMA
11367          .EVEN
11368          .=. +60
11369          R0      =      %0      ; REGISTER
11370          R1      =      %1      ; NAMING
11371          R2      =      %2      ; CONVENTIONS
11372          R3      =      %3
11373          R4      =      %4
11374          R5      =      %5
11375          SP      =      %6
11376          PC      =      %7
11377          ST      =      177776 ;STATUS REGISTER
11378
11379          O.TVEC =      14      ;TRT VECTOR LOCATION
11380          O.STM  =      340     ;PRIORITY MASK - STATUS REGISTER
11381          O.TBT  =      20      ;T-BIT MASK - STATUS REGISTER

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11382      000003      TRT      =      000003      ;TRT INSTRUCTION
11383      000006      RTT      =      000006      ;RTT INSTRUCTION
11384
11385      ; R5 IS USUALLY CONSIDERED SAFE. THE CURRENT ADDRESS WORD
11386      ; RESIDES IN IT. AFTER A BREAKPOINT, IT IS SET TO ZERO, AND SEARCH
11387      ; OPERATIONS LEAVE IT RANDOMLY FILLED. OTHERWISE, IT SHOULD NOT
11388      ; BE USED EXCEPT FOR JSR'S AND THE CURRENT ADDRESS POINTER (CAD).
11389
11390
11391
11392
11393      177562      O.RDB   =      177562      ;R DATA BUFFER
11394      177560      O.RCSR  =      177560      ;R C/SR
11395      177566      O.TDB   =      177566      ;T DATA BUFFER
11396      177564      O.TCSR  =      177564      ;T C/SR
11397
11398
11399      ; INITIALIZE ODT
11400      ; USE O.ODT FOR A NORMAL ENTRY
11401      ; USE O.ODT+2 TO RESTART ODT - WIPING OUT ALL BREAKPOINTS
11402      ; USE O.ODT+4 TO RE-ENTER (I.E. - FAKE A BREAKPOINT)
11403
11404      067264      000413      O.ODT:  BR      O.STRT      ;NORMAL ENTRY
11405      067266      000417      BR      O.RST        ;RESTART
11406      067270      013737      177776      067244      O.ENTR: MOV      ST,O.UST      ;RE-ENTER -- SAVE STATUS
11407      067276      013737      000016      177776      MOV      O.TVEC+2,ST ;SET UP LOCAL STATUS
11408      067304      010737      067242      MOV      PC,O.UPC     ;FAKE THE PC
11409      067310      000137      070442      JMP      O.BK1
11410
11411      067314      012706      067224      O.STRT: MOV      #O.URD,SP ;SET UP STACK
11412      067320      010637      067240      MOV      SP,O.USB     ;FAKE THE SAVED STACK
11413      067324      000414      BR      O.RST1       ;CLEAR BREAKPOINT TABLES
11414      067326      004037      070650      O.RST:  JSR      O,O.SVR ;SAVE REGISTERS
11415      067332      013777      067262      177716      MOV      O.UIN,#O.ADR1 ;REMOVE THE BREAKPOINT
11416      067340      113704      067246      MOV      O.PRI,R4    ;GET ODT PRIORITY
11417      067344      106004      RORB    R4          ;SHIFT
11418      067346      106004      RORB    R4          ;INTO
11419      067350      106004      RORB    R4          ;POSITION
11420      067352      110437      177776      MOV      R4,ST      ;STORE IN STATUS
11421      067356      000127      O.RST1: JMP      (PC)+
11422      067360      000403      BR      O.45
11423      067362      012737      000002      070352      MOV      #RTI,O.RTIT ;SET TO RTI IF 11/20 OR /05
11424      067370      105037      071271      O.45:  CLRB   O.P        ;DISALLOW PROCEED
11425      067374      012737      000340      000016      MOV      #O.STM,O.TVEC+2 ;STATUS WORD TO TRT VECTOR + 2
11426      067402      012737      070432      000014      MOV      #O.BRK,O.TVEC ;PC TO TRT VECTOR
11427      067410      000447      BR      O.RALL      ;CLEAR BREAKPOINT TABLES
11428
11429
11430      ; SPECIAL NAME HANDLER
11431      ; DEPENDS UPON THE EXPLICIT ORDER OF THE TWO TABLES O.TL AND O.URD
11432      067412      004537      071072      O.REGT: JSR      S,O.GET ;SPECIAL NAME, GET ONE MORE CHARACTER
11433      067416      012704      071315      MOV      #O.TL,R4    ;TABLE START ADDRESS
11434      067422      120024      O.RSP:  CMPB   RD,(R4)+ ;IS THIS THE CORRECT CHARACTER?
11435      067424      001413      BEQ     O.SP        ;JUMP IF YES
11436      067426      022704      071323      CMP      #O.TL+O.LG,R4 ;IS THE SEARCH DONE?
11437      067432      101373      BHI     O.RSP      ;BRANCH IF NOT

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11438 067434 042700 177770          BIC      #177770,R0      ;MASK OFF OCTAL
11439 067440 010004          MOV      R0,R4
11440 067442 006304          O.SP1:  ASL      R4
11441 067444 062704 067224          ADD      #0.URD,R4      ;GENERATE ADDRESS
11442 067450 005202          INC      R2              ;SET FOUND FLAG
11443 067452 000444          BR       0.SCAN         ;GO FIND NEXT CHARACTER
11444 067454 162704 071306          O.SP:   SUB      #0.TL-7,R4 ;CORRECT CONSTANT
11445 067460 000770          BR       0.SP1
11446
11447          ;
11448          ; + HANDLER - OPEN INDEXED ON THE PC
11449 067462 004737 071216          O.ORPC: JSR      PC,0.TCLS
11450 067466 010502          MOV      R5,R2          ;CURRENT ADDRESS IN R2
11451 067470 061202          ADD      @R2,R2         ;COMPUTE
11452 067472 006202          ASR      R2              ;MOVE ONE BIT TO CARRY
11453 067474 103421          BCS      0.ERR          ;ERROR IF ODD NUMBER
11454 067476 006302          ASL      R2              ;RESTORE WORD
11455 067500 005722          TST      (R2)+          ;AND INCREMENT BY TWO
11456 067502 010205          MOV      R2,R5          ;UPDATE CAD
11457 067504 000137 067756          JMP      0.OP2          ;GO FINISH UP
11458
11459          ;
11460          ; B HANDLER - SET AND REMOVE BREAKPOINTS
11461 067510 005702          O.BKPT: TST      R2          ;IF NO NUMBER TYPED
11462 067512 001406          BEQ      0.RALL         ;REMOVE BREAKPOINT
11463 067514 006204          ASR      R4              ;CHECK IF ODD
11464 067516 103410          BCS      0.ERR          ;JUMP IF ODD
11465 067520 006304          ASL      R4              ;RESTORE ONE BIT
11466 067522 010437 067256          MOV      R4,0.ADR1     ;SET A BREAKPOINT
11467 067526 000412          BR       0.DCD
11468 067530 012737 071332 067256          O.RALL: MOV      #0.TRTC,0.ADR1 ;CLEAR BREAKPOINT
11469 067536 000406          BR       0.DCD
11470
11471          ;
11472          ; COMMAND DECODER - ODT11
11473          ;
11474          ; REGISTERS R0-R4 MAY BE USED,
11475          ; REGISTER R5 WILL BE CONSIDERED SAFE
11476 067540 052705 000001          O.ERR:  BIS      #1,R5          ;CLOSE EVERYTHING
11477 067544 012700 000077          MOV      #7,R0          ;? TO BE TYPED
11478 067550 004537 071150          JSR      5,0.FTYP       ;OUTPUT ?
11479 067554 004537 071250          O.DCD:  JSR      5,0.CRLS   ;TYPE <CR><LF>*
11480 067560 005004          O.DCD1: CLR      R4              ;R4 CONTAINS THE CONVERTED OCTAL
11481 067562 005002          CLR      R2              ;R2 IS THE NUMBER FOUND FLAG
11482 067564 004537 071072          O.SCAN: JSR      5,0.GET       ;GET A CHAR, RETURN IN R0
11483 067570 022700 000060          CMP      #0,R0          ;COMPARE WITH ASCII 0
11484 067574 101013          BHI      0.CLGL         ;CHECK LEGALITY IF NON-NUMERIC
11485 067576 022700 000067          CMP      #7,R0          ;COMPARE WITH ASCII 7
11486 067602 103410          BLO      0.CLGL         ;CHECK LEGALITY IF NOT OCTAL
11487 067604 042700 177770          BIC      #177770,R0     ;CONVERT TO BCD
11488 067610 006304          ASL      R4              ;MAKE ROOM
11489 067612 006304          ASL      R4              ;IN
11490 067614 006304          ASL      R4              ;R4
11491 067616 060004          ADD      R0,R4          ;PACK THREE BITS IN R4
11492 067620 005202          INC      R2              ;R2 HAS NUMERIC FLAG
11493 067622 000760          BR       0.SCAN         ;AND TRY AGAIN

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11494 067624 005001      0.CLGL: CLR      R1      ;CLEAR INDEX
11495 067626 120061 071301 0.LGL1: CMPB     RO,0.LGCH(R1) ;DO THE CODES MATCH?
11496 067632 001405      BEQ      0.LGL2      ;JUMP IF YES
11497 067634 005201      INC      R1          ;SET INDEX FOR NEXT SEARCH
11498 067636 020127 000014  CMP     R1,#0.CLGT  ;IS THE SEARCH DONE?
11499 067642 103336      BHIS    0.ERR       ;OOPS!
11500 067644 000770      BR      0.LGL1     ;RE-LOOP
11501 067646 006301      0.LGL2: ASL     R1      ;MULTIPLY BY TWO
11502 067650 000171 067654  JMP     @0.LGDR(R1) ;GO TO PROPER ROUTINE
11503
11504 067654 067704      0.LGDR: 0.WRD     ; / OPEN WORD
11505 067656 067736      0.CRET  ; CARRIAGE RETURN CLOSE
11506 067660 067412      0.REGT  ; $ REGISTER OPS
11507 067662 070246      0.GO    ; G GO TO ADDRESS K
11508 067664 067750      0.OP1   ; <LF> MODIFY CLOSE OPEN NEXT
11509 067666 067462      0.ORPC  ; + OPEN RELATED INDEX - PC
11510 067670 070002      0.BACK  ; † OPEN PREVIOUS
11511
11512 067672 070012      0.OFST  ; 0 OFFSET
11513 067674 070070      0.WSCH  ; W SEARCH WORD
11514 067676 070064      0.EFF   ; E SEARCH EFFECTIVE ADDRESS
11515 067700 067510      0.BKPT  ; B BREAKPOINTS
11516 067702 070354      0.PROC  ; P PROCEED
11517 000030      0.LGL   = -0.LGDR ;LGL MUST EQUAL 2X CHLGT ALWAYS
11518
11519      ; PROCESS / - OPEN WORD
11520
11521 067704 005702      0.WRD:  TST     R2      ;GET VALUE IF R2 IS NON-ZERO
11522 067706 001410      BEQ     0.WRDA      ;SKIP OTHERWISE
11523 067710 010405      MOV     R4,R5      ;PUT VALUE IN CAD
11524 067712 006205      0.WRD1: ASR     R5      ;MOVE ONE BIT TO CARRY
11525 067714 103711      0.ERR2: BCS     0.ERR  ;JUMP IF ODD ADDRESS
11526 067716 006305      ASL     R5          ;RESTORE THE CARRY BIT
11527 067720 011500      MOV     @R5,R0     ;GET CONTENTS OF WORD
11528 067722 004537 071006  JSR     5,0.CADV   ;GO GET AND TYPE OUT @CAD
11529 067726 000714      BR      0.DCD1     ;GO BACK TO DECODER
11530 067730 042705 000001  0.WRDA: BIC     #1,R5 ;CLEAR CLOSED BIT
11531 067734 000766      BR      0.WRD1     ;GO BACK TO MAIN-LINE
11532
11533      ; PROCESS CARRIAGE RETURN
11534
11535 067736 004737 071216  0.CRET: JSR     PC,0.TCLS ;CLOSE LOCATION
11536 067742 052705 000001  BIS     #1,R5      ;CLOSE EVERYTHING
11537 067746 000702      BR      0.DCD      ;RETURN TO DECODER
11538
11539      ; PROCESS <LF>, OPEN NEXT WORD
11540
11541 067750 004737 071216  0.OP1:  JSR     PC,0.TCLS ;CLOSE PRESENT CELL
11542 067754 005725      TST     (R5)+      ;GENERATE NEW ADDRESS
11543 067756 004537 071242  0.OP2:  JSR     5,0.CRLF ;<CR><LF>
11544 067762 010500      MOV     R5,R0     ;NUMBER TO TYPE
11545 067764 004537 071006  JSR     5,0.CADV   ;TYPE OUT ADDRESS
11546 067770 012700 000057  MOV     #/,R0     ;TYPE A /
11547 067774 004537 071150  JSR     5,0.FTYP   ;
11548 070000 000744      BR      0.WRD1     ;GO PROCESS IT
11549

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11550 ; PROCESS ↑, OPEN PREVIOUS WORD
11551
11552 070002 004737 071216 0.BACK: JSR PC,0.TCLS ;GENERATE NEW ADDRESS
11553 070006 005745 TST -(R5) ;GO DO THE REST
11554 070010 000762 BR 0.OP2
11555
11556 ; PROCESS 0, COMPUTE OFFSET
11557
11558 070012 006205 0.OFST: ASR R5 ;GET LOW ORDER BIT
11559 070014 103737 BCS 0.ERR2 ;ERROR IF CLOSED
11560 070016 006305 ASL R5 ;RESTORE WORD
11561 070020 012700 000040 MOV #' ,RO ;TYPE ONE BLANK
11562 070024 004537 071150 JSR 5,0.FTYP ; AS A SEPARATOR
11563 070030 160504 SUB R5,R4 ;COMPUTE
11564 070032 005304 DEC R4 ;
11565 070034 005304 DEC R4 ; 16 BIT OFFSET
11566 070036 010400 MOV R4,RO ;TYPE A
11567 070040 010402 MOV R4,R2 ;SAVE R4
11568 070042 004537 071006 JSR 5,0.CADV ;NUMBER IN RO - WORD MODE
11569 070046 010200 MOV R2,RO ;
11570 070050 006200 ASR RO ;DIVIDE BY TWO
11571 070052 103402 BCS 0.OF1 ;BRANCH IF ODD
11572 070054 004537 071006 JSR 5,0.CADV ;NUMBER IN RO - BYTE MODE
11573 070060 000137 067560 0.OF1: JMP 0.DCD1 ;ALL DONE
11574
11575 ; SEARCHES - $MSK HAS THE MASK
11576 ; $MSK+2 HAS THE FWA
11577 ; $MSK+4 HAS THE LWA
11578
11579
11580
11581
11582
11583 070064 005201 0.EFF: INC R1 ;SET EFFECTIVE SEARCH
11584 070066 000401 BR 0.WDS
11585 070070 005001 0.WSCH: CLR R1 ;SET WORD SEARCH
11586 070072 005702 0.WDS: TST R2 ;CHECK FOR OBJECT FOUND
11587 070074 001621 0.ERR1: BEQ 0.ERR ;ERROR IF NO OBJECT
11588 070076 013702 067252 MOV 0.MSK+2,R2 ;SET ORIGIN
11589 070102 013705 067250 MOV 0.MSK,R5 ;SET MASK
11590 070106 005105 COM R5 ;AND COMPLEMENT IT
11591 070110 020237 067254 0.WDS2: CMP R2,0.MSK+4 ; IS THE SEARCH ALL DONE?
11592 070114 101217 BHI 0.DCD ; YES
11593 070116 011200 MOV @R2,RO ; GET OBJECT
11594 070120 005701 TST R1 ;NO
11595 070122 001027 BNE 0.EFF1 ;BRANCH IF EFFECTIVE SEARCH
11596 070124 010046 MOV RO,-(SP)
11597 070126 010403 MOV R4,R3 ;EXCLUSIVE OR
11598 070130 040400 BIC R4,RO ; IS DONE
11599 070132 042603 BIC (SP)+,R3 ;
11600 070134 050003 BIS RO,R3 ; IN A VERY
11601 070136 040503 BIC R5,R3 ; FANCY MANNER HERE
11602 070140 001016 0.WDS3: BNE 0.WDS4 ;AND RESULT WITH MASK
11603 070142 010446 MOV R4,-(SP) ;RE-LOOP IF NO MATCH
11604 070144 004537 071242 JSR 5,0.CRLF ;REGISTERS R2,R4, AND R5 ARE SAFE
11605 070150 010200 MOV R2,RO ;TYPE <CR,LF>
;GET READY TO TYPE

```

11606	070152	004537	071006		JSR	5,0,CADV		: TYPE ADDRESS
11607	070156	012700	000057		MOV	#1/R0		: SLASH TO R0
11608	070162	004537	071150		JSR	5,0,FTYP		: TYPE IT
11609	070166	011200			MOV	R2,R0		: GET CONTENTS
11610	070170	004537	071006		JSR	5,0,CADV		: TYPE CONTENTS
11611	070174	012604			MOV	(SP)+,R4		: RESTORE R4
11612	070176	005722		0.WDS4:	TST	(R2)+		: INCREMENT TO NEXT CELL AND
11613	070200	000743			BR	0.WDS2		: RETURN
11614	070202	020004		0.EFF1:	CMP	R0,R4		: IS (X)=K?
11615	070204	001755			BEQ	0.WDS3		: TYPE IF EQUAL
11616	070206	010003			MOV	R0,R3		: (X) TO R3
11617	070210	060203			ADD	R2,R3		: (X)+X
11618	070212	005203			INC	R3		
11619	070214	005203			INC	R3		: (X)+X+2
11620	070216	020304			CMP	R3,R4		: IS (X)+X+2=K?
11621	070220	001747			BEQ	0.WDS3		: BRANCH IF EQUAL
11622	070222	042700	177400		BIC	#177400,R0		: WIPE OUT EXTRANEIOUS BITS
11623	070226	110000			MOVB	R0,R0		: EXTEND SIGN
11624	070230	000257			CCC			
11625	070232	006300			ASL	R0		: MULTIPLY BY TWO
11626	070234	005200			INC	R0		: ADD TWO
11627	070236	005200			INC	R0		
11628	070240	060200			ADD	R2,R0		: ADD PC
11629	070242	020004			CMP	R0,R4		: IS THE RESULT A PROPER REL. BRANCH?
11630	070244	000735			BR	0.WDS3		
11631								
11632								
11633								
11634	070246	105037	071271		0.GO:	CLRB	0.P	: DISALLOW PROCEED
11635	070252	006204				ASR	R4	: CHECK LOW ORDER BIT
11636	070254	103617				BCS	0.ERR2	: ERROR IF ODD NUMBER
11637	070256	006304				ASL	R4	: RESTORE WORD
11638	070260	010437	067242			MOV	R4,0.UPC	: SET UP NEW PC
11639	070264	112737	000340	177776		MOVB	#0,ST1,ST	: SET HIGH PRIORITY
11640	070272	004537	070740			JSR	5,0,RS1T	: RESTORE TELETYPE
11641	070276	105037	071270		0.TBIT:	CLRB	0.T	: CLEAR BOTH
11642	070302	042737	000020	067244		BIC	#0,TBT,0.UST	: T-BIT FLAGS
11643	070310	017737	176742	067262		MOV	30.ADR1,0.UIN	: SAVE INSTRUCTION
11644	070316	013777	071332	176732		MOV	0,TRTC,30.ADR1	: REPLACE WITH TRAP
11645	070324	012600			0.G02:	MOV	(SP)+,R0	: RESTORE
11646	070326	012601				MOV	(SP)+,R1	: R0
11647	070330	012602				MOV	(SP)+,R2	: THRU
11648	070332	012603				MOV	(SP)+,R3	
11649	070334	012604				MOV	(SP)+,R4	
11650	070336	012605				MOV	(SP)+,R5	
11651	070340	012606				MOV	(SP)+,SP	: AND SP
11652	070342	013746	067244			MOV	0,UST,-(SP)	: AND STATUS
11653	070346	013746	067242			MOV	0,UPC,-(SP)	: AND PC
11654	070352	000006			0.RTIT:	RTT		: CHANGED TO RTI FOR 11/20 AND /05
11655								
11656								
11657								
11658								
11659	070354	105737	071271		0.PROC:	TSTB	0.P	: CHECK LEGALITY OF PROCEED
11660	070360	001645				BEQ	0.ERR1	: NOT LEGAL
11661	070362	105037	071271			CLRB	0.P	: CLEAR PROCEED FLAG

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11662 070366 005702          TST      R2          ; WAS COUNT SPECIFIED?
11663 070370 001402          BEQ      0.PRI      ; NO
11664 070372 010437 067260    MOV      R4,0.CT    ; YES, PUT AWAY COUNT
11665 070376 112737 000340    177776 0.PRI: MOVB   #0,STM,ST      ; FORCE HIGH PRIORITY
11666 070404 004537 070740    JSR      5,0.SVTT   ; RESTORE TTY
11667 070410 112737 000340    177776 0.C1:  MOVB   #0,STM,ST      ; SET HIGH PRIORITY
11668 070416 105237 071270    INCB    0.T         ; SET T-BIT FLAG
11669 070422 052737 000020    067244  BIS    #0.TBT,0.UST ; SET T-BIT
11670 070430 000735          BR       0.G02
11671
11672          ;
11673          ; BREAKPOINT HANDLER
11674          ; A TRT BREAKPOINT CAUSES 0.BRK TO BE ENTERED, WHICH SAVES
11675          ; VARIOUS ODDS AND ENDS, FINDS OUT IF THE BREAKPOINT WAS LEGAL,
11676          ; AND GIVES CONTROL TO THE COMMAND DECODER
11677 070432 012637 067242    0.BRK:  MOV    (SP)+,0.UPC ; PRIORITY IS 7 UPON ENTRY
11678 070436 012637 067244          MOV    (SP)+,0.UST    ; SAVE STATUS AND PC
11679 070442 004037 070650    0.BK1:  JSR    0,0.SVR     ; SAVE VARIOUS REGISTERS
11680 070446 105737 071270          TSTB   0.T           ; CHECK FOR T-BIT SET
11681 070452 001311          BNE    0.TBIT        ; JUMP IF SET
11682 070454 013777 067262    176574  MOV    0.UIN,20.ADR1 ; REMOVE BREAKPOINTS
11683 070462 105737 067246          TSTB   0.PRI        ; CHECK IF PRIORITY
11684 070466 100003          BPL    0.BK2        ; IS AS SAME AS USER PGM
11685 070470 113705 067244          MOVB   0.UST,R5     ; PICK UP USER UST IF SO
11686 070474 000407          BR     0.BK3        ; AND DON'T COMPUTE THE PRIORITY
11687 070476 113705 067246    0.BK2:  MOVB   0.PRI,R5   ; OTHERWISE PICK UP ACTUAL PRIORITY
11688 070502 000257          CCC          ; CLEAR CARRY
11689 070504 106005          RORB   R5           ; SHIFT LOW ORDER BITS
11690 070506 106005          RORB   R5           ; INTO
11691 070510 106005          RORB   R5           ; HIGH ORDER
11692 070512 106005          RORB   R5           ; POSITION
11693 070514 110537 177776    0.BK3:  MOVB   R5,ST       ; PUT THE STATUS AWAY WHERE IT BELONGS
11694 070520 013705 067242          MOV    0.UPC,R5     ; GET PC, IT POINTS TO THE TRT
11695 070524 005745          TST    -(R5)        ; SUBTRACT TWO
11696 070526 010537 067242          MOV    R5,0.UPC    ; FROM THE USER'S PC
11697 070532 020537 067256          CMP    R5,0.ADR1   ; COMPARE WITH LIST
11698 070536 001417          BEQ    0.B2         ; JUMP IF FOUND
11699 070540 004537 070706          JSR    5,0.SVTT    ; SAVE TELETYPE STATUS
11700 070544 004537 071242          JSR    5,0.CRLF
11701 070550 012704 071274          MOV    #0.BD,R4    ; ERROR, NOTHING FOUND
11702 070554 012703 071275          MOV    #0.BD+1,R3
11703 070560 004537 071134          JSR    5,0.TYPE    ; OUTPUT "BE" FOR BAD ENTRY
11704 070564 010500          MOV    R5,R0
11705 070566 042737 000020    067244  BIC    #0.TBT,0.UST ; CLEAR OUT ANY POSSIBLE FAKE T-BIT
11706 070574 000420          BR     0.B3        ; AND CONTINUE
11707 070576 005337 067260    0.B2:  DEC    0.CT
11708 070602 003302          BGT    0.C1
11709 070604 012737 000001    067260  MOV    #1,0.CT
11710 070612 105237 071271          INCB   0.P
11711 070616 004537 070706          JSR    5,0.SVTT
11712 070622 012700 000102          MOV    #1,B,R0
11713 070626 004537 071150          JSR    5,0.FTYP
11714 070632 013700 067256          MOV    0.ADR1,R0
11715 070636 004537 071006    0.B3:  JSR    5,0.CADV
11716 070642 005005          CLR    R5
11717 070644 000137 067554          JMP    0.DCD

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11718
11719
11720
11721 070650 012637 071266
11722 070654 010637 067240
11723 070660 012706 067240
11724 070664 010546
11725 070666 010446
11726 070670 010346
11727 070672 010246
11728 070674 010146
11729 070676 013746 071266
11730 070702 005746
11731 070704 000200
11732
11733
11734
11735 070706 113737 177560 071272
11736 070714 113737 177564 071273
11737 070722 105037 177560
11738 070726 105037 177564
11739 070732 004537 071242
11740 070736 000205
11741
11742
11743
11744 070740 004537 071242
11745 070744 105737 177564
11746 070750 100375
11747 070752 032737 004000 177560
11748 070760 001403
11749 070762 105737 177560
11750 070766 100375
11751 070770 113737 071272 177560
11752 070776 113737 071273 177564
11753 071004 000205
11754
11755
11756
11757
11758 071006 010246
11759 071010 012704 071331
11760 071014 012746 000060
11761 071020 010002
11762 071022 042702 177770
11763 071026 061602
11764 071030 110244
11765 071032 006200
11766 071034 006200
11767 071036 006200
11768 071040 020427 071324
11769 071044 101365
11770 071046 042700 177776
11771 071052 062600
11772 071054 110044
11773 071056 012703 071331

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; SAVE REGISTERS R0-R6 IN INTERNAL STACK
0.SVR: MOV (SP)+,0.XXX ; PICK REGISTER FROM STACK AND SAVE
        MOV SP,0.USP ; SAVE USER STACK ADDRESS
        MOV #0.USP,SP ; SET TO INTERNAL STACK
        MOV R5,-(SP) ; SAVE
        MOV R4,-(SP) ; REGISTERS
        MOV R3,-(SP) ; 1
        MOV R2,-(SP) ; THRU
        MOV R1,-(SP) ; 5
        MOV 0.XXX,-(SP) ; PUT SAVED REGISTER ON STACK
        TST -(SP)
        RTS R0

; SAVE TELETYPE STATUS
0.SVTT: MOV 0.RCSR,0.CSR1 ; SAVE R C/SR
        MOV 0.TCSR,0.CSR2 ; SAVE T C/SR
        CLRB 0.RCSR ; CLEAR ENABLE AND MAINTENANCE
        CLRB 0.TCSR ; BITS IN BOTH C/SR
        JSR 5.0.CRLF ; TYPE <CR,LF>
        RTS R5

; RESTORE TELETYPE STATUS
0.RSTT: JSR 5.0.CRLF ; <CR,LF> BEFORE RESTORING
        TSTB 0.TCSR ; WAIT READY ON PRINTER
        BPL -4
        BIT #4000,0.RCSR ; CHECK BUSY FLAG ON READER
        BEQ 0.RSE1 ; SKIP READY LOOP IF NOT BUSY
        TSTB 0.RCSR ; WAIT READY
        BPL -4 ; ON READER
0.RSE1: MCVB 0.CSR1,0.RCSR ; RESTORE
        MOV 0.CSR2,0.TCSR ; THE STATUS REGISTERS
        RTS R5

; TYPE OUT CONTENTS OF WORD OR BYTE WITH ONE TRAILING SPACE
; WORD IS IN R0
0.CADV: MOV R2,-(SP) ; SAVE R2
        MOV #0.BUF+6,R4 ; BUFFER START ADDRESS
        MOV #0,-(SP) ; CONSTANT ASCII 0
0.SPC: MOV R0,R2 ; GET
        BIC #177770,R2 ; OCTAL CHARACTER
        ADD @SP,R2 ; CONVERT TO ASCII
        MOV R2,-(R4) ; STORE IN BUFFER
        ASR R0 ; SHIFT THIS MESS
        ASR R0 ; RIGHT
        ASR R0 ; THREE WHOLE PLACES
        CMP R4,#0.BUF+1 ; DONE?
        BHI 0.SPC ; NO
        BIC #177776,R0 ; GET LAST BIT
        ADD (SP)+,R0 ; CONVERT TO ASCII
        MOV R0,-(R4) ; AND PUT IT AWAY
        MOV #0.BUF+6,R3 ; LWA

```

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11774 071062 004537 071134      JSR      5,0.TYPE      ;TYPE WHOLE STRING OF CHARACTERS
11775 071066 012602              MOV      (SP)+,R2      ;RESTORE R2
11776 071070 000205              RTS      R5
11777
11778      ; GENERAL CHARACTER INPUT ROUTINE
11779      ; CHARACTER INPUT GOES TO R0
11780
11781 071072 105737 177560      0.GET:  TSTB     0.RCSR      ;WAIT FOR
11782 071076 100375              BPL     -4              ;INPUT FROM KEYBOARD
11783 071100 113700              MOVB    0.RDB,R0       ;GET A CHARACTER
11784 071104 004537 071150      JSR     5,0.FTYP       ;ECHO CHARACTER
11785 071110 042700 177600      BIC     #177600,R0     ;STRIP OFF PARITY FROM CHARACTER
11786 071114 001766              BEQ     0.GET          ;IGNORE NULLS
11787 071116 122700 000040      CMPB    #40,R0        ;CHECK FOR SPACES
11788 071122 001763              BEQ     0.GET          ;IGNORE NULLS
11789 071124 122700 000073      CMPB    #'',R0        ;CHECK FOR SEMI-COLON
11790 071130 001760              BEQ     0.GET          ;IGNORE THEM IF FOUND
11791 071132 000205              RTS      R5
11792
11793      ; GENERAL CHARACTER OUTPUT ROUTINE
11794      ; ADDRESS OF FIRST BYTE IN R4,
11795      ; ADDRESS OF LAST BYTE IN R3, (R3)>(R4)
11796
11797 071134 020304              0.TYPE: CMP      R3,R4      ;CHECK FOR COMPLETION
11798 071136 103426              BLO     0.TYP1        ;EXIT WHEN DONE
11799 071140 112400              MOVB    (R4)+,R0      ;GET A CHARACTER
11800 071142 004537 071150      JSR     5,0.FTYP       ;TYPE ONE CHARACTER
11801 071146 000772              BR      0.TYPE        ;LOOP UNTIL DONE
11802
11803      ; TYPE ONLY ONE CHARACTER (CONTAINED IN R0)
11804
11805 071150 105737 177564      0.FTYP: TSTB     0.TCSR      ;CHECK STATUS
11806 071154 100375              BPL     -4              ;WAIT UNTIL READY
11807 071156 110037 177566      MOVB    R0,0.TDB      ;TYPE ONE CHARACTER
11808 071162 120037 000045      CMPB    R0,#45        ;IS CHAR TO BE FILLED?
11809 071166 001012              BNE     0.TYP1        ;NO
11810 071170 113746 000044      MOVB    #44,-(SP)     ;YES, INIT THE COUNT
11811 071174 105737 177564      0.TYP2: TSTB     0.TCSR      ;CHECK STATUS
11812 071200 100375              BPL     0.TYP2        ;WAIT UNTIL READY
11813 071202 105037 177566      CLRB    0.TDB          ;GENERATE NULL FILLER
11814 071206 105316              DECB    #SP
11815 071210 003371              BGT     0.TYP2        ;GENERATE NULL FILLER
11816 071212 005726              TST     (SP)+         ;POP STACK
11817 071214 000205              0.TYP1: RTS      R5
11818
11819      ; CLOSE WORD OR BYTE AND EXIT
11820      ; UPON ENTERING, R2 HAS NUMERIC FLAG, R4 HAS CONTENTS
11821
11822 071216 006205              0.TCLS: ASR      R5      ;GET LOW ORDER BIT
11823 071220 103405              BCS     0.TC          ;JUMP IF ALREADY CLOSED
11824 071222 006305              ASL     R5
11825 071224 005702              TST     R2            ;IF NO NUMBER WAS TYPED THERE IS
11826 071226 001401              BEQ     0.CLS1        ;NO CHANGE TO THE OPEN CELL
11827 071230 010415              MOV     R4,#R5        ;STORE WORD
11828 071232 000207              0.CLS1: RTS      PC
11829 071234 005746              0.TC:   TST     -(SP)  ;POP EXTRA CELL FROM STACK

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11830 071236 000137 067540          JMP      O.ERR          ;AND SCREAM BLOODY MURDER
11831
11832          ; O.CRLF - TYPE <CR,LF>
11833          ; O.CRLS - TYPE <CR,LF>*
11834
11835 071242 012703 071277          O.CRLF: MOV      #O.CR+1,R3      ;LWA <CR,LF>
11836 071246 000402                    BR      O.CRS
11837 071250 012703 071300          O.CRLS: MOV      #O.CR+2,R3      ;LWA <CR,LF>*
11838 071254 012704 071276          O.CRS:  MOV      #O.CR,R4        ;FWA
11839 071260 004537 071134          JSR     5,O.TYPE          ;TYPE SOMETHING
11840 071264 000205                    RTS      R5
11841
11842 071266 000000                    O.XXX:  .WORD    0              ;TEMPORARY STORAGE
11843 071270          000                    O.T:    .BYTE    0              ; T-BIT FLAG
11844 071271          000                    O.P:    .BYTE    0              ;PROCEED FLAG = 0 IF PROCEED NOT ALLOWED
11845                                     ; = 1 IF PROCEED ALLOWED
11846 071272          000                    O.CSR1: .BYTE    0              ;SAVE CELL - R C/SR
11847 071273          000                    O.CSR2: .BYTE    0              ;SAVE CELL - T C/SR
11848
11849
11850 071274 042502                    O.BD:   .WORD    "BE
11851
11852 071276          015                    O.CR:   .BYTE    015           ; <CR>
11853 071277          012                    .BYTE    012           ; <LF>
11854 071300          052                    .BYTE    '*'           ; *
11855
11856 071301          057                    O.LGCH: .BYTE    '/'           ; /
11857 071302          015                    .BYTE    015           ; CARRIAGE RETURN
11858 071303          044                    .BYTE    '$'           ; $
11859 071304          107                    .BYTE    'G'           ; G
11860 071305          012                    .BYTE    012           ; <LF>
11861 071306          137                    .BYTE    '+'           ; +
11862 071307          136                    .BYTE    '+'           ; +
11863 071310          117                    .BYTE    'O'           ; O
11864 071311          127                    .BYTE    'W'           ; W
11865 071312          105                    .BYTE    'E'           ; E
11866 071313          102                    .BYTE    'B'           ; B
11867 071314          120                    .BYTE    'P'           ; P
11868          000014                    O.CLGT  =      -O.LGCH          ;TABLE LENGTH
11869
11870 071315          123                    O.TL:   .BYTE    'S'           ; DO
11871 071316          120                    .BYTE    'P'           ; NOT
11872 071317          115                    .BYTE    'M'           ; CHANGE
11873 071320          000                    .BYTE    0             ; THE
11874 071321          000                    .BYTE    0             ; ORDER
11875 071322          102                    .BYTE    'B'           ; HERE
11876          000006                    O.LG     =      -O.TL
11877
11878 071323                    O.BUF:   =      +6           ;6 CHAR. BUFFER WITH
11879          071331                    .        ;
11880 071331          040                    .BYTE   ;
11881                                     .EVEN   ;TRAILING BLANK
11882
11883 071332 000003                    O.TRTC: TRT          ;TRACE TRAP PROTOTYPE
11884
11885          ;THE ORDER OF THE FOLLOWING ENTRIES IS CRITICAL

```

11886		
11887		067224
11888	067224	000000
11889	067226	000000
11890	067230	000000
11891	067232	000000
11892	067234	000000
11893	067236	000000
11894	067240	000000
11895	067242	000000
11896	067244	000000
11897	067246	000007
11898	067250	000000
11899	067252	000000
11900	067254	000000
11901		
11902		
11903		
11904		
11905	067256	000000
11906	067260	000000
11907	067262	000000
11908		000001

```

;
O.URO: = 0 ODT-40
          : USER RC
          : R1
          : R2
          : R3
          : R4
          : R5
O.USP: 0 0 USER SP
O.UPC: 0 0 USER PC
O.UST: 0 0 USER ST
O.PRI: 7 ODT PRIORITY
O.MSK: 0 MASK
          0 LOW LIMIT
          0 HIGH LIMIT
;
; BREAK POINT LISTS, ADR1 = ADDRESS OF BREAKPOINT, CT = COUNT,
; UIN = CONTENTS
O.ADR1: 0
O.CT: 0
O.UIN: 0
.END

```

ABASE = 177440	2553	2594	2608#						
ACDW1 = 000000	2553	2596							
ACDW2 = 000000	2553	2597							
ACLO = 000010	1434#								
ACPUOP = 000000	2553	2568							
ACT11 = 005430	2763#	3734*							
ADDW0 = 000000	2553	2598							
ADDW1 = 000000	2553	2599							
ADDW10 = 000000	2553								
ADDW11 = 000000	2553								
ADDW12 = 000000	2553								
ADDW13 = 000000	2553								
ADDW14 = 000000	2553								
ADDW15 = 000000	2553								
ADDW2 = 000000	2553	2600							
ADDW3 = 000000	2553	2601							
ADDW4 = 000000	2553	2602							
ADDW5 = 000000	2553	2603							
ADDW6 = 000000	2553	2604							
ADDW7 = 000000	2553	2605							
ADDW8 = 000000	2553								
ADDW9 = 000000	2553								
ADEVCT = 000000	2553	2559							
ADEVN = 000000	2553	2595							
AENV = 000000	2553	2564							
AENVN = 000000	2553	2565							
AFATAL = 000000	2553	2556							
AMADR1 = 000000	2553	2581							
AMADR2 = 000000	2553	2585							
AMADR3 = 000000	2553	2588							
AMADR4 = 000000	2553	2591							
AMAMS1 = 000000	2553	2575							
AMAMS2 = 000000	2553	2583							
AMAMS3 = 000000	2553	2586							
AMAMS4 = 000000	2553	2589							
AMSGAD = 000000	2553	2561							
AMSGLG = 000000	2553	2562							
AMSGTY = 000000	2553	2555							
AMTYP1 = 000000	2553	2576							
AMTYP2 = 000000	2553	2584							
AMTYP3 = 000000	2553	2587							
AMTYP4 = 000000	2553	2590							
APASS = 000000	2553	2558							
APRIOR = 000000	2553								
APTCSU = 000040	9362	9534#							
APTENV = 000001	9309	9355	9490	9532#					
APTSIZ = 000200	3663	9531#							
APTSP0 = 000100	9357	9492	9533#						
ASWREG = 000000	2553	2566							
ATESTN = 000000	2553	2557							
ATTN = 005304	2698#	7141	7269	7732	7854	8261	8281	8308	8337
AUNIT = 000000	2553	2560							
AUSWR = 000000	2553	2567							
AVECT1 = 000000	2553	2592							
AVECT2 = 000000	2553	2593							
BADHDR = 005300	2688#	3742*	9003						

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 232
CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0231

DOCMD	043372	4236	4263	4303	4437	4596	4712	4758	4807	4853	4909	4961	5016	5084
		5136	5191	5268	5276	5350	5358	5439	5447	5473	5523	5531	5562	5570
		5601	5609	5621	5664	5672	5698	5748	5756	5787	5795	5826	5834	5846
		5894	5902	5948	5956	5969	5977	6027	6035	6081	6089	6102	6110	6177
		6185	6234	6242	6286	6294	6306	6314	6322	6354	6362	6406	6414	6426
		6434	6442	6474	6482	6535	6543	6607	6615	6686	6694	6717	6725	6769
		6777	6800	6808	6867	6875	6910	6918	6931	6959	6967	7002	7010	7023
		7059	7067	7081	7114	7187	7195	7209	7242	7332	7340	7459	7467	7550
		7558	7575	7583	7703	7711	7727	7828	7836	7848	7951	7987	7995	8179#
		8509	8993	9011	9027									
DPAT1	001442	2667#												
DPAT2	001444	2668#												
DRA	= 000001	1431#	3962	4065										
DRAV	044172	4229	4254	4335	4356	4387	4392	4403	4494	4515	4546	4551	4562	4707
		4780	4802	4875	4905	4941	4993	5080	5116	5168	5260	5342	5504	5729
		5929	6062	6169	6226	6555	6575	6627	6647	6706	6734	6789	6817	6898
		6990	7146	7153	7165	7274	7281	7293	7736	7751	7858	7873	8326#	
DRDY	= 000200	1438#												
DRIVS	005434	2765#	3853*	3866*	3883	3899*	3929*	3948	4052*	4119	8025	8120*		
DRIVO	005436	2771#	3746	3855	3901	4014	8101							
DRIV1	005440	2772#												
DRIV2	005442	2773#												
DRIV3	005444	2774#												
DRIV4	005446	2775#												
DRIV5	005450	2776#												
DRIV6	005452	2777#												
DRIV7	005454	2778#												
DROT	= 000040	1436#												
DRPAR	= 000010	1415#												
DRVMSK	= 000007	1395#	3922	4035										
DRVPTR	001342	2626#	3746*	4126	4146*									
DSC	= 040000	1442#												
DSWR	= 177570	1247#	2527	3650										
DTE	= 010000	1424#												
DTYE	= 000040	1417#	3954	4056										
DT1	065512	2805	2811	2817	2823	2834	2840	2845	2850	2855	2860	2865	2870	2891
		2897	2907	2923	2983	2988	2993	2998	3033	3073	3088	3093	3098	3103
		3108	3113	3118	3143	3168	3173	3178	3183	3188	3193	3198	3203	3208
		3213	3218	3223	3228	3233	3238	3243	3248	3253	3258	3263	3268	3273
		3338	3343	3348	3353	3358	3363	3368	3413	3418	3423	3428	3433	3438
		3443	3448	3453	3463	3488	3543	3558	3563	3568	3583	3593	11052#	
DT13	066050	2902	2912	2917	2928	2933	2938	2943	2948	2953	2958	2963	2968	2973
		2978	3018	3023	3028	3038	3043	3048	3053	3058	3063	3068	3078	3083
		3123	3128	3133	3138	3148	3153	3158	3163	3263	3268	3273	3278	3288
		3293	3298	3303	3313	3318	3323	3328	3373	3378	3383	3388	3393	3398
		3403	3408	3493	3498	3503	3508	3533	3538	11100#				
DT14	066130	3003	3008	11110#										
DT15	066220	11121#												
DT3	065552	2880	11059#											
DT4	065556	3483	3548	11060#										
DT6	065624	2875	2885	11068#										
DT7	065670	3458	11076#											
DT8	065736	3478	11084#											
DT9	066004	3013	11092#											
D.ACLO=	000100	1491#												
D.BRHM=	000100	1478#	4277	4312	4341	4410	4446	4500	4569	4605	4727	4767	4822	4862

N02

CZR6GB0 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 234
CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0233

D.SPIN= 010000	1470#	4275	4310	4339	4363	4408	4444	4498	4522	4567	4603	4725	4765
	4820	4860	4922	4945	4974	4997	5023	5047	5097	5120	5149	5172	5198
	5222	5288	5309	5370	5391	5455	5486	5542	5581	5628	5680	5711	5767
	5806	5853	5910	5992	6043	6125	6193	6250	6334	6375	6454	6495	6558
	6630	7094	7121	7222	7249	7372	7507	7526	7627	7646	7760	7799	7881
	7920	7964	9034										
D.SPLS= 010000	1497#	4277	4312	4341	4410	4446	4500	4569	4605	4727	4767	4822	4862
D.SP0K= 001000	1481#	4924	4947	4976	4999	5025	5049	5099	5122	5151	5174	5200	5224
	5311	5372	5393	5457	5488	5544	5583	5630	5682	5713	5769	5808	5855
	5912	5994	6045	6127	6195	6252	6336	6377	6456	6497	6560	6632	7096
	7123	7224	7251	7374	7509	7528	7629	7648	7762	7801	7883	7922	7966
	9036												
D.SSP = 000020	1476#	4277	4312	4341	4410	4446	4500	4569	4605	4727	4767	4822	4862
	4924	4947	4976	4999	5025	5049	5099	5122	5151	5174	5200	5224	5290
	5311	5372	5393	5457	5488	5544	5583	5630	5682	5713	5769	5808	5855
	5912	5994	6045	6127	6195	6252	6336	6377	6456	6497	6560	6632	7096
	7123	7224	7251	7374	7509	7528	7629	7648	7762	7801	7883	7922	7966
	9036												
D.SUNS= 040000	1513#	4239	4275	4310	4339	4363	4408	4444	4498	4522	4567	4603	4725
D.TIB = 002000	1509#	4820	4860	4922	4945	4974	4997	5023	5047	5097	5120	5149	5172
D.UNLD= 040000	1486#	5198	5222	5288	5309	5370	5391	5455	5486	5542	5581	5628	5680
D.UNS = 040000	1499#	5767	5806	5853	5910	5992	6043	6125	6193	6250	6334	6375	6454
D.VV = 000100	1464#	6558	6630	7094	7121	7222	7249	7372	7507	7526	7627	7646	7760
	4765	7920	7964	9034									
	1504#												
D.WCUR= 000040	1505#												
D.WGAT= 000100	1496#												
D.WLE = 004000	1469#												
D.WRL = 004000	1508#												
D.XERR= 001000	1457#												
ECCW = 020000	1418#												
ECH = 000100	1336#	3634*	3635*										
EMTVEC= 000030	8129	10429#											
EM1 057036	2838	10507#											
EM10 057712	2843	10516#											
EM11 057775	2848	2858	2868	3196	3201	3206	3211	3226	3251	3331	3411	3446	3556
EM12 060041	10523#												
EM13 060077	3031	3101	3106	3166	3176	3216	3256	3336	3356	10529#			
EM14 060121	2878	10533#											
EM15 060157	2986	10539#											
EM16 060266	3566	3581	10551#										
EM17 060331	2941	2961	3016	3026	3041	3051	3121	3146	3261	3286	3311	3371	3391
	3491	3531	10557#										
EM18 060352	2900	2915	2931	2946	2966	3056	3076	3126	3151	3266	3291	3316	3376
	3396	3496	10560#										
EM19 060373	2951	2971	3021	3036	3046	3061	3131	3156	3271	3296	3321	3381	3401
	3501	3536	10563#										
EM2 057111	2803	10437#											
EM20 060414	2910	2926	2936	2956	2976	3066	3081	3136	3161	3276	3301	3326	3386
	3406	3506	10566#										
EM21 060435	2853	2863	2895	2905	3426	3451	3486	3541	3561	10569#			

CZR6G80 RK611 DU PORT LGC TST
CZR6GB.P11 03-JAN-78 08:46

MACY11 30A(1052) 03-JAN-78 08:56 PAGE 235
CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0234

EM22	060457	3111	10573#																			
EM23	060564	3116	10585#																			
EM24	060664	2921	10597#																			
EM25	060717	2996	10602#																			
EM26	060771	2981	10610#																			
EM27	061050	3071	10618#																			
EM28	061167	3863	10632#																			
EM29	061275	3351	3441	10645#																		
EM3	057201	9076	10447#																			
EM30	061343	3096	10652#																			
EM31	061364	2991	3091	3181	3361	3416	3461	10655#														
EM32	061407	3141	10659#																			
EM33	061426	3171	3191	3366	10662#																	
EM34	061451	3186	3221	10666#																		
EM35	061500	3246	10670#																			
EM36	061526	3476	3481	3546	10674#																	
EM37	061571	3306	10680#																			
EM38	061622	3341	10685#																			
EM39	061675	3001	3281	3431	10693#																	
EM4	057254	10455#																				
EM40	061744	3006	3436	10700#																		
EM5	057330	2809	3241	10463#																		
EM55	062002	3346	10705#																			
EM6	057351	2815	10466#																			
EM63	062035	3421	10710#																			
EM65	062056	3456	10713#																			
EM69	062100	3591	10716#																			
EM7	057372	2821	10469#																			
EM73	062131	3231	10721#																			
EM74	062152	3086	10724#																			
EM79	062177	3236	10728#																			
EM8	057562	10491#																				
EM80	062220	2873	10731#																			
EM82	062257	2883	10737#																			
EM83	062331	2889	10745#																			
EM84	062366	3232	3237	3242	10750#																	
EM9	057636	2832	10499#																			
EM93	062457	3011	10760#																			
ERRVEC=	000004	1329#	3648	3649*	3660*	3667*	3668*	3681*	3682*	3751*	3755*	3762*	3799*	3817*								
E.A0	005404	3818#	9235	9236*	9238*	9241*																
		2744#	4275*	4310*	4339*	4363*	4364	4408*	4444*	4498*	4522*	4523	4567*	4603*								
		4725*	4765*	4820*	4860*	4922*	4945*	4974*	4997*	5023*	5047*	5097*	5120*	5149*								
		5172*	5198*	5222*	5288*	5309*	5370*	5391*	5455*	5486*	5542*	5581*	5628*	5680*								
		5711*	5767*	5806*	5853*	5910*	5992*	6043*	6125*	6193*	6250*	6334*	6375*	6454*								
		6495*	6558*	6630*	7094*	7121*	7222*	7249*	7372*	7507*	7526*	7627*	7646*	7760*								
		7799*	7881*	7920*	7964*	8373*	8374*	8381*	8385	8387*	8416	9034*	11100	11110								
E.A1	005410	11121																				
		2746#	4277*	4312*	4341*	4364*	4365*	4410*	4446*	4500*	4523*	4524*	4569*	4605*								
		4727*	4767*	4822*	4862*	4924*	4947*	4976*	4999*	5025*	5049*	5099*	5122*	5151*								
		5174*	5200*	5224*	5290*	5311*	5372*	5393*	5457*	5488*	5544*	5583*	5630*	5682*								
		5713*	5769*	5808*	5855*	5912*	5994*	6045*	6127*	6195*	6252*	6336*	6377*	6456*								
		6497*	6560*	6632*	7096*	7123*	7224*	7251*	7374*	7509*	7528*	7629*	7648*	7762*								
E.A2	005414	7801#	7883*	7922*	7966*	8375*	8376*	8389	8391*	8436	9036*	11100	11110	11121								
		2748#	4279*	4314*	4343*	4412*	4448*	4502*	4571*	4607*	4729*	4769*	4824*	4864*								
		4926*	4949*	4978*	5001*	5027*	5051*	5101*	5124*	5153*	5176*	5202*	5226*	5292*								
		5313*	5374*	5395*	5459*	5490*	5546*	5585*	5632*	5684*	5715*	5771*	5810*	5857*								

O.DCD	067554	11467	11469	11479#	11537	11592	11717		
O.DCD1	067560	11480#	11529	11573					
O.EFF	070064	11514	11583#						
O.EFF1	070202	11595	11614#						
O.ENTR	067270	11406#							
O.ERR	067540	11453	11464	11476#	11499	11525	11587	11830	
O.ERR1	070074	11587#	11660						
O.ERR2	067714	11525#	11559	11636					
O.FTYP	071150	11478	11547	11562	11608	11713	11784	11800	11805#
O.GET	071072	11432	11482	11781#	11786	11788	11790		
O.GO	070246	11507	11634#						
O.GO2	070324	11645#	11670						
O.LG =	000006	11436	11876#						
O.LGCH	071301	11495	11856#	11868					
O.LGDR	067654	11502	11504#	11517					
O.LGL =	000030	11517#							
O.LGL1	067626	11495#	11500						
O.LGL2	067646	11496	11501#						
O.MSK	067250	11588	11589	11591	11898#				
O.ODT	067264	1543	11404#	11887					
O.OFST	070012	11512	11558#						
O.OF1	070060	11571	11573#						
O.OP1	067750	11508	11541#						
O.OP2	067756	11457	11543#	11554					
O.ORPC	067462	11449#	11509						
O.P	071271	11424#	11634#	11659	11661*	11710*	11844#		
O.PRI	067246	11416	11683	11687	11897#				
O.PROC	070354	11516	11659#						
O.PR1	070376	11663	11665#						
O.RALL	067530	11427	11462	11468#					
O.RCSR=	177560	11394#	11735	11737#	11747	11749	11751*	11781	
O.RDB =	177562	11393#	11783						
O.REGT	067412	11432#	11506						
O.RSE1	070770	11748	11751#						
O.RSP	067422	11434#	11437						
O.RST	067326	11405	11414#						
O.RSTT	070740	11640	11666	11744#					
O.RST1	067356	11413	11421#						
O.RTIT	070352	11423#	11654#						
O.SCAN	067564	11443	11482#	11493					
O.SP	067454	11435	11444#						
O.SPC	071020	11761#	11769						
O.SP1	067442	11440#	11445						
O.STM =	000340	11380#	11425	11639	11665	11667			
O.STRT	067314	11404	11411#						
O.SVR	070650	11414	11679	11721#					
O.SVTT	070706	11699	11711	11735#					
O.T	071270	11641#	11668#	11680	11843#				
O.TBIT	070276	11641#	11681						
O.TBT =	000020	11381#	11642	11669	11705				
O.TC	071234	11823	11829#						
O.TCLS	071216	11449	11535	11541	11552	11822#			
O.TCSR=	177564	11396#	11736	11738*	11745	11752*	11805	11811	
O.TDB =	177566	11395#	11807*	11813*					
O.TL	071315	11433	11436	11444	11870#	11876			
O.TRTC	071332	11468	11644	11883#					

O.TVEC=	000014	11379#	11407	11425*	11426*				
O.TYPE	071134	11703	11774	11797#	11801	11839			
O.TYP1	071214	11798	11809	11817#					
O.TYP2	071174	11811#	11812	11815					
O.UIN	067262	11415	11643*	11682	11907#				
O.UPC	067242	11408*	11638*	11653	11677*	11694	11696*	11895#	
O.URD	067224	11411	11441	11888#					
O.USP	067240	11412*	11722*	11723	11894#				
O.UST	067244	11406*	11642*	11652	11669*	11678*	11685	11705*	11896#
O.WDS	070072	11584	11586#						
O.WDS2	070110	11591#	11613						
O.WDS3	070140	11602#	11615	11621	11630				
O.WDS4	070176	11602	11612#						
O.WRD	067704	11504	11521#						
O.WRDA	067730	11522	11530#						
O.WRD1	067712	11524#	11531	11548					
O.WSCH	070070	11513	11585#						
O.XXX	071266	11721*	11729	11842#					
O.45	067370	11422	11424#						
PACK =	000003	1367#	4235						
PARAM	001336	2623#	3598*	3601*	3710				
PARSRT	010040	1540	3598#						
PAT =	000020	1448#							
PCA =	004000	1455#							
PCD =	010000	1456#							
PCLKF	005460	2781#	3757*	3764*	8889	8904			
PCVEC	001332	2617#	3758	3765					
PCYL	001352	2632#							
PFSRT	013004	4178#	9151						
PGE =	002000	1403#							
PIP =	020000	1441#							
PIRQ =	177772	1246#							
PIRQVE=	000240	1340#							
PKRB	001324	2613#							
PKS	001320	2611#	3756	3763	8894*	8908*			
PKSB	001322	2612#	8893*						
PPTP	005432	2764#	3701*						
PRGSRT	010054	3599	3602#	3865	8130				
PRO =	000000	1263#	3604	3794					
PR1 =	000040	1264#							
PR2 =	000100	1265#							
PR3 =	000140	1266#							
PR4 =	000200	1267#							
PR5 =	000240	1268#	2610						
PR6 =	000300	1269#	3767						
PR7 =	000340	1270#	3668	3674	3818	9146	9147		
PS =	177776	1243#	1244						
PSW =	177776	1244#							
PWRVEC=	000024	1335#	3638*	3639*	9136*	9145*	9146*		
RCYLA	046100	8461	8643#						
RCYLD	046026	8458	8630#						
RDCHR =	104410	9813	10195#						
RDCYLA	045756	4740	4835	8618#					
RDCYLD	045672	8602#							
RDDATA=	000021	1374#	7364						
RDGATE=	100000	1459#							

H04

CZR6GB0 RK611 DU PORT LGC TST MACY11 30A(1052) 03-JAN-78 08:56 PAGE 255
CZR6GB.P11 03-JAN-78 08:46 CROSS REFERENCE TABLE -- MACRO NAMES

SEQ 0253

.STYPE 1194# 9332
.STYPO 1194# 9535

. ABS. 071334 000

ERRORS DETECTED: 0

RM03:CZR6GB, RM03:CZR6GB.SEQ/SOL/CRF/NL:TOC/DOC=RM03:CZR6GB.P11

RUN-TIME: 36 32 4 SECONDS
RUN-TIME RATIO: 302/72=4.1
CORE USED: 33K (65 PAGES)

DOCUMENT PAGES: 253