

# DLV11-F

DLV11-F OFFLINE TEST  
CVDVCB0

AH-E007B-MC

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This microfiche card contains a grid of frames. The frames are arranged in approximately 15 rows and 10 columns. Each frame contains a small, high-contrast image of a document page, likely containing technical data or test results. The images are very small and difficult to read, but they appear to be organized in a structured manner, possibly representing a sequence of test results or a data table. The right side of the card is a dark, solid area, likely the binding or a blank space.



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IDENTIFICATION  
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PRODUCT CODE AC-E006B-MC  
PRODUCT NAME CVDUCBO DLV11-F OFFLINE TEST  
PRODUCT DATE MARCH, 1978  
AUTHOR ODES CHOATE  
MAINTAINER DIAGNOSTIC ENGINEERING GROUP

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1 0 GENERAL PROGRAM INFORMATION  
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1 1 PROGRAM PURPOSE (ABSTRACT)

THIS DIAGNOSTIC IS A LOGIC TEST TO VERIFY THE OPERATION OF THE DLV11-F SERIAL LINE INTERFACE. THE USER CAN SELECTIVELY ENABLE AND DISABLE TESTING OF THE OPTIONS BY ALTERING THE CONTENTS OF '\$USER'. THE DIAGNOSTIC IS DESIGNED TO TEST AND DETECT FAULTS TO THE LOGIC LEVEL (NOT TO THE CHIP LEVEL). THIS TEST OPERATES ON UP TO SIXTEEN(16) IDENTICALLY CONFIGURED DLV11-F SERIAL LINE INTERFACES. THE DEFAULT ADDRESSES ARE

177560 -CONSOLE INTERFACE DEVICE ADDRESS  
175610 -FIRST SERIAL LINE ADDRESS OF 15 CONSECUTIVE SERIAL LINE DEVICES

60 - VECTOR FOR CONSOLE DEVICE INTERFACE  
300 - VECTOR FOR FIRST OF 15 DEVICES

THIS PROGRAM IS DESIGNED TO RUN ON ANY PDP-11 WITH 4K OF MEMORY AND A DLV11-F (LSI-BUS) MODULE. IT CAN RUN UNDER XXDP, APT, AND ACT MONITORS, AND ON PROCESSORS WITH NO HARDWARE SWITCH REGISTER. A POWER FAILURE WILL CAUSE THE DIAGNOSTIC TO RESTART.

1 2 SYSTEM REQUIREMENTS

HARDWARE REQUIREMENTS

ANY PDP-11 FAMILY PROCESSOR  
4K MEMORY - MINIMUM  
A SPECIAL WRAP CONNECTOR OR EQUIVALENT (OPTIONAL)

SOFTWARE REQUIREMENTS

THIS DIAGNOSTIC IS DESIGNED TO RUN IN ANY OF THE FOLLOWING WAYS:  
STAND ALONE  
WITH APT MONITOR  
WITH ACT MONITOR  
WITH XXDP MONITOR (CHAINABLE)

1 3 RELATED DOCUMENTS AND STANDARDS

DIAGNOSTIC ENGINEERING STANDARDS AND CONVENTIONS  
APT MD-11-DZZMA  
ACT AUTOCAT-11-QZAU8  
SYSMAC MD-11-DZQAC

1 4 DIAGNOSTIC HIERARCHY PREREQUISITES

126 NO SPECIAL DIAGNOSTICS ARE REQUIRED TO RUN BEFORE THIS, BUT  
127 THE PROCESSOR, MEMORY, AND BUS ARE ASSUMED TO BE FULLY  
128 OPERATIONAL

129  
130  
131 1 5 ASSUMPTIONS

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133  
134 THIS DIAGNOSTIC ASSUMES THAT THE OPERATOR HAS INITIALIZED  
135 LOCATION 'SUSWR' AND 'SDEVN' TO THE PROPER VALUES

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137  
138 2 0 OPERATING INSTRUCTIONS  
139 -----

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142 2 1 LOADING AND STARTING PROCEDURES

143  
144 USE STANDARD PROCEDURE FOR PDP-11 ABSOLUTE BINARY FORMATTED  
145 MEDIA

146  
147 THIS DIAGNOSTIC HAS ONLY ONE (1) STARTING ADDRESS 200 FOR  
148 START AND RESTART

149  
150 THE USER CAN SELECT A SPECIFIC TEST TO BE EXECUTED BY SETTING  
151 SWITCH 8 IN THE SWITCH REGISTER AND THE TEST NUMBER (IN OCTAL)  
152 IN THE LOWER BYTE (NOTE ALL TESTS PREVIOUS TO THE SELECTED  
153 ONE ARE EXECUTED WITHOUT ITERATIONS)

154  
155  
156 2 2 SPECIAL ENVIRONMENTS

157  
158 THIS DIAGNOSTIC FOLLOWS THE STANDARD PROCEDURE FOR RUNNING  
159 UNDER APT,ACT,XXDP MONITORS, AS DESCRIBED IN THEIR RESPECTIVE  
160 PROCEDURES MANUAL AND SYSMAC PACKAGE

161  
162 2 3 OPERATIONAL SWITCH SETTINGS

163  
164 IF THE DIAGNOSTIC IS RUN ON A CPU WITHOUT A SWITCH  
165 REGISTER THEN A SOFTWARE SWITCH REGISTER IS USED WHICH  
166 ALLOWS THE USER THE SAME SWITCH OPTIONS AS THE  
167 HARDWARE SWITCH REGISTER IF THE HARDWARE SWITCH  
168 REGISTER DOES NOT EXIST OR IF ONE DOES AND IT CONTAINS  
169 ALL ONES (177777) THEN THE SOFTWARE SWITCH REGISTER  
170 (LOC 176) IS USED

171  
172  
173 CONTROL

174  
175 THIS PROGRAM ALSO SUPPORTS THE DYNAMIC LOADING OF THE SOFTWARE  
176 SWITCH REGISTER (LOC 176) FROM THE TTY THIS CAN BE  
177 ACCOMPLISHED BY DOING THE FOLLOWING

178  
179 1) TYPE CONTROL G < G>. THIS WILL ALLOW THE TTY TO ENTER  
180 DATA INTO LOC 176 AT SELECTED POINTS WITHIN THE

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PROGRAM

- 2) THE MACHINE WILL THEN TYPE 'SWR=XXXXXX NEW=' (XXXXXX IS THE OCTAL CONTENTS OF THE SOFTWARE SWITCH REGISTER )
- 3) AFTER THE 'NEW=' HAS BEEN TYPED THEN THE OPERATOR CAN DO ONE OF THE FOLLOWING AT THE TTY
  - A) TYPE A NUMBER TO BE LOADED INTO LOC 176 FOLLOWED BY A <CR> (ONLY NUMBERS BETWEEN 0-7 WILL BE ACCEPTED) LEADING ZEROS NEED NOT BE TYPED, AND IF MORE THAN 6 DIGITS ARE TYPED THE LAST 6 WILL BE USED IF A <CR> IS THE FIRST KEY DEPRESSED THE SOFTWARE SWITCH REGISTER CONTENTS WILL NOT BE CHANGED
  - B) IF A CONTROL U < U> IS DEPRESSED THEN THE PROGRAM WILL SEND YOU BACK TO STEP 3
  - C) IF THE INPUT CHARACTER IS NOT ONE OF THE CHARACTERS MENTIONED ABOVE THEN A QUESTION MARK (?) WILL BE TYPED FOLLOWED BY A CARRAGE RETURN AND A LINE FEED SEQUENCE THEN PROCEED FROM STEP 3 (ERASING ALL PREVIOUS INPUT)

DYNAMIC SWITCH REGISTER  
-----

- BIT 15 - HALT ON ERROR
- 14 - LOOP ON TEST
- 13 - INHIBIT ERROR TYPEOUTS
- 12 - (UNUSED)
- 11 - INHIBIT ITERATIONS
- 10 - BELL ON ERROR
- 9 - LOOP ON ERROR
- 8 - LOOP ON TEST IN SWR<? 0>
- 7 0 - TEST NUMBER TO LOOP ON (USED WITH B T 8)

2 4 PROGRAM OPTIONS

THIS PROGRAM WILL SUPPORT TESTING OF MULTIPLE DLV11-F'S IT REQUIRES THE ADDRESS OF THE FIRST RCSR (STORED AT '\$BASE') AND ITS INTERRUPT VECTOR (STORED AT '\$VECT1'), AND WILL BE ABLE TO ADDRESS ANY DLV11-F STARTING AT THE SPECIFIED BASE ADDRESS UP TO 16 CONSECUTIVE DEVICES

EXAMPLES \$BASE 175610  
\$VECT1 300

THE PROGRAM WILL BE ABLE TO TEST ANY DLV11-F WITHIN THE ADDRESS RANGE 175610 --> 176000

\$BASE AND \$VECT1 DEFAULT TO 175610 AND 300 RESPECT VELY

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THE PROGRAM ASSOCIATES UNIT NUMBERS AS FOLLOWS (NUMBERS IN PARENTHESES ARE OCTAL)  
UNIT#0 -- BASE ADDRESS STORED AT '\$BASE'  
          ASSOCIATED BASE VECTOR STORED AT '\$VECT1'  
UNIT#1 -- BASE ADDRESS + (10)  
          BASE VECTOR + (10)  
          :  
          : UP TO  
          :  
UNIT#14 -- BASE ADDRESS + (160)  
          BASE VECTOR + (160)

LOCATION '\$DEVN' IS USED AS A BIT MAP TO INDICATE WHICH UNIT NUMBERS ARE PRESENT AND WILL BE TESTED

BIT 15	BIT 14	-	-	-	BIT 1	BIT 0
CON-	UNIT				UNIT	UNIT
SOLE	14				#1	#0

A BIT MAP CAN BE ENTERED AT '\$DEVN' PRIOR TO STARTING THE PROGRAM

EXAMPLE  
\$BASE 175610  
\$VECTOR 300  
\$DEVN 100013

THE PROGRAM WILL TEST-  
UNIT#0 175610 300  
UNIT#1 175620 310  
UNIT#3 175640 330  
CONSOLE 177560 60

OPTIONS  
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LOCAT ON \$USWR CONTAINS ALL THE USER SELECTABLE OPTIONS THE VALUES IN THIS WORD MUST CONFORM TO THE ACTUAL BOARD CONFIGURATION

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THE DEFAULT VALUE OF \$USWR IS AS FOLLOWS

B T POSITION	DEFINITION	DEFAULT VALUE
0-3	# OF DATA BITS	10(8) = 8
4	PARITY ENABLED - (SEE	0 = NO
5	EVEN ODD PARITY-/ NOTE)	0 = ODD
6	COMMON SPEED	1 = YES
7	PROGRAMMABLE BAUD RATE	0 = NO
8-11	BAUD RATE OFFSET (SEE FOLLOWING NOTE)	02(8) = 110 BAUD
12	BREAK GENERATION ENABLED	1 = YES
13	WRAP CONNECTOR INSTALLED	0 = NO
14	MAINT JUMPER (SEE NOTE)	0 = NO
15	ERROR BITS ENABLED	0 = NO

NOTE ON BITS <4 5>  
THIS DIAGNOSTIC DOES NOT TEST THE PARITY LOGIC

NOTE ON BITS <7 11>  
WHEN THE PROGRAMMABLE BAUD RATE OPTION IS  
ENABLED THE PROGRAMMABLE BAUD RATE TEST  
WILL EXIT WITH THE BAUD RATE SET TO THE  
SELECTED VALUE TO CHANGE THE DEFAULT  
VALUE OF 110 BAUD REPLACE BITS <11 8>  
WITH THE OFFSET INDICATED IN THE TABLE  
AT THE END OF THE PBR TEST (TEST #16)

NOTE ON BIT 13  
THIS SWITCH WHEN ON WILL ALLOW THE  
TESTING OF THE EIA DRIVERS AND RECEIVERS  
OR THE 20 MA DRIVERS AND RECEIVERS  
IN ORDER TO TEST THE EIA DRIVERS AND RECEIVERS  
A WRAP CONNECTOR THAT CONNECTS PINS F TO J  
AND PINS M TO E MUST BE INSTALLED IN  
THE 40-PIN HEADER

IN ORDER TO TEST THE 20MA DRIVERS AND  
RECEIVERS A WRAP CONNECTOR THAT CONNECTS  
PINS E TO H AND PINS K TO KK MUST BE  
INSTALLED IN THE 40-PIN HEADER

NOTE ON BIT 14  
THIS SWITCH WHEN ON WILL ALLOW THE DIAGNOSTIC  
TO TEST IN MAINTAINANCE MODE IT IS ASSUMED THAT  
THE MAINTAINANCE JUMPER IS INSTALLED ON ALL OF  
THE DLV11-F MODULES WHEN THIS BIT IS SET

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DLV11-F INDIVIDUAL TEST REQUIREMENTS TABLE



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1 1

TEST #	1	2	3	4	5	6	7	10	11	12	13	14	15	16	17	20	21	22	23	SEQ	0008
CONSOLE DEVICE		++				**			++	++	++	++	++			++	++		++	++	
APT ENVIRONMENT		++							++			++		++	++						
(MAINT) BIT SET			--			**					--	--	--	--					--	--	
(WRAP CON) BIT SET						**													--		
(ERROR BITS) BIT SET													--								
(COM SPD) BIT SET																			--		
(BREAK) BIT SET		--																			--
(PROG BAUD RATE) BIT SET														--							

++ TEST WILL NOT RUN IF THIS CONDITION IS TRUE  
-- TEST WILL NOT RUN IF THIS CONDITION IS FALSE  
\*\* TEST WILL NOT RUN IF ALL OF THE CONDITIONS IN THIS COLUMN ARE FALSE

MF  
CU

331 2 5 EXECUTION TIMES  
332  
333 EXECUTION TIMES ARE FOR AN LSI-11 PROCESSOR WITH ALL OPTIONS  
334 ENABLED ON THE DLV11-F (EXCEPT FOR PROGRAMMABLE BAUD RATE), AT  
335 110 BAUD, AND NOT AT THE CONSOLE ADDRESS  
336  
337 FIRST PASS- 90 SECONDS  
338 ADDITIONAL PASSES 95 SECONDS  
339 ADDITIONAL DEVICES 95 SECONDS  
340  
341 THE TEST TIME IS BAUD RATE DEPENDANT, HIGHER BAUD GIVFS  
342 SHORTER PASS TIMES  
343  
344 IF THE DIAGNOSTIC IS RUN AT THE CONSOLE  
345 ADDRESS THE RUNNING TIME IS 5 SECONDS PER PASS  
346  
347 3 0 ERROR INFORMATION  
348 -----  
349  
350 3 1 ERROR REPORTING PROCEDURE  
351 -----  
352  
353 SINCE THIS DIAGNOSTIC WAS DESIGNED TO FIT IN 4-K OF MEMORY THE  
354 ERROR TYPEOUT IS VERY BRIEF THE FORMAT OF THE ERROR TYPEOUT  
355 IS AS FOLLOWS  
356  
357 TEST#-----, ERROR#-----, PC=-----, ADDRESS=-----, VECTOR=-----  
358  
359 WHERE ALL VALUES TYPED ARE OCTAL  
360 THE ADDRESS AND VECTOR REFER TO THE FAILING SLU'S  
361 FOR FURTHER INFORMATION THE LISTING MUST BE CONSULTED  
362 BITS 15, 13, 10 AND 9 OF THE SWITCH REGISTER CONTROL THE  
363 SEQUENCE OF EVENTS AFTER AN ERROR IS CAUGHT  
364  
365  
366 BIT 15 - CAUSES THE PROGRAM TO HALT IN THE ERROR  
367 ROUTINE CONTINUEING THE PROGRAM CAUSES IT TO  
368 PROCEED  
369  
370 BIT 13 - DISABLES THE PRINTING OF THE ERROR MESSAGE  
371  
372 BIT 10 - CAUSES THE BELL TO RING ON ERROR  
373  
374 BIT 9 - CAUSES THE DIAGNOSTIC TO LOOP FROM BEGINNING  
375 OF TEST TO ERROR  
376  
377 THE ERROR ROUTINE SUPPORTS THE CONTROL G FUNCTION  
378  
379 3 2 ERROR HALTS  
380 -----  
381  
382 THE ONLY HALT IN THIS DIAGNOSTIC IS IN THE ERROR ROUTINE, AND  
383 IS EXECUTED ONLY IF BIT 15 OF THE SWITCH REGISTER IS A ONE  
384 WHEN AN ERROR OCCURS  
385

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4 0 PERFORMANCE AND PROGRESS REPORTS  
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 4 1 PERFORMANCE REPORTS  
 AS EACH DEVICE COMPLETES ONE PASS OF THE DIAGNOSTIC THE  
 FOLLOWING WILL BE TYPED  
 CSR ----- VECTOR ----- ERRORS -----  
 WHERE 'CSR -----' IS THE DEVICE CSR UNDER TEST  
 'VECTOR --' IS THE ASSOCIATED VECTOR  
 AND 'ERRORS --' IS THE TOTAL NUMBER OF ERRORS ON THIS DEVICE  
 ON THIS PASS

NOTE

THIS IS TYPED AFTER THE DEVICE HAS COMPLETED ITS PASS

AFTER ALL DEVICES HAVE BEEN EXERCISED AN END PASS STATEMENT IS  
 TYPED  
 "ENDPASS#-----"

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5 0 DEVICE INFORMATION TABLES  
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	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
RCSR																
RBUF																
TCSR																
TBUF																

NOTE

BLANK BOXES INDICATE UNUSED AND RESERVED  
 BIT POSITIONS SEE THE LISTING FOR AN  
 EXPLANATION OF THE BITS



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6 0 SUMMARY OF TESTS AND SPECIAL SUBROUTINES  
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TEST 1 ADDRESSABILITY  
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THIS TEST VERIFIES THAT THE ADDRESS AS PLACED IN THE  
HARDWARE P-TABLE TO BE CORRECT AND THE DLV11-F  
RESPONDS TO THAT ADDRESS SPACE

TEST 2 BREAK - TCSR0 SET, CLEAR, RESET  
-----

TEST 3 MAINT - TCSR2 SET, CLEAR, RESET  
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TEST 4 XMITIE - TCSR6 SET, CLEAR, RESET  
-----

TEST 5 RCVRIE - RCSR6 SET, CLEAR, RESET  
-----

THE FOLLOWING 4 TESTS VERIFY THAT RESET (INIT) INITIALIZES  
READ ONLY BITS

TEST 6 RCVRDONE - RCSR 7 - IS CLEARED BY INIT  
-----

TEST 7 RCVRACT - RCSR 11 - 15 CLEARED BY INIT  
-----

TEST 10 XMITRDY - TCSR 7 - IS SET BY INIT  
-----

TEST 11 XMIT RDY - TCSR 7 - CLEARS WHEN TBUF IS LOADED  
-----

WITH A CHARACTER AND THAT IT SETS WITHIN A  
REASONABLE AMOUNT OF TIME

TEST 12 OUTPUTTING A CHAR FROM TBUF (WITH MAINT SET)  
-----

RESULTS IN RCVRDONE SETTING WITHIN A  
REASONABLE AMOUNT OF TIME AND THAT RESET  
CLEARS THE BIT

492 TEST 13 RCVRDONE IS CLEARED BY READING RBUF  
493 ---- --  
494  
495 TEST 14 RCVRACT - RCSR 11 - SETS WHEN A START BIT IS  
496 ---- --  
497 RECEIVED AND CLEARS WHEN RCVRDONE - RCSR 7 -  
498 SETS  
499  
500  
501 TEST 15 OVERRUN BIT - RBUF 14  
502 ---- --  
503  
504  
505 TEST 16 PROGRAMMABLE BAUD RATE TEST TEST AT ALL SPEEDS  
506 ---- --  
507 AVAILABLE A COMPARISON WILL BE MADE TO SEE IF  
508 NEW TIME IS LESS THAN PREVIOUS  
509  
510  
511 TEST 17 TRANSMITTER INTERRUPT LOGIC TEST  
512 ---- --  
513 LOGICALLY THIS IS 4 SEPARATE TESTS  
514 A) DOES TRANSMITTER INTERRUPT LOGIC WORK  
515 B) AT PRIORITY OF 0  
516 C) AND ONLY ONCE  
517 D) BUT NOT WITH INTERRUPT ENABLE CLEAR  
518  
519  
520 TEST 20 RECEIVER INTERRUPT LOGIC TEST THIS TEST COVERS ALL  
521 ---- --  
522 OF THE RECEIVER S DE OF THE INTERRUPT LOGIC IN  
523 CHARACTER MODE  
524  
525  
526 TEST 21 TEST ACTUAL DATA TRANSFERED NON-INTERRUPT  
527 ---- --  
528 MAINTENANCE BIT SET  
529  
530  
531 TEST 22 TEST DATA THROUGH WRAP  
532 ---- --  
533  
534  
535 TEST 23 FULL DATA TRANSFER WITH INTERRUPTS AND MAINTFNANCE  
536 ---- --  
537 MODE  
538  
539  
540 TEST 24 TEST BREAK GENERATION LOGIC TRANSMIT KNOWN CHAR  
541 ---- --  
542 WITH BREAK SET AND COMPARE RECEIVED WITH 0  
543  
544  
545 TEST 25 NOT A TEST - SEND BACK TO LOOP  
546 ---- --  
547

NOTE

FOR ALL OF THE FOLLOWING ROUTINES THE USE OF (R5) IS PART OF THE LINKAGE MECHANISM BETWEEN THE CALLER AND THE CALLED

ROUTINE TIMER  
-----

THIS ROUTINE IS USED TO TEST THE STATUS OF ANY BIT IN ANY REGISTER

INPUTS

HOWLONG THE MAXIMUM AMOUNT OF TIME TO SPEND IN THIS ROUTINE  
WHICHBIT A MASK WITH THE BIT(S) SET THAT ARE TO BE CHECKED  
REG A POINTER TO THE REGISTER TO BE CHECKED  
SETCLR THE DESIRED RESULTS -- EITHER SET OR CLEAR

OUTPUT.

THE 'C' BIT IS SET TO INDICATE AN ERROR BUT IT IS TESTED BY THE IF ERROR STATEMENT

ROUTINE DATLNG  
-----

THIS ROUTINE SETS UP A MASK FOR DATA, WITH -

INPUT

NOTHING IS PASSED TO THIS ROUTINE BUT GLOBAL INFORMATION IS ASSUMED TO EXIST  
\$USWR-- THE WORD FOR SOFTWARE PARAMETERS  
DATA-- A MASK FOR THE LOCATION OF THE OCTAL NUMBER OF DATA BITS

OUTPUT----

MASK-- A MASK OF BINARY ZEROS RIGHT-JUST FIED THE NUMBER OF WHICH IS DEFINED IN \$USWR WORD

ROUTINE WAIT  
-----

THIS ROUTINE IS USED TO DELAY EXECUTION OF THE MAIN PROGRAM FOR A SPECIFIED AMOUNT OF TIME THIS IS ACCOMPLISHED BY INCREMENTING A REGISTER UP TO A LIMIT THE INNER LOOP IS SET TO APPROXIMATE 1 MICRO SEC

SERVICE ROUTINE INTSRV  
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THIS GLOBAL ROUTINE DOES NOTHING BUT INCREMENT

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'INTFLAG' EACH TIME IT IS CALLED IT ASSUMES  
THAT THE MAIN CALLING ROUTINE WILL KNOW WHAT  
TO LOOK FOR

ROUTINE CYCLE  
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THIS ROUTINE CAUSES ADRS TO POINT TO THE  
ADDRESS OF DLV11-F UNDER TEST, ADRS +2 TO  
POINT TO THE VECTOR OF THE DLV11-F UNDER TEST  
IT KEEPS TRACK OF THE CURRENT DEVICE AND BIT  
MASKS

```

616          @
617          TITLE MAINDEC-22-CVDVCB-
618          ,*COPYRIGHT (C) 1977
619          ,*DIGITAL EQUIPMENT CORP
520          ,*MAYNARD, MASS 01754
621          ,*
622          ,*PROGRAM BY ODES CHOATE
623          ,*
624          ,*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
625          ,*PACKAGE (MAINDEC-11-DZQAC-C3), JAN 19, 1977
626          ,*
627          SBTTL OPERATIONAL SWITCH SETTINGS
628          ,*
629          ,*          SWITCH          USE
630          ,*          -----
631          ,*          15          HALT ON ERROR
632          ,*          14          LOOP ON TEST
633          ,*          13          INHIBIT ERROR TYPEOUTS
634          ,*          11          INHIBIT ITERATIONS
635          ,*          10          BELL ON ERROR
636          ,*          9          LOOP ON ERROR
637          ,*          8          LOOP ON TEST IN SWR<? 0>
638
639          SBTTL BASIC DEFINITIONS
640
641          ,*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
642          001100 STACK= 1100
643          EQUIV EMT,ERROR          ,,BASIC DEFINITION OF ERROR CALL
644          EQUIV IOT,SCOPE          ,,BASIC DEFINITION OF SCOPE CALL
645
646          ,*MISCELLANEOUS DEFINITIONS
647          000011 HT= 11          ,,CODE FOR HORIZONTAL TPB
648          000012 LF= 12          ,,CODE FOR LINE FEED
649          000015 CR= 15          ,,CODE FOR CARRIAGE RETURN
650          000200 CRLF= 200          ,,CODE FOR CARRIAGE RETURN-LINE FEED
651          177776 PS= 177776          ,,PROCESSOR STATUS WORD
652          EQUIV PS,PSW
653          177774 STK.LMT= 177774          ,,STACK LIMIT REGISTER
654          177772 PIRQ= 177772          ,,PROGRAM INTERRUPT REQUEST REGISTER
655          177570 DSWR= 177570          ,,HARDWARE SWITCH REGISTER
656          177570 DDISP= 177570          ,,HARDWARE DISPLAY REGISTER
657
658          ,*GENERAL PURPOSE REGISTER DEFINITIONS
659          000000 R0= %0          ,,GENERAL REGISTER
660          000001 R1= %1          ,,GENERAL REGISTER
661          000002 R2= %2          ,,GENERAL REGISTER
662          000003 R3= %3          ,,GENERAL REGISTER
663          000004 R4= %4          ,,GENERAL REGISTER
664          000005 R5= %5          ,,GENERAL REGISTER
665          000006 R6= %6          ,,GENERAL REGISTER
666          000007 R7= %7          ,,GENERAL REGISTER
667          000006 SP= %6          ,,STACK POINTER
668          000007 PC= %7          ,,PROGRAM COUNTER
669
670          ,*PRIORITY LEVEL DEFINITIONS
671          000000 PRO= 0          ,,PRIORITY LEVEL 0
  
```

672	000040	PR1=	40	.. PRIORITY LEVEL 1
673	000100	PR2=	100	.. PRIORITY LEVEL 2
674	000140	PR3=	140	.. PRIORITY LEVEL 3
675	000200	PR4=	200	.. PRIORITY LEVEL 4
676	000240	PR5=	240	.. PRIORITY LEVEL 5
677	000300	PR6=	300	.. PRIORITY LEVEL 6
678	000340	PR7=	340	.. PRIORITY LEVEL 7

\*"SWITCH REGISTER" SWITCH DEFINITIONS

680		SW15=	100000
681	100000	SW14=	40000
682	040000	SW13=	20000
683	020000	SW12=	10000
684	010000	SW11=	4000
685	004000	SW10=	2000
686	002000	SW09=	1000
687	001000	SW08=	400
688	000400	SW07=	200
689	000200	SW06=	100
690	000100	SW05=	40
691	000040	SW04=	20
692	000020	SW03=	10
693	000010	SW02=	4
694	000004	SW01=	2
695	000002	SW00=	1
696	000001	EQUIV	SW09, SW9
697		EQUIV	SW08, SW8
698		EQUIV	SW07, SW7
699		EQUIV	SW06, SW6
700		EQUIV	SW05, SW5
701		EQUIV	SW04, SW4
702		EQUIV	SW03, SW3
703		EQUIV	SW02, SW2
704		EQUIV	SW01, SW1
705		EQUIV	SW00, SW0

\*DATA BIT DEFINITIONS (BIT00 TO BIT15)

708		BIT15=	100000
709	100000	BIT14=	40000
710	040000	BIT13=	20000
711	020000	BIT12=	10000
712	010000	BIT11=	4000
713	004000	BIT10=	2000
714	002000	BIT09=	1000
715	001000	BIT08=	400
716	000400	BIT07=	200
717	000200	BIT06=	100
718	000100	BIT05=	40
719	000040	BIT04=	20
720	000020	BIT03=	10
721	000010	BIT02=	4
722	000004	BIT01=	2
723	000002	BIT00=	1
724	000001	EQUIV	BIT09, BIT9
725		EQUIV	BIT08, BIT8
726		EQUIV	BIT07, BIT7
727			



728 EQUIV BIT06,BIT6  
 729 EQUIV BIT05,BIT5  
 730 EQUIV BIT04,BIT4  
 731 EQUIV BIT03,BIT3  
 732 EQUIV BIT02,BIT2  
 733 EQUIV BIT01,BIT1  
 734 EQUIV BIT00,BIT0

736 ,\*BASIC "CPU" TRAP VECTOR ADDRESSES  
 737 ERRVEC= 4 , TIME OUT AND OTHER ERRORS  
 738 RESVEC= 10 , RESERVED AND ILLEGAL INSTRUCTIONS  
 739 TBITVEC=14 , "T" BIT  
 740 TRTVEC= 14 , TRACE TRAP  
 741 BPTVEC= 14 , BREAKPOINT TRAP (BPT)  
 742 IOTVEC= 20 , INPUT/OUTPUT TRAP (IOT) \*\*SCOPE\*\*  
 743 PWRVEC= 24 , POWER FAIL  
 744 EMTVEC= 30 , EMULATOR TRAP (EMT) \*\*ERROR\*\*  
 745 TRAPVEC=34 , "TRAP" TRAP  
 746 TKVEC= 60 , TTY KEYBOARD VECTOR  
 747 TPVEC= 64 , TTY PRINTER VECTOR  
 748 PIRQVEC=240 , PROGRAM INTERRUPT REQUEST VECTOR

749 ILLMEM= 4  
 750 ADRS= R1  
 751 GOOD= R2  
 752 BAD= R3  
 753 REGISTER=R1  
 754 BIT= R2  
 755 FUNCT= R3  
 756 LEAD= R2  
 757 FOLLOW= R4  
 758 DLADDR= 175610

760 , THE FOLLOWING DEFINITIONS APPLY TO THE GLOBAL SUBS  
 761 SET= -1  
 762 CLR= 0

765 ,\*\*\*\*\*  
 766 , RCSR REGISTER BIT NAMES  
 767 ,\*\*\*\*\*  
 768 , UNUSED BIT15  
 769 , UNUSED BIT14  
 770 , UNUSED BIT13  
 771 , UNUSED BIT12  
 772 004000 RCVRACT= BIT11 , RECEIVER ACTIVE INDICATOR  
 773 , UNUSED BIT10  
 774 , UNUSED BIT09  
 775 , UNUSED BIT08  
 776 000200 RCVRDONE= BIT07 , RECEIVER DONE  
 777 000100 RCVRIE= BIT06 , RECEIVER INTERRUPT ENABLE  
 778 , UNUSED BIT05  
 779 , UNUSED BIT04  
 780 , UNUSED BIT03  
 781 , UNUSED BIT02  
 782 , UNUSED BIT01  
 783 000001 RDRRUN= BIT00 , READER RUN

```

784
785      , , *****
786      , RBUF REGISTER BIT NAMES
787      , , *****
788      100000      ERROR=          BIT15      , ERROR INDICATOR
789      040000      ORERR=         BIT14      , OVERRUN ERROR
790      020000      FRERR=         BIT13      , FRAMING ERROR
791      010000      PERR=          BIT12      , PARITY ERROR
792      , UNUSED    BIT11
793      , UNUSED    BIT10
794      , UNUSED    BIT09
795      , UNUSED    BIT08
796      000200      RDATA7=        BIT07      ,
797      000100      RDATA6=        BIT06      ,
798      000040      RDATA5=        BIT05      ,
799      000020      RDATA4=        BIT04      , RECEIVED DATA BITS
800      000010      RDATA3=        BIT03      ,
801      000004      RDATA2=        BIT02      ,
802      000002      RDATA1=        BIT01      ,
803      000001      RDATA0=        BIT00      ,
804
805      , , *****
806      , TCSR REGISTER BIT NAMES
807      , , *****
808      100000      PBAUD3=         BIT15      ,
809      040000      PBAUD2=         BIT14      , PROGRAMMABLE BAUD
810      020000      PBAUD1=         BIT13      , RATE B TS
811      010000      PBAUD0=         BIT12      ,
812      004000      PBAUDSET=       BIT11      , ENABLE SETTING OF
813      , , , , , PROGRAMMABLE BAUDE RATE
814      , UNUSED    BIT10
815      , UNUSED    BIT09
816      , UNUSED    BIT08
817      000200      XMITRDY=        BIT07      , TRANSMITTER READY
818      000100      XMITIE=        BIT06      , TRANSMITTER INTERPUPT ENABLE
819      , UNUSED    BIT05
820      , UNUSED    BIT04
821      , UNUSED    BIT03
822      000004      MAINT=          BIT02      , MAINTENANCE SET BIT
823      , UNUSED    BIT01
824      000001      BREAK=          BIT00      , SEND BREAK (CONTINUOUS SPACE)
825
826
827      , , *****
828      , TBUF REGISTER BIT NAMES
829      , , *****
830      , UNUSED    BIT15
831      , UNUSED    BIT14
832      , UNUSED    BIT13
833      , UNUSED    BIT12
834      , UNUSED    BIT11
835      , UNUSED    BIT10
836      , UNUSED    BIT09
837      , UNUSED    BIT08
838      000200      TDATA7=         BIT07      ,
839      000100      TDATA6=         BIT06      ,
  
```

```

840      000040      TDATA5=      BIT05      /
841      000020      TDATA4=      BIT04      / TRANSMITTER DATA BUFFER
842      000010      TDATA3=      BIT03      /
843      000004      TDATA2=      BIT02      /
844      000002      TDATA1=      BIT01      /
845      000001      TDATA0=      BIT00      /
    
```

```

848      , , *****;*****
849      , FLAG BITS TO BE USE OR CLEARED IN $USWR
    
```

```

851      000017      DATA =      17
852      000020      PARITY =      20
853      000040      EVENODD =      40
854      000100      COMSPD =      100
855      000200      PBR =      200
    
```

```

857      , BAUDE MUST BE ON THE UPPER
858      , BYTE BOUNDRY OF $USWR --4 BITS
    
```

```

859      007400      BAUD =      7400
860      010000      BRK =      10000
861      020000      WRAP =      20000
862      040000      MAINTJUMP =      40000
863      100000      ERBBITS =      100000
    
```

```

864      , , *****;*****
865      SBTTL TRAP CATCHER
    
```

```

866      =0
867      000000
868      , *ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A " +2, HALT"
869      , *SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
870      , *LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS
    
```

```

871      000174      =174
872      000174      000000      DISPREG WORD 0      , , SOFTWARE DISPLAY REGISTER
873      000176      000000      SWREG WORD 0      , , SOFTWARE SWITCH REGISTER
    
```

```

874      SBTTL STARTING ADDRESS(ES)
875      000200      000137      001336      JMP @#START , , JUMP TO STARTING ADDRESS OF PROGRAM
    
```

```

876          SBTTL ACT11 HOOKS
877
878          ;, *****
879          ;HOOKS REQUIRED BY ACT11
880          $SVP=          ;SAVE PC
881          =46
882 000046 011404          ;1)SET LOC 46 TO ADDRESS OF SENDAD IN SEOP
883          =52
884 000052 000000          ;2)SET LOC 52 TO ZERO
885          WORD 0          ; RESTORE PC
886          =1000
887          SBTTL APT PARAMETER BLOCK
888
889          ;, *****
890          ;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
891          ;, *****
892          $X=          ;SAVE CURRENT LOCATION
893          =24          ;SET POWER FAIL TO POINT TO START OF PROGRAM
894 000024 000200          ;FOR APT START UP
895          =44          ;POINT TO APT INDIRECT ADDRESS PNTR
896 000044 001000          $APTHDR ;POINT TO APT HEADER BLOCK
897          = $X          ;RESET LOCATION COUNTER
898          ;, *****
899          ;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
900          ;INTERFACE SPEC
901
902 001000  $APTHD
903 001000 000000  $HIBTS  WORD 0          ;TWO HIGH BITS OF 18 BIT MAILBOX ADDR
904 001002 001174  $MBADR  WORD $MAIL  ;ADDRESS OF APT MAILBOX (BITS 0-15)
905 001004 000005  $STSM   WORD 5          ;RUN TIM OF LONGEST TEST
906 001006 000055  $PASTM  WORD 45         ;RUN TIME IN SECS OF 1ST PASS ON 1 UNIT (QUICK VER FY)
907 001010 000036  $UN TM  WORD 30         ;ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADD TIONAL UNIT
908 001012 000030  WORD  $ETEND-$MAIL/2 ;LENGTH MAILBOX-ETABLE(WORDS)
  
```

```

909          SBTTL  COMMON TAGS
910
911          .. *****
912          . *THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
913          . *USED IN THE PROGRAM
914
915          001100          =1100
916          001100          SCMTAG          .. START OF COMMON TAGS
917          001100          000000          WORD          0
918          001102          000          $TSTNM          BYTE          0          .. CONTAINS THE TEST NUMBER
919          001103          000          $ERFLG          BYTE          0          .. CONTAINS ERROR FLAG
920          001104          000000          $ICNT          WORD          0          .. CONTAINS SUBTEST ITERATION COUNT
921          001106          000000          $LPADR          WORD          0          .. CONTAINS SCOPE LOOP ADDRESS
922          001110          000000          $LPERR          WORD          0          .. CONTAINS SCOPE RETURN FOR ERRORS
923          001112          000000          $ERTTL          WORD          0          .. CONTAINS TOTAL ERRORS DETECTED
924          001114          000          $ITEMB          BYTE          0          .. CONTAINS ITEM CONTROL BYTE
925          001115          001          $ERMAX          BYTE          1          .. CONTAINS MAX ERRORS PER TEST
926          001116          000000          $ERRPC          WORD          0          .. CONTAINS PC OF LAST ERROR INSTRUCTION
927          001120          000000          $GDADR          WORD          0          .. CONTAINS ADDRESS OF 'GOOD' DATA
928          001122          000000          $BDADR          WORD          0          .. CONTAINS ADDRESS OF 'BAD' DATA
929          001124          000000          $GDDAT          WORD          0          .. CONTAINS 'GOOD' DATA
930          001126          000000          $BDDAT          WORD          0          .. CONTAINS 'BAD' DATA
931          001130          000000          WORD          0          .. RESERVED--NOT TO BE USED
932          001132          000000          WORD          0
933          001134          000          $AUTOB          BYTE          0          .. AUTOMATIC MODE INDICATOR
934          001135          000          $INTAG          BYTE          0          .. INTERRUPT MODE INDICATOR
935          001136          000000          WORD          0
936          001140          177570          SWR          WORD          DSWR          .. ADDRESS OF SWITCH REGISTER
937          001142          177570          DISPLAY          WORD          DD SP          .. ADDRESS OF DISPLAY REGISTER
938          001144          177560          $TKS          177560          .. TTY KBD STATUS
939          001146          177562          $TKB          177562          .. TTY KBD BUFFER
940          001150          177564          $TPS          177564          .. TTY PRINTER STATUS REG ADDRESS
941          001152          177566          $TPB          177566          .. TTY PRINTER BUFFER REG ADDRESS
942          001154          000          $NULL          BYTE          0          .. CONTAINS NULL CHARACTER FOR FILLS
943          001155          002          $FILLS          BYTE          2          .. CONTAINS # OF FILLER CHARACTERS REQUIRED
944          001156          012          $FILLC          BYTE          12          .. INSERT FILL CHARS AFTER A "LINE FEED"
945          001157          000          $TPFLG          BYTE          0          .. "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
946          001160          000000          $T MES          0          .. MAX NUMBER OF ITERATIONS
947          001162          000000          $ESCAPE          0          .. ESCAPE ON ERROR ADDRESS
948          001164          177607          000377          $BELL          ASCIZ          <207><377><377>          .. CODE FOR BELL
949          001170          077          $QUES          ASCII          /?/          .. QUESTION MARK
950          001171          015          $CRLF          ASCII          <15>          .. CARRIAGE RETURN
951          001172          000012          $_F          ASCZ          <12>          .. LINE FEED
952          .. *****
953          SBTTL  APT MAILBOX-ETABLE
954
955          .. *****
956          EVEN
957          001174          $MAIL          .. APT MAILBOX
958          001174          000000          $MSGTY          WORD          MSGTY          .. MESSAGE TYPE CODE
959          001176          000000          $FATAL          WORD          AFATAL          .. FATAL ERROR NUMBER
960          001200          000000          $T T N          WORD          ATESTN          .. TEST NUMBER
961          001202          000000          $P S          WORD          APASS          .. PASS COUNT
962          001204          000000          $DEVCT          WORD          ADEVCT          .. DEVICE COUNT
963          001206          000000          $UNIT          WORD          AUN T          .. I/O UNIT NUMBER
964          001210          000000          $MSGAD          WORD          AMSGAD          .. MESSAGE ADDRESS
  
```

965	001212	000000	\$MSGLG	WORD	AMSGLG	.. MESSAGE LENGTH
966	001214		\$ETABLE			.. APT ENVIRONMENT TABLE
967	001214	000	\$ENV	BYTE	AENV	.. ENVIRONMENT BYTE
968	001215	000	\$ENVM	BYTE	AENVM	.. ENVIRONMENT MODE BITS
969	001216	000000	\$SWREG	WORD	ASWREG	.. APT SWITCH REGISTER
970	001220	011110	\$USWR	WORD	AUSWR	.. USER SWITCHES
971	001222	000000	\$CPUOP	WORD	ACPUOP	.. CPU TYPE, OPTIONS
972			,*			BITS 15-11=CPU TYPE
973			,*			11/04=01, 11/05=02, 11/20=03, 11/40=04, 11/45=05
974			,*			11/70=06, PDQ=07, Q=10
975			,*			BIT 10=REAL TIME CLOCK
976			,*			BIT 9=FLOATING POINT PROCESSOR
977			,*			BIT 8=MEMORY MANAGEMENT
978	001224	000	\$MAMS1	BYTE	AMAMS1	.. HIGH ADDRESS, M. S. BYTE
979	001225	000	\$MTYP1	BYTE	AMTYP1	.. MEM TYPE, BLK#1
980			,*			MEM TYPE BYTE -- (HIGH BYTE)
981			,*			900 NSEC CORE=001
982			,*			300 NSEC BIPOLAR=002
983			,*			500 NSEC MOS=003
984	001226	000000	\$MADR1	WORD	AMADR1	.. HIGH ADDRESS, BLK#1
985			,*			MEM LAST ADDR =3 BYTES, THIS WORD AND LOW OF "TYPE" ABOVE
986	001230	000	\$MAMS2	BYTE	AMAMS2	.. HIGH ADDRESS, M. S. BYTE
987	001231	000	\$MTYP2	BYTE	AMTYP2	.. MEM TYPE, BLK#2
988	001232	000000	\$MADR2	WORD	AMADR2	.. MEM LAST ADDRESS, BLK#2
989	001234	000	\$MAMS3	BYTE	AMAMS3	.. HIGH ADDRESS, M. S. BYTE
990	001235	000	\$MTYP3	BYTE	AMTYP3	.. MEM TYPE, BLK#3
991	001236	000000	\$MADR3	WORD	AMADR3	.. MEM LAST ADDRESS, BLK#3
992	001240	000	\$MAMS4	BYTE	AMAMS4	.. HIGH ADDRESS, M. S. BYTE
993	001241	000	\$MTYP4	BYTE	AMTYP4	.. MEM TYPE, BLK#4
994	001242	000000	\$MADR4	WORD	AMADR4	.. MEM LAST ADDRESS, BLK#4
995	001244	000300	\$VECT1	WORD	AVECT1	.. INTERRUPT VECTOR#1, BUS PRIORITY#1
996	001246	000000	\$VECT2	WORD	AVECT2	.. INTERRUPT VECTOR#2, BUS PRIORITY#2
997	001250	175610	\$BASE	WORD	ABASE	.. BASE ADDRESS OF EQUIPMENT UNDER TEST
998	001252	100000	\$DEVN	WORD	ADEVN	.. DEVICE MAP
999	001254		\$ETEND			
1000			MEXIT			



```
1001          SBTTL  ERROR POINTER TABLE
1002
1003          ,*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR
1004          ,*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
1005          ,*LOCATION $ITEMB THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT
1006          ,*NOTE1          IF $ITEMB IS 0 THE ONLY PERTINENT DATA IS ($ERRPC)
1007          ,*NOTE2          EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS
1008
1009          ,*          EM          ,. POINTS TO THE ERROR MESSAGE
1010          ,*          DH          ,. POINTS TO THE DATA HEADER
1011          ,*          DT          ,. POINTS TO THE DATA
1012          ,*          DF          ,. POINTS TO THE DATA FORMAT
1013
1014
1015          001254          $ERRTB
1016          ..          GLOBAL DATA
1017          001254 175610          DLADD  DLADDR
1018          001256 000300          DLVEC  300
1019          001260 175610          RCSR   DLADDR + 0
1020          001262 175612          RBUF   DLADDR + 2
1021          001264 175614          TCSR   DLADDR + 4
1022          001266 175615          TCSRHI DLADDR + 5
1023          001270 175616          TBUF   DLADDR + 6
1024          001272 000000          0
1025          001274 000020          BLKW 20 , FOR R5 STACK
1026          001334 000000          RSSTACK WORD 0
```

```

1027 001336 START
1028 SBTTL INITIALIZE THE COMMON TAGS
1029 . CLEAR THE COMMON TAGS ($CMTAG) AREA
1030 001336 012706 001100 MOV #SCMTAG,R6 // FIRST LOCATION TO BE CLEARED
1031 001342 005026 CLR (R6)+ // CLEAR MEMORY LOCATION
1032 001344 022706 001140 CMP #SWR,R6 // DONE?
1033 001350 001374 BNE -6 // LOOP BACK IF NO
1034 001352 012706 001100 MOV #STACK,SP // SETUP THE STACK POINTER
1035 // INITIALIZE A FEW VECTORS
1036 001356 012737 013326 000020 MOV #SCOPE,@#IOTVEC // IOT VECTOR FOR SCOPE ROUTINE
1037 001364 012737 000340 000022 MOV #340,@#IOTVEC+2 // LEVEL 7
1038 001372 012737 013126 000030 MOV #ERROR,@#EMTVEC // EMT VECTOR FOR ERROR ROUTINE
1039 001400 012737 000340 000032 MOV #340,@#EMTVEC+2 // LEVEL 7
1040 001406 012737 014260 000034 MOV #STRAP,@#TRAPVEC // TRAP VECTOR FOR TRAP CALLS
1041 001414 012737 000340 000036 MOV #340,@#TRAPVEC+2 // LEVEL 7
1042 001422 012737 011440 000024 MOV #SPWRDN,@#PWRVEC // POWER FAILURE VECTOR
1043 001430 012737 000340 000026 MOV #340,@#PWRVEC+2 // LEVEL 7
1044 001436 016767 007710 007700 MOV $ENDCT,$EOPCT // SETUP END-OF-PROGRAM COUNTER
1045 001444 005067 177510 CLR $TIMES // INITIALIZE NUMBER OF ITERATIONS
1046 001450 005067 177506 CLR $ESCAPE // CLEAR THE ESCAPE ON ERROR ADDRESS
1047 001454 112767 000001 177433 MOVB #1,$ERMAX // ALLOW ONE ERROR PER TEST
1048 001462 012767 001462 177416 MOV #,$SLPADR // INITIALIZE THE LOOP ADDRESS FOR SCOPE
1049 001470 012767 001470 177412 MOV #,$SLPERR // SETUP THE ERROR LOOP ADDRESS
1050 // SIZE FOR A HARDWARE SWITCH REGISTER IF NOT FOUND OR IT IS
1051 // EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER
1052 001476 013746 000004 MOV @#ERRVEC,-(SP) // SAVE ERROR VECTOR
1053 001502 012737 001536 000004 MOV #64$,@#ERRVEC // SET UP ERROR VECTOR
1054 001510 012767 177570 177422 MOV #DSWR,$SWR // SETUP FOR A HARDWARE SWICH REGISTER
1055 001516 012767 177570 177416 MOV #DDISP,$DISPLAY // AND A HARDWARE DISPLAY REGISTER
1056 001524 022777 177777 177406 CMP #-1,$SWR // TRY TO REFERENCE HARDWARE SWR
1057 001532 001012 BNE 66$ // BRANCH IF NO TIMEOUT TRAP OCCURRED
1058 // AND THE HARDWARE SWR IS NOT = -1
1059 001534 000403 BR 65$ // BRANCH IF NO TIMEOUT
1060 001536 012716 001544 64$ MOV #65$,(SP) // SET UP FOR TRAP RETURN
1061 001542 000002 RTI
1062 001544 012767 000176 177366 65$ MOV #SWREG,$SWR // POINT TO SOFTWARE SWR
1063 001552 012767 000174 177362 MOV #DISPREG,$DISPLAY
1064 001560 012637 000004 66$ MOV (SP)+,@#ERRVEC // RESTORE ERROR VECTOR
1065
1066 001564 005067 177412 CLR $P // CLEAR PASS COUNT
1067 001570 132767 000200 177417 BITB #AP SIZE,$ENVM // TEST USER SIZE UNDER APT
1068 001576 001403 BEQ 67$ // YES, USE NON-APT SWITCH
1069 001600 012767 001216 177332 MOV #SSWREG,$SWR // NO, USE APT SWITCH REGISTER
1070 001606 67$
1071 SBTTL TYPE PROGRAM NAME
1072 // TYPE THE NAME OF THE PROGRAM IF FIRST PASS
1073 001606 005227 177777 INC #-1 // FIRST TIME?
1074 001612 001037 BNE 68$ // BRANCH IF NO
1075 001614 022737 011404 000042 CMP #SENDAD,@#42 // ACT-11?
1076 001622 001433 BEQ 68$ // BRANCH IF YES
1077 001624 104401 001672 TYPE ,69$ // TYPE ASCIZ STRING
1078 SBTTL GET VALUE FOR SOFTWARE SWITCH REGISTER
1079 001630 005737 000042 TST @#42 // ARE WE RUNNING UNDER XXDP/ACT?
1080 001634 001012 BNE 70$ // BRANCH IF YES
1081 001636 126727 177352 000001 CMPB $ENV,#1 // ARE WE RUNNING UNDER APT?
1082 001644 001406 BEQ 70$ // BRANCH IF YES
  
```

```
1083 001646 026727 177266 00G176      CMP      SWR,#SWREG      ..SOFTWARE SWITCH REG SELECTED?
1084 001654 001005                      BNE      71$            ..BRANCH IF NO
1085 001656 104406                      GTSWR                      ..GET SOFT-SWR SETTINGS
1086 001660 000403                      BR       71$
1087 001662 112767 000001 177244 70$  MOVB     #1,$AUTOB      ..SET AUTO-MODE INDICATOR
1088 001670                      71$
1089 001670 000410                      BR       68$            ..GET OVER THE ASCIZ
1090                      ..69$  ASCIZ <CRLF>*MD-ZZ-CVDVC-B*<CRLF>
1091 001712                      68$
```

```

1092 001712
1093 001712
1094 001712 005767 177334
1095 001716 001101
1096 001720
1097 001762
1098 002050
1099 002116 000000
1100 002120
1101 002120 000674
1102 002122
1103 002122
1104 002122 012767 000001 006754
1105 002130
1106 002130 012767 100000 006744
1107 002136
1108 002136
1109 002136 004767 006520
1110
1111
1112 002142
1113 002142 012167 177106
1114
1115 002146
1116 002146 011167 177104
1117 002152
1118 002152 016701 177076
1119
1120 002156
1121 002156 016767 177072 177074
1122 002164
1123 002164 016767 177064 177070
1124 002172 062767 000002 177062
1125 002200
1126 002200 016767 177050 177056
1127 002206 062767 000004 177050
1128 002214
1129 002214 016767 177034 177044
1130 002222 062767 000005 177036
1131 002230
1132 002230 016767 177020 177032
1133 002236 062767 000006 177024
1134 002244
1135 002244 012705 001334
1136
1137 002250 000005

                                WHILE $DEV# EQ #0 DO
S1      TST      $DEV#
        BNE      $2
        TYPTXT  <<CRLF>>'I HAVE NO DEVICE TO TEST '
        TYPTXT  <<CRLF>>'SET UP $DEV# TO INDICATE ACTUAL CONFIGURATION '
        TYPTXT  <<CRLF>>'TYPE PROCEED (P) TO CONTINUE '
        HALT
                                ENDDO
S2      BR      $1
                                LET INITFLAG = #1
                                LET BITMASK = #BIT15 , START AT CONSOLE
LOOP    MOV      #1, INITFLAG
        MOV      #BIT15, BITMASK
        CALL     CYCLE , NO ARGUMENTS--ADRS -> NEXT ADDRESS
        JSR     PC, CYCLE
                                ,
                                ADRS+2 -> NEXT VECTOR
                                , GET UNIT ADDRESS
        MOV      (ADRS)+, DLADD
                                , GET UNIT VECTOR
        MOV      (ADRS), DLVEC
        LET      DLVEC = (ADRS)
        MOV      DLADD, ADRS
        LET      ADRS = DLADD
                                , RCSR = DLADD + 0
        MOV      DLADD, RCSR
        LET      RCSR = DLADD
        MOV      DLADD, RBUF
        LET      RBUF = DLADD + #2
        ADD     #2, RBUF
        MOV      DLADD, TCSR
        LET      TCSR = DLADD + #4
        ADD     #4, TCSR
        MOV      DLADD, TCSRHI
        LET      TCSRHI = DLADD + #5
        ADD     #5, TCSRHI
        MOV      DLADD, TBUF
        LET      TBUF = DLADD + #6
        ADD     #6, TBUF
        MOV      #R5STACK, R5
        LET      R5 = #R5STACK
        RESET
  
```

```

1138      , , *****
1139      , *TEST 1      ADDRESSABILITY
1140      , *          THIS TEST VERIFIES THAT THE ADDRESS AS PLACED IN
1141      , *          THE HARDWARE P-TABLE TO BE CORRECT AND THE DLV11-F RESPONDS
1142      , *          TO THAT ADDRESS SPACE
1143      , , *****
1144      002252 000004 TST1      SCOPE
1145      002254 012767 000002 176676      MOV      #2, $TIMES      ;, DO 2 ITERATIONS
1146      002262 012767 000001 176710      MOV      #1, $TESTN      ;, SET TEST NUMBER IN APT MAIL BOX
1147      002270      LET      ADRS = DLADD
1148      002270 016701 176760      MOV      DLADD, ADRS
1149      , SET UP INTERRUPT
1150      002274      SETVEC  #ILLMEM, #INTSRV, #PR7
1151      002274 010146      MOV      R1, -(SP)
1152      002276 012701 000004      MOV      #ILLMEM, R1
1153      002302 012721 010652      MOV      #INTSRV, (R1)+
1154      002306 012711 000340      MOV      #PR7, (R1)
1155      002312 012601      MOV      (SP)+, R1
1156      002314      LET      I = #0
1157      002314 005067 176752      CLR      I
1158      002320      REPEAT
1159      002320      $3
1160      002320      BGNSUB
1161      002320 012767 002326 176562      MOV      #64$, $LPERR
1162      , CLEAR FLAG
1163      002326      LET INTFLAG = #0
1164      002326 005067 006326      CLR      INTFLAG
1165      , READ FLAG
1166      002332 005711      TST  @ADRS
1167      002334      IF INTFLAG NE #0 THEN
1168      002334 005767 006320      TST  INTFLAG
1169      002340 001401      BEQ  $4
1170      , FATAL ERROR
1171      002342      ERROF 1, , NOOL
1172      002342 104001      ERROR  1
1173      002344      ENDIF
1174      002344      $4
1175      002344      ENDSUB
1176      002344      LET      I = + #2
1177      002344      LET      ADRS = DLADD +
1178      002344 062767 000002 176720      ADD      #2, I
1179      002352      MOV      DLADD, ADRS
1180      002352 016701 176676      ADD      I, ADRS
1181      002356 066701 176710      UNT L I EQ #8
1182      002362      CMP      I, #8
1183      002362 026727 176704 000010      BNE  $3
1184      002370 001353      CLRVEC ILLMEM
1185      002372      MOV      R1, -(SP)      ;, PUSH R1 ON STACK
1186      002372 010146      MOV      R2, -(SP)      ;, PUSH R2 ON STACK
1187      002374 010246      MOV      #ILLMEM, R1
1188      002376 012701 000004      MOV      R1, R2
1189      002402 010102      ADD      #2, R2
1190      002404 062702 000002      MOV      R2, (R1)+
1191      002410 010221      CLR      (R1)
1192      002412 005011      MOV      (SP)+, R2      ;, POP STACK INTO R2
1193      002414 012602
    
```

```
1194 002416 012601      MOV      (SP)+,R1      ,,POP STACK INTO R1
1195                                     ,END OF TEST
1196 002420                                     ENDTST
```



```
1197      , , *****  
1198      , * THE FOLLOWING 8 TESTS TEST ALL 'READ WRITE' BITS  
1199      , , *****  
1200  
1201  
1202      , , *****  
1203      , *TEST 2          BREAK - TCSR0 SET, CLEAR, RESET  
1204      , *          THE BREAK BIT IS USUALLY USED ON THE CONSOLE  
1205      , *          DEVICE IF ADDITIONAL DLV OPTIONS ARE USED  
1206      , *          IT IS RECOMMENDED TO REMOVE THE 'BG' JUMPER AND  
1207      , *          CLEAR BIT 12 IN $USWR WHICH WILL CAUSE THIS  
1208      , *          TEST TO BE SKIPPED  
1209      , , *****  
1210 002420 000004 TST2 SCOPE  
1211 002422 012767 000010 176530 MOV #10, $TIMES      , , DO 10 ITERATIONS  
1212 002430 012767 000002 176542 MOV #2, $TESTN      , , SET TEST NUMBER IN APT MAIL BOX  
1213  
1214 002436          IF #BRK NOTSETIN $USWR OR #APTENV SETIN $ENVU THE  
1215 002436 032767 010000 176554 BIT #BRK, $USWR  
1216 002444 001404 BEQ $5  
1217 002446 032767 000001 176540 B T #APTENV, $ENVU  
1218 002454 001404 BEQ $6  
1219 002456 $5  
1220 002456          EXIT TEST , BREAK NOT INSTALLED  
1221 002456 012767 000001 176474 MOV #1, $TIMES  
1222 002464 000452 BR TST3      , , ,EXIT THIS TEST  
1223 002466          ENDIF  
1224 002466 $6  
1225  
1226          , SEE IF IT IS CLEAR  
1227 002466          BGNSUB  
1228 002466 012767 002474 176414 MOV #64$, $LPERR  
1229  
1230          IF #BREAK SETIN @TCSR THEN  
1231 002474 032777 000001 176562 B T #BREAK, @TCSR  
1232 002502 001401 BEQ $7  
1233          , BREAK DID NOT RESET IN TCSR  
1234 002504          ERRHRD 2, , DIDNOT  
1235 002504 104002 ERROR 2  
1236 002506          ENDIF  
1237 002506 $7  
1238 002506          ENDSUB  
1239  
1240          , TRY TO SET BREAK BIT  
1241 002506          BGNSUB  
1242 002506 012767 002514 176374 MOV #64$, $LPERR  
1243 002514          LET @TCSR = @TCSR SET BY #BREAK  
1244 002514 052777 000001 176542 BIS #BREAK, @TCSR  
1245          , STUCK TO 0  
1246 002522          IF #BREAK NOTSETIN @TCSR THEN  
1247 002522 032777 000001 176534 BIT #BREAK, @TCSR  
1248 002530 001001 BNE $10  
1249          , BREAK DID NOT SET IN TCSR  
1250 002532          ERRHRD 3, , DIDNOT  
1251 002532 104003 ERROR 3  
1252 002534          ENDIF
```

```
1253 002534          $10
1254 002534
1255
1256
1257 002534          , TRY TO CLEAR A SET BIT
1258 002534 012767 002542 176346      MOV      #645, $LPERR      BGNSUB
1259
1260 002542          LET      @TCSR = @TCSR CLR BY #BREAK
1261 002542 042777 000001 176514      BIC      #BREAK, @TCSR
1262
1263 002550          IF      , SHOULD HAVE CLEARED
1264 002550 032777 000001 176506      B T      #BREAK, @TCSR      #BREAK SET IN @TCSR THEN
1265 002556 001401      BEQ      $11
1266
1267 002560          , BREAK DID NOT CLEAR 'N TCSR
1268 002560 104004      ERROR    4      ERRHRD 4, , D DNOT
1269 002562
1270 002562          $11
1271 002562          ENDSUB
1272
1273          , NOW SEE IF RESET CLEARS IT
1274 002562          BGNSUB
1275 002562 012767 002570 176320      MOV      #645, $LPERR
1276
1277 002570          LET      @TCSR = @TCSR SET BY #BREAK
1278 002570 052777 000001 176466      BIS      #BREAK, @TCSR
1279
1280 002576          , ISSUE BUS RESET
1281 002576 000005      RESET    BRESET
1282 002600          IF      #BREAK SET IN @TCSR THEN
1283 002600 032777 000001 176456      BIT      #BREAK, @TCSR
1284 002606 001401      BEQ      $12
1285
1286 002610          , BREAK DID NOT RESET IN TCSR
1287 002610 104005      ERROR    5      ERRHRD 5, , DIDNOT
1288 002612
1289 002612          $12
1290 002612          ENDSUB
1291 002612          ENDTST
1292
1293
```

```

1294      , , *****
1295      , , *****
1296      , *TEST 3      MAINT - TCSR2 SET, CLEAR, RESET
1297      , , *****
1298 002612 000004      TST3      SCOPE
1299 002614 012767 000010 176336      MOV      #10,$TIMES      ; DO 10 ITERATIONS
1300 002622 012767 000003 176350      MOV      #3,$TESTN      ; SET TEST NUMBER IN APT MAIL BOX
1301
1302      IF #MAINTJUMP NOTSETIN $USWR ORB CONSOLE EQ #TRU
1303 002630 032767 040000 176362      B T      #MAINTJUMP,$USWR
1304 002636 001404      BEQ      $13
1305 002640 126727 006255 000001      CMPB    CONSOLE,#TRUE
1306 002646 001004      BNE      $14
1307 002650      $13
1308 002650      EXIT TEST
1309 002650 012767 000001 176302      MOV      #1,$TIMES
1310 002656 000452      BR      TST4      ; EXIT THIS TEST
1311 002660      ENDIF
1312 002660      $14
1313
1314      , SEE IF IT IS CLEAR
1315 002660      BGNSUB
1316 002660 012767 002666 176222      MOV      #64,$SLPERR
1317
1318 002666      IF      #MAINT SETIN @TCSR THEN
1319 002666 032777 000004 176370      BIT      #MAINT,@TCSR
1320 002674 001401      BEQ      $15
1321
1322      , MAINT DID NOT RESET IN TCSR
1323 002676 104006      ERROR    6      ERRHRD 6,,DIDNOT
1324 002700      ENDIF
1325 002700      $15
1326 002700      ENDSUB
1327
1328      , TRY TO SET MAINT BIT
1329 002700      BGNSUB
1330 002700 012767 002706 176202      MOV      #64,$SLPERR
1331 002706      LET      @TCSR = @TCSR SET BY #MAINT
1332 002706 052777 000004 176350      BIS      #MAINT,@TCSR
1333
1334      , STUCK TO 0
1335 002714      IF      #MAINT NOTSETIN @TCSR THEN
1336 002722 001001      BIT      #MAINT,@TCSR
1337      BNE      $16
1338
1339      , MAINT DID NOT SET IN TCSR
1340 002724 104007      ERROR    7      ERRHRD 7,,DIDNOT
1341 002726      ENDIF
1342 002726      $16
1343
1344      ENDSUB
1345      , TRY TO CLEAR A SET BIT
1346 002726 012767 002734 176154      MOV      #64,$SLPERR
1347
1348 002734      LET      @TCSR = @TCSR CLR BY #MAINT
1349 002734 042777 000004 176322      B C      #MAINT,@TCSR
  
```

```
1350
1351 002742
1352 002742 032777 000004 176314 BIT #MAINT,@TCSR
1353 002750 001401 BEQ $17
1354
1355 002752
1356 002752 104010 ERROR 10
1357 002754
1358 002754 $17
1359 002754
1360
1361
1362 002754
1363 002754 012767 002762 176126 MOV #645,SLPERR
1364
1365 002762
1366 002762 052777 000004 176274 B S #MAINT,@TCSR
1367
1368 002770
1369 002770 000005 RESET
1370 002772
1371 002772 032777 000004 176264 B T #MAINT,@TCSR
1372 003000 001401 BEQ $20
1373
1374 003002
1375 003002 104011 ERROR 11
1376 003004
1377 003004 $20
1378 003004
1379 003004
1380
1381
1382
```

SHOULD HAVE CLEARED  
#MAINT SET IN @TCSR THEN

MAINT DID NOT CLEAR IN TCSR  
ERRHRD 10, DIDNOT

ENDIF

ENDSUB

NOW SEE IF RESET CLEARS IT  
BGNSUB

LET @TCSR = @TCSR SET BY #MAINT

ISSUE BUS RESET  
BRESET

IF #MAINT SET IN @TCSR THEN

MAINT DID NOT RESET IN TCSR  
ERRHRD 11, DIDNOT

ENDIF

ENDSUB

ENDTST

```

1383      , , *****
1384      , , *****
1385      , *TEST 4          XMITIE - TCSR6 SET, CLEAR, RESET
1386      , , *****
1387      003004 000004      TST4      SCOPE
1388      003006 012767 000010 176144      MOV      #10, $TIMES      , , DO 10 ITERATIONS
1389      003014 012767 000004 176156      MOV      #4, $TESTN      , , SET TEST NUMBER IN APT MAIL BOX
1390      , , USE PRIORITY OF 7
1391      003022 012746 000340      MOV      #PR7, -(SP)      , , PUT NEW PS ON STACK
1392      003026 012746 003034      MOV      #64$, -(SP)      , , PUT NEW PC ON STACK
1393      003032 000002      RTI      , , POP NEW PC AND PS
1394      003034      64$
1395
1396      , SEE IF IT IS CLEAR
1397      003034      BGNSUB
1398      003034 012767 003042 176046      MOV      #65$, $LPERR
1399
1400      003042      IF      #XMITIE SET IN @TCSR THEN
1401      003042 032777 000100 176214      BIT      #XMITIE, @TCSR
1402      003050 001401      BEQ      $21
1403      , XMITIE DID NOT RESET IN TCSR
1404      003052      ERRHRD 12, , DIDNOT
1405      003052 104012      ERROR   12
1406      003054      ENDIF
1407      003054      $21
1408      003054      ENDSUB
1409
1410      , TRY TO SET XMITIE BIT
1411      003054      BGNSUB
1412      003054 012767 003062 176026      MOV      #64$, $LPERR
1413      003062      LET      @TCSR = @TCSR SET BY #XMITIE
1414      003062 052777 000100 176174      B S      #XMITIE, @TCSR
1415      , STUCK TO 0
1416      003070      IF      #XMITIE NOT SET IN @TCSR THEN
1417      003070 032777 000100 176166      B T      #XMITIE, @TCSR
1418      003076 001001      BNE     $22
1419      , XMIT DID NOT RESET IN TCSR
1420      003100      ERRHRD 13, , DIDNOT
1421      003100 104013      ERROR   13
1422      003102      ENDIF
1423      003102      $22
1424      003102      ENDSUB
1425
1426      , TRY TO CLEAR A SET BIT
1427      003102      BGNSUB
1428      003102 012767 003110 176000      MOV      #64$, $LPERR
1429
1430      003110      LET      @TCSR = @TCSR CLR BY #XMITIE
1431      003110 042777 000100 176146      BIC      #XMITIE, @TCSR
1432      , SHOULD HAVE CLEARED
1433      003116      IF      #XMITIE SET IN @TCSR THEN
1434      003116 032777 000100 176140      BIT      #XMITIE, @TCSR
1435      003124 001401      BEQ      $23
1436      , XMIT DID NOT CLEAR IN TCSR
1437      003126      ERRHRD 14, , DIDNOT
1438      003126 104014      ERROR   14
    
```

```
1439 003130                                ENDIF
1440 003130                                $23
1441 003130                                ENDSUB
1442
1443                                , NOW SEE IF RESET CLEARS IT
1444 003130                                BGNSUB
1445 003130 012767 003136 175752          MOV    #645,SLPERR
1446
1447 003136                                LET    @TCSR = @TCSR SET BY #XMITIE
1448 003136 052777 000100 176120          BIS    #XMITIE,@TCSR
1449                                , ISSUE BUS RESET
1450 003144                                BRESÉT
1451 003144 000005                                RESET
1452 003146                                IF    #XMITIE SET IN @TCSR THEN
1453 003146 032777 000100 176110          BIT    #XMITIE,@TCSR
1454 003154 001401                                BEQ    $24
1455
1456 003156                                , XMIT DID NOT RESET IN TCSR
1457 003156 104015                                ERRHRD 15, DIDNOT
1458 003160                                ENDF
1459 003160                                $24
1460 003160                                ENDSUB
1461 003160                                ENDTST
1462
1463
1464
```



```

1465      , , *****
1466      , , *****
1467      , *TEST 5          RCVRIE - RCSR6 SET, CLEAR, RESET
1468      , , *****
1469      TST5  SCOPE
1470      003160 000004      MOV #10, $TIMES      , DO 10 ITERATIONS
1471      003170 012767 000010 175770      MOV #5, $TESTN      , SET TEST NUMBER IN APT MAIL BOX
1472      , SEE IF T S CLEAR
1473      003176      BGNSUB
1474      003176 012767 003204 175704      MOV #645, $LPERR
1475
1476      003204      IF #RCVRIE SET IN @RCSR THEN
1477      003204 032777 000100 176046      B T #RCVRIE, @RCSR
1478      003212 001401      BEQ $25
1479
1480      003214      , RCVRIE DID NOT RESET IN RCSR
1481      003214 104035      ERROR 35      ERRHRD 35, , DIDNOT
1482      003216      ENDIF
1483      003216      $25
1484      003216      ENDSUB
1485
1486      , TRY TO SET RCVRIE BIT
1487      003216      BGNSUB
1488      003216 012767 003224 175664      MOV #645, $LPERR
1489      003224      LET @RCSR = @RCSR SET BY #RCVRIE
1490      003224 052777 000100 176026      B S #RCVRIE, @RCSR
1491
1492      003232      IF #RCVRIE NOT SET IN @RCSR THEN
1493      003232 032777 000100 176020      B T #RCVRIE, @RCSR
1494      003240 001001      BNE $26
1495
1496      003242      , RCVRIE DID NOT SET IN RCSR
1497      003242 104036      ERROR 36      ERRHRD 36, , DIDNOT
1498      003244      ENDIF
1499      003244      $26
1500      003244      ENDSUB
1501
1502      , TRY TO CLEAR A SET BIT
1503      003244      BGNSUB
1504      003244 012767 003252 175636      MOV #645 $LPERR
1505
1506      003252      LET @RCSR = @RCSR CLR BY #RCVRIE
1507      003252 042777 000100 176000      BIC #RCVRIE, @RCSR
1508
1509      003260      IF #RCVRIE SET IN @RCSR THEN
1510      003260 032777 000100 175772      BIT #RCVRIE, @RCSR
1511      003266 001401      BEQ $27
1512
1513      003270      , RCVRIE DID NOT CLEAR IN RCSR
1514      003270 104037      ERROR 37      ERRHRD 37, , DIDNOT
1515      003272      ENDIF
1516      003272      $27
1517      003272      ENDSUB
1518
1519      , NOW SEE IF RESET CLEARS IT
1520      003272      BGNSUB
  
```

```

MAINDEC-11-DVDUC-B      MACY11 30A(1052) 02-FEB-78 08 40 PAGE 36      K 3
CVDUCB P11      02-FEB-78 08 39      TS      RCVRIE - RCSR6 SET, CLEAR, RESET      SEQ 0036

1521 003272 012767 003300 175610      MOV      #645,$LPERR
1522
1523 003300      LET      @RCSR = @RCSR SET BY #RCVRIE
1524 003300 052777 000100 175752      B S      #RCVRIE,@RCSR
1525      , ISSUE BUS RESET
1526 003306      BRESET
1527 003306 000005      RESET
1528 003310      IF      #RCVRIE SET IN @RCSR THEN
1529 003310 032777 000100 175742      B T      #RCVRIE,@RCSR
1530 003316 001401      BEQ      $30
1531      , RCVRIE DID NOT RESET IN RCSR
1532 003320      ERRHRD 40,,DIDNOT
1533 003320 104040      ERROR 40
1534 003322
1535 003322      $30
1536 003322
1537 003322      CKLOOP
1538 003322      ENDSUB
1539
1540
1541
1542      ENDTST

```

```
1543 // *****  
1544 ; * THE FOLLOWING 4 TESTS VERIFY  
1545 ; * THAT RESET (INIT) INITIALIZES READ ONLY BITS  
1546 // *****  
1547 // *****  
1548 ; *TEST 6 TEST THAT RCVRDONE - RCSR 7 - IS CLEARED BY INIT  
1549 // *****  
1550 TST6 SCOPE  
1551 003322 000004 MOV #10,$TIMES // DO 10 ITERATIONS  
1552 003324 012767 000010 175626 MOV #6,$TESTN // SET TEST NUMBER IN APT MAIL BOX  
1553 003332 012767 000006 175640  
1554  
1555  
1556  
1557 003340 BGNSUB  
1558 003340 012767 003346 175542 MOV #64,$SLPERR  
1559 003346 IF #RCVRDONE SETIN @RCSR THEN  
1560 003346 032777 000200 175704 B T #RCVRDONE,@RCSR  
1561 003354 001402 BEQ $31  
1562  
1563 ; RCVRDONE SHOULD HAVE CLEARED BY INIT  
1564 ; RCVRDONE DID NOT CLEAR IN RCSR  
1565 003356 ERRHRD 41,HRESET, DIDNOT  
1566 003356 104041 ERROR 41  
1567 ; REISSUE RESET  
1568 003360 BRESET  
1569 003360 000005 RESET  
1570 003362  
1571 003362 $31  
1572  
1573 003362 // ALLOW LOOPING AFTER ERROR  
1574 003362 CKLOOP  
1575 003362 ENDSUB  
1576  
1577  
1578 ENDTST
```

```

1579          , , *****
1580          , , *****
1581          , *TEST 7          TEST THAT RCVRACT - RCSR 11 - IS CLEARED BY INIT
1582          , , *****
1583 003362 000004 TST7 SCOPE
1584 003364 012767 000010 175566 MOV #10, $TIMES          , , DO 10 ITERATIONS
1585 003372 012767 000007 175600 MOV #7, $TESTN          , , SET TEST NUMBER IN APT MAIL BOX
1586
1587
1588
1589
1590 003400          IFB CONSOLE EQ #TRUE THEN
1591 003400 126727 005515 000001 CMPB CONSOLE, #TRUE
1592 003406 001001          BNE $32          , , EXECUTE TEST
1593          ELSE
1594 003410          BR $33
1595 003410 000416          BR $33
1596 003412          $32
1597 003412          IF #WRAP SETIN $USWR THEN
1598 003412 032767 020000 175600 BIT #WRAP, $USWR
1599 003420 001401          BEQ $34          , , EXECUTE TEST
1600          ELSE
1601 003422          BR $35
1602 003422 000411          BR $35
1603 003424          $34
1604 003424          IF #MAINT SETIN $USWR THEN
1605 003424 032767 000004 175566 BIT #MAINT, $USWR
1606 003432 001401          BEQ $36          , , EXECUTE TEST
1607          ELSE
1608 003434          BR $37
1609 003434 000404          BR $37
1610 003436          $36
1611 003436          EXIT TEST          , LINE MUST BE TERMINATED
1612 003436 012767 000001 175514 MOV #1, $TIMES
1613 003444 000414          BR TST10          , , , EXIT THIS TEST
1614 003446          ENDF
1615 003446          $37
1616 003446          ENDF
1617 003446          $35
1618 003446          ENDF
1619 003446          $33
1620
1621          BGNSUB
1622 003446 012767 003454 175434 MOV #64$, $LPERR
1623
1624          IF #RCVRACT SETIN @RCSR THEN
1625 003454 032777 004000 175576 BIT #RCVRACT, @RCSR
1626 003462 001405          BEQ $40
1627
1628          , RESET SHOULD HAVE CLEARED RCVRACT
1629 003464          LET @TCSR = @TCSR CLR BY #MAINT
1630 003464 042777 000004 175572 BIC #MAINT, @TCSR
1631 003472          ERRHRD 44, HRESET, DIDNOT
1632 003472 104044          ERROR 44
1633
1634          , TESTING EFFECT OF RESET ON BIT
  
```

```
1635  
1636  
1637  
1638  
1639 003474  
1640 003474 000005 RESET  
1641 003476  
1642 003476 S40  
1643  
1644 003476  
1645 003476  
1646 003476  
1647  
  
RCVRACT DID NOT CLEAR IN RCSR  
ALLOW ANOTHER TRY  
BRESET  
ENDIF  
ALLOW LOOPING ON ERROR  
CKLOOP  
ENDSUB  
ENDTST
```

```

1648
1649
1650
1651
1652 003476 000004
1653 003500 012767 000010 175452
1654 003506 012767 000010 175464
1655
1656
1657
1658
1659 003514
1660 003514 012767 003522 175366
1661
1662 003522
1663 003522 032777 000200 175534
1664 003530 001002
1665
1666
1667
1668 003532
1669 003532 104042
1670
1671 003534
1672 003534 000005
1673 003536
1674 003536
1675
1676 003536
1677 003536
1678 003536
1679
1680
1681

```

```

, , *****
, , *****
,*TEST 10 TEST THAT XMITRDY - TCSR 7 - IS SET BY INIT
, , *****
TST10 SCOPE
MOV #10, $TIMES , , DO 10 ITERATIONS
MOV #10, $TESTN , , SET TEST NUMBER IN APT MAIL BOX

BGNSUB
MOV #645, $LPERR
IF #XMITRDY NOTSET IN @TCSR THEN
BIT #XMITRDY, @TCSR
BNE $41
, RESET SHOULD HAVE SET BIT
, XMITRDY DID NOT SET IN TCSR (AFTER RESE
ERRHRD 42, HRESET, DIDNOT
, ISSUE ANOTHER RESET
BRESET
ENDIF
, ALLOW LOOPING ON ERROR
CKLOOP
ENDSUB
ENDTST

```

M  
C



```

1682      ,*****
1683      ,*****
1684      ,*TEST 11      TEST THAT XMIT RDY - TCSR 7 - CLEARS
1685      ,*          WHEN TBUF IS LOADED WITH A CHARACTER
1686      ,*          AND THAT IT SETS WITHIN A REASONABLE AMOUNT OF TIME
1687      ,*****
1688      003536 000004      TST11  SCOPE
1689      003540 012767 000001 175412      MOV      #1, $TIMES      , DO 1 ITERATION
1690      003546 012767 000011 175424      MOV      #1, $TESTN      , SET TEST NUMBER IN APT MAIL BOX
1691
1692      003554          IFB CONSOLE EQ #TRUE OR #APTENV SET IN $ENV THEN
1693      003554 126727 005341 000001      CMPB     CONSOLE, #TRUE
1694      003562 001404          BEQ      $42
1695      003564 032767 000001 175422      BIT      #APTENV, $ENV
1696      003572 001404          BEQ      $43
1697      003574          $42
1698      003574          EXIT TEST
1699      003574 012767 000001 175356      MOV      #1, $TIMES
1700      003602 000513      BR       TST12      , , EXIT THIS TEST
1701      003604          ENDF
1702      003604          $43
1703
1704      003604          LET PASS = #1 , INIT COUNT OF TIMES THRU
1705      003604 012767 000001 000212      MOV      #1, PASS
1706      003612          LOOP          , START OF LOOP
1707      003612          $44
1708
1709      003612          LET ERRORFLAG = #CLR , MAX OF 2 TIMES THRU
1710      003612 012767 000000 000206      MOV      #CLR, ERRORFLAG
1711      003620          LET EXITFLAG = #CLR
1712      003620 012767 000000 000202      MOV      #CLR, EXITFLAG
1713      1714          , LOAD TBUF WITH ONE CHARACTER
1715      1715          , WAIT FOR READY TO SET
1716      1716          , (SHOULD BE VERY SHORT WAIT
1717      1717          , SINCE UART DOUBLE BUFFERS ITS INPUT)
1718
1719      003626          , SEND A CHARACTER
1720      003626 105077 175436      CLRB     @TBUF      LET @TBUF B= #0
1721
1722          , WAIT A MAXIMUM
1723          , OF 500 MSEC FOR
1724          , XMIT RDY TO SET IN TCSR
1725      003632          CALL TIMER IN <#500, #XMITRDY, TCSR, #SET>
1726      003634 010546      MOV      R5, -(SP)
1727      003634 012745 177777      MOV      #SET, -(R5)
1728      003640 016745 175420      MOV      TCSR, -(R5)
1729      003644 012745 000200      MOV      #XMITRDY, -(R5)
1730      003650 012745 000500      MOV      #500, -(R5)
1731      003654 004767 004434      JSR      PC, TIMER
1732      003660 012605      MOV      (SP)+, R5
1733
1734          , TIMER RETURNS AN ERROR IF BIT DID
1735          , NOT MEET CONDITION WITHIN TIME LIMIT
1736          , IF ERROR THEN
1737      003664          BCC     $46
1738          , XMIT RDY D D NOT SET IN TCSR
1739          ERRHRD 66, , DIDNOT
  
```

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CVDVCB P11		02-FEB-78 08 39		T11		TEST THAT XM T RDY - TCSR 7 - CLEARS		SEQ 0042	

  

1738	003664	104066			ERROR	66			
1739	003666							ENDIF	
1740	003666			\$46					
1741									
1742									
1743									, LOAD TBUF WITH A SECOND CHARACTER
1744									, CHECK IMMEDIATELY THAT XMITRDY IS CLEAR
1745									, AND THEN WAIT FOR IT TO SET
1746									
1747	003666								, SEND SECOND CHARACTER
1748	003666	105077	175376		CLRB	@TBUF			LET @TBUF B= #0
1749	003672	000240							
1750									, GIVE IT TIME TO CLEAR
1751									, XMITRDY SHOULD HAVE CLEARED UPON
1752	003674								, RECEIPT OF A CHARACTER
1753	003674	032777	000200	175362	B T	#XMITRDY, @TCSR			IF #XMITRDY SET IN @TCSR THEN
1754	003702	001404			BEQ	\$47			
1755									, XMITRDY DID NOT CLEAR IN TCSR
1756	003704								LET ERRORFLAG = #SET
1757	003704	012767	177777	000114	MOV	#SET, ERRORFLAG			
1758									, DEFER ERROR TYPEOUT
1759									
1760	003712							ELSE	
1761	003712	000416			BR	\$50			
1762	003714			\$47					
1763									, WAIT A MAXIMUM
1764									, OF 500 MSEC FOR
1765									, XMIT RDY TO SET IN TCSR
1766	003714								CALL TIMER IN (<#500, #XMITRDY, TCSR, #SET>)
1767	003714	010546			MOV	R5, -(SP)			
1768	003716	012745	177777		MOV	#SET, -(R5)			
1769	003722	016745	175336		MOV	TCSR, -(R5)			
1770	003726	012745	000200		MOV	#XMITRDY, -(R5)			
1771	003732	012745	000500		MOV	#500, -(R5)			
1772	003736	004767	004352		JSR	PC, TIMER			
1773	003742	012605			MOV	(SP)+, R5			
1774	003744								IF EPROR THEN
1775	003744	103001			BCC	\$51			
1776									, XMIT RDY D D NOT SET IN TCSR
1777	003746								ERRHRD 70, DIDNOT
1778	003746	104070			ERROR	70			
1779	003750								ENDIF
1780	003750			\$51					
1781	003750								ENDIF . OF DEFERED ERROR CALL
1782	003750			\$50					
1783	003750								IF ERRORFLAG EQ #SET THEN
1784	003750	026727	000052	177777	CMP	ERRORFLAG, #SET			
1785	003756	001011			BNE	\$52			
1786	003760								IF PASS GT #1 THEN
1787	003760	026727	000040	000001	CMP	PASS, #1			
1788	003766	003404			BLE	\$53			
1789									, CALL ERROR IF 2ND TRY
1790	003770								ERRHRD 67, DIDNOT
1791	003770	104067			ERROR	67			
1792	003772								LET EXITFLAG = #SET
1793	003772	012767	177777	000030	MOV	#SET, EXITFLAG			



```

1815 ,*****
1816 ,*****
1817 ,*TEST 12 TEST THAT OUTPUTTING A CHAR FROM TBUF (WITH MAINT SET)
1818 ,* RESULTS IN RCVRDONE SETTING WITHIN A REASONABLE AMOUNT OF TIME
1819 ,* AND THAT RESET CLEARS THE BIT
1820 ,*****
1821 004032 000004 TST12 SCOPE
1822 004034 012767 000001 175116 MOV #1,$TIMES ;DO 1 ITERATION
1823 004042 012767 000012 175130 MOV #12,$TESTN ;SET TEST NUMBER IN APT MAIL BOX
1824
1825 004050 ; IF #MAINTJUMP NOTSET IN $USWR ORB CONSOLE EQ #TRU
1826 004050 032767 040000 175142 BIT #MAINTJUMP,$USWR
1827 004056 001404 BEQ $55
1828 004060 126727 005035 000001 CMPB CONSOLE,#TRUE
1829 004066 001004 BNE $56
1830 004070 $55
1831 004070 ; EXIT TEST
1832 004070 012767 000001 175062 MOV #1,$TIMES
1833 004076 000442 BR TST13 ;EXIT THIS TEST
1834 004100 ENDF
1835 004100 $56
1836
1837 ; SET THE MAINTENANCE BIT
1838 004100 LET @TCSR = @TCSR SET BY #MAINT
1839 004100 052777 000004 175156 BIS #MAINT,@TCSR
1840
1841 004106 ; BGNSUB
1842 004106 012767 004114 174774 MOV #64,$LPERR
1843 ; SEND A CHARACTER AND LET IT WRAP AROUND
1844
1845 004114 LET @TBUF B= #0
1846 004114 105077 175150 CLRB @TBUF
1847
1848 ; WAIT A MAXIMUM OF 50 MSEC
1849 ; FOR RCVR DONE TO SET IN
1850 ; RCSR
1851 004120 CALL TIMER IN <#500,#RCVRDONE,RCSR,#SET>
1852 004120 010546 MOV R5,-(SP)
1853 004122 012745 177777 MOV #SET,-(R5)
1854 004126 016745 175126 MOV RCSR,-(R5)
1855 004132 012745 000200 MOV #RCVRDONE,-(R5)
1856 004136 012745 000500 MOV #500,-(R5)
1857 004142 004767 004146 JSR PC,TIMER
1858 004146 012605 MOV (SP)+,R5
1859
1860 ; DIDN'T SET N TIME
1861 004150 IF ERROR THEN
1862 004150 103004 BCC $57
1863
1864 ; RCVRDONE DID NOT SET IN RCSR
1865 ; CAN NOT LEAVE WITH MAINT SET
1866 004152 LET @TCSR = @TCSR CLR BY #MAINT
1867 004152 042777 000004 175104 BIC #MAINT,@TCSR
1868 004160 ERRHRD 71 ; DIDNOT
1869 004162 104071 ERROR 71
1870 004162 $57 ENDF
  
```

```
1871
1872 004162                                ENDSUB
1873
1874 004162                                BGNSUB
1875 004162 012767 004170 174720          MOV    #64$,SLPERR
1876                                     , NOW THAT IT IS SET SEE IF IT CAN BE RESET
1877                                     , THIS ALSO WILL CLEAR THE MAINT BIT
1878                                     BRESET
1878 004170
1879 004170 000005                          RESET
1880
1881 004172                                IF #RCVRDONE SET IN @RCSR THEN
1882 004172 032777 000200 175060          BIT    #RCVRDONE,@RCSR
1883 004200 001401                          BEQ    $60
1884                                     , RCVRDONE DID NOT RESET IN RCSR
1885                                     ERRHRD 72, DIDNOT
1886 004202 104072                          ERROR  2
1887 004204                                ENDIF
1888 004204                                $60
1889 004204                                ENDSUB
1890 004204                                ENDTST
```

```

1891      , , *****
1892      , , *****
1893      , *TEST 13      TEST THAT RCVRDONE IS CLEARED BY READING RBUF
1894      , , *****
1895      TST13      SCOPE
1896      004204 000004      MOV      #10, $TIMES      , DO 10 ITERATIONS
1897      004206 012767 000010 174744      MOV      #13, $TESTN      , SET TEST NUMBER IN APT MAIL BOX
1898      004214 012767 000013 174756
1899      004222      IF #MAINTJUMP NOTSET IN $USWR ORB CONSOLE EQ #TRU
1900      004222 032767 040000 174770      B T      #MAINTJUMP, $USWR
1901      004230 001404      BEQ      $61
1902      004232 126727 004663 000001      CMPB    CONSOLE, #TRUE
1903      004240 001004      BNE      $62
1904      004242      $61
1905      004242      EXIT TEST
1906      004242 012767 000001 174710      MOV      #1, $TIMES
1907      004250 000440      BR      TST14      , , , EXIT THIS TEST
1908      004252      ENDIF
1909      004252      $62
1910
1911      , SET MAINT BIT
1912      004252      LET @TCSR = @TCSR SET BY #MAINT
1913      004252 052777 000004 175004      BIS      #MAINT, @TCSR
1914      004260      BGNS'JB
1915      004260 012767 004266 174622      MOV      #64$, $LPERR
1916      , OUTPUT A CHARACTER WITH MAINTENANCE
1917      , SET, AND WAIT FOR XMITRDY TO SET
1918
1919      , OUTPUT A CHARACTER
1920      004266      LET @TBUF B= #0
1921      004266 105077 174776      CLRB    @TBUF
1922      , WAIT MAXIMUM OF 500 MSEC
1923      , FOR RCVRDONE TO SET IN
1924      , RCSR
1925      004272      CALL TIMER IN <#500, #RCVRDONE, RCSR, #SET>
1926      004272 010546      MOV      R5, -(SP)
1927      004274 012745 177777      MOV      #SET, -(R5)
1928      004300 016745 174754      MOV      RCSR, -(R5)
1929      004304 012745 000200      MOV      #RCVRDONE, -(R5)
1930      004310 012745 000500      MOV      #500, -(R5)
1931      004314 004767 003774      JSR      PC, TIMER
1932      004320 012605      MOV      (SP)+, R5
1933      004322      LET @TCSR = @TCSR CLR BY #MAINT
1934      004322 042777 000004 174734      BIC      #MAINT, @TCSR
1935      , DID IT BECAME READY?
1936      004330      IF ERROR THEN
1937      004330 103001      BCC      $63
1938      , RCVRDONE DID NOT SET IN RCSR
1939      004332      ERRHRD 73, , DIDNOT
1940      004332 104073      ERROR    73
1941      004334      ENDIF
1942      004334      $63
1943      004334      ENDSUB
1944
1945      , NOW THAT IT 'S SET LETS SEE F READING THE
1946      , BUFFER CLEARS RCVRDONE
  
```

```
1947  
1948  
1949 004334  
1950 004334 117700 174722      MOVB    @RBUF,R0  
1951  
1952 004340  
1953 004340 032777 000200 174712  BIT    #RCVRDONE,@RCSR  
1954 004346 001401              BEQ    $64  
1955  
1956 004350  
1957 004350 104074              ERROR  74  
1958 004352  
1959 004352              $64  
1960 004352  
                                ENDTST
```

, READ BUFFER  
LET R0 B= @RBUF  
IF #RCVRDONE SET IN @RCSR THEN  
, RCVRDONE DID NOT CLEAR N RCSP  
ERRHRD 74,, DIDNOT  
ENDIF

```

1961
1962
1963
1964
1965
1966
1967 004352 000004
1968 004354 012767 000010 174576
1969 004362 012767 000014 174610
1970 004370
1971 004370 126727 004525 000001
1972 004376 001404
1973 004400 032767 040000 174612
1974 004406 001004
1975 004410 $65
1976 004410
1977 004410 012767 000001 174542
1978 004416 000526
1979 004420
1980 004420 $66
1981 004420
1982 004420 032767 000001 174566
1983 004426 001404
1984 004430
1985 004430 012767 000001 174522
1986 004436 000516
1987 004440
1988 004440 $67
1989
1990
1991
1992 004440
1993 004440 052777 000004 174616
1994 004446
1995 004446 012700 000000
1996 004452
1997 004452 005001
1998
1999
2000
2001
2002 004454
2003 004454 105077 174610
2004 004460
2005 004460 $70
2006 004460
2007 004460 032777 004000 174572
2008 004466 001403
2009 004470
2010 004470 012700 177777
2011 004474
2012 004474 000401
2013 004476 $71
2014 004476
2015 004476 005201
2016 004500

```

```

*****
*****
*TEST 14 TEST THAT RCVRACT - RCSR 11 - SETS
* WHEN A START BIT IS RECEIVED AND
* CLEARS WHEN RCVRDONE - RCSR 7 - SETS
*****
TST14 SCOPE
MOV #10,$TIMES ;DO 10 ITERATIONS
MOV #14,$TESTN ;SET TEST NUMBER IN APT MAIL BOX
;IFB CONSOLE EQ #TRUE OR #MAINTJUMP NOTSET N $USW
CMPB CONSOLE,#TRUE
BEQ $65
BIT #MAINTJUMP,$USWR
BNE $66
EXIT TEST
MOV #1,$TIMES
BR TST15 ;EXIT THIS TEST
ENDIF
IF #APTENV SETIN $ENV THEN
EXIT TEST
MOV #1,$TIMES
BR TST15 ;EXIT THIS TEST
ENDIF
LET @TCSR = @TCSR SET BY #MAINT
LET R0 = #CLR
LET R1 = #0
;LOAD A CHARACTER INTO TBUF
;WAIT FOR RCVRACT TO SET
;SEND A CHARACTER
LET @TBUF B= #0
REPEAT
IF #RCVRACT SETIN @RCSR THEN
LET R0 = #SET
ELSE
LET R1 = R1 + #1
END F

```



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CVDVCB P11 02-FEB-78 08 39		T14		TEST THAT RCVRACT - RCSR 11 - SETS				SEQ 0049	
2017	004500					\$72			
2018	004500								UNTIL RO EQ #SET OR R1 H MAX
2019	004500	020027	177777		CMP		RO, #SET		
2020	004504	001403			BEQ		\$73		
2021	004506	020167	000160		CMP		R1, MAX		
2022	004512	101762			BLOS		\$70		
2023	004514					\$73			
2024	004514								IF R1 HI MAX THEN
2025	004514	020167	000152		CMP		R1, MAX		
2026	004520	101410			BLOS		\$74		
2027									, IT NEVER SET
2028									, RCVRACT DID NOT SET IN RCSR
2029									, CAN NOT LEAVE WITH MAINT SET
2030	004522								LET @TCSR = @TCSR CLR BY #MAINT
2031	004522	042777	000004	174534	B C		#MAINT, @TCSR		
2032	004530								ERRHRD 75,, DIDNOT
2033	004530	104075			ERROR		75		
2034	004532								EXIT TEST
2035	004532	012767	000001	174420	MOV		#1, \$TIMES		
2036	004540	000455			BR		TST15		...EXIT THIS TEST
2037	004542								ENDIF
2038	004542					\$74			
2039									
2040									
2041									, CHECK FOR TIMING OF RCVRACT CLEARING
2042									, VS RCVRDONE SETTING
2043									
2044									
2045	004542								WHILE #RCVRACT SETIN @RCSR DO
2046	004542					\$75			
2047	004542	032777	004000	174510	BIT		#RCVRACT, @RCSR		
2048	004550	001421			BEQ		\$76		
2049									
2050	004552								IF #RCVRDONE SETIN @RCSR THEN
2051	004552	032777	000200	174500	B T		#RCVRDONE, @RCSR		
2052	004560	001414			BEQ		\$77		
2053	004562								IF #RCVRACT SETIN @RCSR THEN
2054	004562	032777	004000	174470	BIT		#RCVRACT, @RCSR		
2055	004570	001410			BEQ		\$100		
2056									, RCVRDONE AND RCVRACT
2057									, BOTH SET
2058									, CAN NOT LEAVE WITH MAINT SET
2059	004572								LET @TCSR = @TCSR CLR BY #MAINT
2060	004572	042777	000004	174464	BIC		#MAINT, @TCSR		
2061	004600								ERRHRD 76, DONEACT
2062	004600	104076			ERROR		76		
2063									, NO USE CONTINUING
2064	004602								EXIT TST
2065	004602	012767	000001	174350	MOV		#1, \$TIMES		
2066	004610	000431			BR		TST15		...EXIT THIS TEST
2067	004612								ENDIF
2068	004612					\$100			
2069	004612								ENDIF
2070	004612					\$77			
2071	004612								ENDDO
2072	004612	000753			BR		\$75		

```
2073 004614          $76
2074
2075
2076 004614          ,RCVRACT = 0 NOW
2077 004614 032777 000200 174436      BIT    #RCVRDONE,@RCSR
2078 004622 001010          BNE    $101
2079
2080
2081 004624          ,RCVRDONE DID NOT SET IN RCSR
2082 004624 042777 000004 174432      BIC    #MAINT,@TCSR
2083 004632          , CAN NOT LEAVE WITH MAINT SET
2084 004632 104077          LET    @TCSR = @TCSR CLR BY #MAINT
2085 004634          ERRHRD 77,,DIDNOT
2086 004634 012767 000001 174316      MOV    #1,$TIMES
2087 004642 000414          BR     TST15
2088 004644          ,,,EXIT THIS TEST
2089 004644          $101
2090
2091          ,TEST THAT READING THE RECEIVER
2092          ,BUFFER CLEARS RCVRDONE
2093
2094
2095 004644          ,READ CHAR
2096 004644 017700 174412          MOV    @RBUF,R0
2097
2098 004650          IF #RCVRDONE SET IN @RCSR THEN
2099 004650 032777 000200 174402      BIT    #RCVRDONE,@RCSR
2100 004656 001404          BEQ    $102
2101
2102          ,RCVRDONE DID NOT CLEAR IN RCSR
2103 004660          , CAN NOT LEAVE WITH MAINT SET
2104 004660 042777 000004 174376      BIC    #MAINT,@TCSR
2105 004666          LET    @TCSR = @TCSR CLR BY #MAINT
2106 004666 104100          ERRHRD 100,,DIDNOT
2107 004670          ENDIF
2108 004670          $102
2109
2110 004670          EXIT
2111 004670 000401          BR     TST15
2112 004672 070000          ,,,EXIT THIS TEST
2113
2114 004674          MAX 70000
2115          ENDTST
```

```

2116
2117
2118
2119
2120
2121 004674 000004
2122 004676 012767 000010 174254
2123 004704 012767 000015 174266
2124
2125 004712
2126 004712 032767 100000 174300
2127 004720 001404
2128 004722 126727 004173 000001
2129 004730 001004
2130 004732 $103
2131 004732
2132 004732 012767 000001 174220
2133 004740 000547
2134 004742
2135 004742 $104
2136 004742
2137 004742 032767 040000 174250
2138 004750 001004
2139 004752
2140 004752 012767 000001 174200
2141 004760 000537
2142 004762
2143 004762 $105
2144
2145 004762
2146 004762 052777 000004 174274
2147
2148
2149
2150 004770
2151 004770 012767 004776 174112
2152
2153
2154
2155
2156
2157 004776
2158 004776 105077 174266
2159
2160 005002
2161 005002 010546
2162 005004 012745 000310
2163 005010 004767 003556
2164 005014 012605
2165
2166
2167 005016
2168 005016 105077 174246
2169
2170 005022
2171 005022 010546
  
```

\*\*\*\*\*  
 \*\*\*\*\*  
 \*TEST 15 TEST THE OVERRUN BIT - RBUF 14  
 \*\*\*\*\*  
 TST15 SCOPE

..DO 10 ITERATIONS  
 ..SET TEST NUMBER IN APT MAIL BOX  
 IF #ERRBITS NOTSET IN \$USWR ORB CONSOLE EQ #TRUE

BIT #ERRBITS, \$USWR  
 BEQ \$103  
 CMPB CONSOLE, #TRUE  
 BNE \$104

EXIT TEST

MOV #1, \$TIMES  
 BR TST16

...EXIT THIS TEST  
 ENDIF

IF #MAINTJUMP NOTSET IN \$USWR THEN

BIT #MAINTJUMP, \$USWR  
 BNE \$105

EXIT TEST

MOV #1, \$TIMES  
 BR TST16

...EXIT THIS TEST  
 ENDIF

LET @TCSR := @TCSR SET BY #MAINT

BIS #MAINT, @TCSR

BGNSUB

MOV #64\$, \$LPERR  
 , OUTPUT 2 CHARACTERS WITH  
 , AMPL DELAYS BETWEEN FOR RECEPTION  
 , THIS SHOULD AN CAUSE OVERRUN ERROR

, OUTPUT 1 CHARACTER  
 LET @TBUF B= #0

CLRB @TBUF

, GO AWAY FOR 200 M SEC  
 WAITMS 200

MOV R5, -(SP)  
 MOV #200, -(R5)  
 JSR PC, WAIT  
 MOV (SP)+, R5

, OUTPUT 2ND CHARACTER  
 LET @TBUF B= #0

CLRB @TBUF

, LET OVERRUN HAPPEN  
 WAITMS 200

MOV R5, -(SP)

```

2172 005024 012745 000310      MOV    #200 ,-(R5)
2173 005030 004767 003536      JSR    PC, WAIT
2174 005034 012605              MOV    (SP)+, R5
2175
2176                                , READ BUFFER AND ERROR BITS
2177 005036                                LET R4 = @RBUF
2178 005036 017704 174220      MOV    @RBUF, R4
2179
2180                                , IT DIDN'T SET
2181 005042                                IF #ORERR NOTSET IN R4 THEN
2182 005042 032704 040000      B T    #ORERR, R4
2183 005046 001010              BNE    $106
2184
2185                                , ORERR DID NOT SET IN RBUF
2186 005050                                , CAN NOT LEAVE WITH MAINT SET
2187 005050 042777 000004 174206  BIC    #MAINT, @TCSR
2188 005056                                LET    @TCSR = @TCSR CLR BY #MAINT
2189 005056 104101              ERROR  101
2190                                ERRHRD 101,, DIDNOT
2191
2192                                , NO USE COMPOUNDING ERRORS
2193 005060                                EXIT TST
2194 005060 012767 000001 174072  MOV    #1, $TIMES
2195 005066 000474              BR     TST16
2196 005070                                ,,, EXIT THIS TEST
2197 005070                                ENDIF
2198                                $106
2199                                ENDSUB
2200                                , NOW SEE IF ERROR BIT SET WITH OVERRUN ERROR
2201 005070                                BGNSUB
2202 005070 012767 005076 174012  MOV    #64$, $LPERR
2203 005076 032704 100000      BIT    #ERROR, R4
2204 005102 001010              BNE    $107
2205
2206                                , ERROR DID NOT SET IN RBUF
2207                                , CAN NOT LEAVE WITH MAINT SET
2208 005104                                LET    @TCSR = @TCSR CLR BY #MAINT
2209 005104 042777 000004 174152  BIC    #MAINT, @TCSR
2210 005112                                ERRHRD 102,, DIDNOT
2211 005112 104102              ERROR  102
2212
2213                                , -WHEN ORERR SET
2214                                , GET OUT NOW
2215 005114                                EXIT TST
2216 005114 012767 000001 174036  MOV    #1, $TIMES
2217 005122 000456              BR     TST16
2218 005124                                ,,, EXIT THIS TEST
2219 005124                                ENDIF
2220 005124                                $107
2221                                ENDSUB
2222                                BGNSUB
2223 005124 012767 005132 173756  MOV    #64$, $LPERR
2224                                , CHECK REAL RBUF TO SEE IF ORERR IS STILL SET
2225
2226                                IF #ORERR NOTSET IN @RBUF THEN
2227 005132 032777 040000 174122  B T    #ORERR, @RBUF
  
```

```

2228 005140 001010      BNE      $110
2229
2230
2231
2232 005142
2233 005142 042777 000004 174114      BIC      #MAINT, @TCSR
2234 005150
2235 005150 104103      ERROR    103
2236
2237 005152
2238 005152 012767 000001 174000      MOV      #1, $TIMES
2239 005160 000437      BR       TST16
2240 005162
2241 005162
2242 005162
2243
2244 005162
2245 005162 012767 005170 173720      MOV      #64$, $LPERR
2246
2247
2248
2249 005170
2250 005170 105077 174074      CLRB    @TBUF
2251
2252 005174
2253 005174 010546      MOV      R5, -(SP)
2254 005176 012745 000310      MOV      #200, -(R5)
2255 005202 004767 003364      JSR     PC, WAIT
2256 005206 012605      MOV      (SP)+, R5
2257
2258 005210
2259 005210 032777 040000 174044      BIT      #ORERR, @RBUF
2260 005216 001410      BEQ     $111
2261
2262
2263 005220
2264 005220 042777 000004 174036      BIC      #MAINT, @TCSR
2265 005226
2266 005226 104104      ERROR    104
2267
2268
2269
2270 005230
2271 005230 012767 000001 173722      MOV      #1, $TIMES
2272 005236 000410      BR       TST16
2273 005240
2274 005240
2275
2276 005240
2277 005240 032777 100000 174014      BIT      #ERROR, @RBUF
2278 005246 001404      BEQ     $112
2279
2280
2281 005250
2282 005250 042777 000004 174006      BIC      #MAINT, @TCSR
2283 005256
  
```

, READING RBUF CLEARED ORERR  
 , CAN NOT LEAVE WITH MAINT SET  
 LET @TCSR = @TCSR CLR BY #MAINT  
 ERRHRD 103, ITCLRED  
 , SKIP REST OF TEST  
 EXIT TEST

...EXIT THIS TEST  
 ENDIF

ENDSUB

BGNSUB

, NOW SEE IF THEY CLEAR WHEN ANOTHER CHAR IS RECEIVED

, SEND A CHARACTER AROUND  
 LET @TBUF = #0

, LET IT CIRCULATE  
 WAITMS 200

IF #ORERR SET IN @RBUF THEN

, ORERR DID NOT CLEAR IN RBUF  
 , CAN NOT LEAVE WITH MAINT SET  
 LET @TCSR = @TCSR CLR BY #MAINT

ERRHRD 104, DIDNOT

, -AFTER RECEIVING ANOTHER CHAR  
 , SKIP AROUND REST  
 EXIT TST

...EXIT THIS TEST  
 ENDIF

IF #ERROR SET IN @RBUF THEN

, ERROR DID NOT CLEAR IN RBUF  
 , CAN NOT LEAVE WITH MAINT SET  
 LET @TCSR = @TCSR CLR BY #MAINT

ERRHRD 105, DIDNOT

2284 005256 104105

ERROR 105

2285

2286 005260

ENDIF

2287 005260

\$112

2288 005260

ENDSUB

2289 005260

ENDTST

2290

```

2291
2292
2293
2294
2295
2296
2297
2298
2299 005260 000004
2300 005262 012767 000010 173670
2301 005270 012767 000016 173702
2302
2303
2304
2305 005276
2306 005276 032767 000200 173714
2307 005304 001404
2308 005306 032767 040000 173704
2309 005314 001004
2310 005316 $113
2311 005316
2312 005316 012767 000001 173634
2313 005324 000553
2314 005326
2315 005326 $114
2316
2317 005326
2318 005326 132767 000001 173660
2319 005334 001404
2320 005336
2321 005336 012767 000001 173614
2322 005344 000543
2323 005346
2324 005346 $115
2325
2326 005346
2327 005346 005067 002614
2328 005352
2329 005352 012767 177777 000270
2330 005360
2331 005360 012767 177777 000264
2332 005366
2333 005366 052777 000004 173670
2334
2335 005374
2336 005374 005003
2337 005376 000401
2338 005400 $117
2339 005400 005203
2340 005402 $116
2341 005402 020327 000017
2342 005406 003062
2343 005410
2344 005410 017700 173646
2345
2346 005414

```

```

*****
*****
*TEST 16 PROGRAMMABLE BAUD RATE TEST
* TEST AT ALL SPEEDS AVAILABLE
* A COMPARISON WILL BE MADE TO SEE
* IF NEW TIME IS LESS THAN PREVIOUS
*****
TST16 SCOPE
MOV #10,$TIMES ;DO 10 ITERATIONS
MOV #16,$TESTN ;SET TEST NUMBER IN APT MAIL BOX

IF #PBR NOTSETIN $USWR OR #MAINTJUMP NOTSETIN $U
BT #PBR,$USWR
BEQ $113
BIT #MAINTJUMP,$USWR
BNE $114

EXIT TEST
MOV #1,$TIMES
BR TST17 ;EXIT THIS TEST
ENDIF

IFB #APTENV SETIN $ENV THEN
EXIT TST
MOV #1,$TIMES
BR TST17 ;EXIT THIS TEST
ENDIF

LET ERRCHK = #0 ; CLEAR ERROR WORD
LET OLD = #-1
LET OLD+2 = #-1
LET @TCSR = @TCSR SET BY #MA NT
; EACH BAUD RATE
INCR R3 FROM #0 TO #15 BY #1

CLR R3
BR $116

INC R3

CMP R3,#15
BGT $120

LET RO = @RBUF

; CHANGE BAUDE RATE
LET @TCSRHI B= RATES(R3)

```





```

2403                                     , NEW >= OLD)
2404                                     , BAUD RATE DIDN'T CHANGE
2405 005530 012767 000004 002430          MOV  #BIT2,ERRCHK          LET ERRCHK = #BIT2 , SET ERROR INDICATOR
2406 005530                                     ENDIF
2407 005536                                     ENDIF
2408 005536          $130
2409 005536                                     ENDIF
2410 005536          $126
2411                                     , UPDATE OLD T ME
2412 005536          LET OLD = NEW
2413 005536 016767 000102 000104          MOV  NEW, OLD
2414 005544          LET OLD+2 = NEW+2
2415 005544 016767 000076 000100          MOV  NEW+2, OLD+2
2416
2417 005552          ENDINC , BAUD RATE
2418 005552 000712          BR    $117
2419 005554          $120
2420 005554          LET R3 B= $USWR+1 AND #17          , PUT BAUD BACK
2421 005554 116703 173441          MOVB  $USWR+1,R3
2422 005560 110346          MOVB  R3,-(SP)
2423 005562 142716 000017          BICB  #17,(SP)
2424 005566 142603          BICB  (SP)+,R3
2425 005570          LET R3 = R3 CLR BY #177400
2426 005570 042703 177400          BIC   #177400,R3
2427 005574          LET @TCSRHI B= RATES(R3)          , LIKE HE WANTED IT
2428 005574 116377 005624 173464          MOVB  RATES(R3),@TCSRHI
2429
2430                                     , CAN NOT LEAVE WITH MAINT SET
2431 005602          LET @TCSR = @TCSR CLR BY #MA NT
2432 005602 042777 000004 173454          BIC   #MAINT,@TCSR
2433 005610          IF #BIT2 SET IN ERRCHK THEN
2434 005610 032767 000004 002350          B T   #BIT2,ERRCHK
2435 005616 001401          BEQ   $131
2436                                     , REPORT DEFERED ERROR
2437 005620          ERRHRD 126
2438 005620 104126          ERROR 126
2439 005622          ENDIF
2440 005622          $131
2441 005622          EXIT , SKIP TABLE
2442 005622 000414          BR    TST17          , , , EXIT THIS TEST
2443
2444 005624          RATES , A TABLE OF THE ACTUAL BYTES TO MOVE INTO THE
2445          , UPPER BYTE OF XCSR FOR EACH BAUD RATE
2446          , ** NOTE THE VALUE INDICATED IN THE COLUMN 'OFFSET
2447          , ** INTO TABLE' CAN BE PLACED INTO BITS<11 8>
2448          , ** OF LOCATION '$USWR' TO CAUSE THE CORROSPONDING
2449          , ** BAUD TO BE SELECTED IN THE DLV11-F UPON
2450          , ** COMPLETION OF THIS TEST
2451
2452          BAUD  OFFSET INTO TABLE
2453 005624 010  R0050  BYTE 010  , 50  0
2454 005625 030  R0070  BYTE 030  , 70  1
2455 005626 050  R0110  BYTE 050  , 110 2
2456 005627 070  R0135  BYTE 070  , 135 3
2457 005630 110  R0150  BYTE 110  , 150 4
2458 005631 130  R0300  BYTE 130  , 300 5

```

2459	005632	150		R0600	BYTE	150	.	600	6
2460	005633	170		R0200	BYTE	170	.	1200	7
2461	005634	210		R1800	BYTE	210	.	1800	10
2462	005635	230		R2000	BYTE	230	.	2000	11
2463	005636	250		R2400	BYTE	250	.	2400	12
2464	005637	270		R3600	BYTE	270	.	3600	13
2465	005640	310		R4800	BYTE	310	.	4800	14
2466	005641	330		R7200	BYTE	330	.	7200	15
2467	005642	350		R9600	BYTE	350	.	9600	16
2468	005643	370		R10000	BYTE	370	.	19200	17

2469  
2470 005644 000000 000000 NEW 0.0  
2471 005650 000000 000000 OLD 0.0

ENDTST

2472 005654  
2473  
2474  
2475

```

2476 // *****
2477 // *****
2478 // *TEST 17 TRANSMITTER INTERRUPT LOGIC TEST
2479 // * LOGICALLY THIS IS 4 SEPARATE TESTS
2480 // * A) DOES TRANSMITTER INTERRUPT LOGIC WORK
2481 // * B) AT PRIORITY OF 0
2482 // * C) AND ONLY ONCE
2483 // * D) BUT NOT WITH INTERRUPT ENABLE CLEAR
2484 // *****
2485 005654 000004 TST17 SCOPE
2486 005656 012767 000010 173274 MOV #10, $TIMES // DO 10 ITERATIONS
2487 005664 012767 000017 173306 MOV #17, $TESTN // SET TEST NUMBER IN APT MAIL BOX
2488
2489
2490
2491 005672 IF #APTENV SET IN $ENV THEN
2492 005672 032767 000001 173314 BIT #APTENV, $ENV
2493 005700 001404 BEQ $132
2494 005702 EXIT TEST
2495 005702 012767 000001 173250 MOV #1, $TIMES
2496 005710 000532 BR TST20 //, EXIT THIS TEST
2497 005712 ENDIF
2498 005712 $132
2499
2500
2501 // CLEAR 'INTERRUPT OCCURED' FLAG
2502 005712 LET INTFLAG = #0
2503 005712 005067 002742 CLR INTFLAG
2504
2505 // GET VECTOR ADDRESS
2506 005716 LET R3 = DLVEC
2507 005716 016703 173334 MOV DLVEC, R3
2508
2509 // FOR THE TRANSMITTER
2510 005722 LET R3 = R3 + #4
2511 005722 062703 000004 ADD #4, R3
2512 // SET VECTOR TO POINT TO TRANS SRV AT PRI
2513 005726 SETVEC R3, #INTSRV, #PR7
2514 005730 MOV R1, -(SP)
2515 005732 012721 010652 MOV #INTSRV, (R1)+
2516 005736 012711 000340 MOV #PR7, (R1)
2517 005742 012601 MOV (SP)+, R1
2518 005744 BGNSUB
2519 005744 012767 005752 173136 MOV #64$, $LPERR
2520 // MAKE SURE THAT TRANSMITTER READY IS SET
2521 005752 CALL TIMER IN (<#500, #XMITRDY, TCSR, #SET>)
2522 005752 MOV R5, -(SP)
2523 005754 012745 177777 MOV #SET, -(R5)
2524 005760 016745 173300 MOV TCSR, -(R5)
2525 005764 012745 000200 MOV #XMITRDY, -(R5)
2526 005770 012745 000500 MOV #500, -(R5)
2527 005774 004767 002314 JSR PC, TIMER
2528 006000 012605 MOV (SP)+, R5
2529
2530 // CLEAR INTERRUPT ENABLE
2531 006002 LET @TCSR = @TCSR CLR BY #XMITIE
  
```

```

1 5
MAINDEC-11-DVDVC-B MACY11 30A(1052) 02-FEB-78 08 40 PAGE 60
CVDVCB P11 02-FEB-78 08 39 T17 TRANSMITTER INTERRUPT LOGIC TEST SEQ 0060

2532 006002 042777 000100 173254 BIC #XMITIE,@TCSR
2533
2534
2535 006010 012746 000000 MOV #PRO,-(SP) ,,PUT NEW PS ON STACK ,SET IT TO 0
2536 006014 012746 006022 MOV #655,-(SP) ,,PUT NEW PC ON STACK
2537 006020 00C002 RTI ,,POP NEW PC AND PS
2538 006022 655
2539
2540
2541 006022 ,NOW SET I E BIT
2542 006022 052777 000100 173234 BIS #XMITIE,@TCSR LET @TCSR = @TCSR SET BY #XMITIE
2543
2544 ,LET INTERRUPT HAVE TIME TO OCCUR
2545 006030 WAITMS 20C
2546 006030 010546 MOV R5,-(SP)
2547 006032 012745 000310 MOV #200,-(R5)
2548 006036 004767 002530 JSR PC,WAIT
2549 006042 012605 MOV (SP)+,R5
2550
2551 ,DID EXACTLY 1 INTERRUPT OCCUR
2552 006044 IF INTFLAG NE #1 THEN
2553 006044 026727 002610 000001 CMP INTFLAG,#1
2554 006052 001406 BEQ $133
2555
2556 006054 ,NO - WAS IT 0 OR MORE THAN ONCE
2557 006054 005767 002600 TST NTFLAG IF INTFLAG EQ #0 THEN
2558 006060 001002 BNE $134
2559
2560 006062 ,TRANSMITTER DID NOT INTERRUPT IN TIME
2561 006062 104106 ERROR 106 ERRHRD 106,,DIDNOT
2562 006064 ELSE
2563 006064 000401 BR $135
2564 006066 $134
2565
2566 ,TWICE
2567 006066 ,TRANSMITTER INTERRUPTED TWICE
2568 006066 104107 ERROR 107 ERRHRD 107,,TWICE
2569 006070
2570 006070 $135
2571 006070
2572 006070 $133
2573 006070
2574
2575 006070 ENDSUB
2576 006070 012767 006076 173012 MOV #645,$LPERR , INTERRUPT WITHOUT INTERRUPT ENABLE SET
2577
2578 006076 ,CLEAR 'INTERRUPT OCCURED' FLAG
2579 006076 005067 002556 CLR NTFLAG LET INTFLAG = #0
2580
2581 006102 ,CLEAR INTERRUPT ENABLE
2582 006102 042777 000100 173154 BIC #XMITIE,@TCSR LET @TCSR = @TCSR CLR BY #XMITIE
2583
2584 006110 ,NO NTERRUPTS SHOULD OCCUR
2585 006114 012746 000000 MOV #PRO,-(SP) ,,PUT NEW PS ON STACK
2586 006120 000002 MOV #655,-(SP) ,,PUT NEW PC ON STACK
2587 006122 655 RT ,,POP NEW PC AND PS

```

```

2588                                     , DARE IT TO HAPPEN
2589 006122                               WAITMS 2
2590 006122 010546                       MOV    R5, -(SP)
2591 006124 012745 000002                MOV    #2, -(R5)
2592 006130 004767 002436                JSR    PC, WAIT
2593 006134 012605                       MOV    (SP)+, R5
2594 006136                                     IF INTFLAG NE #0 THEN
2595 006136 005767 002516                TST    INTFLAG
2596 006142 001401                       BEQ    $136
2597                                     , INTERRUPT OCCURED WITH I E CLEARED
2598 006144                                     ERRHRD 110, NOTENAB
2599 006144 104110                       ERROR  110
2600 006146                                     ENDIF
2601 006146                               $136
2602 006146                                     BRESET
2603 006146 000005                       RESET
2604 006:50                                     ENDSUB
2605                                     , RESTORE VECTOR AREA
2606 006150                               CLRVEC R3
2607 006150 010146                       MOV    R1, -(SP)    ;, PUSH R1 ON STACK
2608 006152 010246                       MOV    R2, -(SP)    ;, PUSH R2 ON STACK
2609 006154 012701 000003                MOV    #R3, R1
2610 006160 010102                       MOV    R1, R2
2611 006162 062702 000002                ADD    #2, R2
2612 006166 010221                       MOV    R2, (R1)+
2613 006170 005011                       CLR    (R1)
2614 006172 012602                       MOV    (SP)+, R2    ;, POP STACK INTO R2
2615 006174 012601                       MOV    (SP)+, R1    ;, POP STACK INTO R1
2616
2617 006176                               ENDTST
2618
2619
2620
2621
2622
2623
  
```

```

2624
2625
2626
2627
2628
2629
2630
2631 006176 000004
2632 006200 012767 000010 172752
2633 006206 012767 000020 172764
2634 006214
2635 006214 032767 040000 172776
2636 006222 001404
2637 006224 126727 002671 000001
2638 006232 001002
2639 006234 $137
2640 006234 000167 000242
2641 006240
2642 006240 $140
2643
2644
2645
2646 006240
2647 006240 010146
2648 006242 016701 173010
2649 006246 012721 010652
2650 006252 012711 000340
2651 006256 012601
2652
2653 006260
2654 006260 012767 006266 172622
2655 006266
2656 006266 005067 002366
2657
2658 006272
2659 006272 052777 000004 172764
2660
2661 006300
2662 006300 042777 000100 172752
2663
2664
2665 006306 012746 000000
2666 006312 012746 006320
2667 006316 000002
2668 006320 655
2669
2670
2671 006320
2672 006320 105077 172744
2673
2674
2675
2676 006324
2677 006324 010546
2678 006326 012745 177777
2679 006332 016745 172722

```

```

*****
*****
*TEST 20 RECEIVER INTERRUPT LOGIC TEST
* THIS TEST COVERS ALL OF THE RECEIVER
* SIDE OF THE INTERRUPT LOGIC IN
* CHARACTER MODE
*****
TST20 SCOPE
MOV #10,$TIMES // DO 10 ITERATIONS
MOV #20,$TESTN // SET TEST NUMBER IN APT MAIL BOX
// IF #MAINTJUMP NOTSET IN SUSWR ORB CONSOLE EQ #TRU
BIT #MAINTJUMP,$USWR
BEQ $137
CMPB CONSOLE,#TRUE
BNE $140
JMP TST21 // EXIT TEST
ENDIF
// CLEAR INTERRUPT OCCURED FLAG
// SET UP RECEIVER INTER VECTOR
SETVEC DLVEC,#INTSRV,#PR7
MOV R1,-(SP)
MOV DLVEC,R1
MOV #INTSRV,(R1)+
MOV #PR7,(R1)
MOV (SP)+,R1
// PRIORITY 0 AND MULTIPLE INTERRUPT TEST -RCURIE
BGNSUB
MOV #64,$SLPERR
LET INTFLAG = #0
CLR INTFLAG
// SET MAINT BIT
LET @TCSR = @TCSR SET BY #MAINT
// CLEAR INTERRUPTS
LET @RCSR = @RCSR CLR BY #RCURIE
// CHANGE PRIORITY
// TO 0
MOV #PRO,-(SP) // PUT NEW PS ON STACK
MOV #65,-(SP) // PUT NEW PC ON STACK
RTI // POP NEW PC AND PS
// SEND A CHARACTER
LET @TBUF B= #0
// WAIT A MAXIMUM
// OF 500 MSEC FOR
// RCUR DONE TO SET IN RCSR
CALL TIMER IN (<#500,#RCURDONE,RCSR,#SET>)
MOV R5,-(SP)
MOV #SET,-(R5)
MOV RCSR,-(R5)

```

```

2680 006336 012745 000200      MOV      #RCVRDONE, -(R5)
2681 006342 012745 000500      MOV      #500, -(R5)
2682 006346 004767 001742      JSR      PC, TIMER
2683 006352 012605      MOV      (SP)+, R5
2684
2685 006354      , SET INTERRUPT ENABLE
2686 006354 052777 000100 172676      BIS      #RCVRIE, @RCSR
2687
2688      , LET IT COME IN
2689 006362      WAITMS 1
2690 006364 010546      MOV      R5, -(SP)
2691 006370 012745 000001      MOV      #1, -(R5)
2692 006374 004767 002176      JSR      PC, WAIT
2693 006374 012605      MOV      (SP)+, R5
2694 006376
2695 006376 017700 172660      MOV      @RBUF, R0
2696
2697 006402      , DID HE DO IT RIGHT?
2698 006402 026727 002252 000001      IF INTFLAG NE #1 THEN
2699 006410 001411      CMP      INTFLAG, #1
2700      BEQ      $141
2701
2702      , NONE OCCURED
2703      , CAN NOT LEAVE WITH MAINT SET
2704 006412      LET      @TCSR = @TCSR CLR BY #MAINT
2705 006412 042777 000004 172644      BIC      #MAINT, @TCSR
2706 006420
2707 006420 005767 002234      IF INTFLAG EQ #0 THEN
2708 006424      TST      INTFLAG
2709 006426 104111      BNE      $142
2710
2711      , RECEIVER DID NOT INTERRUPT IN TIME
2712      ERRHRD 111, D DNOT
2713 006426 000401      ERROR 111
2714
2715 006430      , TWICE OR MORE
2716 006430 000401      ELSE
2717 006432      BR      $143
2718
2719      , RECEIVER INTERRUPTED TWICE
2720 006432 104112      ERRHRD 112, TW CE
2721 006434
2722      ENDIF
2723
2724      ENDIF
2725 006434      ENDSUB
2726 006434 042777 000100 172616      , CLEAR THE WORLD
2727      LET @RCSR = @RCSR CLR BY #RCVRIE
2728
2729
2730      , RESET MAINT BIT
2731 006442      LET @TCSR = @TCSR CLR BY #MAINT
2732 006442 042777 000004 172614      BIC      #MAINT, @TCSR
2733
2734 006450      LET R4 = @DLVEC
2735 006454 017704 172602      MOV      @DLVEC, R4
2736      CLRVEC R4
  
```

```
2736 006454 010146      MOV     R1, -(SP)      ;; PUSH R1 ON STACK
2737 006456 010246      MOV     R2, -(SP)      ;; PUSH R2 ON STACK
2738 006460 012701 000004   MOV     #R4, R1
2739 006464 010102      MOV     R1, R2
2740 006466 062702 000002   ADD     #2, R2
2741 006472 010221      MOV     R2, (R1)+
2742 006474 005011      CLR     (R1)
2743 006476 012602      MOV     (SP)+, R2      ;; POP STACK INTO R2
2744 006500 012601      MOV     (SP)+, R1      ;; POP STACK INTO R1
2745 006502                          ENDTST
```



```

2746
2747
2748
2749
2750
2751 006502 00C004
2752 006504 012767 000001 172446
2753 006512 012767 000021 172460
2754 006520
2755 006520 032767 040000 172472
2756 006526 001404
2757 006530 126727 002365 000001
2758 006536 001004
2759 006540
2760 006540
2761 006540 012767 000001 172412
2762 006546 000526
2763 006550
2764 006550
2765
2766 006550
2767 006550 005067 001412
2768
2769 006554
2770 006554 052777 000004 172502
2771
2772
2773
2774 006562 012746 000000
2775 006566 012746 006574
2776 006572 000002
2777 006574
2778
2779 006574
2780 006574 162705 000002
2781 006600 004767 001666
2782 006604 012501
2783 006606
2784 006606 017700 172450
2785
2786
2787 006612
2788 006612 005002
2789 006614 000401
2790 006616
2791 006616 005202
2792 006620
2793 006620 020227 000377
2794 006624 003062
2795
2796
2797
2798
2799 006626
2800 006626 010546
2801 006630 012745 177777
  
```

```

*****
*****
*TEST 21      TEST ACTUAL DATA TRANSFERED
*            NON-INTERRUPT MAINTENANCE BIT SET
*****
TST21  SCOPE
MOV     #1, $TIMES      ; DO 1 ITERATION
MOV     #21, $TESTN    ; SET TEST NUMBER IN APT MAIL BOX
                        IF #MAINTJUMP NOTSET IN $USWR ORB CONSOLE EQ #TRU
BIT     #MAINTJUMP, $USWR
BEQ     $144
CMPB   CONSOLE, #TRUE
BNE     $145
$144
                        EXIT TEST
MOV     #1, $TIMES
BR      TST22          ; ; EXIT THIS TEST
                        ENDIF
$145
                        LET ERRCHK = #0
                        ; SET MAINT BIT
                        LET @TCSR = @TCSR SET BY #MAINT
                        ; CHANGE PRIORITY
                        ; TO 0
MOV     #PRO, -(SP)    ; PUT NEW PS ON STACK
MOV     #64$, -(SP)   ; PUT NEW PC ON STACK
RTI     ; POP NEW PC AND PS
$64$
                        ; GET DATA MASK
                        CALL DATLNG OUT <R1>
SUB     #1*2, R5
JSR    PC, DATLNG
MOV    (R5)+, R1
                        LET R0 = @RBUF , START CLEAR
                        ; ALL BINARY CHAR
                        INCR R2 FROM #0 TO #377 BY #1
$147
INC     R2
$146
CMP    R2, #377
BGT    $150
                        ; TRANSMIT CHAR IN R2
                        CALL TIMER IN <#500, #XMITRDY, TCSR, #SET>
MOV    R5, -(SP)
MOV    #SET, -(R5)
  
```

MAINDEC-11-DVDVC-B CVDVCB P11	MACY11 08 39	30A(1052) T21	02-FEB-78 TEST	08 40 ACTUAL	PAGE 66 DATA TRANSFERED	SEQ 0066
2802	006634	016745	172424		MOV TCSR, -(R5)	
2803	006640	012745	000200		MOV #XMITRDY, -(R5)	
2804	006644	012745	000500		MOV #500, -(R5)	
2805	006650	004767	001440		JSR PC, TIMER	
2806	006654	012605			MOV (SP)+, R5	
2807	006656					IF ERROR THEN
2808	006656	103003			BCC \$151	LET ERRCHK = ERRCHK SET BY #BIT3
2809	006660					ENDIF
2810	006660	052767	000010	001300	BIS #B T3, ERRCHK	
2811	006666					
2812	006666			\$151		
2813						
2814						, TRANSMIT IT
2815	006666					LET @TBUF = R2
2816	006666	110277	172376		MOVB R2, @TBUF	
2817						
2818	006672					CALL TIMER IN <#500, #RCURDONE, RCSR, #SET>
2819	006672	010546			MOV R5, -(SP)	
2820	006674	012745	177777		MOV #SET, -(R5)	
2821	006700	016745	172354		MOV RCSR, -(R5)	
2822	006704	012745	000200		MOV #RCURDONE, -(R5)	
2823	006710	012745	000500		MOV #500, -(R5)	
2824	006714	004767	001374		JSR PC, TIMER	
2825	006720	012605			MOV (SP)+, R5	
2826	006722					IF ERROR THEN
2827	006722	103003			BCC \$152	LET ERRCHK = ERRCHK SET BY #BIT4
2828	006724					ENDIF
2829	006724	052767	000020	001234	B S #BIT4, ERRCHK	
2830	006732					
2831	006732			\$152		
2832						, AND SAVE IT
2833	006732					LET R3 = @RBUF
2834	006732	017703	172324		MOV @RBUF, R3	
2835						
2836						
2837						, COMPARE TO SEE IF WE RECEIVED IT ALL
2838						
2839						, CLEAN OFF NON-DATA BITS
2840						, ON BOTH TRANSMITTED AND
2841	006736					LET R4 = R2 CLR BY R1
2842	006736	010204			MOV R2, R4	
2843	006740	040104			BIC R1, R4	
2844	006742					LET R3 = R3 CLR BY R1
2845	006742	040103			BIC R1, R3	
2846						
2847						, RECEIVED DATA
2848	006744					IF R4 NE R3 THEN
2849	006744	020403			CMP R4, R3	
2850	006746	001410			BEQ \$153	
2851						, DATA COMPARE ERROR
2852						, CAN NOT LEAVE WITH MAINT SET
2853	006750					LET @TCSR = @TCSR CLR BY #MAINT
2854	006750	042777	000004	172306	BIC #MAINT, @TCSR	
2855	006756					ERRHRD 116, COMP, SBWAS
2856	006756	104116			ERROR 116	
2857	006760					EXIT TEST, ON ERROR

```
2858 006760 012767 000001 172172      MOV    #1, $TIMES
2859 006766 000416                      BR     TST22      , , , , EXIT THIS TEST
2860 006770                                ENDIF
2861 006770                                $153
2862 006770                                ENDINC , R2
2863 006770 000712      BR     $147
2864 006772                                $150
2865
2866                                , RESET MAINT BIT
2867 006772                                LET @TCSR = @TCSR CLR BY #MAINT
2868 006772 042777 000004 172264      BIC    #MAINT, @TCSR
2869 007000                                IF #BIT3 SET IN ERRCHK THEN
2870 007000 032767 000010 001160      B T    #BIT3, ERRCHK
2871 007006 001401      BEQ    $154
2872 007010                                ERRHRD 130
2873 007010 104130      ERROR    130
2874 007012                                ENDIF
2875 007012                                $154
2876 007012                                IF #BIT4 SET IN ERRCHK THEN
2877 007012 032767 000020 001146      BIT    #BIT4, ERRCHK
2878 007020 001401      BEQ    $155
2879 007022                                ERRHRD 131
2880 007022 104131      ERROR    131
2881 007024                                ENDIF
2882 007024                                $155
2883 007024                                ENDTST
2884
2885
2886
```

```

2887      , , *****
2888      , , *****
2889      , *TEST 22      TEST DATA THROUGH WRAP
2890      , , *****
2891      007024 000004      TST22  SCOPE
2892      007026 012767 000001 172124      MOV      #1, $TIMES      , DO 1 ITERATION
2893      007034 012767 000022 172136      MOV      #22, $TESTN      , SET TEST NUMBER IN APT MAIL BOX
2894      007042      , IF #WRAP NOTSET IN $USWR OR #COMSPD NOTSET IN $USW
2895      007042 032767 020000 172150      BIT      #WRAP, $USWR
2896      007050 001404      BEQ      $156
2897      007052 032767 000100 172140      BIT      #COMSPD, $USWR
2898      007060 001004      BNE      $157
2899      007062      $156
2900      , CAN'T TEST WITHOUT A WRAP
2901      007062      EXIT TST
2902      007062 012767 000001 172070      MOV      #1, $TIMES
2903      007070 000516      BR      TST23      , , EXIT THIS TEST
2904      007072      ENDIF
2905      007072      $157
2906      , DON'T USE MAINT
2907      007072      LET @TCSR = @TCSR CLR BY #MAINT
2908      007072 042777 000004 172164      BIC      #MAINT, @TCSR
2909      , IF A SPECIAL TURN AROUND CARD IS
2910      , CONNECTED IN PLACE OF THE WRAP
2911      , SETTING READER RUN WILL ENABLE IT
2912      , THIS MODULE IS ONLY USED IN MANUFACTUR
2913      , AND ONLY ON THE CONSOLE DLV11-F
2914      , IF NO SPECIAL MODULE IS AVAILABLE,
2915      , AND THE WRAP BIT IS SET IN $USWR
2916      , THEN THIS TEST WILL ERROR ON THE CONSO
2917
2918      007100      LET @RCSR = @RCSR SET BY #11
2919      007100 052777 000011 172152      B S      #11, @RCSR
2920      , CHANGE PRIORITY
2921      , TO 0
2922      007106 012746 000000      MOV      #PRO, -(SP)      , PUT NEW PS ON STACK
2923      007112 012746 007120      MOV      #64$, -(SP)      , PUT NEW PC ON STACK
2924      007116 000002      RT      , POP NEW PC AND PS
2925      007120      64$
2926      , GET DATA MASK
2927      007120      CALL DATLNG OUT <R1>
2928      007120 162705 000002      SUB      #1*2, R5
2929      007124 004767 001342      JSR      PC, DATLNG
2930      007130 012501      MOV      (R5)+, R1
2931      007132      LET R0 = @RBUF , START CLEAN
2932      007132 017700 172124      MOV      @RBUF, R0
2933      , BINARY COUNT PATTERN
2934      007136      INCR R2 FROM #0 TO #377 BY #1
2935      007136 005002      CLR      R2
2936      007140 000401      BR      $160
2937      007142      $161
2938      007142 005202      INC      R2
2939      007144      $160
2940      007144 020227 000377      CMP      R2, #377
2941      007150 003063      BGT      $162
2942
    
```

```

2943
2944
2945
2946
2947 007152
2948 007152 010546      MOV    R5, -(SP)
2949 007154 012745 177777  MOV    #SET, -(R5)
2950 007160 016745 172100  MOV    TCSR, -(R5)
2951 007164 012745 000200  MOV    #XMITRDY, -(R5)
2952 007170 012745 000500  MOV    #500, -(R5)
2953 007174 004767 001114  JSR    PC, TIMER
2954 007200 012605      MOV    (SP)+, R5
2955 007202
2956 007202 103005      BCC    $163
2957 007204 104123      ERROR  123
2958 007206
2959 007206 012767 000001 171744  MOV    #1, $TIMES
2960 007214 000444      BR     TST23
2961 007216
2962 007216      $163
2963
2964
2965 007216
2966 007216 110277 172046  MOVB   R2, @TBUF
2967
2968 007222
2969 007222 010546      MOV    R5, -(SP)
2970 007224 012745 177777  MOV    #SET, -(R5)
2971 007230 016745 172024  MOV    RCSR, -(R5)
2972 007234 012745 000200  MOV    #RCVRDONE, -(R5)
2973 007240 012745 000500  MOV    #500, -(R5)
2974 007244 004767 001044  JSR    PC, TIMER
2975 007250 012605      MOV    (SP)+, R5
2976 007252
2977 007252 103005      BCC    $164
2978 007254
2979 007254 104124      ERROR  124
2980
2981 007256
2982 007256 012767 000001 171674  MOV    #1, $TIMES
2983 007264 000420      BR     TST23
2984 007266
2985 007266      $164
2986
2987
2988 007266
2989 007266 017703 171770  MOV    @RBUF, R3
2990
2991
2992 007272
2993 007272 010204      MOV    R2, R4
2994 007274 040104      BIC   R1, R4
2995 007276
2996 007276 040103      BIC   R1, R3
2997
2998

```

```

, TRANSMIT THE CHAR IN R2
, MAKE SURE IT'S READY
CALL TIMER IN <#500, #XMITRDY, TCSR, #SET>
IF ERROR THEN
, TRANSMITTER NEVER BECAME READY
EXIT TEST
,,,EXIT THIS TEST
ENDIF
, START IT ON ITS WAY
LET @TBUF B= R2
, NOW WAIT FOR RECIEVER DONE
CALL TIMER IN <#500, #RCVRDONE, RCSR, #SET>
IF ERROR THEN
ERRHRD 124
, RECIEVER NEVER BECAME READY
EXIT TEST
,,,EXIT THIS TEST
ENDIF
, RETRIEVE
LET R3 = @RBUF
, STRIP OFF JUNK ON BOTH
LET R4 = R2 CLR BY R1
LET R3 = R3 CLR BY R1
, WE HAVE TROUBLE

```

```
2999 007300
3000 007300 020403      CMP      R4,R3
3001 007302 001405      BEQ      $165
3002
3003 007304              ,DATA COMPARE ERROR
3004 007304 104117      ERROR    117
3005 007306              ,ERRHRD 117,COMP,SBWAS
3006 007306 012767 000001 171644  MOV     #1,$TIMES
3007 007314 000404      BR       TS-23
3008 007316              ,,,EXIT THIS TEST
3009 007316              ENDIF
3010              $165
3011 007316              ENDINC , R2
3012 007316 000711      BR       $161
3013 007320              $162
3014
3015              , NOW THAT THE TEST IS DONE
3016              , WE WILL TOGGLE READER RUN
3017 007320              , TO TURN OFF THE SPECIAL MODULE
3018 007320 052777 000011 171732  B S    #11,@RCSR
3019
3020
3021
3022 007326              LET @RCSR = @RCSR SET BY #11
3023
3024
3025
3026
ENDTST
```

```
3027
3028
3029
3030
3031
3032 007326 00C004
3033 007330 012767 000001 171622
3034 007336 012767 000023 171634
3035
3036 007344
3037 007344 032767 040000 171646
3038 007352 001404
3039 007354 126727 001541 000001
3040 007362 001004
3041 007364
3042 007364
3043 007364 012767 000001 171566
3044 007372 000553
3045 007374
3046 007374
3047
3048
3049 007374
3050 007374 162705 000002
3051 007400 004767 001066
3052 007404 012503
3053
3054
3055
3056
3057
3058
3059
3060
3061
3062
3063
3064
3065
3066
3067 007406 012746 000000
3068 007412 012746 007420
3069 007416 000002
3070 007420
3071
3072 007420
3073 007420 016701 171632
3074
3075 007424
3076 007424 012721 007626
3077 007430
3078 007430 012721 000340
3079
3080
3081 007434
3082 007434 012721 007564
```

\*\*\*\*\*  
\*\*\*\*\*  
\*TEST 23 FULL DATA TRANSFER WITH INTERRUPTS  
\* AND MAINTENANCE MODE  
\*\*\*\*\*  
TST23 SCOPE  
MOV #1, \$TIMES ; DO 1 ITERATION  
MOV #23, \$TESTN ; SET TEST NUMBER IN APT MAIL BOX  
IF #MAINTJUMP NOTSETIN \$USWR ORB CONSOLE EQ #TRU  
BT #MAINTJUMP, \$USWR  
BEQ \$166  
CMPB CONSOLE, #TRUE  
BNE \$167  
\$166  
EXIT TEST  
MOV #1, \$TIMES  
BR TST24 ; ; EXIT THIS TEST  
\$167  
ENDIF  
; GET DATA MASK  
CALL DATLNG OUT <R3>  
SUB #1\*2, R5  
JSR PC, DATLNG  
MOV (R5)+, R3  
; THIS TEST WILL RUN BOTH TRANSMITTER AND  
; RECIEVER AT FULL SPEED TESTING  
; THE ABILITY OF THE MODULE  
; TO HANDLE INTERRUPTS FROM BOTH SIDES  
; AT ONCE ALSO, THE DOUBLE BUFFERING LOG C  
; OF THE UART WILL BE FULLY TESTED  
; THIS TEST WILL TRANSFER A MAXIMUM OF 400(8)  
; CHARACTERS THROUGH THE MODULE, BUT IF AN ERROR  
; IS DETECTED BY THE TEST A PREMATURE SHUTDOWN OCCURS  
; CHANGE PRIORITY  
; TO 0  
MOV #PRO, -(SP) ; PUT NEW PS ON STACK  
MOV #64\$, -(SP) ; PUT NEW PC ON STACK  
PTI ; POP NEW PC AND PS  
64\$  
; GET VECTOR ADDRESS  
LET R1 = DLVEC  
; RCVR VECTOR  
LET (R1)+ = #REC  
LET (R1)+ = #PR7  
; POINT TO TRANSMITTER VECTOR  
; AND SET IT UP ALSO  
LET (R1)+ = #TRAN

MAIN	DEC-11-DVDVC-B	MACY11	30A(1052)	02-FEB-78	08 40	PAGE 72	H 6	NTERRUPTS	SEQ 0072
CVDVCB	P11	02-FEB-78	08 39	T23	FULL	DATA TRANSFER WITH			
3083	007440							LET (R1) = #PP7	
3084	007440	012711	000340		MOV	#PR7, (R1)			
3085									
3086									
3087	007444							, CLEAR ERROR COUNTER	
3088	007444	005067	000106		CLR	ERRCNT		LET ERCNT = #0	
3089									
3090	007450							, INITIAL ZE COUNTERS	
3091	007450	012701	177777		MOV	#-1, R1		LET R1 = #-1	
3092									
3093	007454							, RECEIVER STORAGE	
3094	007454	005002			CLR	R2		LET R2 = #0	
3095									
3096	007456							, # OF RECEIVED CHAR COUNT	
3097	007456	012704	177777		MOV	#-1, R4		LET R4 = #-1	
3098									
3099	007462							BRESET , SET UP ALL REG STERS	
3100	007462	000005			RESET				
3101									
3102	007464							, SET UP MAINTENANCE	
3103	007464	052777	000004	171572	BIS	#MAINT, @TCSR		LET @TCSR = @TCSR SET BY #MAINT	
3104									
3105									
3106	007472							, SET I E IN TRANSMITTER	
3107	007472	052777	000100	171564	BIS	#XMITIE, @TCSR		LET @TCSR = @TCSR SET BY #XMITIE	
3108									
3109	007500							, AND RECEIVER	
3110	007500	052777	000100	171552	BIS	#RCVRIE, @RCSR		LET @RCSR = @RCSR SET BY #RCVRIE	
3111									
3112									
3113									
3114	007506							, NOW WE WAIT UNTIL R4 COUNT (RECEIVED) IS EQUAL	
3115	007506							REPEAT	
3116	007506								
3117	007506	020467	000046		CMP	R4, NUMBER			
3118	007512	001403			BEQ	\$171			
3119	007514	005767	000036		TST	ERRCNT			
3120	007520	003772			BLE	\$170			
3121	007522								
3122									
3123	007522								
3124	007522	042777	000004	171534	BIC	#MAINT, @TCSR		LET @TCSR = @TCSR CLR BY #MAINT	
3125									
3126	007530							, CHECK FOR DATA COMPARE ERRORS	
3127	007530	005767	000022		TST	ERRCNT		IF ERCNT NE #0 THEN	
3128	007534	001401			BEQ	\$172			
3129									
3130	007536							, DATA COMPARE ERROR	
3131	007536	104120			ERROR	120		ERRHRD 120, COMP, FIRST	
3132	007540								
3133	007540								
3134									
3135	007540								
3136	007540	042777	000100	171516	B C	#XMITIE, @TCSR		LET @TCSR = @TCSR CLR BY #XMITIE	
3137	007546								
3138	007546	042777	000100	171504	B C	#RCVRIE, @RCSR		LET @RCSR = @RCSR CLR BY #RCVRIE	



```

3139 007554                                EXIT      ,SKIP OVER SUPPORT ROUTINES & STORAGE
3140 007554 000462                        BR        TST24      ,EXIT THIS TEST
3141
3142 007556 000000                        ERRCNT    0
3143 007560 001000                        NUMBER    1000
3144 007562      000                      SB        BYTE 0
3145 007563      000                      WAS       BYTE 0
3146
3147
3148      ,*****
3149      ,TRANSMIT INTERRUPT HANDLER
3150 007564                                BGNSRV  TRAN
3151 007564
3152      ,*****
3153
3154      , INCREMENT CHAR COUNT
3155 007564                                LET R1   = R1 + #1
3156 007564 005201                        INC      R1
3157
3158 007566                                ,SET UP FOR TRANSFER
3159 007566 010167 0C0030                  MOV      R1,HOLD
3160 007572 040367 000024                  BIC      R3,HOLD
3161
3162 007576                                ,AND SEND
3163 007576 016777 000020 171464          MOV      HOLD,@TBUF
3164
3165 007604                                ,ALL DONE
3166 007604 020167 177750                  CMP      R1,NUMBER
3167 007610 001003                        BNE      $173
3168
3169 007612                                ,STOP  INTERRUPT PROCESS NG
3170 007612 042777 000100 171444          B C     #XMITIE,@TCSR
3171 007620                                LET @TCSR = @TCSR CLR BY #XMITIE
3172 007620                                ENDIF
3173      $173
3174 007620 000401                        BR        ZZZ
3175
3176 007622 000000                        HOLD 0
3177
3178 007624                                , EXIT SRV
3179 007624 000002                        ZZZ
3180
3181
3182
3183      ,*****
3184      ,RECEIVER INTERRUPT HANDLER
3185 007626                                BGNSRV  REC
3186 007626
3187      ,*****
3188
3189      ,COUNT THIS CHAR
3190 007626                                LET R4   = R4 + #1
3191 007626 005204                        INC      R4
3192
3193 007630                                ,GET CHAR IN + MASK IT
3194 007630 017702 171426                  MOV      @RBUF,R2
  
```

3195	007634	040302			BIC	R3,R2	
3196							,RHLD WILL CONTAIN EXPECTED INPUT
3197	007636						LET RHLD = R4 CLR BY R3
3198	007636	010467	000054		MOV	R4,RHLD	
3199	007642	040367	000050		BIC	R3,RHLD	
3200							
3201							,DO THEY COMPARE
3202	007646						IF R2 NE RHLD THEN
3203	007646	020267	000044		CMP	R2,RHLD	
3204	007652	001412			BEQ	\$174	
3205							,FIRST ERROR
3206	007654						IF ERRCNT EQ #0 THEN
3207	007654	005767	177676		TST	ERRCNT	
3208	007660	001005			BNE	\$175	
3209							,SAVE RECORD OF FIRST MISS
3210	007662						LET SB B= RHLD
3211	007662	116767	000030	177672	MOVB	RHLD,SB	
3212	007670						LET WAS B= R2
3213	007670	110267	177667		MOVB	R2,WAS	
3214	007674						ENDIF
3215	007674			\$175			
3216							,COUNT IT
3217	007674						LET ERRCNT = ERRCNT + #1
3218	007674	005267	177656		INC	ERRCNT	
3219	007700						ENDIF
3220	007700			\$174			
3221							
3222							,ALL DONE?
3223	007700						IF R4 EQ NUMBER THEN
3224	007700	020467	177654		CMP	R4,NUMBER	
3225	007704	001003			BNE	\$176	
3226							,STOP RECEIVER INTERRUPTS
3227	007706						LET @RCSR = @RCSR CLR BY #RCVRIE
3228	007706	042777	000100	171344	BIC	#RCVRIE,@RCSR	
3229							,INDICATE ALL DONE TO T MER
3230							,MAIN REPEAT LOOP IS CHECKING
3231							,FOR 'R4 = NUMBER' ALSO
3232	007714						ENDIF
3233	007714			\$176			
3234							
3235	007714	000401			BR	2222	,EXIT SRV
3236							
3237	007716	000000					RHLD 0
3238	007720			2222			
3239	007720						ENDSRV
3240	007720	000002			RTI		
3241							
3242	007722						ENDTST
3243							
3244							
3245							

```

3246 // *****
3247 // *****
3248 *TEST 24 TEST BREAK GENERATION LOGIC
3249 * TRANSMIT KNOWN CHAR WITH BREAK SET
3250 * AND COMPARE RECEIVED WITH 0
3251 * FRAMING ERROR WILL ALSO BE CHECKED
3252 * IF ERROR BITS ARE ENABLED
3253 // *****
3254 007722 000004 TST24 SCOPE
3255 007724 012767 000010 171226 MOV #10, $TIMES // DO 10 ITERATIONS
3256 007732 012767 000024 171240 MOV #24, $TESTN // SET TEST NUMBER IN APT MAIL BOX
3257 007740 // IF #MAINTJUMP NOTSETIN $USWR OR #BRK NOTSETIN $U
3258 007740 032767 040000 171252 BIT #MAINTJUMP, $USWR
3259 007746 001404 BEQ $177
3260 007750 032767 010000 171242 B T #BRK, $USWR
3261 007756 001004 BNE $200
3262 007760 $177
3263 007760 // EXIT TEST
3264 007760 012767 000001 171172 MOV #1, $TIMES
3265 007766 000500 BR TST25 // ...EXIT THIS TEST
3266 007770 // ENDF
3267 007770 $200
3268 007770 // IFB CONSOLE EQ #TRUE THEN
3269 007770 126727 001125 000001 CMPB CONSOLE, #TRUE
3270 007776 001004 BNE $201
3271 010000 // EXIT TEST
3272 010000 012767 000001 171152 MOV #1, $TIMES
3273 010006 000470 BR TST25 // ...EXIT THIS TEST
3274 010010 // ENDF
3275 010010 $201
3276
3277 010010 // LET ERRCHK = #0 // CLEAR ERROR WORD
3278 010010 005067 000152 CLR ERRCHK
3279 // SET MAINTENANCE BIT
3280 010014 LET @TCSR = @TCSR SET BY #MAINT
3281 010014 052777 000004 171242 B S #MAINT, @TCSR
3282 // SET BREAK BIT
3283 010022 LET @TCSR = @TCSR SET BY #BREAK
3284 010022 052777 000001 171234 B S #BREAK, @TCSR
3285 // NON-ZERO CHAR '*'
3286 010030 LET @TBUF = #252
3287 010030 012777 000252 171232 MOV #252, @TBUF
3288 // WAIT FOR DONE
3289 010036 CALL TIMER IN (<#500, #RCVRDONE, RCSR, #SET>)
3290 010036 010546 MOV R5, -(SP)
3291 010040 012745 177777 MOV #SET, -(R5)
3292 010044 016745 171210 MOV RCSR, -(R5)
3293 010050 012745 000200 MOV #RCVRDONE, -(R5)
3294 010054 012745 000500 MOV #500, -(R5)
3295 010060 004767 000230 JSR PC, TIMER
3296 010064 012605 MOV (SP)+, R5
3297 010066 // IF ERROR THEN
3298 010066 103001 BCC $202
3299 // RECIEVER DONE DID NOT SET
3300 010070 ERRHRD 115
3301 010070 104115 ERROR 115
  
```

3302	010072						ENDIF
3303	010072			\$202			
3304							
3305	010072						IFB @RBUF NE #0 THEN
3306	010072	105777	171164		TSTB	@RBUF	
3307	010076	001404			BEQ	\$203	
3308							, BREAK DID NOT EQUAL 0
3309	010100						LET ERRCHK = ERRCHK SET BY #B TO
3310	010100	052767	000001	000060	B S	#B TO, ERRCHK	
3311	010106						ELSE
3312	010106	000413			BR	\$204	
3313	010110			\$203			
3314	010110						IF #ERRBITS SETIN \$USWR THEN
3315	010110	032767	100000	171102	BIT	#ERRBITS, \$USWR	
3316	010116	001407			BEQ	\$205	
3317	010120						IF #FRERR NOTSETIN @RBUF THEN
3318	010120	032777	020000	171134	BIT	#FRERR, @RBUF	
3319	010126	001003			BNE	\$206	
3320	010130						LET ERRCHK = ERRCHK SET BY #BIT1
3321	010130	052767	000002	000030	BIS	#BIT1, ERRCHK	
3322	010136						ENDIF
3323	010136			\$206			ENDIF
3324	010136						ENDIF
3325	010136			\$205			ENDIF
3326	010136						
3327	010136			\$204			
3328							
3329	010136						BRESET , CLEAN UP
3330	010136	000005			RESET		
3331							
3332	010140						IF #BIT0 SETIN ERRCHK THEN
3333	010140	032767	000001	000020	BIT	#BIT0, ERRCHK	
3334	010146	001401			BEQ	\$207	
3335	010150						ERRHRD 121 , BREAK ERROR
3336	010150	104121			ERROR	121	
3337	010152						ENDIF
3338	010152			\$207			
3339	010152						IF #BIT1 SETIN ERRCHK THEN
3340	010152	032767	000002	000006	BIT	#BIT1, ERRCHK	
3341	010160	001401			BEQ	\$210	
3342	010162						ERRHRD 122 , FRAMING ERROR
3343	010162	104122			ERROR	122	
3344	010164						ENDIF
3345	010164			\$210			
3346	010164						EXIT
3347	010164	000401			BR	TST25	...EXIT THIS TEST
3348	010166	000000			ERRCHK	WORD 0	
3349	010170						ENDTST
3350							

```

3351
3352
3353
3354
3355 010170 000004
3356 010172 012767 000001 170760
3357 010200 104401 010206
3358 010204 000404
3359
3360 010216
3361 010216 016746 171032
3362 010222 104402
3363 010224 104401 010232
3364 010230 000405
3365
3366 010244
3367 010244 016746 171006
3368 010250 104402
3369 010252 104401 010260
3370 010256 000405
3371
3372 010272
3373 010272 016746 170614
3374 010276 104405
3375 010300 005067 170606
3376 010304 104401 001171
3377 010310 000167 171622

      *****
      *TEST 25      NOT A TEST - SEND BACK TO LOOP
      *****
TST25  SCOPE
      MOV      #1, $TIMES      ;; DO 1 ITERATION
      TYPE     , 65$          ;; TYPE ASCIZ STRING
      BR       64$           ;; GET OVER THE ASCIZ
      ;; 65$  ASCIZ <CRLF>*CSR *
      64$
      MOV      DLADD, -(SP)    ;; SAVE DLADD FOR TYPEOUT
      TYPOC    ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
      TYPE     , 67$          ;; TYPE ASCIZ STRING
      BR       66$           ;; GET OVER THE ASCIZ
      ;; 67$  ASCIZ *, VECTOR *
      66$
      MOV      DLVEC, -(SP)   ;; SAVE DLVEC FOR TYPEOUT
      TYPOC    ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
      TYPE     , 69$          ;; TYPE ASCIZ STRING
      BR       68$           ;; GET OVER THE ASCIZ
      ;; 69$  ASCIZ *, ERRORS *
      68$
      MOV      $ERTTL, -(SP)  ;; SAVE $ERTTL FOR TYPEOUT
      TYPDS    ;; GO TYPE--DECIMAL ASCII WITH SIGN
      CLR      $ERTTL        ;; RESET FOR NEXT DEVICE/PASS
      TYPE     , $CRLF
      JMP      LOOP          ;; BACK UP TO THE BEGINNING
  
```

3378  
3379  
3380  
3381 010314  
3382 010314  
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3402  
3403  
3404  
3405  
3406 010314  
3407 010314 016567 000004 000136  
3408 010322  
3409 010322 016567 000000 000132  
3410 010330  
3411 010330 112767 000000 000126  
3412  
3413  
3414  
3415  
3416 010336  
3417 010336  
3418  
3419 010336  
3420 010336 036577 000002 000114  
3421 010344 001004  
3422 010346  
3423 010346 112767 000000 000111  
3424 010354  
3425 010354 000403  
3426 010356  
3427 010356  
3428 010356 112767 177777 000101  
3429 010364  
3430 010364  
3431  
3432  
3433 010364

```

      BGNMOD      SUBS
*****
ROUTINE TIMER <HOWLONG,WHICHBIT,REG,SETCLR>
TIMER
* ROUTINE TIMER
* THIS ROUTINE IS USED TO TEST THE STATUS OF ANY BIT
* IN ANY REGISTER
* INPUTS
* HOWLONG THE MAXIMUM AMOUNT OF TIME TO SPEND IN
* THIS ROUTINE
* WHICHBIT A MASK WITH THE BIT(S) SET THAT ARE
* TO BE CHECKED
* REG A POINTER TO THE REGISTER TO BE CHECKED
* SETCLR THE DESIRED RESULTS
* EITHER #SET OR #CLEAR
* OUTPUT
* THE 'C' BIT IS SET TO INDICATE AN ERROR
* BUT IT IS TESTED BY THE IF ERROR STATEMENT
*
* NOTE THE USE OF (R5) IS PART OF THE LINKAGE
* MECHANISM BETWEEN THE CALLER AND THE CALLED
*****
```

000001  
000000

TRUE= 1  
FALSE= 0

```

      LET REGSAV = REG(R5) , GET POINTER TO REGIST
      LET TIMSAV = HOWLONG(R5) , SAVE HOWLONG FOR
      LET FLAG B= #FALSE , INITIALIZE THE EXIT FLA
      , START OF AN INFIN TE LOOP
      LOOP
      IF TEST TO SEE IF WHICHBIT IS SET
      WHICHBIT(R5) NOTSETIN @REGSAV THEN
      LET HOLDSC B= #CLR
      ELSE
      BR $216
      LET HOLDSC B= #SET , REMEMBER TH S
      ENDIF
      , NOW SEE IF THAT WAS WHAT WE WANTED
      IFB HOLDSC EQ SETCLR(R5) THEN
```

\$213

\$215

\$216

```

      B 7
MAINDEC-11-DVDVC-B      MACY11 30A(1052) 02-FEB-78 08 40 PAGE 79
CVDVCB P11      02-FEB-78 08 39      ROUTINE - TIMER
                                                    SEQ 0079

3434 010364 126765 000075 000006      CMPB  HOLDSC, SETCLR(R5)
3435 010372 001003      BNE   $217
3436                                     , JUST THE THING WE NEEDED
3437 010374                                     LET   FLAG B= #TRUE
3438 010374 112767 000001 000062      MOVB  #TRUE, FLAG
3439 010402                                     ENDIF
3440 010402                                     $217
3441
3442 010402                                     EXIFB FLAG EQ #TRUE OR TIMSAV LE #0
3443 010402 126727 000056 000001      CMPB  FLAG, #TRUE
3444 010410 001414      BEQ   $214
3445 010412 005767 000044      TST  TIMSAV
3446 010416 003411      BLE  $214
3447
3448                                     , ONE WAY OR THE OTHER, WE ARE DONE
3449                                     , IF WE ARE STILL HERE THEN HANG AROUND A WHILE
3450 010420                                     WAITMS 1      , WAIT FOR 1 MILLI-SECONDS
3451 010420 010546      MOV  R5, -(SP)
3452 010422 012745 000001      MOV  #1, -(R5)
3453 010426 004767 000140      JSR  PC, WAIT
3454 010432 012605      MOV  (SP)+, R5
3455 010434                                     LET  TIMSAV = TIMSAV - #1 , COUNTING DOWN
3456 010434 005367 000022      DEC  TIMSAV
3457 010440      ENDLOOP                                     , CONTINUED AT THE TOP
3458 010440 000736      BR   $213
3459 010442      $214
3460
3461                                     , ONLY 2 WAYS TO GET HERE
3462                                     , 1) WE RAN OUT OF TIME---ERROR ' '
3463                                     , 2) THE BIT IS IN THE CORRECT CONDITION--GOOD ' '
3464
3465 010442                                     IFB  FLAG EQ #TRUE THEN
3466 010442 126727 000016 000001      CMPB  FLAG, #TRUE
3467 010450 001001      BNE  $220
3468                                     RETURN NO ERROR , GOOD
3469 010452 000405      BR   $211
3470 010454      ENDIF
3471 010454      $220
3472 010454      RETURN ERROR , BAD
3473 010454 000261      SEC
3474 010456 000404      BR   $212
3475
3476 010460 000000      REGSAV WORD 0
3477 010462 000000      TIMSAV WORD 0
3478 010464 000      FLAG  BYTE 0
3479 010465 000      HOLDSC BYTE 0
3480                                     , WE ARE DONE GO BACK HOME
3481                                     ENDRTN
3482 010466      $211
3483 010466 000241      CLC
3484 010470      $212
3485 010470 000207      RTS  PC

```

```

3486
3487
3488 010472
3489 010472
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3499
3500
3501
3502
3503 010472
3504 010472 005065 000000
3505 010476
3506 010476 016767 170516 000062
3507 010504 016746 000056
3508 010510 042716 000017
3509 010514 042667 000046
3510
3511 010520
3512 010520 012767 000001 170544
3513 010526 000402
3514 010530
3515 010530 005267 170536
3516 010534
3517 010534 026767 170532 000024
3518 010542 003006
3519 010544
3520 010544 006365 000000
3521 010550
3522 010550 052765 000001 000000
3523 010556
3524 010556 000764
3525 010560
3526 010560
3527 010560 005165 000000
3528 010564
3529 010564 000401
3530 010566 000000
3531 010570
3532 010570
3533 010570
3534 010570 000207
  
```

```

*****
ROUTINE DATLNG <MASK>
DATLNG
* ROUTINE DATLNG
* THIS ROUTINE SETS UP A MASK FOR DATA, WITH
* NPUT - NOTHING IS PASSED TO THIS ROUTINE
* BUT GLOBAL INFORMATION IS ASSUMED TO EXIST
* $USWR-- THE WORD FOR SOFTWARE PARAMETERS
* DATA-- A MASK FOR THE LOCATION OF THE OCTAL
* NUMBER OF DATA BITS
* OUTPUT----
* MASK-- A MASK OF BINARY ONES RIGHT-JUSTIFIED
* THE NUMBER OF WHICH IS DEFINED IN $USWR WORD
*****
          LET MASK(R5) = #0          , START
          LET NUMBR = $USWR AND #DATA
          MOV $USWR, NUMBR
          MOV NUMBR, -(SP)
          BIC #DATA, (SP)
          BIC (SP)+, NUMBR

          INCR I FROM #1 TO NUMBR BY #1
          MOV #1, I
          BR $223
          INC I
          CMP I, NUMBR
          BGT $225
          ASL MASK(R5)
          LET MASK(R5) = MASK(R5) SHIFT 1
          LET MASK(R5) = MASK(R5) SET BY #1
          ENDINC
          BR $224
          LET MASK(R5) = COMP MASK(R5)
          COM MASK(R5)
          RETURN
          BR $221
          NUMBR 0
          ENDRTN
          RTS PC
  
```



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 3559  
 3560  
 3561  
 3562  
 3563  
 3564  
 3565  
 3566  
 3567  
 3568  
 3569  
 3570  
 3571  
 3572  
 3573  
 3574  
 3575  
 3576  
 3577  
 3578  
 3579

010572  
 010572  
  
 010572    010146  
 010574    010246  
 010576    010346  
 010600  
 010600    016501    000000  
 010604  
 010604    012702    000001  
 010610    000402  
  
 010612                    \$231  
 010612    062702    000001  
  
 010616                    \$230  
 010616    020201  
 010620    101010  
 010622  
 010622    005003  
 010624    000401  
  
 010626                    \$234  
 010626    005203  
  
 010630                    \$233  
 010630    020327    000100  
 010634    003001  
 010636  
 010636    000773  
  
 010640                    \$235  
 010640  
 010640    000764  
  
 010642                    \$232  
 010642    012603  
 010644    012602  
 010646    012601  
  
 010650  
 010650  
 010650  
 010650    000207

```

    , , *****
ROUTINE WAIT   <TIME>
WAIT
* ROUTINE WAIT
*   THIS ROUTINE IS USED TO DELAY EXECUTION OF THE
*   MAIN PROGRAM FOR A SPECIFIED AMOUNT OF TIME
*   THIS IS ACCOMPLISHED BY INCREMENTING A
*   REGISTER UP TO A LIMIT THE INNER LOOP IS SET
*   TO APPROXIMATE 1 MILLI SEC
    , , *****
    MOV     R1, -(SP)      , , PUSH R1 ON STACK
    MOV     R2, -(SP)      , , PUSH R2 ON STACK
    MOV     R3, -(SP)      , , PUSH R3 ON STACK
    LET R1 = TIME(R5)
    MOV     TIME(R5), R1
    INCRU R2 FROM #1 TO R1 BY #1
    MOV     #1, R2
    BR     $230
    ADD     #01, R2
    CMP     R2, R1
    BHI    $232
    INCR R3 FROM #0 TO #100 BY #1
    CLR     R3
    BR     $233
    BR     $234
    INC     R3
    BR     $233
    CMP     R3, #100
    BGT    $235
    ENDINC
    BR     $234
    BR     $235
    ENDINC
    BR     $231
    MOV     (SP)+, R3      , , POP STACK INTO R3
    MOV     (SP)+, R2      , , POP STACK INTO R2
    MOV     (SP)+, R1      , , POP STACK INTO R1
    ENDRTN
    $226
    $227
    RTS     PC
  
```

3580  
3581  
3582  
3583 010652  
3584  
3585  
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3589  
3590  
3591  
3592 010652  
3593 010652 005267 000002  
3594 010656  
3595 010656 000002  
3596 010660 000000

```
SBTTL INTSRV INTERRUPT SERVICE ROUTINE  
*****  
NTSRV  
* SERVICE ROUTINE INTSRV  
* THIS GLOBAL ROUTINE DOES NOTHING BUT INCREMENT  
* 'INTFLAG' EACH TIME IT IS CALLED IT ASSUMES  
* THAT THE MAIN CALLING ROUTINE WILL KNOW WHAT  
* TO LOOK FOR  
*****  
ADD 1 TO 'INTERRUPT OCCURED' FLAG  
LET INTFLAG = INTFLAG + #1  
NC INTFLAG  
ENDSRV THAT'S ALL  
RT  
NTFLAG 0
```

```

3597 010662          ROUTINE CYCLE
3598 010662          CYCLE
3599                , , *****
3600                , * ROUTINE      CYCLE
3601                , *          THIS ROUTINE CAUSES ADRS TO POINT TO THE
3602                , *          ADDRESS OF DLV11-F UNDER TEST, ADRS +2 TO
3603                , *          POINT TO THE VECTOR OF THE DLV11-F UNDER TEST
3604                , *          IT KEEPS TRACK OF THE CURRENT DEVICE AND BIT
3605                , *          MASKS THE CONSOLE IS TREATED SPECIAL BY THIS ROUTINE
3606                , *          IT IS ONLY TESTED ONCE IF UNDER APT IF NOT UNDER APT
3607                , *          ALL TESTS THAT REQUIRE THE MAINT BIT ARE NOT RUN
3608                , , *****
3609 010662          LET APTCON B= #FALSE , SET DEFAULT VALUE
3610 010662 112767 000000 000230      MOV B #FALSE, APTCON
3611 010670          LET CONSOLE B= #FALSE
3612 010670 112767 000000 000223      MOV B #FALSE, CONSOLE
3613 010676          REPEAT          , UNTIL BITMASK SET IN $DEV M
3614 010676          $240
3615 010676          IF BITMASK EQ #0 THEN
3616 010676 005767 000200      TST BITMASK
3617 010702 001027          BNE $241
3618 010704          IF INITFLAG EQ #1 THEN
3619 010704 026727 000174 000001      CMP IN TFLAG, #1
3620 010712 001003          BNE $242
3621 010714          LET INITFLAG = #0
3622 010714 005067 000164          CLR INITFLAG
3623 010720          ELSE
3624 010720 000403          BR $243
3625 010722          $242
3626 010722          CALL $EOP , AS A SUBROUTINE
3627 010722 004767 000370      JSR PC, $EOP
3628
3629 010726          SPECIALADDRESS , BECAUSE $EOP RETURNS AS A JUMP
3630 010726          LET RO = POP
3631 010726 012600          MOV (SP)+, RO
3632 010730          ENDIF
3633 010730          $243
3634 010730          LET BITMASK = #1
3635 010730 012767 000001 000144      MOV #1, BITMASK
3636 010736          LET $DEVCT = #1
3637 010736 012767 000001 170240      MOV #1, $DEVCT
3638 010744          LET ADDRESS = $BASE
3639 010744 016767 170300 000134      MOV $BASE, ADDRESS
3640 010752          LET VECTOR = $VECT1
3641 010752 016767 170266 000130      MOV $VECT1, VECTOR
3642 010760          ELSE
3643 010760 000410          BR $244
3644 010762          $241
3645 010762          LET R4 = #10
3646 010762 012704 000010      MOV #10, R4
3647 010766          LET BITMASK = BITMASK ROTATE 1
3648 010766 006167 000110      ROL B TMASK
3649 010772          LET ADDRESS = ADDRESS + R4
3650 010772 060467 000110      ADD R4, ADDRESS
3651 010776          LET VECTOR = VECTOR + R4
3652 010776 060467 000106      ADD R4, VECTOR
  
```

```

3653 011002                                ENDIF
3654 011002                                $244
3655 011002                                UNT L BITMASK SET N $DEVN
3656 011002 036767 000074 170242          BIT B TMASK, $DEVN
3657 011010 001732                          BEQ $240
3658 011012                                IF B TMASK EQ #BIT15 THEN
3659 011012 026727 000064 100000          CMP BITMASK, #B T15
3660 011020 001023                          BNE $245
3661 011022                                LET CONSOLE B= #TRUE
3662 011022 112767 000001 000071          MOVB #TRUE, CONSOLE
3663 011030                                LET ADDRESS = CONADR
3664 011030 016767 000060 000050          MOV CONADR, ADDRESS
3665 011036                                LET VECTOR = CONVECT
3666 011036 016767 000054 000044          MOV CONVECT, VECTOR
3667                                *****
3668                                *****
3669                                *****
3670 011044                                IF #CONMAINT NOTSETIN $USWR THEN
3671 011044 032767 000001 170142          BIT #APTENV, $ENV
3672 011052 001406                          BEQ $246
3673 011054                                IF $PASS NE #0 THEN , NOT FIRST PASS
3674 011054 005767 170122                  TST $PASS
3675 011060 001403                          BEQ $247
3676                                , DEFINE DEVICE AS APT CONSOLE
3677 011062                                LET APTCON B= #TRUE
3678 011062 112767 000001 000030          MOVB #TRUE, APTCON
3679 011070                                ENDIF , FIRST PASS
3680 011070                                $247
3681 011070                                ENDIF , APT
3682 011070                                $246
3683 011070                                ENDIF , BITMASK
3684 011070                                $245
3685
3686 011070                                LET ADRS = #ADDRESS
3687 011070 012701 011106                  MOV #ADDRESS, ADRS
3688 011074                                LET $DEVCT = $DEVCT + #1
3689 011074 005267 170104                  INC $DEVCT
3690 011100                                RETURN
3691 011100 000411                          BR $236
3692 011102 100000                          B TMASK 100000 , CONSOLE FIRST
3693 011104 000001                          NITFLAG 1
3694 011106 000000                          ADDRESS 0
3695 011110 000000                          VECTOR 0
3696 011112 000000                          OK 0
3697 011114 177560                          CONADR 177560 , CONSOLE ADDRESS
3698 011116 000060                          CONVECT 60 , CONSOLE VECTOR
3699 011120 000                                APTCON BYTE 0
3700 011121 000                                CONSOLE BYTE 0
3701 011122 000                                NOCONMANT BYTE 0
3702                                011124 EVEN
3703
3704                                $236
3705 011124                                $237
3706 011124
3707 011124 000207                          RTS PC
3708

```

MAINDEC-11-DVDUC-B      MACY11 30A(1052) 02-FEB-78 08 40 PAGE 85  
DVDUCB P11      02-FEB-78 08 39      ROUT NE - CYCLE

H 7

SEQ 0085

3709

```

3710
3711 011126          ROUT NE MYTYPE
3712 011126          MYTYPE
3713                ..*****
3714 011126 104401 011134          TYPE ,65$          ..TYPE ASCIZ STRING
3715 011132 000405          BR 64$          ..GET OVER THE ASCIZ
3716                ..65$  ASCIZ <CRLF>*TEST # *
3717 011146          64$
3718 011146 016746 170026          MOV $TESTN,-(SP)      ..SAVE $TESTN FOR TYPEOUT
3719 011152 104402          TYPOC          ..GO TYPE--OCTAL ASCII(ALL DIGITS)
3720 011154 104401 011162          TYPE ,67$          ..TYPE ASCIZ STRING
3721 011160 000405          BR 66$          ..GET OVER THE ASCIZ
3722                ..67$  ASC Z *.ERROR # *
3723 011174          66$
3724 011174 116767 167714 167774          MOVB $ITEMB,$FATAL  ..APT FATAL ERROR NUMBER
3725 011202 016746 167770          MOV $FATAL,-(SP)      ..SAVE $FATAL FOR TYPEOUT
3726 011206 104403          TYPOS          ..GO TYPE--OCTAL ASCII
3727 011210 006          BYTE 6          ..TYPE 6 DIGITS
3728 011211 000          BYTE 0          ..SUPPRESS LEADING ZEROS
3729 011212 104401 011220          TYPE ,69$          ..TYPE ASCIZ STRING
3730 011216 000404          BR 68$          ..GET OVER THE ASCIZ
3731                ..69$  ASCIZ *.PC = *
3732 011230          68$
3733 011230 016746 167662          MOV $ERRPC,-(SP)      ..SAVE $ERRPC FOR TYPEOUT
3734 011234 104402          TYPOC          ..GO TYPE--OCTAL ASCII(ALL DIGITS)
3735 011236 104401 011244          TYPE ,71$          ..TYPE ASCIZ STRING
3736 011242 000404          BR 70$          ..GET OVER THE ASCIZ
3737                ..71$  ASCIZ *.CSR *
3738 011254          70$
3739 011254 016746 167774          MOV DLADD,-(SP)      ..SAVE DLADD FOR TYPEOUT
3740 011260 104402          TYPOC          ..GO TYPE--OCTAL ASCII(ALL DIGITS)
3741 011262 104401 011270          TYPE ,73$          ..TYPE ASCIZ STRING
3742 011266 000405          BR 72$          ..GET OVER THE ASCIZ
3743                ..73$  ASCIZ *.VECTOR *
3744 011302          72$
3745 011302 016746 167750          MOV DLVEC,-(SP)      ..SAVE DLVEC FOR TYPEOUT
3746 011306 104402          TYPOC          ..GO TYPE--OCTAL ASCII(ALL DIGITS)
3747 011310 104401 001171          TYPE ,SCLF
3748 011314          ENDRTN
3749 011314          $250
3750 011314          $251
3751 011314 000207          RTS PC
  
```

```

3752          SBTTL  END OF PASS ROUTINE
3753
3754          ,, *****
3755          ,* INCREMENT THE PASS NUMBER ($PASS)
3756          ,* INDICATE END-OF-PROGRAM AFTER 1 PASSES THRU THE PROGRAM
3757          ,* TYPE "END PASS #XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)
3758          ,* IF THERES A MONITOR GO TO IT
3759          ,* IF THERE ISN'T JUMP TO SPECIALADDRESS
3760
3761          SEOP
3762          011316 000004          SCOPE
3763          011320 005067 167556  CLR          $STNM          .. ZERO THE TEST NUMBER
3764          011324 005067 167630  CLR          $TIMES          .. ZERO THE NUMBER OF ITERATIONS
3765          011330 005267 167646  INC          $PASS          .. INCREMENT THE PASS NUMBER
3766          011334 042767 100000 167640 BIC          #100000,$PASS .. DON'T ALLOW A NEG NUMBER
3767          011342 005327          DEC          (PC)+          .. LOOP?
3768          011344 000001          SEOPCT  WORD          1
3769          011346 003022          BGT          $DOAGN          .. YES
3770          011350 012737          MOV          (PC)+,@(PC)+ .. RESTORE COUNTER
3771          011352 000001          SENDCT  WORD          1
3772          011354 011344          SEOPCT  WORD
3773          011356 104401 011423  TYPE          ,SENDMG          .. TYPE "END PASS #"
3774          011362 016746 167614  MOV          $PASS,-(SP)      .. SAVE $PASS FOR TYPEOUT
3775          011366 104405          TYPDS          .. GO TYPE--DECIMAL ASCII WITH SIGN
3776          011370 104401 011420  TYPE          ,SENULL          .. TYPE A NULL CHARACTER
3777          011374 013700 000042  $GET42  MOV          @#42,RO    .. GET MONITOR ADDRESS
3778          011400 001405          BEQ          $DOAGN          .. BRANCH IF NO MONITOR
3779          011402 000005          RESET          .. CLEAR THE WORLD
3780          011404 004710          SENDAD  JSR          PC,(RO)   .. GO TO MONITOR
3781          011406 000240          NOP          .. SAVE ROOM
3782          011410 000240          NOP          .. FOR
3783          011412 000240          NOP          .. ACT11
3784          011414          $DOAGN
3785          011414 000137          JMP          @(PC)+          .. RETURN
3786          011416 010726          $RTNAD  WORD          SPECIALADDRESS
3787          011420 377 377 000  $ENULL  BYTE          -1,-1,0 .. NULL CHARACTER STR NG
3788          011423 015 042412 042116 $ENDMG  ASCIZ        <15><12>/END PASS #/
3789          011430 050040 051501 020123
3790          011436 000043
  
```

SBTTL POWER DOWN AND UP ROUTINES

```

3791
3792
3793      , , *****
3794      , POWER DOWN ROUTINE
3795 011440 012737 011604 000024 $PWRDN  MOV    $ILLUP, @PWRVEC  , , SET FOR FAST UP
3796 011446 012737 000340 000026      MOV    #340, @PWRVEC+2 , , PRIO 7
3797 011454 010046      MOV    R0, -(SP)      , , PUSH R0 ON STACK
3798 011456 010146      MOV    R1, -(SP)      , , PUSH R1 ON STACK
3799 011460 010246      MOV    R2, -(SP)      , , PUSH R2 ON STACK
3800 011462 010346      MOV    R3, -(SP)      , , PUSH R3 ON STACK
3801 011464 010446      MOV    R4, -(SP)      , , PUSH R4 ON STACK
3802 011466 010546      MOV    R5, -(SP)      , , PUSH R5 ON STACK
3803 011470 017746 167444      MOV    @SWR, -(SP)    , , PUSH @SWR ON STACK
3804 011474 010667 000110      MOV    SP, $SAVR6    , , SAVE SP
3805 011500 012737 011512 000024      MOV    $PWRUP, @PWRVEC , , SET UP VECTOR
3806 011506 000000      HALT
3807 011510 000776      BR     -2            , , HANG UP
3808
3809      , , *****
3810      , POWER UP ROUTINE
3811 011512 012737 011604 000024 $PWRUP  MOV    $ILLUP, @PWRVEC  , , SET FOR FAST DOWN
3812 011520 016706 000064      MOV    $SAVR6, SP    , , GET SP
3813 011524 005067 000060      CLR    $SAVR6        , , WAIT LOOP FOR THE TTY
3814 011530 005267 000054      15     INC    $SAVR6    , , WAIT FOR THE INC
3815 011534 001375      BNE    15            , , OF WORD
3816 011536 012677 167376      MOV    (SP)+, @SWR   , , POP STACK INTO @SWR
3817 011542 012605      MOV    (SP)+, R5     , , POP STACK INTO R5
3818 011544 012604      MOV    (SP)+, R4     , , POP STACK INTO R4
3819 011546 012603      MOV    (SP)+, R3     , , POP STACK INTO R3
3820 011550 012602      MC     (SP)+, R2     , , POP STACK INTO R2
3821 011552 012601      MOV    (SP)+, R1     , , POP STACK INTO R1
3822 011554 012600      MOV    (SP)+, R0     , , POP STACK INTO R0
3823 011556 012737 011440 000024      MOV    $PWRDN, @PWRVEC , , SET UP THE POWER DOWN VECTOR
3824 011564 012737 000340 000026      MOV    #340, @PWRVEC+2 , , PRIO 7
3825 011572 104401      TYPE   $POWER        , , REPORT THE POWER FAILURE
3826 011574 011612      $PWRMG WORD $POWER   , , POWER FAIL MESSAGE POINTER
3827 011576 012716      MOV    (PC)+, (SP)   , , RESTART AT START
3828 011600 001336      $PWRAD WORD START    , , RESTART ADDRESS
3829 011602 000002      RTI
3830 011604 000000      $ILLUP HALT          , , THE POWER UP SEQUENCE WAS STARTED
3831 011606 000776      BR     -2            , , BEFORE THE POWER DOWN WAS COMPLETE
3832 011610 000000      $SAVR6 0             , , PUT THE SP HEPE
3833 011612 005015 047520 042527 $POWER  ASCII <15><12>"POWER"
3834 011620 000122
3835      EVEN
  
```



SBTTL TYPE ROUTINE

```

3836
3837
3838
3839
3840
3841
3842
3843
3844
3845
3846
3847
3848
3849
3850
3851
3852
3853 011622 105767 167331
3854 011626 100002
3855 011630 000000
3856 011632 000430
3857 011634 010046
3858 011636 017600 000002
3859 011642 122767 000001 167344
3860 011650 001011
3861 011652 132767 000100 167335
3862 011660 001405
3863 011662 010067 000004
3864 011666 004767 000774
3865 011672 000000
3866 011674 132767 000040 167313
3867 011702 001003
3868 011704 112046
3869 011706 001005
3870 011710 005726
3871 011712 012600
3872 011714 062716 000002
3873 011720 000002
3874 011722 122716 000011
3875 011726 001430
3876 011730 122716 000200
3877 011734 001006
3878 011736 005726
3879 011740 104401
3880 011742 001171
3881 011744 105067 000130
3882 011750 000755
3883 011752 004767 000056
3884 011756 126726 167174
3885 011762 001350
3886 011764 016746 167164
3887
3888 011770 105366 000001
3889 011774 002770
3890 011776 004767 000032
3891 012002 105367 000072

*****
*ROUTINE TO TYPE ASCIZ MESSAGE MESSAGE MUST TERMINATE WITH A 0 BYTE
*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED
*NOTE1 $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER
*NOTE2 $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED
*NOTE3 $FILLC CONTAINS THE CHARACTER TO FILL AFTER
*
*CALL
*1) USING A TRAP INSTRUCTION
* TYPE ,MESADR ..MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
*OR
* TYPE
* MESADR
*
$TYPE TSTB $TFPLG .. IS THERE A TERMINAL?
BPL 1$ .. BR IF YES
HALT .. HALT HERE IF NO TERMINAL
BR 3$ .. LEAVE
MOV RO, -(SP) .. SAVE RO
MOV @2(SP), RO .. GET ADDRESS OF ASCIZ STRING
CMPB #APTENV, $ENV .. RUNNING IN APT MODE
BNE 62$ .. NO, GO CHECK FOR APT CONSOLE
BITB #APTPOOL, $ENV .. SPOOL MESSAGE TO APT
BEQ 62$ .. NO, GO CHECK FOR CONSOLE
MOV RO, 61$ .. SETUP MESSAGE ADDRESS FOR APT
JSR PC, $ATY3 .. SPOOL MESSAGE TO APT
WORD 0 .. MESSAGE ADDRESS
BITB #APTCSUP, $ENV .. APT CONSOLE SUPPRESSED
BNE 60$ .. YES, SKIP TYPE OUT
MOVB (RO)+, -(SP) .. PUSH CHARACTER TO BE TYPED ONTO STACK
BNE 4$ .. BR IF IT ISN'T THE TERMINATOR
TST (SP)+ .. IF TERMINATOR POP IT OFF THE STACK
MOV (SP)+, RO .. RESTORE RO
ADD #2, (SP) .. ADJUST RETURN PC
RTI .. RETURN
CMPB #HT (SP) .. BRANCH IF <HT>
BEQ 8$
CMPB #CRLF, (SP) .. BRANCH IF NOT <CRLF>
BNE 5$
TST (SP)+ .. POP <CR><LF> EQUIV
TYPE .. TYPE A CR AND LF
$CRLF
CLRB $CHARCNT .. CLEAR CHARACTER COUNT
BR 2$ .. GET NEXT CHARACTER
JSR PC, $TYPEC .. GO TYPE THIS CHARACTER
CMPB $F LLC, (SP)+ .. IS IT TIME FOR FILLER CHARS ?
BNE 2$ .. IF NO GO GET NEXT CHAR
MOV $NULL, -(SP) .. GET # OF FILLER CHARS NEEDED
.. AND THE NULL CHAR
DECB 1(SP) .. DOES A NULL NEED TO BE TYPED?
BLT 6$ .. BR IF NO--GO POP THE NULL OFF OF STACK
JSR PC, $TYPEC .. GO TYPE A NULL
DECB $CHARCNT .. DO NOT COUNT AS A COUNT
  
```

```

3892 012006 000770          BR      7$          ..LOOP
3893
3894          ,HORIZONTAL TAB PROCESSOR
3895
3896 012010 112716 000040      8$      MOVB   #' ,(SP)          ..REPLACE TAB WITH SPACE
3897 012014 004767 000014      9$      JSR    PC,$TYPEC          ..TYPE A SPACE
3898 012020 132767 000007 000052  BITB   #7,$CHARCNT          ..BRANCH IF NOT AT
3899 012026 001372          BNE    9$          ..TAB STOP
3900 012030 005726          TST   (SP)+          ..POP SPACE OFF STACK
3901 012032 000724          BR     2$          ..GET NEXT CHARACTER
3902 012034 105777 167110  $TYPEC TSTB   @5TPS          ..WAIT UNTIL PRINTER IS READY
3903 012040 100375          BPL   $TYPEC
3904 012042 116677 000002 167102  MOVB   2(SP),@5TPB          ..LOAD CHAR TO BE TYPED INTO DATA REG
3905 012050 122766 000015 000002  CMPB   #CR,2(SP)          ..IS CHARACTER A CARRIAGE RETURN?
3906 012056 001003          BNE   1$          ..BRANCH IF NO
3907 012060 105067 000014          CLRB  $CHARCNT          ..YES--CLEAR CHARACTER COUNT
3908 012064 000406          BR    $TYPEX          ..EXIT
3909 012066 122766 000012 000002 1$      CMPB   #LF,2(SP)          ..IS CHARACTER A LINE FEED?
3910 012074 001402          BEQ   $TYPEX          ..BRANCH IF YES
3911 012076 105227          INCB (PC)+          ..COUNT THE CHARACTER
3912 012100 000000          $CHARCNT WORD 0          ..CHARACTER COUNT STORAGE
3913 012102 000207          $TYPEX RTS   PC
3914
  
```

```
3915          SBTTL  TTY INPUT ROUTINE
3916
3917          .. *****
3918          ENABL  LSB
3919
3920          .. *****
3921          *SOFTWARE SWITCH REGISTER CHANGE ROUTINE
3922          *ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
3923          *SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP CALL
3924          *WHEN OPERATING IN TTY FLAG MODE
3925 012104 022767 000176 167026 SCKSWR  CMP      #SWREG, SWR      .. IS THE SOFT-SWR SELECTED?
3926 012112 001074           BNE      15$          .. BRANCH IF NO
3927 012114 105777 167024       TSTB    @STKS        .. CHAR THERE?
3928 012120 100071           BPL      15$          .. IF NO, DON'T WAIT AROUND
3929 012122 117746 167020       MOVB    @STKB, -(SP)  .. SAVE THE CHAR
3930 012126 042716 177600       BIC     # C177, (SP) .. STRIP-OFF THE ASCII
3931 012132 022726 000007       CMP     #7, (SP)+    .. IS IT A CONTROL G?
3932 012136 001062           BNE     15$          .. NO, RETURN TO USER
3933 012140 126727 166770 000001  CMPB    $AUTOB, #1   .. ARE WE RUNNING IN AUTO-MODE?
3934 012146 001456           BEQ     15$          .. BRANCH IF YES
3935
3936 012150 104401 012631           TYPE    , $CNTLG    .. ECHO THE CONTROL-G ( G )
3937 012154 104401 012636       SGTSWR  TYPE    , $MSWR  .. TYPE CURRENT CONTENTS
3938 012160 016746 166012       MOV     SWREG, -(SP) .. SAVE SWREG FOR TYPEOUT
3939 012164 104402           TYPOC   .. GO TYPE--OCTAL ASCII (ALL DIGITS)
3940 012166 104401 012647       TYPE    , $MNEW     .. PROMPT FOR NEW SWR
3941 012172 005046           CLR     -(SP)       .. CLEAR COUNTER
3942 012174 005046           CLR     -(SP)       .. THE NEW SWR
3943 012176 105777 166742       TSTB    @STKS        .. CHAR THERE?
3944 012202 100375           BPL     7$          .. IF NOT TRY AGAIN
3945
3946 012204 117746 166736       MOVB    @STKB, -(SP) .. P CK UP CHAR
3947 012210 042716 177600       BIC     # C177, (SP) .. MAKE IT 7-BIT ASCII
3948
3949
3950
3951 012214 021627 000025           9$     CMP     (SP), #25  .. IS IT A CONTROL-U?
3952 012220 001005           BNE     10$         .. BRANCH IF NOT
3953 012222 104401 012624       TYPE    , $CNTLU   .. YES, ECHO CONTROL-U ( U )
3954 012226 062706 000006           20$    ADD     #6, SP      .. IGNORE PREVIOUS INPUT
3955 012232 000757           BR      19$         .. LET'S TRY IT AGAIN
3956
3957
3958 012234 021627 000015           10$    CMP     (SP), #15   .. IS IT ^ <CR>?
3959 012240 001022           BNE     16$         .. BRANC - NO
3960 012242 005766 000004       TST     4(SP)       .. YES, IS IT THE FIRST CHAR?
3961 012246 001403           BEQ     11$         .. BRANCH IF YES
3962 012250 016677 000002 166662  MOV     2(SP), @SWR  .. SAVE NEW SWR
3963 012256 062706 000006           11$    ADD     #6, SP      .. CLEAR UP STACK
3964 012262 104401 001171           14$    TYPE    , $CRLF    .. ECHO <CR> AND <LF>
3965 012266 126727 166643 000001  CMPB    $INTAG, #1  .. RE-ENABLE TTY KBD INTERRUPTS?
3966 012274 001003           BNE     15$         .. BRANCH IF NOT
3967 012276 012777 000100 166640  MOV     #100, @STKS .. RE-ENABLE TTY KBD INTERRUPTS
3968 012304 000002           RTI                        .. RETURN
3969 012306 004767 177522           16$    JSR     PC, $TYPEC  .. ECHO CHAR
3970 012312 021627 000060       CMP     (SP), #60   .. CHAR < 0?
```

```

3971 012316 002420          BLT      18$          .. BRANCH IF YES
3972 012320 021627 000067    CMP      (SP), #67      .. CHAR > ??
3973 012324 003015          BGT      18$          .. BRANCH IF YES
3974 012326 042726 000060    BIC      #60, (SP)+    .. STRIP-OFF ASCII
3975 012332 005766 000002    TST      2(SP)         .. IS THIS THE FIRST CHAR
3976 012336 001403          BEQ      17$          .. BRANCH IF YES
3977 012340 006316          ASL      (SP)         .. NO, SHIFT PRESENT
3978 012342 006316          ASL      (SP)         .. CHAR OVER TO MAKE
3979 012344 006316          ASL      (SP)         .. ROOM FOR NEW ONE
3980 012346 005266 000002    17$ INC      2(SP)         .. KEEP COUNT OF CHAR
3981 012352 056616 177776    BIS      -2(SP), (SP) .. SET IN NEW CHAR
3982 012356 000707          BR       7$           .. GET THE NEXT ONE
3983 012360 104401 001170    18$ TYPE   , $QUES     .. TYPE ?(CR)(LF)
3984 012364 000720          BR       20$         .. SIMULATE CONTROL-U
3985          DSABL  LSB
3986
3987
3988          .. *****
3989          .. *THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
3990          .. *CALL
3991          .. *      RDCHR          .. INPUT A SINGLE CHARACTER FROM THE TTY
3992          .. *      RE JRN HERE   .. CHARACTER IS ON THE STACK
3993          .. *          .. WITH PARITY BIT STRIPPED OFF
3994          .. *
3995
3996 012366 011646          $RDCHR MOV      (SP), -(SP) .. PUSH DOWN THE PC
3997 012370 016666 000004 000002    MOV      4(SP), 2(SP) .. SAVE THE PS
3998 012376 105777 166542    1$ TSTB     @STKS        .. WAIT FOR
3999 012402 100375          BPL      1$           .. A CHARACTER
4000 012404 117766 166536 000004    MOVB     @STKB, 4(SP) .. READ THE TTY
4001 012412 042766 177600 000004    BIC      # (<177>, 4(SP)) .. GET RID OF JUNK IF ANY
4002 012420 026627 000004 000023    CMP      4(SP), #23   .. IS IT A CONTROL-S?
4003 012426 001013          BNE      3$           .. BRANCH IF NO
4004 012430 105777 166510    2$ TSTB     @STKS        .. WAIT FOR A CHARACTER
4005 012434 100375          BPL      2$           .. LOOP UNTIL ITS THERE
4006 012436 117746 166504    MOVB     @STKB, -(SP) .. GET CHARACTER
4007 012442 042716 177600          BIC      # (177), (SP) .. MAKE IT 7-BIT ASCII
4008 012446 022627 000021          CMP      (SP)+, #21   .. IS IT A CONTROL-Q?
4009 012452 001366          BNE      2$           .. IF NOT DISCARD IT
4010 012454 000750          BR       1$           .. YES, RESUME
4011 012456 026627 000004 000140    3$ CMP      4(SP), #140 .. IS IT UPPER CASE?
4012 012464 002407          BLT      4$           .. BRANCH IF YES
4013 012466 026627 000004 000175    CMP      4(SP), #175 .. IS IT A SPECIAL CHAR?
4014 012474 003003          BGT      4$           .. BRANCH IF YES
4015 012476 042766 000040 000004    BIC      #40, 4(SP)   .. MAKE IT UPPER CASE
4016 012504 000002    4$ RTI          .. GO BACK TO USER
4017          .. *****
4018          .. *THIS ROUTINE WILL INPUT A STRING FROM THE TTY
4019          .. *CALL
4020          .. *      RDLIN          .. INPUT A STRING FROM THE TTY
4021          .. *      RETURN HERE   .. ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
4022          .. *          .. TERMINATOR WILL BE A BYTE OF ALL 0'S
4023
4024 012506 010346          $RDLIN MOV      R3, -(SP) .. SAVE R3
4025 012510 012703 012614    1$ MOV      #$TTYIN, R3 .. GET ADDRESS
4026 012514 022703 012624    2$ CMP      #$TTYIN+8, R3 .. BUFFER FULL?

```

4027	012520	101405				BLOS	4%		.. BR IF YES
4028	012522	104410				RDCHR			.. GO READ ONE CHARACTER FROM THE TTY
4029	012524	112613				MOVB	(SP)+, (R3)		.. GET CHARACTER
4030	012526	122713	000177		10%	CMPB	#177, (R3)		.. IS IT A RUBOUT
4031	012532	001003				BNE	3%		.. SKIP IF NOT
4032	012534	104401	001170			TYPE	, \$QUES		.. TYPE A '?'
4033	012540	000763				BR	1%		.. CLEAR THE BUFFER AND LOOP
4034	012542	111367	000044			MOVB	(R3), 9%		.. ECHO THE CHARACTER
4035	012546	104401	012612			TYPE	, 9%		
4036	012552	122723	000015			CMPB	#15, (R3)+		.. CHECK FOR RETURN
4037	012556	001356				BNE	2%		.. LOOP IF NOT RETURN
4038	012560	105063	177777			CLRB	-1(R3)		.. CLEAR RETURN (THE 15)
4039	012564	104401	001172			TYPE	, \$LF		.. TYPE A LINE FEED
4040	012570	012603				MOV	(SP)+, R3		.. RESTORE R3
4041	012572	011646				MOV	(SP), -(SP)		.. ADJUST THE STACK AND PUT ADDRESS OF THE
4042	012574	016666	000004	000002		MOV	4(SP), 2(SP)		.. FIRST ASCII CHARACTER ON IT
4043	012602	012766	012614	000004		MOV	#\$TTYIN, 4(SP)		
4044	012610	000002				RTI			.. RETURN
4045	012612	000			9%	BYTE	0		.. STORAGE FOR ASCII CHAR TO TYPE
4046	012613	000				BYTE	0		.. TERMINATOR
4047	012614	000010				\$TTYIN	BLKB	8	.. RESERVE 8 BYTES FOR TTY INPUT
4048	012624	052536	005015	000		\$CNTLU	ASCIZ	/ U/<15><12>	.. CONTROL "U"
4049	012631	136	006507	000012		\$CNTLG	ASCIZ	/ G/<15><12>	.. CONTROL "G"
4050	012636	005015	053523	020122		\$MSWR	ASCIZ	<15><12>/SWR = /	
4051	012644	020075	000						
4052	012647	040	047040	053505		\$MNEW	ASCIZ	/ NEW = /	
4053	012654	036440	000040						

SBTTL APT COMMUNICATIONS ROUTINE

```

4054
4055
4056
4057 012660 112767 000001 000236 SATY1 MOVB #1,$FFLG // TO REPORT FATAL ERROR
4058 012666 112767 000001 000226 SATY3 MOVB #1,$MFLG // TO TYPE A MESSAGE
4059 012674 00040J BR SATYC
4060 012676 112767 000001 000220 SATY4 MOVB #1,$FFLG // TO ONLY REPORT FATAL ERROR
4061 012704 SATYC
4062 012704 010046 MOV RD,-(SP) // PUSH RD ON STACK
4063 012706 010146 MOV R1,-(SP) // PUSH R1 ON STACK
4064 012710 105767 000206 TSTB $MFLG // SHOULD TYPE A MESSAGE?
4065 012714 001450 BEQ 5$ // IF NOT BR
4066 012716 122767 000001 166270 CMPB #APTENV,$ENV // OPERATING UNDER APT?
4067 012724 001031 BNE 3$ // IF NOT BR
4068 012726 132767 000100 166261 BITB #APTSPOOL,$ENVM // SHOULD SPOOL MESSAGES?
4069 012734 001425 BEQ 3$ // IF NOT BR
4070 012736 017600 000004 MOV @4(SP),RD // GET MESSAGE ADDR
4071 012742 062766 000002 000004 ADD #2,4(SP) // BUMP RETURN ADDR
4072 012750 005767 166220 1$ TST $MSGTYPE // SEE IF DONE W/ LAST XMISSION?
4073 012754 001375 BNE 1$ // IF NOT WAIT
4074 012756 010067 166226 MOV RD,$MSGAD // PUT ADDR IN MAILBOX
4075 012762 105720 2$ TSTB (RD)+ // FIND END OF MESSAGE
4076 012764 001376 BNE 2$
4077 012766 166700 166216 SUB $MSGAD,RD // SUB START OF MESSAGE
4078 012772 006200 ASP RD // GET MESSAGE LNGTH IN WORDS
4079 012774 010067 166212 MOV RD,$MSGLGT // PUT LENGTH IN MAILBOX
4080 013000 012767 000004 166166 MOV #4,$MSGTYPE // TELL APT TO TAKE MSG
4081 013006 000413 BR 5$
4082 013010 017667 000004 000016 3$ MOV @4(SP),4$ // PUT MSG ADDR IN JSR L NKAGE
4083 013016 062766 000002 000004 ADD #2,4(SP) // BUMP RETURN ADDRESS
4084 013024 016746 164746 MOV 177776,-(SP) // PUSH 177776 ON STACK
4085 013030 004767 176566 JSR PC,$TYPE // CALL TYPE MACRO
4086 013034 000000 4$ WORD 0
4087 013036 5$
4088 013036 105767 000062 10$ TSTB $FFLG // SHOULD REPORT FATAL ERROR?
4089 013042 001416 BEQ 12$ // IF NOT BR
4090 013044 005767 166144 TST $ENV // RUNNING UNDER APT?
4091 013050 001413 BEQ 12$ // IF NOT BR
4092 013052 005767 166116 11$ TST $MSGTYPE // FINISHED LAST MESSAGE?
4093 013056 001375 BNE 11$ // IF NOT WAIT
4094 013060 017667 000004 166110 MOV @4(SP),$FATAL // GET ERROR #
4095 013066 062766 000002 000004 ADD #2,4(SP) // BUMP RETURN ADDR
4096 013074 005267 166074 INC $MSGTYPE // TELL APT TO TAKE ERROR
4097 013100 105067 000020 12$ CLRB $FFLG // CLEAR FATAL FLAG
4098 013104 105067 000013 CLRB $LFLG // CLEAR LOG FLAG
4099 013110 105067 000006 CLRB $MFLG // CLEAR MESSAGE FLAG
4100 013114 012601 MOV (SP)+,R1 // POP STACK INTO R1
4101 013116 012600 MOV (SP)+,RO // POP STACK INTO RO
4102 013120 000207 RTS PC // RETURN
4103 013122 000 $MFLG BYTE 0 // MESSG FLAG
4104 013123 000 $LFLG BYTE 0 // LOG FLAG
4105 013124 000 $FFLG BYTE 0 // FATAL FLAG
4106 013126 EVEN
4107 000200 APTSIZE=200
4108 000001 APTENV=001
4109 000100 APTSPOOL=100
  
```

MAINDEC-11-DVDVC-B MACY11 30A(1052) 02-FEB-78 08 40 PAGE 95 E 8  
DVDVCB P11 02-FEB-78 08 39 APT COMMUNICATIONS ROUT NE

SEQ 0095

4110

000040

APTC SUP=040

```
4111          SBTTL  ERROR HANDLER ROUTINE
4112
4113          ,*****
4114          ,*THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
4115          ,*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
4116          ,*AND GO TO MYTYPE ON ERROR
4117          ,*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE
4118          ,*SW15=1      HALT ON ERROR
4119          ,*SW13=1      INHIBIT ERROR TYPEOUTS
4120          ,*SW10=1      BELL ON ERROR
4121          ,*SW09=1      LOOP ON ERROR
4122          ,*CALL
4123          ,*      ERROR      N      , , ERROR=EMT AND N=ERROR ITEM NUMBER
4124
4125          SERROR
4126          013126      104407      CKSWR      , , TEST FOR CHANGE IN SOFT-SWR
4127          013130      105267      165747      7$      INCB      $ERFLG      , , SET THE ERROR FLAG
4128          013134      001775      BEQ      7$      , , DON'T LET THE FLAG GO TO ZERO
4129          013136      016777      165740      165776      MOV      $TSTNM, @DISPLAY      , , DISPLAY TEST NUMBER AND ERROR FLAG
4130          013144      032777      002000      165766      BIT      #BIT10, @SWR      , , BELL ON ERROR?
4131          013152      001402      BEQ      1$      , , NO - SKIP
4132          013154      104401      001164      TYPE      , $BELL      , , RING BELL
4133          013160      005267      165726      1$      INC      $ERTTL      , , COUNT THE NUMBER OF ERRORS
4134          013164      011667      165726      MOV      (SP), $ERRPC      , , GET ADDRESS OF ERROR INSTRUCTION
4135          013170      162767      000002      165720      SUB      #2, $ERRPC
4136          013176      117767      165714      165710      MOVB      @ $ERRPC, $ITEMB      , , STRIP AND SAVE THE ERROR ITEM CODE
4137          013204      032777      020000      165726      BIT      #BIT13, @SWR      , , SKIP TYPEOUT IF SET
4138          013212      001004      BNE      20$      , , SKIP TYPEOUTS
4139          013214      004767      175706      JSR      PC, MYTYPE      , , GO TO USER ERROR ROUTINE
4140          013220      104401      001171      TYPE      , $CRLF
4141          013224      20$
4142          013224      122767      000001      165762      CMPB      #APTENV, $ENV      , , RUNNING IN APT MODE
4143          013232      001007      BNE      2$      , , NO, SKIP APT ERROR REPORT
4144          013234      116767      165654      000004      MOVB      $ITEMB, 21$      , , SET ITEM NUMBER AS ERROR NUMBER
4145          013242      004767      177430      JSR      PC, $ATY4      , , REPORT FATAL ERROR TO APT
4146          013246      000      21$      BYTE      0
4147          013247      000      BYTE      0
4148          013250      000777      22$      BR      22$      , , APT ERROR LOOP
4149          013252      005777      165662      2$      TST      @SWR      , , HALT ON ERROR
4150          013256      100002      BPL      3$      , , SKIP IF CONTINUE
4151          013260      000000      HALT      , , HALT ON ERROR!
4152          013262      104407      CKSWR      , , TEST FOR CHANGE IN SOFT-SWR
4153          013264      032777      001000      165646      3$      BIT      #BIT09, @SWR      , , LOOP ON ERROR SWITCH SET?
4154          013272      001402      BEQ      4$      , , BR IF NO
4155          013274      016716      165610      MOV      $LPERR, (SP)      , , FUDGE RETURN FOR LOOPING
4156          013300      005767      165656      4$      TST      $ESCAPE      , , CHECK FOR AN ESCAPE ADDRESS
4157          013304      001402      BEQ      5$      , , BR IF NONE
4158          013306      016716      165650      MOV      $ESCAPE, (SP)      , , FUDGE RETURN ADDRESS FOR ESCAPE
4159          013312      5$
4160          013312      022737      011404      000042      CMP      #$ENDAD, @#42      , , ACT-11 AUTO-ACCEPT?
4161          013320      001001      BNE      6$      , , BRANCH IF NO
4162          013322      000000      HALT      , , YES
4163          013324      6$
4164          013324      000002      RTI      , , RETURN
```



```

4165          SBTTL  SCOPE HANDLER ROUTINE
4166
4167          , *****
4168          , *THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS IT WILL INCREMENT
4169          , *AND LOAD THE TEST NUMBER($TSTNM) INTO THE DISPLAY REG (DISPLAY<7 0>)
4170          , *AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15 08>
4171          , *THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE
4172          , *SW14=1      LOOP ON TEST
4173          , *SW11=1      INHIBIT ITERATIONS
4174          , *SW09=1      LOOP ON ERROR
4175          , *SW08=1      LOOP ON TEST IN SWR<7 0>
4176          , *CALL
4177          , *          SCOPE          , ,SCOPE=10T
4178
4179          $SCOPE
4180          013326      104407      CKSWR          , ,TEST FOR CHANGE IN SOFT-SWR
4181          013330      032777      040000 165602 1$      BIT      #BIT14, @SWR      , ,LOOP ON PRESENT TEST?
4182          013336      001114      BNE          $OVER          , ,YES IF SW14=1
4183          , *****START OF CODE FOR THE XOR TESTER*****
4184          013340      000416      SXTSTR BR      6$          , ,IF RUNNING ON THE "XOR" TESTER CHANGE
4185          , ,THIS INSTRUCTION TO A "NOP" (NOP=240)
4186          013342      013746      000004      MOV      @#ERRVEC, -(SP)      , ,SAVE THE CONTENTS OF THE ERROR VECTOR
4187          013346      012737      013366 000004      MOV      #5$, @#ERRVEC      , ,SET FOR TIMEOUT
4188          013354      005737      177060      TST      @#177060          , ,TIME OUT ON XOR?
4189          013360      012637      000004      MOV      (SP)+, @#ERRVEC      , ,RESTORE THE ERROR VECTOR
4190          013364      000463      BR          $$VLAD          , ,GO TO THE NEXT TEST
4191          013366      022626      5$      CMP      (SP)+, (SP)+          , ,CLEAR THE STACK AFTER A TIME OUT
4192          013370      012637      000004      MOV      (SP)+, @#ERRVEC      , ,RESTORE THE ERROR VECTOR
4193          013374      000423      BR          7$          , ,LOOP ON THE PRESENT TEST
4194          013376      6$ , *****END OF CODE FOR THE XOR TESTER*****
4195          013376      032777      000400 165534      BIT      #BIT08, @SWR      , ,LOOP ON SPEC TEST?
4196          013404      001404      BEQ      2$          , ,BR IF NO
4197          013406      127767      165526 165466      CMPB     @SWR, $TSTNM      , ,ON THE RIGHT TEST? SWR<7 0>
4198          013414      001465      BEQ      $OVER          , ,BR IF YES
4199          013416      105767      165461      2$      TSTB     $ERFLG          , ,HAS AN ERROR OCCURRED?
4200          013422      001421      BEQ      3$          , ,BR IF NO
4201          013424      126767      165465 165451      CMPB     $ERMAX, $ERFLG      , ,MAX ERRORS FOR THIS TEST OCCURRED?
4202          013432      101015      BH       3$          , ,BR IF NO
4203          013434      032777      001000 165476      BIT      #BIT09, @SWR      , ,LOOP ON ERROR?
4204          013442      001404      BEQ      4$          , ,BR IF NO
4205          013444      016767      165440 165434      7$      MOV      $LPERR, $LPADR      , ,SET LOOP ADDRESS TO LAST SCOPE
4206          013452      000446      BR          $OVER          , ,
4207          013454      105067      165423      4$      CLRB     $ERFLG          , ,ZERO THE ERROR FLAG
4208          013460      005067      165474      CLR      $T MES          , ,CLEAR THE NUMBER OF ITERATIONS TO MAKE
4209          013464      000415      BR          1$          , ,ESCAPE TO THE NEXT TEST
4210          013466      032777      004000 165444      3$      BIT      #BIT11, @SWR      , ,INHIBIT ITERATIONS?
4211          013474      001011      BNE      1$          , ,BR IF YES
4212          013476      005767      165500      TST      $PASS          , ,IF FIRST PASS OF PROGRAM
4213          013502      001406      BEQ      1$          , ,INHIBIT ITERATIONS
4214          013504      005267      165374      NC       $ICNT          , ,INCREMENT ITERATION COUNT
4215          013510      026767      165444 165366      CMP      $TIMES, $ICNT      , ,CHECK THE NUMBER OF ITERATIONS MADE
4216          013516      002024      BGE      $OVER          , ,BR IF MORE ITERATION REQUIRED
4217          013520      012767      000001 165356      1$      MOV      #1, $ICNT          , ,REINITIALIZE THE ITERATION COUNTER
4218          013526      016767      000052 165424      MOV      $MXCNT, $TIMES      , ,SET NUMBER OF ITERATIONS TO DO
4219          013534      105267      165342      $$VLAD  INCB     $TSTNM          , ,COUNT TEST NUMBERS
4220          013540      116767      165336 165432      MOVB     $TSTNM, $TESTN      , ,SET TEST NUMBER N APT MA LBOX
    
```

```
4221 013546 011667 165334      MOV      (SP), $LPADR      // SAVE SCOPE LOOP ADDRESS
4222 013552 011667 165332      MOV      (SP), $LPERR     // SAVE ERROR LOOP ADDRESS
4223 013556 005067 165400      CLR      $ESCAPE         // CLEAR THE ESCAPE FROM ERROR ADDRESS
4224 013562 112767 000001 165325  MOVB     #1, $ERMAX       // ONLY ALLOW ONE(1) ERROR ON NEXT TEST
4225 013570 016777 165306 165344 $OVER    MOV      $TSTNM, @D SPLAY // DISPLAY TEST NUMBER
4226 013576 016716 165304      MOV      $LPADR, (SP)    // FUDGE RETURN ADDRESS
4227 013602 000002      RTI                       // F XES PS
4228 013604 003720      SMXCNT 2000             // MAX NUMBER OF ITERATIONS
```

```

4229          SBTTL  CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
4230
4231          ,*****
4232          ,*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
4233          ,*SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT  DEPENDING ON WHETHER THE
4234          ,*NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
4235          ,*BEFORE THE FIRST DIGIT OF THE NUMBER  LEADING ZEROS WILL ALWAYS BE
4236          ,*REPLACED WITH SPACES
4237          ,*CALL
4238          ,*      MOV      NUM, -(SP)      ,.PUT THE BINARY NUMBER ON THE STACK
4239          ,*      TYPDS      ,.GO TO THE ROUTINE
4240
4241          STYPDS
4242          013606      010046      MOV      R0, -(SP)      ,.PUSH R0 ON STACK
4243          013610      010146      MOV      R1, -(SP)      ,.PUSH R1 ON STACK
4244          013612      010246      MOV      R2, -(SP)      ,.PUSH R2 ON STACK
4245          013614      010346      MOV      R3, -(SP)      ,.PUSH R3 ON STACK
4246          013616      010546      MOV      R5, -(SP)      ,.PUSH R5 ON STACK
4247          013620      012746      020200      MOV      #20200, -(SP)  ,.SET BLANK SWITCH AND SIGN
4248          013624      016605      000020      MOV      20(SP), R5    ,.GET THE INPUT NUMBER
4249          013630      100004      BPL      1$           ,.BR IF INPUT IS POS
4250          013632      005405      R5           ,.MAKE THE BINARY NUMBER POS
4251          013634      112766      000055      000001      MOV      #'-, 1(SP)    ,.MAKE THE ASCII NUMBER NEG
4252          013642      005000      1$           CLR      R0           ,.ZERO THE CONSTANTS INDEX
4253          013644      012703      014022      MOV      #SDBLK, R3    ,.SETUP THE OUTPUT POINTER
4254          013650      112723      000040      MOV      #' , (R3)+    ,.SET THE FIRST CHARACTER TO A BLANK
4255          013654      005002      2$           CLR      R2           ,.CLEAR THE BCD NUMBER
4256          013656      016001      014012      MOV      $DTBL(R0), R1 ,.GET THE CONSTANT
4257          013662      160105      3$           SUB      R1, R5        ,.FORM THIS BCD DIGIT
4258          013664      002402      BLT      4$           ,.BR  F DONE
4259          013666      005202      INC      R2           ,.INCREASE THE BCD DIGIT BY 1
4260          013670      000774      BR       3$
4261          013672      060105      4$           ADD      R1, R5        ,.ADD BACK THE CONSTANT
4262          013674      005702      TST      R2           ,.CHECK IF BCD DIGIT=0
4263          013676      001002      BNE      5$           ,.FALL THROUGH IF 0
4264          013700      105716      TSTB    (SP)         ,.STILL DOING LEADING 0'S?
4265          013702      100407      BM       7$           ,.BR IF YES
4266          013704      106316      5$           ASLB    (SP)         ,.MSD?
4267          013706      103003      BCC      6$           ,.BR IF NO
4268          013710      116663      000001      177777      MOV      1(SP), -1(R3) ,.YES--SET THE SIGN
4269          013716      052702      000060      6$           BIS      #'0, R2      ,.MAKE THE BCD DIGIT ASCII
4270          013722      052702      000040      7$           BIS      #' , R2      ,.MAKE IT A SPACE IF NOT ALREADY A DIG T
4271          013726      110223      MOV      R2, (R3)+    ,.PUT THIS CHARACTER IN THE OUTPUT BUFFER
4272          013730      005720      TST      (R0)+        ,.JUST INCREMENTING
4273          013732      020027      000010      CMP      R0, #10      ,.CHECK THE TABLE INDEX
4274          013736      002746      BLT      2$           ,.GO DO THE NEXT DIGIT
4275          013740      003002      BGT      8$           ,.GO TO EXIT
4276          013742      010502      MOV      R5, R2       ,.GET THE LSD
4277          013744      000764      BR       6$           ,.GO CHANGE TO ASCII
4278          013746      105726      8$           TSTB    (SP)+        ,.WAS THE LSD THE FIRST NON-ZERO?
4279          013750      100003      BPL      9$           ,.BR IF NO
4280          013752      116663      177777      177776      MOV      -1(SP), -2(R3) ,.YES--SET THE SIGN FOR TYP NG
4281          013760      105013      9$           CLRB    (R3)         ,.SET THE TERMINATOR
4282          013762      012605      MOV      (SP)+, R5    ,.POP STACK INTO R5
4283          013764      012603      MOV      (SP)+, R3    ,.POP STACK INTO R3
4284          013766      012602      MOV      (SP)+, R2    ,.POP STACK INTO R2
  
```

4285	013770	012601			MOV	(SP)+,R1		..POP STACK INTO R1
4286	013772	012600			MOV	(SP)+,R0		..POP STACK INTO R0
4287	013774	104401	014022		TYPE	,SDBLK		..NOW TYPE THE NUMBER
4288	014000	016666	000002	000004	MOV	2(SP),4(SP)		..ADJUST THE STACK
4289	014006	012616			MOV	(SP)+,(SP)		
4290	014010	000002			RTI			..RETURN TO USER
4291	014012	023420		SDBLK	10000			
4292	014014	001750			1000			
4293	014016	000144			100			
4294	014020	000012			10			
4295	014022	000004		SDBLK	BLKW	4		

```

4296          SBTTL  BINARY TO OCTAL (ASCII) AND TYPE
4297
4298          ; *****
4299          ; THIS ROUTINE IS USED TO CHANGE A 16-BIT B NARY NUMBER TO A 6-DIG T
4300          ; OCTAL (ASCII) NUMBER AND TYPE IT
4301          ; $TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
4302          ; $CALL
4303          ; *      MOV      NUM, -(SP)          ; NUMBER TO BE TYPED
4304          ; *      TYPOS          ; CALL FOR TYPEOUT
4305          ; *      BYTE  N          ; N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
4306          ; *      BYTE  M          ; M=1 OR 0
4307          ; *                                  ; 1=TYPE LEADING ZEROS
4308          ; *                                  ; 0=SUPPRESS LEADING ZEROS
4309          ; *
4310          ; $STYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
4311          ; $TYPOS OR $TYPOC
4312          ; $CALL
4313          ; *      MOV      NUM, -(SP)          ; NUMBER TO BE TYPED
4314          ; *      TYPON          ; CALL FOR TYPEOUT
4315          ; *
4316          ; $STYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
4317          ; $CALL
4318          ; *      MOV      NUM, -(SP)          ; NUMBER TO BE TYPED
4319          ; *      TYPOC          ; CALL FOR TYPEOUT
4320
4321 014032 017646 000000          $TYPOS MOV      @ (SP), -(SP)          ; PICKUP THE MODE
4322 014036 116667 000001 000211 MOV      1 (SP), $OFILL          ; LOAD ZERO FILL SWITCH
4323 014044 112667 000207          MOV      (SP)+, $OMODE+1          ; NUMBER OF DIGITS TO TYPE
4324 014050 062716 000002          ADD      #2, (SP)          ; ADJUST RETURN ADDRESS
4325 014054 000406          BR      $TYPON
4326 014056 112767 000001 000171 $TYPOC MOV      #1, $OFILL          ; SET THE ZERO FILL SWITCH
4327 014064 112767 000006 000165 MOV      #6, $OMODE+1          ; SET FOR SIX(6) DIGITS
4328 014072 112767 000005 000154 $STYPON MOV      #5, $OCNT          ; SET THE ITERATION COUNT
4329 014100 010346          MOV      R3, -(SP)          ; SAVE R3
4330 014102 010446          MOV      R4, -(SP)          ; SAVE R4
4331 014104 010546          MOV      R5, -(SP)          ; SAVE R5
4332 014106 116704 000145          MOV      $OMODE+1, R4          ; GET THE NUMBER OF DIGITS TO TYPE
4333 014112 005404          NEG      R4
4334 014114 062704 000006          ADD      #6, R4          ; SUBTRACT IT FOR MAX ALLOWED
4335 014120 110467 000132          MOV      R4, $OMODE          ; SAVE IT FOR USE
4336 014124 116704 000125          MOV      $OFILL, R4          ; GET THE ZERO FILL SWITCH
4337 014130 016605 000012          MOV      12(SP), R5          ; PICKUP THE INPUT NUMBER
4338 014134 005003          CLR      R3          ; CLEAR THE OUTPUT WORD
4339 014136 006105          1$     ROL      R5          ; ROTATE MSB INTO "C"
4340 014140 000404          BR      3$
4341 014142 006105          2$     ROL      R5          ; GO DO MSB
4342 014144 006105          ROL      R5          ; FORM THIS DIGIT
4343 014146 006105          ROL      R5
4344 014150 010503          MOV      R5, R3
4345 014152 006103          3$     ROL      R3          ; GET LSB OF THIS DIGIT
4346 014154 105367 000076          DECB    $OMODE          ; TYPE THIS DIGIT?
4347 014160 100016          BPL     7$          ; BR IF NO
4348 014162 042703 177770          BIC     #177770, R3          ; GET RID OF JUNK
4349 014166 001002          BNE     4$          ; TEST FOR 0
4350 014170 005704          TST     R4          ; SUPPRESS THIS 0?
4351 014172 001403          BEQ     5$          ; BR IF YES
  
```

4352	014174	005204		45	INC	R4	.. DON'T SUPPRESS ANYMORE 0'S
4353	014176	052703	000060		B S	#'0,R3	.. MAKE THIS DIGIT ASCII
4354	014202	052703	000040	55	B S	#',R3	.. MAKE ASCII IF NOT ALREADY
4355	014206	110367	000040		MOVB	R3,85	.. SAVE FOR TYPING
4356	014212	104401	014252		TYPE	,85	.. GO TYPE THIS DIGIT
4357	014216	105367	000032	75	DECB	%OCNT	.. COUNT BY 1
4358	014222	003347			BGT	25	.. BR IF MORE TO DO
4359	014224	002402			BLT	65	.. BR IF DONE
4360	014226	005204			INC	R4	.. INSURE LAST DIGIT ISN'T A BLANK
4361	014230	000744			BR	25	.. GO DO THE LAST DIGIT
4362	014232	012605		65	MOV	(SP)+,R5	.. RESTORE R5
4363	014234	012604			MOV	(SP)+,R4	.. RESTORE R4
4364	014236	012603			MOV	(SP)+,R3	.. RESTORE R3
4365	014240	016666	000002 000004		MOV	2(SP),4(SP)	.. SET THE STACK FOR RETURNING
4366	014246	012616			MOV	(SP)+,(SP)	
4367	014250	000002			RTI		.. RETURN
4368	014252	000		85	BYTE	0	.. STORAGE FOR ASCII DIGIT
4369	014253	000			BYTE	0	.. TERMINATOR FOR TYPE ROUTINE
4370	014254	000			%OCNT	0	.. OCTAL DIGIT COUNTER
4371	014255	000			%OFILL	0	.. ZERO FILL SWITCH
4372	014256	000000			%OMODE	0	.. NUMBER OF DIGITS TO TYPE

4373 SBTTL TRAP DECODER  
 4374  
 4375 ,, \*\*\*\*\*  
 4376 , \*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION  
 4377 , \*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS  
 4378 , \*OF THE DESIRED ROUTINE THEN USING THE ADDRESS OBTAINED IT WILL  
 4379 , \*GO TO THAT ROUTINE

4380  
 4381 014260 010046 \$TRAP MOV RO, -(SP) ,, SAVE RO  
 4382 014262 016600 000002 MOV 2(SP), RO ,, GET TRAP ADDRESS  
 4383 014266 005740 TST -(RO) ,, BACKUP BY 2  
 4384 014270 111000 MOVB (RO), RO ,, GET RIGHT BYTE OF TRAP  
 4385 014272 006300 ASL RO ,, POSITION FOR INDEXING  
 4386 014274 016000 014314 MOV \$TRPAD(RO), RO ,, IF EX TO TABLE  
 4387 014300 000200 RTS RO ,, GO TO ROUTINE

4388  
 4389  
 4390 ,, THIS IS USE TO HANDLE THE "GETPRI" MACRO

4391  
 4392 014302 011646 \$TRAP2 MOV (SP), -(SP) ,, MOVE THE PC DOWN  
 4393 014304 016666 000004 000002 MOV 4(SP), 2(SP) ,, MOVE THE PSW DOWN  
 4394 014312 000002 RTI ,, RESTORE THE PSW

4395  
 4396 SBTTL TRAP TABLE

4397  
 4398 , \*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED  
 4399 , \*BY THE "TRAP" INSTRUCTION

4400  
 4401 ROUTINE  
 4402 -----  
 4403 014314 014302 \$TRPAD WORD \$TRAP2  
 4404 014316 011622 \$TYPE ,, CALL=TYPE TRAP+1(104401) TTY TYPEOUT ROUTINE  
 4405 014320 014056 \$TYPOC ,, CALL=TYPOC TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS)  
 4406 014322 014032 \$TYPOS ,, CALL=TYPOS TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS)  
 4407 014324 014072 \$TYPON ,, CALL=TYPON TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL)  
 4408 014326 013606 \$TYPDS ,, CALL=TYPDS TRAP+5(104405) TYPE DECIMAL NUMBER (WITH SIGN)

4409  
 4410 014330 012154 \$GTSWR ,, CALL=GTSWR TRAP+6(104406) GET SOFT-SWR SETTING  
 4411  
 4412 014332 012104 \$CKSWR ,, CALL=CKSWR TRAP+7(104407) TEST FOR CHANGE IN SOFT-SWR  
 4413 014334 012366 \$RDCHR ,, CALL=RDCHR TRAP+10(104410) TTY TYPEIN CHARACTER ROUTINE  
 4414 014336 012506 \$RDLIN ,, CALL=RDLIN TRAP+11(104411) TTY TYPEIN STRING ROUTINE  
 4415 000001

END

ABASE = 175610	1#	956	997								
ACDW1 = 000000	956										
ACDW2 = 000000	956										
ACPUOP= 000000	956	971									
ADDRES 011106	3639*	3650*	3664*	3687	3694*						
ADDW0 = 000000	956										
ADDW1 = 000000	956										
ADDW10= 000000	956										
ADDW11= 000000	956										
ADDW12= 000000	956										
ADDW13= 000000	956										
ADDW14= 000000	956										
ADDW15= 000000	956										
ADDW2 = 000000	956										
ADDW3 = 000000	956										
ADDW4 = 000000	956										
ADDW5 = 000000	956										
ADDW6 = 000000	956										
ADDW7 = 000000	956										
ADDW8 = 000000	956										
ADDW9 = 000000	956										
ADEVCT= 000000	956	962									
ADEVN = 100000	1#	956	998								
AENV = 000000	956	967									
AENVN = 000000	956	968									
AFATAL= 000000	956	959									
AMADR1= 000000	956	984									
AMADR2= 000000	956	988									
AMADR3= 000000	956	991									
AMADR4= 000000	956	994									
AMAMS1= 000000	956	978									
AMAMS2= 000000	956	986									
AMAMS3= 000000	956	989									
AMAMS4= 000000	956	992									
AMSGAD= 000000	956	964									
AMSGLG= 000000	956	965									
AMSGTY= 000000	956	958									
AMTYP1= 000000	956	979									
AMTYP2= 000000	956	987									
AMTYP3= 000000	956	990									
AMTYP4= 000000	956	993									
APASS = 000000	956	961									
APRIOR= 000000	956										
APTCOM 011120	3610*	3678*	3699*								
APTCSU= 000040	3866	4110*									
APTENV= 000001	1217	1695	1982	2318	2492	3671	3859	4066	4108*	4142	
APTSIZ= 000200	1067	4107*									
APTSPO= 000100	3861	4068	4109*								
ASWREG= 000000	956	969									
AESTN= 000000	956	960									
AUNIT = 000000	956	963									
AUSWR = 011110	1#	956	970								
AVECT1= 000300	1#	956	995								
AVECT2= 000000	956	996									
BAUD = 007400	859*										
B TMS 011102	1106*	3616	3635*	3648*	3656	3659	3692*				

M C S 5













SDEVCT	001204	962#	3637*	3689*										
SDEVH	001252	998#	1094	3656										
SDOAGN	011414	3769	3778	3784#										
SDTBL	014012	4256	4291#											
SENDAD	011404	882	1075	3780#	4160									
SENDCT	011352	1044	3771#											
SENDMG	011423	3773	3788#											
SENULL	011420	3776	3787#											
SENV	001214	967#	1081	1217	1695	1982	2318	2492	3671	3859	4066	4090	4142	
SENVH	001215	968#	1067	3861	3866	4068								
SEOP	011316	3627	3761#											
SEOPCT	011344	1044*	3768#	3772										
SERFLG	001103	919#	4127*	4165	4170	4199	4201	4207*	4229					
SERMAX	001115	925#	1047*	4201	4224*	4229								
SERRFL=	000000	1104#	1105	1106#	1107	1109#	1113#	1114	1116#	1117	1118#	1119	1121#	1122
		1123#	1125	1126#	1128	1129#	1131	1132#	1134	1135#	1136	1148#	1149	1151#
		1152#	1153#	1154#	1155#	1156	1157#	1158	1161#	1162	1164#	1165	1178#	1179
		1180#	1182	1188#	1189#	1191#	1192#	1193	1221#	1222	1228#	1229	1242#	1243
		1244#	1245	1258#	1259	1261#	1262	1275#	1276	1278#	1279	1309#	1310	1316#
		1317	1330#	1331	1332#	1333	1346#	1347	1349#	1350	1363#	1364	1366#	1367
		1398#	1399	1412#	1413	1414#	1415	1428#	1429	1431#	1432	1445#	1446	1448#
		1449	1474#	1475	1488#	1489	1490#	1491	1504#	1505	1507#	1508	1521#	1522
		1524#	1525	1558#	1559	1612#	1613	1622#	1623	1630#	1631	1660#	1661	1699#
		1700	1705#	1706	1710#	1711	1712#	1713	1720#	1721	1725#	1748#	1749	1757#
		1758	1767#	1793#	1794	1800#	1801	1832#	1833	1839#	1840	1842#	1843	1846#
		1847	1852#	1866#	1867	1875#	1876	1906#	1907	1913#	1914	1915#	1916	1921#
		1922	1926#	1934#	1935	1950#	1951	1977#	1978	1985#	1986	1993#	1994	1995#
		1996	1997#	1998	2003#	2004	2010#	2011	2015#	2016	2031#	2032	2035#	2036
		2060#	2061	2065#	2066	2082#	2083	2086#	2087	2096#	2097	2104#	2105	2132#
		2133	2140#	2141	2146#	2147	2151#	2152	2158#	2159	2161#	2168#	2169	2171#
		2178#	2179	2187#	2188	2193#	2194	2201#	2202	2209#	2210	2216#	2217	2223#
		2224	2233#	2234	2238#	2239	2245#	2246	2250#	2251	2257#	2264#	2265	2271#
		2272	2282#	2273	2312#	2313	2321#	2322	2327#	2328	2329#	2330	2331#	2332
		2333#	2334	2336#	2337	2339	2343	2344#	2345	2347#	2348	2350#	2351	2353#
		2354	2356#	2357	2358#	2359	2368#	2369	2374#	2375	2376#	2377	2406#	2407
		2413#	2414	2415#	2416	2421#	2425	2426#	2427	2428#	2429	2432#	2433	2495#
		2496	2503#	2504	2507#	2508	2510#	2511	2513#	2514#	2515#	2516#	2517#	2518
		2519#	2520	2522#	2532#	2533	2542#	2543	2546#	2576#	2577	2579#	2580	2582#
		2583	2590#	2609#	2610#	2612#	2613#	2614	2647#	2648#	2649#	2650#	2651#	2652
		2654#	2655	2656#	2657	2659#	2660	2662#	2663	2672#	2673	2677#	2686#	2687
		2689#	2695#	2696	2703#	2704	2726#	2727	2731#	2732	2734#	2735	2738#	2739#
		2741#	2742#	2743	2761#	2762	2767#	2768	2770#	2771	2780#	2784#	2785	2788#
		2789	2791	2795	2800#	2810#	2811	2816#	2817	2819#	2829#	2830	2834#	2835
		2842#	2844	2845#	2846	2854#	2855	2858#	2859	2868#	2869	2902#	2903	2908#
		2909	2919#	2920	2928#	2932#	2933	2935#	2936	2938	2942	2948#	2959#	2960
		2966#	2967	2969#	2982#	2983	2989#	2990	2993#	2995	2996#	2997	3006#	3007
		3018#	3019	3043#	3044	3050#	3073#	3074	3076#	3077	3078#	3079	3082#	3083
		3084#	3085	3088#	3089	3091#	3092	3094#	3095	3097#	3098	3103#	3104	3107#
		3108	3110#	3111	3124#	3125	3136#	3137	3138#	3139	3156#	3157	3159#	3161
		3163#	3164	3170#	3171	3191#	3192	3194#	3196	3198#	3200	3211#	3212	3213#
		3214	3218#	3219	3228#	3229	3264#	3265	3272#	3273	3278#	3279	3281#	3282
		3284#	3285	3287#	3288	3290#	3310#	3311	3321#	3322	3407#	3408	3409#	3410
		3411#	3412	3423#	3424	3428#	3429	3438#	3439	3451#	3456#	3457	3469#	3470
		3473#	3475	3504#	3505	3506#	3510	3512#	3513	3515	3519	3520#	3521	3522#
		3523	3527#	3528	3550#	3551	3552#	3553	3555	3559	3560#	3561	3563	3567
		3593#	3594	3610#	3611	3612#	3613	3622#	3623	3627#	3631#	3632	3635#	3636

	3637#	3638	3639#	3640	3641#	3642	3646#	3647	3648#	3649	3650#	3651	3652#
\$ERROR 013126	3653	3662#	3663	3664#	3665	3666#	3667	3678#	3679	3687#	3688	3689#	3690
\$ERRPC 001116	1038	4125#											
\$ERRTB 001254	926#	3733	4134*	4135*	4136	4165							
\$ERTTL 001112	1015#												
\$ESCAP 001162	923#	3373	3375*	4133*	4165								
\$ETABL 001214	947#	1046*	4156	4158	4165	4223*							
\$ETEND 001254	966#												
\$FATAL 001176	908	999#											
\$FFLG 013124	959#	3724*	3725	4094*									
\$FILLC 001156	4057*	4060*	4088	4097*	4105#								
\$FILLS 001155	944#	3884	3915										
\$FSAND= 000310	943#	3915											
	1#	1096	1171	1217	1233	1249	1266	1285	1305	1321	1337	1354	1373
	1403	1419	1436	1455	1479	1495	1512	1531	1562	1593	1600	1607	1627
	1665	1695	1755	1786	1789	1828	1884	1902	1955	1973	1984	2009	2027
	2049	2053	2056	2079	2101	2128	2139	2184	2205	2229	2261	2279	2308
	2320	2363	2366	2387	2395	2436	2494	2555	2559	2597	2637	2700	2707
	2757	2851	2872	2879	2897	3002	3039	3129	3168	3205	3209	3226	3260
	3271	3308	3317	3320	3335	3342	3422	3436	3468	3618	3621	3661	3673
	3676												
\$FSBAD= 000401	1#	1096	1171	1220	1233	1249	1266	1285	1308	1321	1337	1354	1373
	1403	1419	1436	1455	1479	1495	1512	1531	1562	1593	1600	1607	1627
	1665	1698	1755	1786	1789	1831	1884	1905	1955	1976	1984	2009	2027
	2049	2053	2056	2079	2101	2131	2139	2184	2205	2229	2261	2279	2311
	2320	2363	2366	2387	2397	2436	2494	2555	2559	2597	2640	2700	2707
	2760	2851	2872	2879	2900	3002	3042	3129	3168	3205	3209	3226	3263
	3271	3308	3317	3320	3335	3342	3422	3436	3468	3618	3621	3661	3673
	3676												
\$FSBLA= 000170	1#												
\$FSCAS= 000150	1#												
\$FSDC= 000220	1#												
\$FSGOO= 000400	1#	1093	1094	1169	1215	1231	1247	1264	1283	1303	1319	1335	1352
	1371	1401	1417	1434	1453	1477	1493	1510	1529	1560	1591	1598	1605
	1625	1663	1693	1735	1753	1775	1784	1787	1826	1862	1882	1900	1937
	1953	1971	1982	2007	2025	2046	2047	2051	2054	2077	2099	2126	2137
	2182	2203	2227	2259	2277	2306	2318	2360	2361	2364	2385	2393	2434
	2492	2553	2557	2595	2635	2698	2705	2755	2808	2827	2849	2870	2877
	2895	2956	2977	3000	3037	3127	3166	3203	3207	3224	3258	3269	3298
	3306	3315	3318	3333	3340	3420	3434	3466	3616	3619	3659	3671	3674
\$FSIF 000110	1#	1169	1175	1215	1217	1224	1231	1237	1247	1253	1264	1270	1283
	1289	1303	1305	1312	1319	1325	1335	1341	1352	1358	1371	1377	1401
	1407	1417	1423	1434	1440	1453	1459	1477	1483	1493	1499	1510	1516
	1529	1535	1560	1571	1591	1595	1598	1602	1605	1609	1615	1617	1619
	1625	1642	1663	1674	1693	1695	1702	1735	1740	1753	1761	1775	1780
	1782	1784	1787	1795	1797	1802	1826	1828	1835	1862	1870	1882	1888
	1900	1902	1909	1937	1942	1953	1959	1971	1973	1980	1982	1988	2007
	2012	2017	2025	2038	2051	2054	2068	2070	2077	2089	2099	2108	2126
	2128	2135	2137	2143	2182	2196	2203	2219	2227	2241	2259	2274	2277
	2287	2306	2308	2315	2318	2324	2364	2370	2378	2385	2389	2393	2395
	2399	2408	2410	2434	2440	2492	2498	2553	2557	2563	2570	2572	2595
	2601	2635	2637	2642	2698	2705	2712	2718	2720	2755	2757	2764	2808
	2812	2827	2831	2849	2861	2870	2875	2877	2882	2895	2897	2905	2956
	2962	2977	2985	3000	3009	3037	3039	3046	3127	3133	3166	3172	3203
	3207	3215	3220	3224	3233	3258	3260	3267	3269	3275	3298	3303	3306
	3312	3315	3318	3323	3325	3327	3333	3338	3340	3345	3420	3425	3430

	3434	3440	3466	3471	3616	3619	3624	3633	3643	3654	3659	3671	3674
	3680	3682	3684										
SFSINC= 000210	1#	2336	2418	2788	2863	2935	3012	3512	3524	3552	3560	3568	3571
SFSL00= 000200	1#	1707	1804	1807	3417	3443	3458						
SFSNAM= 000160	1#												
SFSNO = 000403	1#	1094	1169	1215	1217	1231	1247	1264	1283	1303	1319	1335	1352
	1371	1401	1417	1434	1453	1477	1493	1510	1529	1560	1598	1605	1625
	1663	1695	1753	1784	1787	1826	1882	1900	1953	1973	1982	2007	2025
	2047	2051	2054	2077	2099	2126	2137	2182	2203	2227	2259	2277	2306
	2308	2361	2364	2385	2393	2395	2434	2492	2553	2557	2595	2635	2698
	2705	2755	2849	2870	2877	2895	2897	3000	3037	3127	3166	3203	3207
	3224	3258	3260	3315	3318	3333	3340	3420	3616	3619	3659	3671	3674
SFSOP = 000320	1#	1096	1171	1217	1233	1249	1266	1285	1305	1321	1337	1354	1373
	1403	1419	1436	1455	1479	1495	1512	1531	1562	1593	1600	1607	1627
	1665	1695	1755	1786	1789	1828	1884	1902	1955	1973	1984	2009	2027
	2049	2053	2056	2079	2101	2128	2139	2184	2205	2229	2261	2279	2308
	2320	2363	2366	2387	2395	2397	2436	2494	2555	2559	2597	2637	2700
	2707	2757	2851	2872	2879	2897	3002	3039	3129	3168	3205	3209	3226
	3260	3271	3308	3317	3320	3335	3342	3422	3436	3468	3618	3621	3661
	3673	3676											
SFSRTN= 000300	1#	3383	3482	3490	3532	3539	3577	3599	3705	3713	3749		
SFSSEL= 000140	1#												
SFSUNT= 000130	1#	1159	1183	2005	2019	3115	3117	3614	3656				
SFSWHI= 000120	1#	1093	1094	1101	1217	1305	1695	1828	1902	1973	2046	2047	2072
	2128	2308	2360	2361	2381	2395	2637	2757	2897	3039	3260		
SFSYES= 000402	1#	1094	1169	1215	1217	1231	1247	1264	1283	1303	1305	1319	1335
	1352	1371	1401	1417	1434	1453	1477	1493	1510	1529	1560	1591	1598
	1605	1625	1663	1693	1695	1753	1784	1787	1826	1828	1882	1900	1902
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		1781	1782	1783	1795	1796	1798	1799	1802	1803	1808	1809	1830	1831
		1835	1836	1870	1871	1888	1889	1904	1905	1909	1910	1942	1943	1959
		1960	1975	1976	1980	1981	1988	1989	2005	2006	2013	2014	2017	2018
		2023	2024	2038	2039	2046	2047	2068	2069	2070	2071	2073	2074	2089
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		1896	1968	2122	2300	2486	2632	2752	2892	3033	3255	3356	3758	3764
		3779	3785	3787	3829	4117	4118	4119	4120	4121	4130	4137	4149	4153
		4165	4171	4172	4173	4174	4175	4181	4193	4195	4196	4199	4200	4201
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MAINDEC-11-DVDVC-B CVDVCB P11		MACY11 02-FEB-78 08 39		30A(1052)		02-FEB-78 08 40		PAGE 123		F 10		CROSS REFERENCE TABLE -- USER SYMBOLS		SEQ 0122
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ERRORS DETECTED 0

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CORE USED 34K (67 PAGES)