

11/21+
DMV-11

DMV11 MCTRL DIAG #2
CNDMBAO

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JUL 1984
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This table contains 16 rows and 20 columns of technical diagrams. Each cell displays a specific timing diagram or control signal for a component within the DMV11 system. The diagrams consist of waveforms, logic gates, and signal paths. Key labels include:

- Component identifiers: **DMV11**, **IC: 7418**.
- Timing parameters: **100ns**, **50ns**, **20ns**, **10ns**, **5ns**, **1μs**, **10μs**, **1ms**.
- Signal names: **ENABLE**, **SELECT**, **DATA**, **CLOCK**, **RESET**, **STATUS**, **INPUT**, **OUTPUT**.

The diagrams are arranged in a regular grid, with each row likely representing a different functional block or a sequence of related signals within the control logic.

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.TITLE CNDMBAO DMV11 MCTRL DIAG #2
.SBTTL PROGRAM DOCUMENT
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IDENTIFICATION

PRODUCT CODE: AC-T828A-MC
PRODUCT NAME: CNDMBAO DMV11 MICRO-CONTROLLER STATIC DIAGNOSTIC PART 2
PRODUCT DATE: APRIL 1984
MAINTAINER: ISS DIAGNOSTICS
AUTHORS: CHRIS BRIENEN
RAY MARSHALL
MODIFIED BY: JAKI BERG 9-APR-1984
PURPOSE: THIS DIAGNOSTIC IS DESIGNED TO PERFORM STATIC LOGIC TESTS FOR
THE M8053 OR M8064 (HEREAFTER REFERRED TO AS THE DMV OR DMV-11)

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PROGRAM DOCUMENT

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51
52
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***** MODIFICATION HISTORY *****

REV A: ORIGINAL RELEASE CHRIS BRIENEN, RAY MARSHALL 14-JAN-81

REV B: INSTALLED OUTSTANDING PATCHES 11-JUL-83

CVDMBB => CNDMBA JAKI BERG 9-APR-84

CHANGES WERE MADE TO CVDMBB TO PRODUCE CNDMBA FOR THE FALCON-PLUS PROJECT (SBC-11/21*). CHANGES, MARKED BY ";JB REV A-0", ARE:

- SET THE ODT BREAK VECTOR (LOCATION 140) TO THE STARTING ADDRESS OF FALCON'S ODT ROM (170000-OCTAL).
- FALCON * CANNOT BE STRAPPED TO MODE 0 ON POWER UP. THE "STRAPPED TO MODE 0" QUESTION WAS REMOVED. THE DEFAULTS AND P-TABLE WERE SET APPROPRIATELY.

PROGRAM DOCUMENT

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68
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97
98
99
100
101
102
103
104
105
106
107
108
109
110
111
112
113

CONTENTS

- 1.0 INTRODUCTION
- 2.0 HARDWARE REQUIREMENTS
- 3.0 PRELIMINARY PROGRAM REQUIREMENTS
- 4.0 GENERAL PROGRAM CONSIDERATIONS
 - 4.1 DIAGNOSTIC SUPERVISOR
 - 4.2 EXECUTION TIME
 - 4.3 XXDP.
 - 4.4 ACT/SLIDE
 - 4.5 APT
 - 4.6 MEMORY MANAGEMENT
 - 4.7 ERROR LOGGING
- 5.0 PROGRAM LOAD MEDIA
- 6.0 OPERATING INSTRUCTIONS
 - 6.1 LOADING AND STARTING PROCEDURES
 - 6.1.1 LOADING PROCEDURES
 - 6.1.2 STARTING PROCEDURES
 - 6.1.3 ** STEPS FOR QUICK AND SIMPLE EXECUTION **
 - 6.2 INITIAL DIALOGUE
 - 6.3 PROGRAM OPTIONS
 - 6.3.1 START COMMAND
 - 6.3.2 RESTART COMMAND
 - 6.3.3 CONTINUE COMMAND
 - 6.3.4 PROCEED COMMAND
 - 6.3.5 ADD COMMAND
 - 6.3.6 DROP COMMAND
 - 6.3.7 PRINT COMMAND
 - 6.3.8 DISPLAY COMMAND
 - 6.3.9 FLAGS COMMAND
 - 6.3.10 ZFLAGS COMMAND
 - 6.3.11 CONTROL CHARACTERS
 - 6.3.12 HARDWARE PARAMETERS
 - 6.3.13 SOFTWARE PARAMETERS
 - 6.3.14 EXTENDED DISCUSSION OF P-TABLE DIALOGUE
- 7.0 TEST DESCRIPTIONS
- 8.0 ERROR INFORMATION
 - 8.1 ERROR REPORTING

PROGRAM DOCUMENT

115
116
117
118
119
120
121
122
123
124
125
126
127
128
129
130
131
132
133
134
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1.0 INTRODUCTION

THE M8053 AND M8064 ARE SINGLE-LINE SYNCHRONOUS, MICRO-PROCESSOR BASED COMMUNICATIONS INTERFACES WHICH CAN SUPPORT BOTH CHARACTER-ORIENTED (DDCMP, BSC, ETC.) AND BIT-ORIENTED (SDLC, MDLC, ETC.) PROTOCOLS. THE PURPOSE OF THIS PROGRAM IS TO PERFORM DIAGNOSTIC TESTING OF THE CSRS, RAM, AND BASIC MICRO-PROCESSOR LOGIC ON THESE BOARDS. THE FOLLOWING FUNCTIONS WILL BE PERFORMED: DMV RESIDENT U-DIAG EXECUTION CSR ADDRESSING, VIA REGISTER STATIC BIT INTERACTION AND READ/WRITE TESTING, AND ON-BOARD RAM TESTING.

THE STATIC LOGIC TESTS WILL PROVIDE EXTENSIVE TROUBLESHOOTING CAPABILITIES, SUCH AS TIGHT SCOPE LOOPS, SWITCH OPTIONS, AND ABILITY TO "LOCK" ONTO INTERMITTENT ERRORS. IN ADDITION TESTS ARE DESIGNED AND STRUCTURED TO ACHIEVE MAXIMUM FAULT RESOLUTION AND FACILITATE REPLACEMENT OF THE SMALLEST FIELD REPLACEABLE UNIT.

THIS PROGRAM IS IMPLEMENTED USING THE DIAGNOSTIC SUPERVISOR AND A STRUCTURED PROGRAMMING APPROACH. BECAUSE THE DESIGN CONFORMS TO THE SUPERVISOR (STANDALONE VERSION) THE PROGRAM IS COMPATIBLE WITH ACT, APT, XXDP., AND SLIDE.

THROUGH DIALOGUE WITH THE OPERATOR, THE PROGRAM ALLOWS MODIFICATION OF DEVICE PARAMETERS, SUCH AS LSI-BUS ADDRESS, VECTOR ADDRESSES AND DEVICE PRIORITY. IN ADDITION, THE OPERATOR CAN SPECIFY PARTICULAR TESTS TO BE RUN AND A VARIETY OF LOOPING, RUNNING, AND REPORTING MODES.

DEVICE ERRORS WILL BE REPORTED AS THEY OCCUR. THE REPORT WILL INCLUDE A TEST NUMBER AND DESCRIPTION OF THE ERROR, GOOD AND BAD TEST DATA, AND APPLICABLE DEVICE REGISTER CONTENTS.

2.0 HARDWARE REQUIREMENTS

THE FOLLOWING HARDWARE IS REQUIRED TO RUN THE M8053/8064 STATIC LOGIC TESTS:

- SBC-11/21.
- 16K WORDS OF MEMORY
- CONSOLE TERMINAL
- M8053 OR M8064 COMMUNICATIONS INTERFACE

3.0 PRELIMINARY PROGRAM REQUIREMENTS

THIS PROGRAM (CNDMB) SHOULD BE THE SECOND OF THE FIVE DMV-11 STATIC DIAGNOSTICS TO BE RUN (CNDMA SHOULD BE RUN FIRST). ERRORS FOUND IN THIS PROGRAM SHOULD BE CORRECTED BEFORE RUNNING ANY OF THE LINE UNIT DIAGNOSTICS (CNDMC, CNDMD, OR CNDME).

PROGRAM DOCUMENT

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4.0 GENERAL PROGRAM CONSIDERATIONS

4.1 DIAGNOSTIC SUPERVISOR

THIS PROGRAM IS COMPATIBLE WITH THE STANDALONE DIAGNOSTIC SUPERVISOR, AND MUST BE LOADED TO BE CO-RESIDENT WITH THE SUPERVISOR, OR BE PREVIOUSLY COMBINED WITH THE SUPERVISOR AND LOADED AS A SINGLE FILE. IN EITHER CASE, THE COMBINED PROGRAM WILL NOT EXCEED 16K OF MEMORY.

4.2 EXECUTION TIME

THE MAXIMUM TIME REQUIRED TO RUN THIS PROGRAM IS ABOUT ONE MINUTE PER PASS FOR EACH UNIT.

4.3 XXDP.

THIS PROGRAM MAY BE LOADED UNDER XXDP., AND MAY BE RUN IN DUMP MODE OR CHAIN MODE.

4.4 ACT/SLIDE

THIS PROGRAM MAY BE LOADED UNDER ACT OR SLIDE AND MAY BE RUN IN DUMP MODE OR CHAIN MODE.

4.5 APT

THIS PROGRAM MAY BE LOADED BY THE APT SYSTEM (INCLUDING APT-RD) AND RUN IN PROGRAM MODE OR SCRIPT MODE.

4.6 MEMORY MANAGEMENT

MEMORY MANAGEMENT IS UTILIZED IN THIS PROGRAM TO VERIFY THE DMV-11'S ABILITY TO NPR INTO (AND OUT OF) EXTENDED MEMORY.

4.7 ERROR LOGGING

AT THE END OF EACH PASS ON ALL UNITS, THE PROGRAM PRINTS OUT THE CUMULATIVE TOTAL NUMBER OF ERRORS SINCE THE LAST START OR RESTART COMMAND.

5.0 PROGRAM LOAD MEDIA

THIS PROGRAM CAN BE LOADED FROM PAPER TAPE USING THE ABSOLUTE LOADER OR FROM ACT, SLIDE, OR APT SYSTEMS, OR FROM ANY MEDIA SUPPORTED BY XXDP.. WHEN USING THE PAPER TAPE ABSOLUTE LOADER, THE PROGRAM SHOULD BE LOADED FIRST, FOLLOWED BY THE DIAGNOSTIC SUPERVISOR. WHEN USING XXDP., THE

PROGRAM DOCUMENT

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6.3.1 START COMMAND

```
*****
STA(RT)/TESTS:<TEST-LIST>/PASS:<PASS-CNT>/FLAGS:
<FLAG-LIST>/EOP:<INCR>
*****
```

6.3.1.1 TESTS SWITCH (/TESTS:<TEST-LIST>)

<TEST-LIST> IS A SEQUENCE OF DECIMAL NUMBERS (1:2 ETC.) OR RANGES OF DECIMAL NUMBERS (1-5:8-10 ETC.) THAT SPECIFY THE TESTS TO BE EXECUTED. THE NUMBERS ARE SEPARATED BY COLONS. THE NUMBERS RANGE FROM 1 TO THE LARGEST TEST NUMBER IN THE DIAGNOSTIC. THEY MAY BE SPECIFIED IN ANY ORDER. TESTS WILL BE EXECUTED IN NUMERICAL ORDER REGARDLESS OF THE ORDER OF SPECIFICATION. THE DEFAULT IS TO EXECUTE ALL TESTS. ON THIS AND ALL SWITCHES, THE ANGLE BRACKETS <> ARE PUNCTUATION USED IN THE DEFINITION ONLY, AND ARE NOT TO BE TYPED BY THE OPERATOR. SEE EXAMPLE AT END OF 6.3.1.5.

6.3.1.2 PASS SWITCH (/PASS:<PASS-CNT>)

<PASS-CNT> IS A DECIMAL NUMBER INDICATING THE DESIRED NUMBER OF PASSES. A PASS IS DEFINED AS THE EXECUTION OF THE FULL DIAGNOSTIC (ALL SELECTED TESTS) AGAINST ALL UNITS SUBMITTED. THE DEFAULT IS NON-ENDING EXECUTION. IN THIS CASE EXIT FROM THE PROGRAM IS ACCOMPLISHED EITHER BY TYPING A CONTROL/C OR BY OCCURANCE OF AN ERROR WITH THE HALT ON ERROR FLAG BEING SET. THE EXIT IS A RETURN TO COMMAND MODE. SEE EXAMPLE AT END OF 6.3.1.5.

6.3.1.3 FLAGS SWITCH (/FLAGS:<FLAG-LIST>)

<FLAG-LIST> IS A SEQUENCE OF ELEMENTS OF THE FORM <FLAG>, <FLAG=1>, OR <FLAG=0>, SEPARATED BY COLONS, WHERE <FLAG> HAS ONE OF THE FOLLOWING VALUES:

- MOE HALT ON ERROR, CAUSING COMMAND MODE TO BE ENTERED WHEN AN ERROR IS ENCOUNTERED
- LOE LOOP ON ERROR, CAUSING THE DIAGNOSTIC TO LOOP CONTINUOUSLY WITHIN THE SMALLEST DEFINED BLOCK OF CODING (SEGMENT, SUBTEST, OR TEST) CONTAINING THE ERROR
- IER INHIBIT ERROR REPORTING
- IBE INHIBIT BASIC ERROR REPORTS
- IXE INHIBIT EXTENDED ERROR REPORTS
- PRI DIRECT ALL MESSAGES TO A LINE PRINTER
- PNT PRINT NUMBER OF TEST BEING EXECUTED
- BOE BELL ON ERROR
- JAM RUN IN UNATTENDED MODE, BYPASSING MANUAL INTERVENTION TESTS
- ISR INHIBIT STATISTICAL REPORTS
- IDU INHIBIT DROPPING OF UNITS BY DIAGNOSTIC

PROGRAM DOCUMENT

LOT LOOP ON TEST

THE FLAGS NAMED OR EQUATED TO 1 ARE SET, THOSE EQUATED TO 0 ARE CLEARED. A FLAG NOT SPECIFIED IS CLEARED. IF THE FLAGS SWITCH IS NOT GIVEN ALL FLAGS ARE CLEARED. SEE EXAMPLE AT END OF 6.3.1.5.

6.3.1.4 END OF PASS SWITCH (/EOP:<INCR>)

<INCR> IS A DECIMAL NUMBER INDICATING HOW OFTEN (IN TERMS OF PASSES) IT IS DESIRED THAT THE END OF PASS MESSAGE BE PRINTED. THE DEFAULT IS AT THE END OF EVERY PASS. SEE EXAMPLE AT END OF 6.3.1.5.

6.3.1.5 EFFECT OF START COMMAND

THE EFFECT OF THE START COMMAND IS TO INITIATE THE HARDWARE PARAMETER DIALOGUE, THE SOFTWARE PARAMETER DIALOGUE, AND THEN THE DIAGNOSTIC TESTS THEMSELVES.

THE HARDWARE PARAMETER DIALOGUE COMMENCES WITH THE QUESTION "# UNITS?" TO WHICH THE OPERATOR REPLIES WITH A DECIMAL NUMBER N FROM 1 TO 16. THE TERM "UNIT" REFERS TO THE DEVICE TO WHICH THIS SERIES OF DIAGNOSTICS IS DEDICATED. FOLLOWING THIS ARE THE QUESTIONS WHEREBY THE P-TABLES THEMSELVES WILL BE BUILT. EACH P-TABLE IS A CORE-RESIDENT TABLE CONTAINING ALL THE HARDWARE INFORMATION FOR ONE UNIT. THE OPERATOR MUST SUPPLY N (NUMBER OF UNITS) VALUES FOR EACH QUESTION. HE MAY DO THIS BY GIVING ONE ANSWER TO EACH QUESTION (IN WHICH CASE THE SERIES OF QUESTIONS WILL BE POSED N TIMES) OR BY GIVING N VALUES, SEPARATED BY COMMAS, TO EACH QUESTION (SERIES WILL BE POSED ONCE). EACH QUESTION IS FOLLOWED BY THE RESPONSE RADIX (D FOR DECIMAL, B FOR BINARY, O FOR OCTAL, L FOR YES/NO) IN PARENTHESES AND THE DEFAULT VALUE AFTER THE PARENTHESES.

FOLLOWING THE HARDWARE QUESTIONS ARE THE SOFTWARE QUESTIONS TO BUILD THE SOFTWARE TABLES, WHICH DEFINE THE MODE (QUICK VERIFY ETC.) THAT THE DIAGNOSTIC WILL EXECUTE IN.

WHEN THE QUESTION "# UNITS?" IS ANSWERED, MEMORY STORAGE IS ALLOCATED FOR THE P-TABLES, AND IF THERE IS NOT ENOUGH TO ACCOMMODATE THEM THE MESSAGE "TOO MANY UNITS" IS ISSUED. IN THIS CASE THE DIAGNOSTIC MUST BE EXECUTED MORE THAN ONCE TO TEST ALL UNITS.

EXAMPLE:

STA/TESTS:1:2-4:6:8-10/PASS:3/FLAGS:IER:HOE=1:UAM:LOE

THIS COMMAND WILL CAUSE THREE PASSES TO BE MADE, EACH PASS CONSISTING OF TESTS 1,2,3,4,6,8,9, AND 10 EXECUTED AGAINST ALL UNITS. THERE IS NO DIFFERENCE BETWEEN SAYING <FLAG> AND SAYING <FLAG=1>. THE NOTATION <FLAG=0> IS MEANINGFUL ONLY ON A COMMAND OTHER THAN START TO CLEAR A FLAG THAT WAS

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PROGRAM DOCUMENT

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PREVIOUSLY SET. NOTE THAT ON ALL COMMANDS ONLY THE FIRST THREE LETTERS ARE SCANNED.

6.3.2 RESTART COMMAND

```
*****
RES(TART)/TESTS:<TEST-LIST>/PASS:<PASS-CNT>/FLAGS:
<FLAG-LIST>/UNITS:<UNIT-LIST>
*****
```

6.3.2.1 TESTS, PASS, AND FLAGS SWITCHES

<TEST-LIST>, <PASS-CNT>, AND <FLAG-LIST> ARE AS IN THE START COMMAND.

6.3.2.2 UNITS SWITCH (/UNITS:<UNIT-LIST>)

<UNIT-LIST> IS A SEQUENCE OF DECIMAL NUMBERS (0,1 ETC.) OR RANGES OF DECIMAL NUMBERS (0-5, 8-10 ETC.) THAT SPECIFY THE UNITS TO BE TESTED. THE NUMBERS ARE SEPARATED BY COLONS. THE NUMBERS MAY RANGE FROM 0 THRU N-1 (N IS THE NUMBER OF UNITS SPECIFIED IN THE PREVIOUS START COMMAND). THE NUMBER INDICATES THE POSITION OF THE P-TABLE AS THE DATA WAS ENTERED DURING THE HARDWARE DIAGLOGUE. THE UNITS WHICH ARE SELECTED MUST NOT HAVE BEEN DROPPED BY THE DROP COMMAND. SEE THE DISCUSSION OF ADD AND DROP COMMANDS BELOW. DEFAULT IS TO TEST ALL UNITS WHICH HAVE NOT BEEN DROPPED BY A DROP COMMAND.

6.3.2.3 EFFECT OF RESTART COMMAND

THE RESTART COMMAND DIFFERS FROM THE START COMMAND IN THAT THE P-TABLES FROM THE PREVIOUS START COMMAND (THERE MUST HAVE BEEN ONE) ARE USED, INSTEAD OF NEW ONES BEING BUILT. THE UNITS SWITCH GIVES THE ABILITY TO SELECT A SUBSET OF THESE. THE SOFTWARE DIALOGUE MAY OPTIONALLY BE REEXECUTED (OPERATOR WILL BE ASKED). THE COMMAND CAN BE USED AFTER COMMAND MODE HAS BEEN REENTERED IN ANY OF THE THREE NORMAL WAYS: A) THE REQUESTED NUMBER OF PASSES HAVE BEEN MADE B) AN ERROR WAS ENCOUNTERED WITH THE HALT ON ERROR FLAG SET C) A CONTROL/C WAS ENTERED BY THE OPERATOR.

6.3.3 CONTINUE COMMAND

```
*****
CON(TINUE)/PASS:<PASS-CNT>/FLAGS:<FLAG-LIST>
*****
```

6.3.3.1 PASS SWITCH (/PASS:<PASS-CNT>)

<PASS-CNT> IS SAME AS IN START COMMAND, BUT THE DEFAULT IS

PROGRAM DOCUMENT

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THE UNSATISFIED PASS-CNT FROM THE PREVIOUS START OR RESTART.
IF NONE REMAINS, THE DEFAULT IS NON-ENDING EXECUTION.

6.3.3.2 FLAG SWITCH (/FLAGS:<FLAG-LIST>)

<FLAG-LIST> IS SAME AS IN START COMMAND, BUT UNSPECIFIED
FLAGS RETAIN THEIR CURRENT VALUE.

6.3.3.3 EFFECT OF CONTINUE COMMAND

CONTINUE MUST FOLLOW A START OR RESTART, AND COMMAND MODE
MUST HAVE BEEN ENTERED DUE TO A HALT ON ERROR OR A
CONTROL/C. THE EFFECT OF THE COMMAND IS TO GO TO THE
BEGINNING OF THE TEST THAT WAS BEING EXECUTED WHEN THE HALT
OR CONTROL/C TOOK PLACE. SOFTWARE DIALOGUE MAY OPTIONALLY
BE REEXECUTED. HARDWARE PARAMETERS MAY NOT BE CHANGED.

6.3.4 PROCEED COMMAND

PRO(CCEED)/FLAGS:<FLAG-LIST>

6.3.4.1 FLAGS SWITCH (/FLAGS:<FLAG-LIST>)

<FLAG-LIST> IS AS IN THE START COMMAND, BUT UNSPECIFIED
FLAGS RETAIN THEIR CURRENT VALUE.

6.3.4.2 EFFECT OF PROCEED COMMAND

PROCEED MUST FOLLOW A START, RESTART, OR CONTINUE. COMMAND
MODE MUST HAVE BEEN ENTERED VIA A HALT ON ERROR. THE EFFECT
OF THE COMMAND IS TO BEGIN EXECUTION AT THE LOCATION
FOLLOWING THE ERROR CALL. NEITHER HARDWARE NOR SOFTWARE
PARAMETERS MAY BE ALTERED.

6.3.5 ADD COMMAND

ADD/UNITS:<UNIT-LIST>

6.3.5.1 UNITS SWITCH (/UNITS:<UNIT-LIST>)

<UNIT-LIST> IS AS IN THE RESTART COMMAND.

6.3.5.2 EFFECT OF ADD COMMAND

THE UNITS SPECIFIED ARE ADDED TO THE TEST SEQUENCE. EACH

PROGRAM DOCUMENT

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UNIT MUST HAVE A P-TABLE IN MEMORY DUE TO AN EARLIER
 HARDWARE DIALOGUE. THIS COMMAND MUST BE FOLLOWED BY A
 RESTART OR CONTINUE. THE UNITS SWITCH MUST BE SPECIFIED.
 THE ADD COMMAND IS MEANINGFUL ONLY FOR UNITS THAT WERE
 PREVIOUSLY DROPPED.

6.3.6 DROP COMMAND

 DRO(P)/UNITS:<UNIT-LIST>

6.3.6.1 UNITS SWITCH (/UNITS:<UNIT-LIST>)

<UNIT-LIST> IS AS IN THE RESTART COMMAND.

6.3.6.2 EFFECT OF DROP COMMAND

THE UNITS SPECIFIED WILL BE DROPPED FROM TESTING. THE UNITS
 WILL BE RESELECTED ONLY BY THE EXECUTION OF AN ADD OR START
 COMMAND. THE UNITS SWITCH MUST BE ENTERED. THIS COMMAND
 MUST BE FOLLOWED BY A RESTART OR A CONTINUE COMMAND.

6.3.7 PRINT COMMAND

 PRI(NT)

6.3.7.1 EFFECT OF PRINT COMMAND

THE TOTAL NUMBER OF ERRORS FOR EACH UNIT SINCE THE LAST
 START OR RESTART COMMAND ARE PRINTED. THE ISR (INHIBIT
 STATISTICAL REPORTING) FLAG IS CLEARED.

6.3.8 DISPLAY COMMAND

 DIS(PLAY)/UNITS:<UNIT-LIST>

6.3.8.1 UNITS SWITCH (/UNITS:<UNIT-LIST>)

<UNIT-LIST> IS AS IN THE RESTART COMMAND.

6.3.8.2 EFFECT OF DISPLAY COMMAND

THE HARDWARE P-TABLES FOR ALL UNITS UNDER TEST ARE PRINTED
 OUT IN THE FORMAT IN WHICH THEY WERE ENTERED. ANY UNITS

PROGRAM DOCUMENT

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2. DEVICE VECTOR ADDRESS : (0) 300 ?

THIS IS THE ADDRESS OF THE INPUT INTERRUPT VECTOR FOR THIS DEVICE. THE ALLOWABLE RANGE IS 000-674 (OCTAL), AND THE DEFAULT VALUE IS 300.

3. DEVICE PRIORITY LEVEL : (0) 4 ?

THIS IS THE CPU PRIORITY AT WHICH THE INTERRUPT HANDLERS OF THIS DEVICE WILL BE EXECUTED. THE ALLOWABLE RANGE IS 0-7, AND THE DEFAULT VALUE IS 4.

4. BOARD TYPE (0=M8064, 1=M8053-V35, 2=M8053-EIA) : (0) 0 ?

THIS IS THE TYPE OF DMV-11 CURRENTLY INSTALLED. NOTE THAT THE M8053 IS SWITCH SELECTABLE BETWEEN V.35 AND EIA.

5. IS THIS A MANUFACTURING TEST STAND : (L) N ?

THIS QUESTION REFERS TO A SPECIFIC MEMORY CONFIGURATION THAT IS REQUIRED TO RUN TEST #8 (SEE SEC. 7.0).

6.3.13 SOFTWARE PARAMETERS

NO SOFTWARE PARAMETER QUESTIONS ARE ASKED BY THIS PROGRAM.

6.3.14 EXTENDED DISCUSSION OF P-TABLE DIALOGUE

THE FULL CAPABILITY OF THE HARDWARE DIALOGUE IS REVEALED BY THE FOLLOWING DISCUSSION OF WHAT HAPPENS INTERNALLY.

AS SOON AS THE QUESTION "# UNITS?" IS ANSWERED (WITH THE NUMBER N, SAY) SPACE IN CORE IS ALLOCATED FOR N P-TABLES. ALL OF THE P-TABLES ARE OF THE SAME FORMAT, AND THERE IS A ONE-TO ONE CORRESPONDENCE BETWEEN THE HARDWARE PARAMETER QUESTIONS AND THE SLOTS IN THE P-TABLE FORMAT.

ON THE FIRST TRIP THRU THE QUESTIONS, ALL OF THE SLOTS IN ALL OF THE P-TABLES ARE FILLED. IF THE OPERATOR TYPES IN LESS THAN N EXPLICIT VALUES IN RESPONSE TO A PARTICULAR QUESTION, THESE VALUES ARE PLACED IN THE P-TABLES (ONE VALUE GOING INTO THE PROPER SLOT OF EACH P-TABLE BEGINNING WITH THE FIRST P-TABLE) UNTIL THE STRING OF VALUES IS EXHAUSTED. THE LAST VALUE IN THE STRING BECOMES THE NEW DEFAULT AND IS USED TO FILL THAT SLOT IN THE REMAINING P-TABLES.

ON SUBSEQUENT TRIPS THRU THE QUESTIONS, THE SAME PROCESS IS CARRIED OUT, EXCEPT THAT THE EARLIEST P-TABLE NOT TO HAVE RECEIVED AN EXPLICIT VALUE IN ANY OF ITS SLOTS NOW ASSUMES THE ROLE THAT TABLE NUMBER ONE PLAYED IN THE FIRST TRIP.

THE SERIES OF QUESTIONS IS REISSUED UNTIL AT LEAST ONE QUESTION HAS RECEIVED N EXPLICIT VALUES FROM THE OPERATOR.

IN GIVING A STRING OF VALUES, COMMAS WITHOUT INTERVENING VALUES MAY BE USED TO INDICATE A REPETITION OF THE LAST

PROGRAM DOCUMENT

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NAMED VALUE.

A STRING OF VALUES MAY BE GIVEN AS A RANGE (6-10 FOR EXAMPLE). IF THE VALUES REPRESENT PURE NUMERICAL DATA, THIS SAMPLE RANGE TRANSLATES TO THE STRING 6,7,8,9,10 (AN INCREMENT OF 1). IF THE VALUES ARE ADDRESSES, THE SAMPLE RANGE TRANSLATES TO THE STRING 6,8,10 (AN INCREMENT OF 2).

NOW LET US SEE HOW WE COULD USE THESE CAPABILITIES TO CONSTRUCT A SET OF P-TABLES. ASSUME THAT WE HAVE 16 UNITS, AND THAT THERE ARE THREE HARDWARE PARAMETERS FOR EACH (THREE SLOTS IN THE P-TABLE, THREE HARDWARE QUESTIONS IN THE DIALOGUE). LET THE DESIRED VALUE FOR THE FIRST PARAMETER BE THE NUMBER 75 FOR ALL 16 TABLES. LET THE DESIRED VALUE FOR THE SECOND PARAMETER BE EQUAL TO THE UNIT NUMBER (0,1,2,...,15) EXCEPT FOR UNIT 12, WHICH SHOULD RECEIVE THE VALUE 11. LET THE DESIRED VALUE FOR THE THIRD PARAMETER BE THE NUMBER 76 FOR THE FIRST 7 UNITS AND THE NUMBER 77 FOR THE LAST 9 UNITS.

THE FOLLOWING DIALOGUE WOULD ACCOMPLISH THIS GOAL:

0 UNITS (0) ? 16
UNIT 0
<QUESTION 1> ? 75
<QUESTION 2> ? 0-6
<QUESTION 3> ? 76

UNIT 7
<QUESTION 1> ?
<QUESTION 2> ? 7-11,,13-15
<QUESTION 3> ? 77

THE FIRST TIME THE SERIES IS ASKED, SLOT ONE RECEIVES A 75 IN ALL 16 TABLES. SLOT TWO RECEIVES THE VALUES 0,1,2,...,6 IN TABLES 0 THRU 6 AND A CONSTANT 6 IN TABLES 7 THRU 15. SLOT THREE RECEIVES A CONSTANT 76 IN ALL 16 TABLES.

THE SECOND TIME THRU THE SERIES, TABLES 7 THRU THE END ARE GOING TO BE AFFECTED (NOTE THAT THIS PIECE OF INFORMATION IS PRINTED OUT FOR THE THE OPERATOR IN THE FORM "UNIT xx" AT THE BEGINNING OF EACH SERIES). QUESTION 1 IS RESPONDED TO BY A <CR>, SO SLOT ONE STAYS AT CONSTANT 75 IN TABLES 7 THRU 15, SINCE NO NEW EXPLICIT VALUES ARE TYPED IN. SLOT TWO GETS THE VALUES 7,8,9,10,11 IN TABLES 7 THRU 11, AND GETS AN 11 IN SLOT 12, AND GETS THE VALUES 13,14,15 IN TABLES 13 THRU 15. SLOT THREE GETS THE VALUE 77 IN TABLES 7 THRU 15.

THE DIALOGUE IS TERMINATED WHEN THE SOFTWARE RECOGNIZES THAT 16 EXPLICIT VALUES HAVE BEEN GIVEN FOR AT LEAST ONE QUESTION (NAMELY QUESTION 2).

PROGRAM DOCUMENT

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7.0 TEST DESCRIPTIONS

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:*****
:*   TEST 1 <VIA TIMER 2 ONE SHOT MODE>
:*
:*   THIS TEST VERIFIES THAT THE TIMER 2 COUNTER IS OPERATIONAL IN
:*   INTERVAL-TIMER (ONE-SHOT) MODE.
:*
:*   THE FOLLOWING IS PERFORMED :
:*
:*   A MASTER CLEAR IS DONE & THE TIMER IS PLACED IN INTERVAL-TIMER MODE
:*   BY SETTING ACR5 = 0 AND THE PROGRAM CHECKS FOR "T2" (BIT 5 IN IFR)
:*   TO BE INITIALLY CLEARED.
:*
:*   T2L-L (ADR 08) & T2C-H (ADR 09) ARE BOTH LOADED WITH 252 (OCTAL).
:*   (THIS IS EQUIVALENT TO AAAA (HEX) OR 43,690 (DECIMAL).) LOADING
:*   T2C-H STARTS THE COUNTER.
:*
:*   T2L-L IS LOADED WITH 001 AND T2C-H IS LOADED WITH 000 IN ORDER TO
:*   SET "T2" WITH A QUICK UNDERFLOW. THE "T2" FLAG BIT IN IFR IS READ
:*   AND CHECKED TO BE SET.
:*
:*   T2C-H IS CHECKED TO = 0. CHECKING T2C-H SHOULD NOT HAVE CLEARED "T2"
:*   -- THIS IS VERIFIED.
:*
:*   T2C-L IS CHECKED TO = 0. CHECKING T2C-L SHOULD HAVE CLEARED "T2" --
:*   THIS TOO IS VERIFIED.
:*
:*   T2C-H IS LOADED WITH 0 AGAIN TO INITIATE A NEW COUNT DOWN,(WHICH
:*   SHOULD UNDERFLOW ALMOST IMMEDIATELY) AND THE "T2" BIT IN IFR IS
:*   CHECKED TO BE SET AGAIN.
:*
:*   T2L-L IS LOADED WITH 125 (OCTAL) AND "T2" BIT IS CHECKED TO BE STILL
:*   SET.
:*
:*   T2C-H IS LOADED WITH 125, AND THE "T2" BIT IS READ AND CHECKED TO BE
:*   CLEARED BY THE LOADING OF T2C-H.
:*
:*****
:*   TEST 2 <VIA'S SR INPUT (MODE 2) - SYSTEM CLOCK MODE>
:*
:*   A MASTER CLEAR IS DONE. THEN THE SHIFT REG IS PLACED IN INPUT MODE
:*   UNDER CONTROL OF VIA CLK, BY SETTING ACR BIT 4 TO 0, BIT 3 TO 1, AND BIT 2
:*   TO 0. THE PROGRAM CHECKS FOR THE SR FLAG (BIT 2) IN THE IFR TO BE INITIALLY
:*   CLEARED. THEN, THE SR IS LOADED TO INITIALIZE THE SR OPERATION, AND THE
:*   PROGRAM CHECKS FOR SR FLAG = 1 AFTER ABOUT 8 US. AND READS SR REGISTER TO
:*   VERIFY THAT SHIFTING OCCURRED.
:*****
  
```


PROGRAM DOCUMENT

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853 ;*****
854 ;*      TEST 6 <NPR XFER ABORT>
855 ;*
856 ;* FIRST SUBTEST :
857 ;* THE PROGRAM PERFORMS AN OUTPUT NPR REQUEST TO A NON-EXISTENT MEMORY
858 ;* LOCATION, AND CHECKS FOR THE ASSERTION OF ABORT XFER BIT IN THE NPR CONTROL
859 ;* REGISTER. THEN, AN OUTPUT NPR IS DONE AND CHECKED, TO A LOCATION IN THE
860 ;* PROGRAM, USING 125252 FOR DATA, AND THE PROGRAM CHECKS FOR ABORT XFER TO
861 ;* BE CLEARED BY SETTING THE DONE BIT.
862 ;* SECOND SUBTEST :
863 ;* THE ABOVE SUBTEST IS REPEATED USING INPUT NPR'S.
864 ;*****
865
866
867 ;*****
868 ;*      TEST 7 <NPR EXTENDED ADDRESS BIT TEST>
869 ;*
870 ;* THIS TEST WILL ONLY BE RUN IF THERE IS AT LEAST 32K WORDS OF MEMORY ON THE
871 ;* SYSTEM. IF THERE IS, THE PROGRAM CHOOSES A LOCATION TO USE IN THE ADDRESS
872 ;* RANGE 200000-377776 (OCTAL). THEN, THE FOLLOWING 2 SUBTESTS ARE PERFORMED :
873 ;*
874 ;* FIRST SUBTEST :
875 ;* AN INPUT NPR IS PERFORMED AND CHECKED USING THE MEMORY LOCATION, WITH
876 ;* 125252 FOR DATA. THE PROGRAM CHECKS THAT THE ABORT XFER BIT REMAINS
877 ;* CLEARED.
878 ;* SECOND SUBTEST :
879 ;* AN OUTPUT NPR IS PERFORMED AND CHECKED USING THE MEMORY LOCATION, WITH
880 ;* 125252 FOR DATA. THE PROGRAM CHECKS THAT THE ABORT XFER BIT REMAINS
881 ;* CLEARED.
882 ;*****
883
884
885 ;*****
886 ;*      TEST 8 <SPECIAL MFG EXTENDED BIT TEST>
887 ;*
888 ;* THIS TEST WAS DESIGNED SPECIFICALLY TO ALLOW MANUFACTURING TO CHECK THE
889 ;* NPRAIX/NPRAOX BITS WITHOUT A FULL 4 M. OF MEMORY.
890 ;*
891 ;* IT WILL CHECK THE 12 DMV EXTENDED ADDRESS BITS (6:NPRAIX/6:NPRAOX) ON
892 ;* A Q22 SYSTEM IF MEMORY IS PRESENT AT THE FOLLOWING PHYSICAL ADDRESSES:
893 ;*
894 ;*      17600000      17400000      17200000
895 ;*      16600000      15600000      13600000
896 ;*      7600000
897 ;*
898 ;* FIRST SUBTEST :      TEST "NPRAIX" EXTENDED ADDRESS BITS
899 ;* SECOND SUBTEST :      TEST "NPRAOX" EXTENDED ADDRESS BITS
900 ;*****
901
902
903 ;*****
904 ;*      TEST 9 <Q-BUS INTERRUPT "A" & "B" SELECTION>
905 ;*
906 ;* THIS TEST CONTAINS SUBTESTS IN WHICH A SEQUENCE OF STEPS IS
907 ;* PERFORMED. IN GENERAL, EACH SUBTEST PERFORMS THE FOLLOWING:
908 ;*
909 ;*      1. INTERRUPTS ARE DISABLED FOR BOTH "A" & "B"
  
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PROGRAM DOCUMENT

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: *
: *     2. THE INTERRUPT REQUEST REGISTER IS WRITTEN INTO
: *
: *     3. A TEST IS MADE TO BE SURE THAT NEITHER INTERRUPT OCCURS
: *
: *     4. BOTH INTERRUPTS ARE ENABLES
: *
: *     5. A TEST IS MADE TO BE SURE THAT IF AN INTERRUPT IS EXPECTED, IT IS
: *       RECEIVED AND IF IT ISN'T EXPECTED IT DOESN'T HAPPEN.
: *
: * ALL TESTING IS DONE HERE WITH THE PROCESSOR'S PRIORITY SET AT 0.
: *****
:
: *****
: * TEST 10 <BUS RESET WITH DISABLE INIT SET> .PAGE
: *
: * A BYTE SELECT REGISTER (BSEL3) IS LOADED WITH 377, DISABLE INIT BIT IS SET
: * IN THE NPR CONTROL REGISTER, AND A BUS RESET INSTRUCTION IS EXECUTED. THE
: * PROGRAM THEN CHECKS THAT THE DMV-11 WAS NOT CLEARED, BY CHECKING FOR 377
: * STILL IN BSEL3
: *****
:
: *****
: * TEST 11 <MASTER CLEAR WITH DISABLE INIT SET>
: *
: * THE "DISABL INIT" BIT IN THE NPR CONTROL REGISTER IS SET AND A MASTER CLEAR
: * IS ISSUED. IF THE MASTER CLEAR SUBROUTINE DETECTS AN ERROR, THE MASTER
: * CLEAR WILL NOT HAVE FUNCTIONED PROPERLY. WHERE THE NORMAL ERROR MESSAGE
: * (QUEUED UP BY "MASCLR") IS NORMALLY PRINTED, THIS TEST WILL PRINT ITS OWN
: * INSTEAD.
: *****
:
: *****
: * TEST 12 <DCOK H LO BIT>
: *
: * DCOK H LO IS SET IN THE NPR CONTROL REGISTER WHICH SHOULD CAUSE A VECTOR TO
: * THE FIRST INTERRUPT HANDLER WHERE THE VECTOR IS CHANGED TO POINT TO THE
: * SECOND HANDLER. THIS SECOND HANDLER WILL THEN STALL FOR A WHILE WAITING FOR
: * THE POWER-UP INTERRUPT WHICH SHOULD KICK US INTO THE SECOND HANDLER. IN
: * BOTH HANDLERS FLAGS ARE SET TO SAY THAT WE GOT THERE. WHEN WE FINALLY
: * RETURN TO OUR MAINLINE CODE, WE WILL RESUME THE DELAY FUNCTION WE WERE IN
: * AND THEN CHECK THE FLAGS.
: *
: * IN SUBTEST # 1, WE EXPECT THE DMV TO BE RESET.
: *****
:
: *****
: * TEST 13 <HALT MODE VERIFICATION>
: *
: * THIS TEST CONTAINS TWO (2) SUBTESTS DESIGNED TO VERIFY THE FUNCTIONALITY
: * OF THE "HALT" CONTROL CONTAINED WITHIN THE NPR CONTROL REGISTER. IN EACH
: * CASE, MICROCODE IS LOADED INTO THE DMV IN ORDER TO CONTROL THE TESTING

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PROGRAM DOCUMENT

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967           ; * FROM THERE.
968           ; *
969           ; * -----
970           ; *
971           ; * SUBTEST # 1:
972           ; *
973           ; * HERE WE VERIFY THAT WE CAN CONTROL NPR'S AND DCOK PROPERLY WHILE THE 11 CPU
974           ; * IS HALTED.
975           ; *
976           ; *           11 CPU'S OPERATIONS:                         DMV-11'S OPERATIONS:
977           ; *
978           ; * THE MICROCODE IS MOVED INTO THE DMV.
979           ; *
980           ; * CLEAR TMPO. THIS WILL BE OUR TEST
981           ; * LOCATION FOR THE NPR OPERATION.
982           ; *
983           ; * SETUP FOR POWER-FAIL VECTORING THROUGH
984           ; * LOCATION 24.
985           ; *
986           ; * THE MICROCODE IS INITIATED & BSEL7 IS
987           ; * SET TO -1 AS A FLAG.
988           ; *
989           ; * WAIT FOR BSEL7 TO BE CLEARED
990           ; *
991           ; *
992           ; *
993           ; * SAVE R6 IN OLDSP FOR RECOVERY LATER.
994           ; * CLEAR TMPO, LOAD INTO SEL4 THE
995           ; * ADDRESS OF TMPO, AND SET BSEL7 TO -1.
996           ; *
997           ; * START LOOPING -- INCREMENTING TMPO
998           ; *
999           ; *
1000          ; *
1001          ; *
1002          ; *
1003          ; *           GET THE ADDRESS OF TMPO FROM SEL6
1004          ; *           AND SAVE IT FOR LATER
1005          ; *
1006          ; *           HALT THE 11 CPU.
1007          ; *
1008          ; *           NPR-IN THE CURRENT CONTENTS OF TMPO
1009          ; *           & PUT IT INTO SEL4 (THE FULL WORD).
1010          ; *
1011          ; *           DELAY FOR ABOUT 100 MICROSECONDS
1012          ; *           (THE TIME ISN'T CRITICAL).
1013          ; *
1014          ; *
1015          ; *
1016          ; *           THE 11 CPU SHOULD NOT BE EXECUTING
1017          ; *           ANYTHING NOW -- NOT EVEN "ODT"
1018          ; *
1019          ; *
1020          ; *           DROP THE "HALT" SIGNAL TO RELEASE
1021          ; *           THE 11 CPU AND SET "DCOK H LO" &
1022          ; *           "DISABL INIT". DROP "DCOK H LO"
1023          ; *
1024          ; * WE SHOULD GO
1025          ; * THROUGH A POWER-UP SEQUENCE. R6 IS
1026          ; * RESTORED FROM OLDSP, INTERRUPT
1027          ; * PRIORITY LEVEL IS RESTORED TO 0, &
1028          ; * INTERRUPT VECTOR 24 IS RETURNED TO
1029          ; * THE DIAGNOSTIC SUPERVISOR. SEL4 IS
1030          ; * COMPARED AGAINST TMPO -- THEY SHOULD
1031          ; * BE EQUAL.

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PROGRAM DOCUMENT

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8.0 ERROR INFORMATION

8.1 ERROR REPORTING

ERRORS ARE REPORTED BY THE PROGRAM AS THEY OCCUR (IF NOT INHIBITED). THE REPORT CONFORMS TO THE DIAGNOSTIC SUPERVISOR ERROR REPORT FORMAT, AND CONSISTS OF A DESCRIPTION OF THE ERROR, THE TEST NUMBER, SUBTEST NUMBER, PC OF THE ERROR CALL, DEVICE ADDRESS, AND BASIC AND EXTENDED ERROR INFORMATION.

THE FOLLOWING EXAMPLE PROVIDES A TYPICAL ERROR REPORT, WHICH DESCRIBES A "MASTER CLEAR FAILURE" ERROR, AND PROVIDES THE PC OF THE ERROR CALL AND THE DEVICE REGISTER CONTENTS :

CNDMB DVC FTL ERR 00001 ON UNIT 00 TST 002 SUB 000 PC: 021122
MASTER CLEAR FAILURE

THE CONTENTS OF ALL BYTE SELECT REG'S ARE:
BSEL0 BSEL1 BSEL2 BSEL3
000 000 000 000
BSEL4 BSEL5 BSEL6 BSEL7
000 000 121 000
BSEL10 BSEL11 BSEL12 BSEL13
000 000 000 000
BSEL14 BSEL15 BSEL16 BSEL17
000 000 000 000

FOR OTHER ERRORS, THE REPORT MAY BE MORE EXTENSIVE, AND REQUIRE ADDITIONAL DATA TO BE REPORTED.

IF EXTENDED ERROR INFORMATION HAD BEEN INHIBITED USING THE IXE FLAG PRIOR TO RUNNING THE TEST, THE ABOVE ERROR WOULD HAVE BEEN REPORTED IN THE FOLLOWING SHORTENED FORM :

CNDMB DVC FTL ERR 00001 ON UNIT 00 TST 002 SUB 000 PC: 021122
MASTER CLEAR FAILURE

†

LISTING & ASSEMBLY CONTROL

```

1170      .SBTTL LISTING & ASSEMBLY CONTROL
1171
1172      000000      HELP=0      ; CONTROL LISTING OF HELP INFORMATION
1173                                     ; HELP=0   NO LIST
1174                                     ; HELP=1   LIST
1175
1181      002000      . =2000
1182
1183      .MCALL SVC
1184 002000      SVC      ; INITIALIZE SUPERVISOR MACROS
1185
1186 002000      BGNMOD LU1MOD
1187
1188
1189      000001      $LSTIN= 1
1190      000001      $LSTTAG= 1
1191      000001      SVCINS= 1      ; LIST INSTRUCTIONS, SHIFTED RIGHT
1192      000001      SVCTST= 1     ; LIST TEST TAGS, SHIFTED RIGHT
1193      000001      SVCSUB= 1    ; LIST SUBTEST TAGS, SHIFTED RIGHT
1194      000001      SVCGBL= 1   ; LIST GLOBAL TAGS, SHIFTED RIGHT
1195      000001      SVCTAG= 1   ; LIST OTHER TAGS, SHIFTED RIGHT
1196
1197      ; CHANGE THE VALUES OF THE SVC... SYMBOLS TO BE ZERO IF YOU WISH
1198      ; TO ALIGN THE MACRO CALLS AND THEIR EXPANSIONS. CHANGE THE
1199      ; SYMBOLS TO BE MINUS-ONE TO NOT LIST THE EXPANSIONS. YOU MAY
1200      ; CHANGE THE SYMBOLS AT ANY POINT IN YOUR PROGRAM.
1201
1202 002000      POINTER BGNAU,BGNDU,ERRTBL
1203

```


M2

PROGRAM HEADER

002052	
002052	000000
002054	000000
002056	
002056	000000
002060	
002060	003254
002062	
002062	000000
002064	
002064	000000
002066	
002066	000000
002070	
002070	023120
002072	
002072	023114
002074	
002074	000000
002076	
002076	003274
002100	
002100	104035
002102	
002102	002202
002104	
002104	022020
002106	
002106	023076
002110	
002110	022752
002112	
002112	022012
002114	
002114	000000
002116	
002116	000000
002120	
002120	000000

1223
1229

.EVEN

L\$EF::		
	.WORD	0
	.WORD	0
L\$SPC::		
	.WORD	0
L\$DEVP::		
	.WORD	L\$DVTYP
L\$REPP::		
	.WORD	0
L\$EXP4::		
	.WORD	0
L\$EXP5::		
	.WORD	0
L\$AUT::		
	.WORD	L\$AU
L\$DUT::		
	.WORD	L\$DU
L\$LUN::		
	.WORD	0
L\$DESP::		
	.WORD	L\$DESC
L\$LOAD::		
	EMT	E\$LOAD
L\$ETP::		
	.WORD	L\$ERRTBL
L\$ICP::		
	.WORD	L\$INIT
L\$CCP::		
	.WORD	L\$CLEAN
L\$ACP::		
	.WORD	L\$AUTO
L\$PRT::		
	.WORD	L\$PROT
L\$TEST::		
	.WORD	0
L\$DLY::		
	.WORD	0
L\$HIME::		
	.WORD	0

SOFTWARE P-TABLE

1280
 1281
 1282
 1283
 1284
 1285
 1286
 1287 002200
 002200 000000
 002202
 002202
 1288 002202
 002202

.SBTTL SOFTWARE P-TABLE

```

;////////////////////////////////////
;/ THE SOFTWARE P-TABLE CONTAINS THE VALUES OF THE PROGRAM
;/ PARAMETERS THAT CAN BE CHANGED BY THE OPERATOR.
;////////////////////////////////////
  
```

BGNSW SFPTBL

ENDSW

```

              .WORD L10001-L$SW/2
L$SW::
SFPTBL::
L10001:
  
```

GLOBAL EQUATES SECTION

1290
1291
1292
1293
1294
1295
1296
1297
1298 002202

.SBTTL GLOBAL EQUATES SECTION

```
;/
;/   THE GLOBAL EQUATES SECTION CONTAINS PROGRAM EQUATES THAT
;/   ARE USED IN MORE THAN ONE TEST.
;/
;/
```

EQUALS

;
; BIT DIFINITIONS

100000	BIT15	==	100000
040000	BIT14	==	40000
020000	BIT13	==	20000
010000	BIT12	==	10000
004000	BIT11	==	4000
002000	BIT10	==	2000
001000	BIT09	==	1000
000400	BIT08	==	400
000200	BIT07	==	200
000100	BIT06	==	100
000040	BIT05	==	40
000020	BIT04	==	20
000010	BIT03	==	10
000004	BIT02	==	4
000002	BIT01	==	2
000001	BIT00	==	1

;
; BIT9== BIT09
; BIT8== BIT08
; BIT7== BIT07
; BIT6== BIT06
; BIT5== BIT05
; BIT4== BIT04
; BIT3== BIT03
; BIT2== BIT02
; BIT1== BIT01
; BIT0== BIT00

;
; EVENT FLAG DEFINITIONS
; EF32:EF17 RESERVED FOR SUPERVISOR TO PROGRAM COMMUNICATION

000040	EF.START	==	32.	;	BIT POSITION IN SECOND STATUS WORD
000037	EF.RESTART	==	31.	;	(100000) START COMMAND WAS ISSUED
000036	EF.CONTINUE	==	30.	;	(040000) RESTART COMMAND WAS ISSUED
000035	EF.NEW	==	29.	;	(020000) CONTINUE COMMAND WAS ISSUED
000034	EF.PWR	==	28.	;	(010000) A NEW PASS HAS BEEN STARTED
				;	(004000) A POWER-FAIL/POWER-UP OCCURRED

;
; PRIORITY LEVEL DEFINITIONS

000340	PRI07	==	340
000300	PRI06	==	300
000240	PRI05	==	240
000200	PRI04	==	200

GLOBAL EQUATES SECTION

000140	PRI03== 140	
000100	PRI02== 100	
000040	PRI01== 40	
000000	PRI00== 0	
	; OPERATOR FLAG BITS	
	; EVL== 4	
000004	LOT== 10	
000010	ADR== 20	
000020	IDU== 40	
000040	ISR== 100	
000100	UAM== 200	
000200	BOE== 400	
000400	PNT== 1000	
001000	PRI== 2000	
002000	IXE== 4000	
004000	IBE== 10000	
010000	IER== 20000	
020000	LOE== 40000	
040000	MOE== 100000	
100000		
1299		
1300	.SBTTL DEFINE THE NUMBER OF CSR'S	
1301	CSREGS = 8.	
1302		
1303		
1304		
1305	.SBTTL NPR ADDRESS REGISTER EQUATES	
1306	NPRAOL = 70	;OUT NPR ADRS LO REG
1307	NPRAOH = NPRAOL+1	;OUT NPR ADRS HI REG
1308	NPRAOX = NPRAOL+2	;OUT NPR EXTENDED ADRS REG
1309	NPRAIL = NPRAOL+4	;IN NPR ADRS LO REG
1310	NPRAIH = NPRAOL+5	;IN NPR ADRS HI REG
1311	NPRAIX = NPRAOL+6	;IN NPR EXTENDED ADRS REG
1312	NPRBS7 = BIT7	; "BANK SELECT 7" BIT -- W/IN EXTENDED ADRS. REG.
1313		
1314		
1315		
1316	.SBTTL NPR DATA REG EQUATES	
1317	NPRDRL = 123000	;NPR DATA REGISTER -- LOW BYTE
1318	NPRDRH = NPRDRL+1	;NPR DATA REGISTER -- HIGH BYTE
1319		
1320		
1321		
1322	.SBTTL NPR CONTROL REG EQUATES	
1323	NPRCTL = NPRDRL+4	;NPR CONTROL REGISTER
1324	NPRABT = BIT7	;=1 IF BUS TIME-OUT ON NPR
1325	NPRGO = BIT6	;SET FOR NOP, CLEAR TO "GO" / 0=DONE, 1=BUSY
1326	NPRIO = BIT5	;0 = (LSI ==> DMV); 1 = (DMV ==> LSI)
1327	LSIHLT = BIT4	;SETTING THIS WILL "HALT" THE LSI-11 !!
1328	NPRBYT = BIT3	;SET TO 1 TO WRITE BYTE ONLY TO LSI-11
1329	DMVPU = BIT2	;SET BY MICRO-DIAG. MUST REMAIN SET!!!
1330	LSIDCL = BIT1	;IF SET, WILL CAUSE POWER DOWN CONDITION IN LSI!
1331	DMVDAI = BIT0	; "DISABLE INIT" FROM EFFECTING DMV-11
1332		
1333	.SBTTL NPR REQUEST FUNCTIONS	
1334		

NPR REQUEST FUNCTIONS

```
1335      000004      NPRLD      = DMVPU      ;WORD XFER: LSI ==> DMV
1336      000044      NPRDL      = DMVPU!NPRIO    ;WORD XFER: DMV ==> LSI
1337      000054      NPRDLB     = DMVPU!NPRIO!NPRBYT ;BYTE XFER: DMV ==> LSI
1338
1339      ;-----
1340
1341      .SBTTL INTERRUPT REG EQUATES
1342      123005      IRQREG     = 123005      ;INTERRUPT REQUEST REG
1343      000004      IRQA      = BIT2      ;REQUEST BIT FOR XX0 INTERRUPT -- "A"
1344      000002      IRQB      = BIT1      ;REQUEST BIT FOR XX4 INTERRUPT -- "B"
1345
1346      ;-----
1347
1348      .SBTTL CONTROL FLAGS FROM P-TABLE ENTRIES
1349      000001      PU24      = BIT0      ;POWER-FAIL VECTURING MODE. 1 = MODE 0
1350                                  ; (I.E. JUMPERS W5 & W6 BOTH REMOVED)
```

SWITCH PACKS

1352
1353
1354
1355
1356
1357
1358
1359
1360

121000
121400

.SBTTL SWITCH PACKS

;* SWITCH PACKS

SWPBOT = 121000
SWPDDCMP = 121400

; "BOOT ADDRESS" SWITCH PACK [A200]
; "DDCMP ADDRESS" SWITCH PACK [A300]

DMV INTERNAL ADDRESSES

```

1396      .SBTTL  DMV INTERNAL ADDRESSES
1397
1398      ;+*****
1399      ;      DMV INTERNAL ADDRESSES
1400      ;--*****
1401
1402
1403      ;##### << MICROPROCESSOR REGISTER ADDRESS EQUATES >> #####
1404
1405      .SBTTL      BYTE & WORD SELECT REGISTERS
1406
1407      000020      SLT0      =020
1408      000020      BSLT0     =SLT0
1409      000021      BSLT1     =SLT0+1
1410      000022      SLT2      =SLT0+2
1411      000022      BSLT2     =SLT0+2
1412      000023      BSLT3     =SLT0+3
1413      000024      SLT4      =SLT0+4
1414      000024      BSLT4     =SLT0+4
1415      000025      BSLT5     =SLT0+5
1416      000026      SLT6      =SLT0+6
1417      000026      BSLT6     =SLT0+6
1418      000027      BSLT7     =SLT0+7
1419
1420      .SBTTL      VIA'S REGISTERS
1421
1422      120000      ORB        =120000
1423      120001      ORA        =ORB+1
1424      120002      DDRB      =ORB+2
1425      120003      DDRA      =ORB+3
1426      120004      T1CL     =ORB+4
1427      120005      T1CH     =ORB+5
1428      120005      T1LHGO   =ORB+5
1429      120006      T1LL     =ORB+6
1430      120007      T1LH     =ORB+7
1431      120010      T2LL     =ORB+10
1432      120010      T2CL     =T2LL
1433      120011      T2CH     =ORB+11
1434      120012      SR        =ORB+12
1435      120013      ACR      =ORB+13
1436      120014      PCR      =ORB+14
1437      120015      IFR      =ORB+15
1438      120016      IENR     =ORB+16
1439      120017      ORAM     =ORB+17
1440
1441      .SBTTL      VIA'S "IFR" REGISTER'S BIT ASSIGNMENTS
1442
1443      000200      IFRIRQ    =BIT7   ; "IRQ" HAS BEEN ISSUED -- LOGICAL "OR" OF BITS 0 --> 6
1444      000100      IFRT1     =BIT6   ; "T1" -- TIMER # 1 TIMED-OUT
1445      000040      IFRT2     =BIT5   ; "T2" -- TIMER # 1 TIMED-OUT
1446      000020      IFRCB1    =BIT4   ; "CB1" EDGE DETECTED ("K2 LINE UNIT STEP" O/P SIGNAL FROM SR)
1447      000010      IFRCB2    =BIT3   ; "CB2" EDGE DETECTED (UNUSED!)
1448      000004      IFRSR     =BIT2   ; "SR" REGISTER COMPLETED SHIFT OPERATION
1449      000002      IFRCA1    =BIT1   ; "CA1" EDGE DETECTED ("K6 MOD RDY H")
1450      000001      IFRCA2    =BIT0   ; "CA2" EDGE DETECTED ("K2 CTS H")
1451
1452

```

J3

NTST -- BASIC TEXT FOR STARTING EACH TEST

1701

;-----

.INITT2 -- INITIALIZE TIMER # 2

```

2495 004724 002440                TMPB
2496 004726 103424                BCS      63$      ;EXIT ON ERROR
2497
2498 004730 113701 002440          MOVB     TMPB,R1   ;GET THAT VALUE
2499 004734 042701 000040          BIC     #BIT5,R1  ;CLEAR THE CURRENT SETTING OF BIT 5
2500 004740 150137 002441          BISB    R1,TMPB+1 ;SET REMAINING BITS IN THE VALUE TO BE WRITTEN
2501
2502 004744 004537 004042          JSR     R5,WRITE  ;WRITE TO
2503 004750 120013                    ACR     ;THE VIA'S ACR
2504 004752 002441          TMPB+1
2505 004754 103411                BCS      63$      ;EXIT ON ERROR
2506
2507 004756 004537 004042          JSR     R5,WRITE  ;WRITE TO
2508 004762 120010                    T2LL   ;LOW ORDER LATCH & COUNTER (T2L-L)
2509 004764 002433                    TMPB+1 ;THE PASSED VALUE
2510 004766 103404                BCS      63$      ;EXIT ON ERROR
2511
2512 004770 004537 004042          JSR     R5,WRITE  ;WRITE TO
2513 004774 120011                    T2CH   ;HIGH ORDER COUNTER (T2C-M) <ALSO STARTS CTR>
2514 004776 002435                    TMP9+1 ;THE PASSED VALUE
2515
2516                               ; DON'T WAIT AROUND FOR ANYTHING TO HAPPEN -- JUST (JEST) RETURN!
2517
2518 005000 012601          63$: MOV     (SP)+,R1 ;BUT FIRST RESTORE R1
2519 005002 000205          RTS     R5      ;THEN RETURN
2520

```

MOVSW -- MOVE A STRING OF WORDS

2522
2523
2524
2525
2526
2527
2528
2529
2530
2531
2532
2533
2534
2535 005004 010146
2536 005006 010246
2537 005010 010346
2538
2539 005012 012501
2540 005014 012502
2541 005016 012503
2542
2543 005020 012122
2544 005022 077302
2545
2546
2547 005024 012603
2548 005026 012602
2549 005030 012601
2550
2551 005032 000205
2552

```

.SBTTL MOVSW -- MOVE A STRING OF WORDS
;*****
; MOVSW -- MOVE A STRING OF WORDS
;
;         CALLING SEQUENCE:
;
;         JSR     R5,MOVSW
;         .WORD   <ADDRESS OF SOURCE STRING>
;         .WORD   <ADDRESS OF DESTINATION STRING>
;         .WORD   <# OF WORDS TO MOVE>
;-----
MOVSW:  MOV     R1,-(SP)             ;SAVE THE REGISTERS WE'LL BE USING
       MOV     R2,-(SP)
       MOV     R3,-(SP)
              MOV     (R5)+,R1         ;INITIALIZE SOURCE POINTER
              MOV     (R5)+,R2         ;         DESTINATION POINTER
              MOV     (R5)+,R3         ;         COUNTER
1$:     MOV     (R1)+,(R2)+         ;MOVE IN 1 WORD OF DATA
          SOB     R3,1$             ;IF MORE DATA, LOOP
                  ;ELSE, RESTORE REGISTERS AND RETURN
              MOV     (SP)+,R3         ;RESTORE REGISTERS
              MOV     (SP)+,R2
              MOV     (SP)+,R1
              RTS     R5             ;RETURN TO CALLING ROUTINE
  
```


XORSW -- XOR TWO WORD TABLES

2585
2586
2587
2588
2589
2590
2591
2592
2593
2594
2595
2596
2597
2598
2599 005064 010146
2600 005066 010246
2601 005070 010346
2602 005072 010446
2603
2604 005074 012501
2605 005076 012502
2606 005100 012503
2607 005102 012504
2608
2609 005104 010546
2610
2611 005106 012113
2612 005110 012205
2613 005112 074523
2614 005114 077404
2615
2616
2617 005116 012605
2618 005120 012604
2619 005122 012603
2620 005124 012602
2621 005126 012601
2622
2623 005130 000205
2624
2625
2626
2627
2628
2629
2630
2631 005132 000207
2632
2633

```

.SBTTL XORSW -- XOR TWO WORD TABLES
;*****
; XORSW -- DEVELOP THE EXCLUSIVE OR'S BETWEEN TWO STRINGS OF WORDS
;
;   CALLING SEQUENCE:
;
;   JSR   R5,XORSW
;   .WORD <ADDRESS OF FIRST SOURCE STRING>
;   .WORD <ADDRESS OF SECOND SOURCE STRING>
;   .WORD <ADDRESS OF "XOR" STRING>
;   .WORD <# OF BYTES TO MOVE>
;*****
XORSW:  MOV   R1,-(SP)           ;SAVE THE REGISTERS WE'LL BE USING
        MOV   R2,-(SP)
        MOV   R3,-(SP)
        MOV   R4,-(SP)
        MOV   (R5)+,R1         ;INITIALIZE SOURCE POINTER # 1
        MOV   (R5)+,R2         ;        SOURCE POINTER # 2
        MOV   (R5)+,R3         ;        "XOR" STRING POINTER
        MOV   (R5)+,R4         ;        COUNTER
        MOV   R5,-(SP)         ;NOW WE CAN SAVE R5 FOR THE RETURN
1$:     MOV   (R1)+,(R3)        ;MOVE ONE WORD TO THE DESTINATION FIELD
        MOV   (R2)+,R5        ;GET SECOND WORD & SETUP FOR XOR INSTRUCTION
        XOR   R5,(R3)+        ;PERFORM ACTUAL XOR
        SOB   R4,1$           ;IF MORE DATA, LOOP
                                           ;ELSE, RESTORE REGISTERS AND RETURN
        MOV   (SP)+,R5        ;RESTORE REGISTERS
        MOV   (SP)+,R4
        MOV   (SP)+,R3
        MOV   (SP)+,R2
        MOV   (SP)+,R1
        RTS   R5              ;RETURN TO CALLING ROUTINE

.SBTTL STALL -- DELAY FOR 10.5 MICRO-SEC'S (ON LSI-11)
;*****
; STALL -- THIS SUBROUTINE STALLS FOR ABOUT 10.5 MICRO-SECONDS
;*****
STALL:  RTS   PC

```


NPRMOV -- WORD/BYTE BLOCK MOVE USING THE NPR HARDWARE

```

2845 005774 010237 006006      MOV      R2,52$      ;POINT TO LSI'S I/P BUFFER
2846 006000 004537 003616      JSR      R5,READ    ;GET ONE BYTE
2847 006004 123001              NPRDRM              ;   FROM THE HIGH ORDER HALF OF THE DATA REG.
2848 006006 000000      52$:      0              ;*** MODIFIED FROM ABOVE *** DESTINATION ADDR.
2849 006010 103422      BCS      63$      ;ON ERROR, EXIT
2850 006012 005202      INC      R2        ;POINT TO NEXT BYTE OF THE BUFFER
2851 006014 000411      BR       56$      ;DONE RETRIEVING DATA -- CHECK FOR MORE
2852
2853      ; - - - - - DMV ==> LSI -- JUST ADVANCE LSI-11 ADDRESS - - - - -
2854
2855 006016 005202      54$:      INC      R2        ;BUMP THE LSI-11 ADDRESS
2856 006020 032737 000010 005612      BIT      #NPRBYT,42$ ;IS THIS A BYTE OR WORD TRANSFER?
2857 006026 001004      BNE      56$      ;BYTE, THEN ADDRESS IS OK AS IS
2858 006030 005202      INC      R2        ;WORD, BUMP ADDR. -- WE ALREADY DID THE HIGH BYTE
2859 006032 000402      BR       56$
2860
2861      ; - - - - - TEST FOR MORE - - - - -
2862
2863 006034 000137 005354      55$:      JMP      6$        ;THIS LITTLE BIT IF CUTE LOGIC IS NECESSARY
2864                                ;BECAUSE "6$" IS TOO FAR AWAY FOR A BRANCH!
2865 006040 077303      56$:      SOB      R3,55$   ;DO IT AGAIN IF THERE IS MORE DATA
2866 006042 005737 002276      TST      ERRFLG    ;WAS AN ERROR DETECTED?
2867 006046 001402      BEQ      61$      ;NO, TAKE NORMAL EXIT
2868
2869      ; - - - - - CLEAN UP & EXIT - - - - -
2870
2871 006050 000261      60$:      SEC              ;INDICATE ERROR CONDITION
2872 006052 000401      BR       63$
2873
2874 006054 000241      61$:      CLC              ;INDICATE NO ERROR
2875
2876 006056 012603      63$:      MOV      (SP)+,R3   ;RESTORE THE REGISTERS AGAIN
2877 006060 012602      MOV      (SP)+,R2
2878 006062 012601      MOV      (SP)+,R1
2879
2880 006064 000205      RTS      R5        ;RETURN

```

INTERRUPT HANDLER -- MPIHAN

```

2882      .SBTTL  INTERRUPT HANDLER -- MPIHAN
2883
2884      ;*****
2885      ; MPIHAN -- COUNT INTERRUPTS -- USUALLY INTERRUPT "A"
2886      ;
2887      ;     THIS ROUTINE WILL INCREMENT THE LOW BYTE OF "INTFLG" EACH TIME IT IS
2888      ;     ENTERED.  IF "IHILNK" IS NON-ZERO, VECTOR TO THE ADDRESS THEREIN USING
2889      ;     A "JSR PC"
2890      ;-----*****
2891
2892      006066      BGNSRV  MPIHAN
2893      006066      MOV      RO, -(SP)      ;SAVE RO
2894      006070      TSTB   INTWCH      ;HAVE WE BEEN TOLD TO WATCH FOR TYPE "A" INT'S?
2895      006074      BNE    5$          ;YES, DO NORMAL INTERRUPT PROCESSING
2896      006076      JSR    PC,GETBSR   ;NO, DUMP REGISTERS AND
2897      006102      GEDF   EM34,ERR3   ;     REPORT "UNEXPECTED INTERRUPT"
2898      006102      TRAP   C$ERDF     ;     "DEVICE FATAL" ERROR # 7
2899      006104      .WORD  7
2900      006106      .WORD  EM34
2901      006110      .WORD  ERR3
2902      006112      BR     10$        ;GO TO EXIT
2903      5$:      INCB   INTFLG      ;INCREMENT LOW BYTE OF INTERRUPT COUNTER
2904      006114      TST   IHILNK     ;ARE WE EXPECTED TO EXECUTE ANOTHER ROUTINE?
2905      006120      BEQ   10$        ;NO, GET OUT
2906      006124      JSR   PC,@IHILNK  ;YES, GO TO IT -- I HOPE IT'S VALID!
2907      006126      MOV   (SP)+,RO   ;RESTORE RO
2908      006132      ENDSRV          ;RETURN TO INTERRUPTED PROCESS
2909      006134      L10002:      RTI
2910      006136      IHILNK: .WORD  0      ;POINTER TO AUXILIARY INT. HANDLING ROUTINE

```


ERROR HANDLER SUBROUTINE -- ERR10\$ & ERR10.

```

013014 005046
013016 153716 002256
013022 005046
013024 153716 002254
013030 012746 013127
013034 012746 000004
013040 010600
013042 104415
013044 062706 000012
3243 013050          PRINTX  #FMT09A,TDATA  ;LSI ADDRESS
013050 013746 002252
013054 012746 013673
013060 012746 000002
013064 010600
013066 104415
013070 062706 000006
3244 013074 000207          RTS      PC
3245
3246
3247
3248
3249
3250 013076          NULERR: PRINTB  #ENDEMB          ;TERMINATE ERROR MESSAGE
013076 012746 013120
013102 012746 000001
013106 010600
013110 104414
013112 062706 000004
3251 013116 000207          RTS      PC
3252
;-----
;SBTTL          SUBROUTINE TO PERFORM "PRINTB  #ENDEMB"
;-----
MOV      #ENDEMB,-(SP)
MOV      #1,-(SP)
MOV      SP,RO
TRAP    C$PNTB
ADD      #4,SP

```


ERROR MESSAGES -- "EM_..."

3391	021151	042	124	062	EM51G:	.ASCIZ	\ "T2" FLAG CLEARED BY READING T2CH\
3392	021213	042	124	062	EM51L:	.ASCIZ	\ "T2" FLAG NOT SET AFTER RE-LOADING T2CH & TIMEOUT\
3393	021275	042	124	062	EM51M:	.ASCIZ	\ "T2" FLAG CLEARED BY LOADING T2LL\
3394	021337	042	124	062	EM51N:	.ASCIZ	\ "T2" FLAG NOT CLEARED BY LOADING T2CH\
3395	021405	042	124	062	EM51P:	.ASCIZ	\ "T2" FLAG NOT SET AFTER APPROPRIATE DELAY\
3396	021457	042	123	122	EM52A:	.ASCIZ	\ "SR" FLAG SET BEFORE ACCESSING SHIFT REGISTER\
3397	021535	116	117	040	EM52B:	.ASCIZ	\ NO "SR" INT. USING MODE 2\
3398	021567	111	116	103	EM52C:	.ASCIZ	\ INCOMPLETE SHIFTING OPERATION IN MODE 2 -- GOT INT.\
3399	021653	116	117	040	EM52D:	.ASCIZ	\ NO "SR" INT. AFTER READING SR\
3400	021711	104	115	126	EM60N:	.ASCIZ	/DMV EXTENDED NPR WRITE ERROR/
3401							
3402						.EVEN	

TEXT ADDRESS TABLES FOR ERROR HANDLERS -- "TXT_T"

```

3404                                    .SBTTL  TEXT ADDRESS TABLES FOR ERROR HANDLERS -- "TXT_T"
3405                                    :-----
3406                                    :----- TEXT ADDRESS TABLES USED BY ERROR HANDLERS -----
3407                                    :-----
3408
3409 021746 015515 015521 015535 TXTMLT: .WORD  TXTML0,TXTML1,TXTML2,TXTML3,TXTML4,TXTML5,TXTML6,TXTML7
3410
3411 021766 015745                    .WORD  TXTNP
3412 021770 015752 015762 015772 TXTNPT: .WORD  TXTNP0,TXTNP1,TXTNP2,TXTNP3,TXTNP4,TXTNP5,TXTNP6,TXTNP7,TXTNP8
3413
3414                                    .LIST  BEX

```

LOAD DEVICE PROTECTION TABLE

.SBTTL LOAD DEVICE PROTECTION TABLE

```

://////
:// THIS TABLE IDENTIFIES THE LOAD DEVICE TO THE SUPERVISOR, SO THAT IT CAN BE
:// PROTECTED FROM TESTING. IF DESIRED.
://////

```

```

3416
3417
3418
3419
3420
3421
3422
3423 022012
      022012
3424 022012 177777
3425 022014 177777
3426 022016 177777
3427 022020

```

```

BGNPROT
      .WORD -1      ;DON'T CHK CSR ADRS
      .WORD -1      ;DON'T CHK MASSBUS UNIT NO.
      .WORD -1      ;DON'T CHK DRIVE NO.
ENDPROT
L$PROT::

```


D8

AUTO DROP UNIT SECTION

3615 023074 000002
3616

RTI

;RETURN

DROP UNIT SECTION

3630
 3631
 3632
 3633
 3634
 3635
 3636
 3637 023114
 023114
 3638
 3639 023114
 023114 104433
 3640 023116
 023116
 023116 104453

.SBTTL DROP UNIT SECTION

```

:////////////////////
:// THE DROP-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
:// TO NO LONGER BE TESTED.
:////////////////////

```

```

          BGNDU
:ISSUE UNIBUS RESET TO CLEAN UP
          BRESET
          ENDDU

```

```

L$DU::
          TRAP   C$RESET
L10025: TRAP   C$DU

```

ADD UNIT SECTION

3642
 3643
 3644
 3645
 3646
 3647
 3648
 3649
 3650 023120
 023120
 3651 023120
 023120
 023120 104452

.SBTTL ADD UNIT SECTION

```

;////////////////////////////////////
;/ THE ADD-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
;/ TO BE (A) TESTED FOR THE FIRST TIME, OR (B) RESUMED IN TESTING. IF
;/ "EF.AUNIT" IS SET, THE UNIT WILL BE TESTED AS A NEW UNIT.
;////////////////////////////////////

```

BGNAU
 ENDAU

L\$AU::
 L10026: TRAP C\$AU

TEST 8 -- SPECIAL MFG EXTENDED BIT TEST

```

        032306 104455
        032310 000061
        032312 016437
        032314 007456
5204 032316 000404
5205 032320
                                :           "DEVICE FATAL" ERROR # 49
                                TRAP      C$ERDF
                                .WORD     49
                                .WORD     EM27A
                                .WORD     ERR12
                                ;AND EXIT
                                ;HANG, REPORT IT AS SUCH.
                                ;           "DEVICE FATAL" ERROR # 50
                                TRAP      C$ERDF
                                .WORD     50
                                .WORD     EM27B
                                .WORD     ERR12
        032320 104455
        032322 000062
        032324 016461
        032326 007456
5206 032330
12$:  ESCAPE  SUB                ; AND EXIT SUBTEST
                                TRAP      C$ESCAPE
                                .WORD     L10050-.
        032330 104410
        032332 000124
5207
5208
;***** READ EXTENDED MEM INTO LOCAL RAM (RXVAL0-6) *****
5209 032334 005002          14$:  CLR      R2                ;CLEAR LOCAL INDEX
5210 032336 016237 032462 002436 15$:  MOV      XLOC0(R2),TMPA ;SETUP/ADJUST PAR VALUE
5211 032344 006337 002436          ASL      TMPA
5212 032350 006337 002436          ASL      TMPA
5213 032354 004537 030734          JSR      R5,XMREAD          ;READ EXTENDED MEM BACKGROUND PATTERN
5214 032360 002436          TMPA          ; POINTER TO PAR VALUE
5215 032362 002440          TMPB          ; POINTER TO DATA STORAGE
5216 032364 013762 002440 032516  MOV      TMPB,RXVAL0(R2) ;# SAVE ACTUAL EXTENDED DATA
5217 032372 062702 000002          ADD      #2,R2          ;BUMP INDEX
5218 032376 020227 000016          CMP      R2,#14.        ;ALL 'XLOC' EXTENDED ADDRESSES READ?
5219 032402 001355          BNE     15$            ; NO: READ ANOTHER
5220
5221
;***** NOW CHECK EXPECTED VS. ACTUAL EXT. MEM VALUES *****
5222 032404 005002
5223 032406 026262 032516 032500 16$:  CLR      R2
5224 032414 001406          CMP      RXVAL0(R2),XVAL0(R2)
5225 032416
                                BEQ      17$
                                GEDF     EM60N,ERR60
                                ;           "DEVICE FATAL" ERROR # 51
                                TRAP      C$ERDF
                                .WORD     51
                                .WORD     EM60N
                                .WORD     ERR60
        032416 104455
        032420 000063
        032422 021711
        032424 011720
5226 032426
                                ESCAPE  SUB
                                TRAP      C$ESCAPE
                                .WORD     L10050-.
        032426 104410
        032430 000026
5227 032432 005722
17$:  TST      (R2).          ;BUMP LOCAL INDEX
5228 032434 020227 000016          CMP      R2,#14.        ;ALL VALUES CHECKED ?
5229 032440 001362          BNE     16$
5230
20$:  TST      (R4).
5231 032442 005724          CMP      R4,#14.
5232 032444 020427 000016          BEQ     63$
5233 032450 001402          JMP     T8LP
5234 032452 000137 031734          ; YES: BUMP INDEX
5235 032456
63$:  ENDSUB          ;ARE WE DONE W/ALL EXTENDED LOCATIONS
                                ; YES: END
                                ; NO: GO DO SOME MORE
                                L10050:  TRAP      C$ESUB
                                .WORD     C$ESUB
5236 032456 104403
MFEND:  ENDTST
                                L10046:  TRAP      C$ETST
                                .WORD     C$ETST
        032460
        032460 104401
5237

```

TEST 8 -- SPECIAL MFG EXTENDED BIT TEST

5238					
5239	032462	037400	XLOC0:	37400	; ADDRESS 17600000 POINTER
5240	032464	037000	XLOC1:	37000	; ADDRESS 17400000 POINTER
5241	032466	036400	XLOC2:	36400	; ADDRESS 17200000 POINTER
5242	032470	035400	XLOC3:	35400	; ADDRESS 16600000 POINTER
5243	032472	033400	XLOC4:	33400	; ADDRESS 15600000 POINTER
5244	032474	027400	XLOC5:	27400	; ADDRESS 13600000 POINTER
5245	032476	017400	XLOC6:	17400	; ADDRESS 07600000 POINTER
5246					
5247	032500	000000	XVAL0:	0	
5248	032502	000000	XVAL1:	0	
5249	032504	000000	XVAL2:	0	
5250	032506	000000	XVAL3:	0	
5251	032510	000000	XVAL4:	0	
5252	032512	000000	XVAL5:	0	
5253	032514	000000	XVAL6:	0	
5254					
5255	032516	000000	RXVAL0:	0	
5256	032520	000000	RXVAL1:	0	
5257	032522	000000	RXVAL2:	0	
5258	032524	000000	RXVAL3:	0	
5259	032526	000000	RXVAL4:	0	
5260	032530	000000	RXVAL5:	0	
5261	032532	000000	RXVAL6:	0	

TEST 9 -- Q-BUS INTERRUPT "A" & "B" SELECTION

5282

.SBTTL TEST 9 -- Q-BUS INTERRUPT "A" & "B" SELECTION

```

;+*****
;+
;+ TEST 9 -- Q-BUS INTERRUPT "A" & "B" SELECTION
;+
;+ THIS TEST CONTAINS SUBTESTS IN WHICH A SEQUENCE OF STEPS IS
;+ PERFORMED. IN GENERAL, EACH SUBTEST PERFORMS THE FOLLOWING:
;+
;+ 1. INTERRUPTS ARE DISABLED FOR BOTH "A" & "B"
;+
;+ 2. THE INTERRUPT REQUEST REGISTER IS WRITTEN INTO
;+
;+ 3. A TEST IS MADE TO BE SURE THAT NEITHER INTERRUPT OCCURS
;+
;+ 4. BOTH INTERRUPTS ARE ENABLES
;+
;+ 5. A TEST IS MADE TO BE SURE THAT IF AN INTERRUPT IS EXPECTED, IT IS
;+ RECEIVED AND IF IT ISN'T EXPECTED IT DOESN'T HAPPEN.
;+
;+ ALL TESTING IS DONE HERE WITH THE PROCESSOR'S PRIORITY SET AT 0.
;+
;-----*****
;
;

```

```

032534
5283 032534 004737 003514
5284 032540 103003
5285 032542
032542 104460
5286 032544
032544 104432
032546 001272

```

```

;
; BGNTST
;
; JSR    PC,MSTCLR      ;ISSUE MASTER CLEAR & ENTER MAINT. LOOP
; BCC   1$              ;IF NO ERROR, CONTINUE
; ERROR                     ;ELSE, REPORT IT AND
;
; EXIT   TST             ;EXIT THIS TEST
;
; TRAP   C$ERROR
; TRAP   C$EXIT
; .WORD  L10051-

```

```

5287
5288
5289
5290
5291
5292
5293
5294
5295
5296
5297
5298
5299
5300
5301
5302
5303
5304

```

```

-----
;
; TEST FOR NO INTERRUPT WHEN ENABLED
;
; 1. DISABLE BOTH INTERRUPTS
;
; 2. ASSERT BOTH REQUEST BITS TO 1
;
; 3. CHECK FOR NO "A" INTERRUPT
;
; 4. CHECK FOR NO "B" INTERRUPT
;
; 5. ENABLE BOTH INTERRUPTS
;
; 6. CHECK FOR NO "A" INTERRUPT
;
; 7. CHECK FOR NO "B" INTERRUPT
;
;
;
; 1$:
; BGNSUB
;

```

```

5305 032550
032550
032550 104402
5306 032552 012737 177777 002274
5307 032560 005037 002272
5308 032564 112777 000000 147526

```

```

;
; TRAP   C$BSUB
; TRAP   C$BSUB
; MOV   #-1,INTWCH      ;TELL BOTH HANDLERS TO "WATCH" FOR INTERRUPTS
; CLR   INTFLG          ;CLEAR BOTH INTERRUPT FLAGS
; MOVB  #0,@BSELO       ;DISABLE BOTH INTERRUPTS

```


TEST 9 -- Q-BUS INTERRUPT "A" & "B" SELECTION

5341
5342
5343
5344
5345
5346
5347
5348
5349
5350
5351
5352
5353
5354
5355
5356
5357
5358
5359

- ```

 :-----:
 : TEST FOR "A" INTERRUPT WHEN ENABLED
 :
 : 1. DISABLE BOTH INTERRUPTS
 :
 : 2. ASSERT "B" REQUEST BIT TO 1:
 : DISABLING "B" & FORCING "A"
 :
 : 3. CHECK FOR NO "A" INTERRUPT
 :
 : 4. CHECK FOR NO "B" INTERRUPT
 :
 : 5. ENABLE BOTH INTERRUPTS
 :
 : 6. CHECK FOR "A" INTERRUPT
 :
 : 7. CHECK FOR NO "B" INTERRUPT

```

5360  
5361  
5362  
5363  
5364  
5365  
5366  
5367  
5368  
5369  
5370  
5371  
5372  
5373  
5374  
5375  
5376  
5377  
5378  
5379  
5380  
5381  
5382

```

 BGNSUB
 T9.2:
 TRAP C$BSUB
 MOV #-1,INTWCH ;TELL BOTH HANDLERS TO "WATCH" FOR INTERRUPTS
 CLR INTFLG ;CLEAR BOTH INTERRUPT FLAGS
 MOV #0,BSELO ;DISABLE INTERRUPTS AGAIN
 JSR R5,WRITEI ;CAUSE AN INTERRUPT PENDING ON "A"
 IRQREG ; BUT NOT ON "B"
 IRQB
 BCC 31$;IF AN ERROR OCCURED,
 ERROR ;REPORT IT &
 TRAP C$ERROR
 ESCAPE TST ; QUIT
 TRAP C$ESCAPE
 .WORD L10051-.
 31$:
 TSTB INTFLG ;DID AN "A" INTERRUPT OCCUR?
 BEQ 10$;NO, GOOD. GO TEST THE "B" INTERRUPT
 MOV #5,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
 GEDF EM34,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
 ; "DEVICE FATAL" ERROR # 56
 TRAP C$ERDF
 .WORD 56
 .WORD EM34
 .WORD ERR1
 10$:
 TSTB INTFLG+1 ;DID A "B" INTERRUPT OCCUR?
 BEQ 11$;NO, GOOD. NOW TRY LETTING ONE THROUGH
 MOV #6,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
 GEDF EM34B,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
 ; "DEVICE FATAL" ERROR # 57
 TRAP C$ERDF
 .WORD 57
 .WORD EM34B
 .WORD ERR1
 11$:
 CLR INTFLG ;CLEAR BOTH INTERRUPT FLAGS
 MOV #IENBA!IENBB,BSELO ;ENABLE BOTH INTERRUPTS

```

TEST 9 -- Q-BUS INTERRUPT "A" & "B" SELECTION

```

5383 033100 012703 001000 MOV #1000,R3 ;GIVE THE INTERRUPT SOME TIME TO HAPPEN
5384 033104 077301 SOB R3 ; BY SITTING HERE FOR A WHILE
5385 033106 105737 002272 TSTB INTFLG ;DID AN "A" INTERRUPT OCCUR?
5386 033112 001007 BNE 12$;YES, GOOD. GO TEST THE "B" INTERRUPT
5387 033114 012737 000007 002254 MOV #7,GDATA ;NO, TELL ERROR HANDLER WHAT WE HAD DONE
5388 033122 GEDF EM35,ERR1 ;REPORT MISSING INTERRUPT ON "ENABLE"
; ; "DEVICE FATAL" ERROR # 58
; TRAP C$ERDF
; .WORD 58
; .WORD EM35
; .WORD ERR1
 033122 104455
 033124 000072
 033126 016552
 033130 006212
5389
5390 033132 105737 002273 12$: TSTB INTFLG+1 ;DID A "B" INTERRUPT OCCUR?
5391 033136 001407 BEQ 13$;NO, GOOD. NOW TRY HITTING THE "B" INTERRUPT
5392 033140 012737 000010 002254 MOV #8,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
5393 033146 GEDF EM34B,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
; ; "DEVICE FATAL" ERROR # 59
; TRAP C$ERDF
; .WORD 59
; .WORD EM34B
; .WORD ERR1
 033146 104455
 033150 000073
 033152 016540
 033154 006212
5394
5395 033156 13$: ENDSUB
; L10053:
; TRAP C$ESUB
 033156
 033156 104403

```

```

5396
5397
5398 :-----:
5399 : :
5400 : : TEST FOR "B" INTERRUPT WHEN ENABLED
5401 : :
5402 : : 1. DISABLE BOTH INTERRUPTS
5403 : :
5404 : : 2. ASSERT "A" REQUEST BIT TO 1:
5405 : : DISABLING "A" & FORCING "B"
5406 : :
5407 : : 3. CHECK FOR NO "A" INTERRUPT
5408 : :
5409 : : 4. CHECK FOR NO "B" INTERRUPT
5410 : :
5411 : : 5. ENABLE BOTH INTERRUPTS
5412 : :
5413 : : 6. CHECK FOR NO "A" INTERRUPT
5414 : :
5415 : : 7. CHECK FOR "B" INTERRUPT

```

```

5415 033160 BGNSUB
 033160 T9.3:
 033160 104402 TRAP C$BSUB
5416 033162 012737 177777 002274 MOV #-1,INTWCH ;TELL BOTH HANDLERS TO "WATCH" FOR INTERRUPTS
5417 033170 005037 002272 CLR INTFLG ;CLEAR BOTH INTERRUPT FLAGS
5418 033174 112777 000000 147116 MOVB #0,@BSELO ;DISABLE INTERRUPTS AGAIN
5419 033202 004537 004054 JSR R5,WRITEI ;CAUSE AN INTERRUPT PENDING ON "B"
5420 033206 123005 IRQREG ; BUT NOT ON "A"
5421 033210 000004 IRQA
5422 033212 103003 BCC 32$;IF AN ERROR OCCURED,
5423 033214 ERROR ;REPORT IT &
; TRAP C$ERROR
 033214 104460
5424 033216 ESCAPE TST ; EXIT

```

TEST 9 - Q-BUS INTERRUPT "A" & "B" SELECTION

```

033216 104410
033220 000620 TRAP C$ESCAPE
 .WORD L10051-.
5425
5426 033222 105737 002272 32$: TSTB INTFLG ;DID AN "A" INTERRUPT OCCUR?
5427 033226 001407 BEQ 14$;NO, GOOD. GO TEST THE "B" INTERRUPT
5428 033230 012737 000011 002254 MOV @9.,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
5429 033236 GEDF EM34,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
 ; "DEVICE FATAL" ERROR # 60
033236 104455 TRAP C$ERDF
033240 000074 .WORD 60
033242 016526 .WORD EM34
033244 006212 .WORD ERR1
5430
5431 033246 105737 002273 14$: TSTB INTFLG*1 ;DID A "B" INTERRUPT OCCUR?
5432 033252 001407 BEQ 15$;NO, GOOD. NOW TRY LETTING ONE THROUGH
5433 033254 012737 000012 002254 MOV @10.,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
5434 033262 GEDF EM34B,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
 ; "DEVICE FATAL" ERROR # 61
033262 104455 TRAP C$ERDF
033264 000075 .WORD 61
033266 016540 .WORD EM34B
033270 006212 .WORD ERR1
5435
5436 033272 005037 002272 15$: CLR INTFLG ;CLEAR BOTH INTERRUPT FLAGS
5437 033276 112777 000021 147014 MOV @IENBA:IENBB,@BSELO ;ENABLE BOTH INTERRUPTS
5438 033304 012703 001000 MOV @1000,R3 ;GIVE THE INTERRUPT SOME TIME TO HAPPEN
5439 033310 077301 SOB R3,, ; BY SITTING HERE FOR A WHILE
5440 033312 105737 002272 TSTB INTFLG ;DID AN "A" INTERRUPT OCCUR?
5441 033316 001407 BEQ 16$;NO, GOOD. GO TEST THE "B" INTERRUPT
5442 033320 012737 000013 002254 MOV @11.,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
5443 033326 GEDF EM34,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
 ; "DEVICE FATAL" ERROR # 62
033326 104455 TRAP C$ERDF
033330 000076 .WORD 62
033332 016526 .WORD EM34
033334 006212 .WORD ERR1
5444
5445 033336 105737 002273 16$: TSTB INTFLG*1 ;DID A "B" INTERRUPT OCCUR?
5446 033342 001007 BNE 17$;YES, GOOD. NOW TRY HITTING THE "B" INTERRUPT
5447 033344 012737 000014 002254 MOV @12.,GDATA ;NO, TELL ERROR HANDLER WHAT WE HAD DONE
5448 033352 GEDF EM35B,ERR1 ;REPORT MISSING INTERRUPT ON "ENABLE"
 ; "DEVICE FATAL" ERROR # 63
033352 104455 TRAP C$ERDF
033354 000077 .WORD 63
033356 016573 .WORD EM35B
033360 006212 .WORD ERR1
5449
5450 033362 17$: ENDSUB
 L10054: TRAP C$ESUB
033362 104403
5451
5452 ;-----
5453 ; TEST FOR "A" INTERRUPT BUT NO "B" WHEN BOTH ENABLED & FORCED
5454 ;
5455 ; 1. DISABLE BOTH INTERRUPTS
5456 ;
5457 ; 2. ASSERT BOTH "A" & "B" REQUEST BITS TO 0:

```

TEST 9 -- Q-BUS INTERRUPT "A" & "B" SELECTION

```

5458 : FORCING BOTH "A" & "B" (BUT ONLY GETTING "A")
5459 :
5460 : 3. CHECK FOR NO "A" INTERRUPT
5461 :
5462 : 4. CHECK FOR NO "B" INTERRUPT
5463 :
5464 : 5. ENABLE BOTH INTERRUPTS
5465 :
5466 : 6. CHECK FOR "A" INTERRUPT
5467 :
5468 : 7. CHECK FOR NO "B" INTERRUPT
5469 :
5470 033364 BGNSUB
5471 033364 104402 T9.4: TRAP C$BSUB
5472 033364 012737 177777 002274 MOV #1,INTWCH ;TELL BOTH HANDLERS TO "WATCH" FOR INTERRUPTS
5473 033374 005037 002272 CLR INTFLG ;CLEAR BOTH INTERRUPT FLAGS
5474 033400 112777 000000 146712 MOVB #0,0BSELO ;DISABLE INTERRUPTS AGAIN
5475 033406 004537 004054 JSR R5,WRITEI ;CAUSE AN INTERRUPT PENDING ON BOTH "A" & "B"
5476 033412 123005 IRQREG
5477 033414 000000 O
5478 033416 103003 BCC 31$;IF AN ERROR OCCURED,
5479 033420 104460 ERROR ;REPORT IT &
5480 033422 ESCAPE TST ; QUIT TRAP C$ERROR
5481 033422 104410 .WORD L10051- TRAP C$ESCAPE
5482 033424 000414 .WORD
5483 033426 105737 002272 31$: TSTB INTFLG ;DID AN "A" INTERRUPT OCCUR?
5484 033432 001407 BEQ 10$;NO. GOOD. GO TEST THE "B" INTERRUPT
5485 033434 012737 000015 002254 MOV #13,,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
5486 033442 104455 GEDF EM34,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
5487 033444 000100 ; "DEVICE FATAL" ERROR # 64
5488 033446 016526 TRAP C$ERDF
5489 033450 006212 .WORD 64
5490 033452 105737 002273 10$: TSTB INTFLG+1 ;DID A "B" INTERRUPT OCCUR?
5491 033456 001407 BEQ 11$;NO. GOOD. NOW TRY LETTING ONE THROUGH
5492 033460 012737 000016 002254 MOV #14,,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
5493 033466 104455 GEDF EM34B,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
5494 033470 000101 ; "DEVICE FATAL" ERROR # 65
5495 033472 016540 TRAP C$ERDF
5496 033474 006212 .WORD 65
5497 033476 005037 002272 11$: CLR INTFLG ;CLEAR BOTH INTERRUPT FLAGS
5498 033502 112777 000021 146610 MOVB #1ENBA;!ENBB,0BSELO ;ENABLE BOTH INTERRUPTS
5499 033510 012703 001000 MOV #1000,R3 ;GIVE THE INTERRUPT SOME TIME TO HAPPEN
5500 033514 077301 SOB R3,.. ; BY SITTING HERE FOR A WHILE
5501 033516 105737 002272 TSTB INTFLG ;DID AN "A" INTERRUPT OCCUR?
5502 033522 001007 BNE 12$;YES. GOOD. GO TEST THE "B" INTERRUPT
5503 033524 012737 000017 002254 MOV #15,,GDATA ;NO, TELL ERROR HANDLER WHAT WE HAD DONE
5504 033532 000017 002254 GEDF EM35,ERR1 ;REPORT MISSING INTERRUPT ON "ENABLE"
5505 033532 000017 002254 ; "DEVICE FATAL" ERROR # 66

```



TEST 9 -- Q-BUS INTERRUPT "A" & "B" SELECTION

```
033662 006212 .WORD ERR1
5536
5537 033664 105737 002273 12$: TSTB INTFLG.1 ;DID A "B" INTERRUPT OCCUR?
5538 033670 001407 BEQ 13$;NO, GOOD. NOW TRY HITTING THE "B" INTERRUPT
5539 033672 012737 000024 002254 MOV #20.,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
5540 033700 GEDF EM34B,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
; ; "DEVICE FATAL" ERROR # 69
 TRAP C$ERDF
 .WORD 69
 .WORD EM34B
 .WORD ERR1
033700 104455
033702 000105
033704 016540
033706 006212
5541
5542 033710 13$: ENDSUB
033710
033710 104403 L10056: TRAP C$ESUB
5543
5544
5545
5546
5547
5548
5549
5550
5551
5552
5553
5554
5555
5556 033712
033712
033712 104402 T9.6: TRAP C$BSUB
5557 033714 012737 177777 002274 MOV #-1,INTWCH ;TELL BOTH HANDLERS TO "WATCH" FOR INTERRUPTS
5558 033722 005037 002272 CLR INTFLG ;CLEAR BOTH INTERRUPT FLAGS
5559 033726 112777 000021 146364 MOVB #IENBA:IENBB,@BSALO ;ENABLE BOTH INTERRUPTS
5560 033734 004537 004054 JSR R5,WRITEI ;CAUSE AN INTERRUPT PENDING ON "B"
5561 033740 123005 IRQREG ; BUT NOT ON "A"
5562 033742 000004 IRQA
5563 033744 103003 BCC 32$;IF AN ERROR OCCURED.
5564 033746 ERROR ;REPORT IT &
 TRAP C$ERROR
033746 104460
5565 033750 ESCAPE TST ; QUIT
033750 104410 TRAP C$ESCAPE
033752 000066 .WORD L10051-.
5566
5567 033754 012703 001000 32$: MOV #1000,R3 ;GIVE THE INTERRUPT SOME TIME TO HAPPEN
5568 033760 077301 SOB R3 ; BY SITTING HERE FOR A WHILE
5569 033762 105737 002272 TSTB INTFLG ;DID AN "A" INTERRUPT OCCUR?
5570 033766 001407 BEQ 16$;NO, GOOD. GO TEST THE "B" INTERRUPT
5571 033770 012737 000025 002254 MOV #21.,GDATA ;YES, TELL ERROR HANDLER WHAT WE HAD DONE
5572 033776 GEDF EM34,ERR1 ;REPORT THE UNEXPECTED INTERRUPT
; ; "DEVICE FATAL" ERROR # 70
 TRAP C$ERDF
 .WORD 70
 .WORD EM34
 .WORD ERR1
033776 104455
034000 000106
034002 016526
034004 006212
5573
5574 034006 105737 002273 16$: TSTB INTFLG.1 ;DID A "B" INTERRUPT OCCUR?
```

TEST 9 -- Q-BUS INTERRUPT "A" & "B" SELECTION

```

5575 034012 001007 BNE 17$;YES, GOOD. NOW TRY HITTING THE "B" INTERRUPT
5576 034014 012737 000026 002254 MOV #22.,GDATA ;NO, TELL ERROR HANDLER WHAT WE HAD DONE
5577 034022 GEDF EM35B,ERR1 ;REPORT MISSING INTERRUPT ON "ENABLE"
; "DEVICE FATAL" ERROR # 71
 TRAP C$ERDF
 .WORD 71
 .WORD EM35B
 .WORD ERR1
 034022 104455
 034024 000107
 034026 016573
 034030 006212
5578
5579 034032 17$: ENDSUB L10057: TRAP C$ESUB
 034032
 034032 104403
5580
5581 034034 005037 002274 CLR INTWCH ;TELL HANDLERS TO STOP WATCHING FOR INTERRUPTS
5582 034040 ENDTST L10051: TRAP C$ETST
 034040 104401

```

TEST 10 -- BUS RESET WITH DISABLE INIT SET

5591

.SBTTL TEST 10 -- BUS RESET WITH DISABLE INIT SET

```

:*
:* TEST 10 -- BUS RESET WITH DISABLE INIT SET
:*
:* A BYTE SELECT REGISTER (BSEL3) IS LOADED WITH 377, DISABLE INIT BIT IS SET
:* IN THE NPR CONTROL REGISTER, AND A BUS RESET INSTRUCTION IS EXECUTED. THE
:* PROGRAM THEN CHECKS THAT THE DMV-11 WAS NOT CLEARED, BY CHECKING FOR 377
:* STILL IN BSEL3
:*
:--*****

```

```

; BGNTST
;
5592 034042 032737 000001 002316 BIT #BIT0,PFLAG ;IF BUS RESETS ARE NOT ALLOWED,
5593 034050 001031 BNE 10$; BYPASS THIS TEST
5594 ;ELSE,
5595 034052 004737 003514 JSR PC,MSTCLR ;INIT DMV & START UP THE MAINT. LOOP
5596 034056 103003 BCC 1$;IF AN ERROR OCCURED,
5597 034060 ERROR ;REPORT IT &
5598 034062 104460 ESCAPE TST ; EXIT TRAP C$ERROR
034062 104410 ; TRAP C$ESCAPE
034064 000050 ; .WORD L10060-.
5599
5600 034066 004537 004054 1$: JSR R5,WRITEI ;NOW SET "DISABLE INIT"
5601 034072 123004 NPRCTL
5602 034074 000105 DMVDIAI!DMVPU!NPRGO
5603 ;THE "NPRGO" BIT IS SET BECAUSE ASSERTING IT
5604 ;TO A ZERO WOULD KICK OFF AN NPR OPERATION!
5605 ;THE "DMVPU" BIT MUST ALWAYS BE SET WHENEVER
5606 ;THE NPR-CONTROL REGISTER IS LOADED.
5607
5608 034076 112777 000377 146222 MOVB #377,@BSEL3 ;THIS REGISTER WILL ONLY GET ALTERED IF THE
5609 ;DMV-11 IS SUCCESSFULLY RESET: THE "DMVPU"
5610 ;BIT WILL BE CLEARED, THE MICRO-DIAGNOSTIC
5611 ;WILL BE STARTED, AND FINDING "DMVPU" CLEARED.
5612 ;IT WILL CLEAR ALL BSEL REGISTERS (INCLUDING
5613 ;BSEL3) AND PERFORM THE 17 TESTS IS CONTAINS.
5614 ;OF COURSE, IF THIS ALL HAPPENS, THAN THIS
5615 ;TEST WILL HAVE FAILED!
5616
5617 034104 BRESET ;THE "SUPERVISOR" WILL DO A BUS RESET FOR US
034104 104433 TRAP C$RESET
5618 034106 012703 001000 MOV #1000,R3 ;DELAY FOR A BIT SO THE MICRO-DIAG. CAN DO
5619 034112 077301 SOB R3, ;ITS THING IF IT'S GOING TO
5620 034114 122777 000377 146204 CMPB #377,@BSEL3 ;IF A FAILURE OCCURED, THIS SHOULD HAVE BEEN
5621 034122 001404 BEQ 10$;ALTERED BY NOW. IF NOT, ALL'S WELL -- EXIT
5622 034124 GEDF EM40,ERR1 ;ELSE, "DISABL INIT" DIDN'T STOP "BUS RESET"
; "DEVICE FATAL" ERROR # 72
034124 104455 TRAP C$ERDF
034126 000110 .WORD 72
034130 016614 .WORD EM40
034132 006212 .WORD ERR1
5623
5624 034134 10$: ENDTST

```

H12

CNDMBA0 DMV11 MCTRL DIAG #2    MACRO M1200    05-MAR-84 14:54    PAGE 62-1

SEQ 0150

TEST 10 -- BUS RESET WITH DISABLE INIT SET

034134  
034134 104401

L10060:    TRAP    C\$ETST



TEST 12 -- DCOK H LO BIT

5667

.SBTTL TEST 12 -- DCOK H LO BIT

```

:*****
:*
:* TEST 12 -- DCOK H LO BIT
:*
:* DCOK H LO IS SET IN THE NPR CONTROL REGISTER WHICH SHOULD CAUSE A VECTOR TO
:* THE FIRST INTERRUPT HANDLER WHERE THE VECTOR IS CHANGED TO POINT TO THE
:* SECOND HANDLER. THIS SECOND HANDLER WILL THEN STALL FOR A WHILE WAITING FOR
:* THE POWER-UP INTERRUPT WHICH SHOULD KICK US INTO THE SECOND HANDLER. IN
:* BOTH HANDLERS FLAGS ARE SET TO SAY THAT WE GOT THERE. WHEN WE FINALLY
:* RETURN TO OUR MAINLINE CODE, WE WILL RESUME THE DELAY FUNCTION WE WERE IN
:* AND THEN CHECK THE FLAGS.
:*
:* IN SUBTEST # 1, WE EXPECT THE DMV TO BE RESET.
:*
:*****

```

```

:
: BGNTST
:
5668 034202 000014 DCOKTS = $T ;DEFINE TEST # FOR "INIT" SECTION
5669 034202 032737 000001 002370 BIT #PU24,PT.CTL ;IS POWER-UP STRAPPED FOR OPTION 0?
5670 034210 001002 BNE 1$;YES, THEN WE CAN DO THIS TEST
5671 034212 EXIT TST ;NO, WE CAN'T DO THIS TEST UNLESS IT IS!
 034212 104432 TRAP C$EXIT
 034214 000476 .WORD L10062-.
5672
5673 034216 032737 000001 002316 1$: BIT #BIT0,PFLAG ;IF BUS RESETS ARE ALLOWED,
5674 034224 001402 BEQ 2$; PERFORM THIS TEST
5675 034226 EXIT TST ;ELSE, BYPASS IT
 034226 104432 TRAP C$EXIT
 034230 000462 .WORD L10062-.
5676
5677 034232 2$: DELAY 40. ;DELAY TO PREVENT TST # ROACHING
 034232 012727 000050 MOV #40.,(PC)+
 034236 000000 .WORD 0
 034240 013727 002116 MOV L$DLY,(PC)+
 034244 000000 .WORD 0
 034246 005367 177772 DEC -6(PC)
 034252 001375 BNE -.4
 034254 005367 177756 DEC -22(PC)
 034260 001367 BNE .-20
5678
5679
5680
:-----
: SUBTEST #1: DCOK H LO (RESET DMV)
:-----
5681 034262 034262 BGNSUB ; <====> TEST FOR POWER-DOWN/UP & DMV-11 RESET
 034262 104402 T12.1:
5682 034264 004737 003514 JSR PC,MSTCLR ;INIT DMV & START UP THE MAINT. LOOP
5683 034270 103003 BCC 3$;IF AN ERROR OCCURED,
5684 034272 ERROR ;REPORT IT &
 034272 104460 TRAP C$ERROR
5685 034274 ESCAPE SUB ; EXIT
 034274 104410 TRAP C$ESCAPE
 034276 000220 .WORD L10063-;
5686 034300 3$: SETVEC #24,#5$,#7 ;SETUP VECTOR FOR POWER FAIL INTERRUPT HANDLER

```

TEST 12 -- DCOK H LO BIT

```

034300 012746 000007 MOV #7,-(SP)
034304 012746 034406 MOV #5,-(SP)
034310 012746 000024 MOV #24,-(SP)
034314 012746 000003 MOV #3,-(SP)
034320 104437 TRAP C$SVEC
034322 062706 000010 ADD #10,SP
5687 034326 112737 177777 002274 MOVB #-1,INTWCH ;EXPECT AN "A" INTERRUPT (IF "DCOK" FAILS!)
5688 034334 105037 002272 CLRB INTFLG ;CLEAR THE FLAG IN CASE WE WANT TO DETECT IT
5689 034340 152777 000001 145752 BISB #IENBA,@BSEL0 ;NOW ENABLE "A" INTERRUPTS
5690 ;
5691 034346 010637 002454 MOV SP,OLDSP ;SAVE THE STACK POINTER
5692 ;
5693 ; SETUP "DISABL INIT" TO ALLOW BINIT TO RESET THE DMV AND THEN CAUSE
5694 ; A POWER-FAIL CONDITION TO BE SIMULATED.
5695 ;
5696 034352 012777 177506 145754 MOV #177400!LSIDCL!DMVPU!NPRGO,@SEL6 ;VALUE TO BE LOADED INTO
5697 034360 012777 123004 145742 MOV #NPRCTL,@SEL4 ;THE NPR CONTROL REGISTER
5698 034366 112777 000042 145730 MOVB #WRILOC!<IRQA*8.>,@BSEL2 ;TELL M-LOOP TO WRITE IT & INTERRUPT
5699 ;US JUST BEFORE REQUESTING ANOTHER
5700 ;M-LOOP COMMAND.
5701 ;
5702 ; SETTING "NPRGO" PREVENTS AN "NPR" OPERATION FROM OCCURRING. THE HIGH
5703 ; ORDER BYTE IS SET TO -1 AND IS USED AS A FLAG -- WHEN THE RESET
5704 ; OCCURS, THE SELECT REGISTERS WILL ALL BE CLEARED; WE WILL BE
5705 ; LOOKING AT THE RUN BIT TO BE SET AND AT BSEL7 TO BE CLEARED AS POSITIVE
5706 ; PROOF THAT THE DMV GOT RESET.
5707 ;
5708 ;
5709 034374 000001 4$: WAIT ;HANG HERE UNTIL INTERRUPTED
5710 ;
5711 ;
5712 ; IF THE DCOK WORKS AS IT SHOULD, THE NEXT INSTRUCTION TO BE EXECUTED
5713 ; IS AT THE LABEL "5$". IF I DOESN'T WORK, THE DMV SHOULD FINISH THE
5714 ; "WRITE" COMMAND AND GENERAT A Q-BUS INTERRUPT. ON RETURNING FROM
5715 ; THAT INTERRUPT, WE FALL INTO THE ERROR CALL BELOW:
5716 ;
5717 034376 GEDF EM42A,ERR1 ;REPORT MISSING POWER-UP
; ; ; "DEVICE FATAL" ERROR # 74
034376 104455 TRAP C$ERDF
034400 000112 .WORD 74
034402 016746 .WORD EM42A
034404 006212 .WORD ERR1
5718 ;
5719 034406 5$: ;
5720 ;
5721 ; IN EITHER CASE: RESTORE THE POWER FAIL VECTOR, STACK POINTER, AND
5722 ; THE FLAGS USED BY THE Q-BUS INTERRUPT SERVICE ROUTINE.
5723 ;
5724 ;
5725 ; B U T F I R S T !
5726 ;
5727 ; IN SOME CASES THE Q-BUS GETS CONFUSED WHEN WE PERFORM THE ABOVE
5728 ; (HIGHLY NON-STANDARD) "DCOK" MANIPULATION. EXPERIENCE HAS SHOWN
5729 ; THAT THE INSTRUCTION BEING EXECUTED CAN BE CORRUPTED -- USUALLY (BUT
5730 ; NOT ALWAYS) BEING CLEARED TO ZERO (A HALT INSTRUCTION). THIS IS NOT
5731 ; A FAILURE OF THE DMV AND THEREFORE SHOULD NOT BE OUR CONCERN HERE.
5732 ; THE FAILURE SHOULD NOT AFFECT THE TEST WITHIN THE PASS IN WHICH IT
; ; ; OCCURS BUT IN A SUBSEQUENT PASS. IN AN EFFORT TO ELIMINATE ANY

```

TEST 12 -- DCOK H LO BIT

```

5733 ; PROBLEMS FROM THIS GLITCH, WE RESTORE THE INSTRUCTION:
5734
5735 034406 012737 000001 034374 MOV #1,4$;RESTORE THE "WAIT" INSTRUCTION -- JUST IN
5736 ; CASE IT GOT MODIFIED!
5737 034414 013706 002454 MOV OLDSP,SP ;RESTORE THE STACK
5738 034420 142777 000001 145672 BICB #IENBA,#BSELO ;DISABLE THE "A" INTERRUPT
5739 034426 105037 002274 CLRB INTWCH ;STOP EXPECTING Q-BUS INTERRUPTS
5740 034432 CLRVEC #24 ;RETURN THE VECTOR TO THE SUPERVISOR
 034432 012700 000024 MOV #24,RO
 034436 104436 TRAP C$CVEC
5741 034440 SETPRI #0 ;MAKE SURE WE'RE BACK RUNNING AT 0 AGAIN!
 034440 012700 000000 MOV #0,RO
 034444 104441 TRAP C$SPRI
5742
5743 034446 013701 002262 MOV DELAY1,R1 ;INITIALIZE THE LOOP COUNTER FOR DELAY LOOP
5744 034452 001402 10$: BEQ 11$; EXIT DELAY LOOP IF TIME HAS EXPIRED
5745 034454 005301 DEC R1 ; ELSE, DECREMENT THE LOOP COUNTER AND
5746 034456 000775 BR 10$; CONTINUE TO LOOP
5747 034460 11$: BITB #RUN,#BSEL1 ; TIME UP !
5748 034460 132777 000200 145634 BEQ 12$;CHECK RUN BIT
5749 034466 001403 BEQ 12$; NOT SET... REPORT ERROR.
5750 034470 105777 145642 TSTB #BSEL7 ;THIS REGISTER SHOULD HAVE BEEN CLEARED
5751 034474 001404 12$: BEQ 13$;IT IS, EVERYTHING HERE IS OK -- EXIT SUBTEST
5752 034476 GEDF EM42B,ERR1 ;NO, THEN REPORT THE FAILURE
 ; "DEVICE FATAL" ERROR # 75
 TRAP C$ERDF
 .WORD 75
 .WORD EM42B
 .WORD ERR1
 034476 104455 TRAP C$ERDF
 034500 000113 .WORD 75
 034502 017020 .WORD EM42B
 034504 006212 .WORD ERR1
5753 034506 004737 003346 13$: JSR PC,MASCLR ;RESTORE DMV-11 TO A NORMAL STATE!
5754 034512 103001 BCC 14$;NO ERRORS, EXIT SUBTEST
5755 034514 ERROR ;REPORT MSTCLR ERROR
 TRAP C$ERROR
5756 034516 14$: ENDSUB
 L10063: TRAP C$ESUB
5757 034516
5758 034516 104403

5758 ; SUBTEST #2: DCOK H LO (DMV-11 SHOULDN'T BE RESET)
5759 ;
5760 ;
5761 ; SINCE HITTING "DCOK H LO" WITHOUT "HALT" OCCASIONALLY CORRUPTS
5762 ; PROGRAM MEMORY: WE SET BOTH "HALT" AND "DCOK H LO" IN THIS
5763 ; SUB-TEST (IF HALT FAILS, THIS TEST MAY BLOW UP).
5764 ;
5765 034520 BGNSUB ; <==> TEST FOR POWER-DOWN/UP & NO DMV-11 RESET
 034520 T12.2: TRAP C$BSUB
 034520 104402
5766
5767 034522 004737 003514 JSR PC,MSTCLR ;INIT DMV & START UP THE MAINT. LOOP
5768 034526 103003 BCC 2$;IF AN ERROR OCCURED,
5769 034530 ERROR ;REPORT IT &
 TRAP C$ERROR
5770 034532 ESCAPE SUB ; EXIT
 TRAP C$ESCAPE
 .WORD L10064-.
5771 034536 2$: SETVEC #24,#5$,#7 ;SETUP VECTOR FOR POWER FAIL INTERRUPT HANDLER
 MOV #7,-(SP)
 034536 012746 000007

```

TEST 12 -- DCOK H LO BIT

```

034542 012746 034640 MOV #5,-(SP)
034546 012746 000024 MOV #24,-(SP)
034552 012746 000003 MOV #3,-(SP)
034556 104437 TRAP C$SVEC
034560 062706 000010 ADD #10,SP
5772 034564 010637 002454 MOV SP,OLDSP ;SAVE THE STACK POINTER
5773
5774 034570 004537 004166 JSR R5,MOVLTD ;MOVE THE MICRO CODE INTO THE DMV
5775 034574 034714 ; THIS IS WHERE IT STARTS
5776 034576 000040 ; THIS IS ITS SIZE IN BYTES
5777
5778 034600 112777 000377 145526 MOVB #377,@BSEL6 ;WRITE ALL 1'S TO BSEL6
5779
5780 034606 012777 000077 145514 MOV #77,@SEL4 ;START ADDRESS OF MICROCODE
5781 034614 012777 000005 145502 MOV #EXECUT,@SEL2 ;INITIATE M-CODE
5782
5783 ; *** IF THE RESET GETS THROUGH, THE MICRO-DIAGNOSTIC WILL CLEAR BSEL4 ***
5784
5785 034622 012703 001000 MOV #1000,R3 ;STALL FOR A BIT (UCODE SHOULD HALT US HERE)
5786 034626 077301 SOB R3,..
5787
5788 ; IF WE GET HERE, WE NEVER GOT THE EXPECTED POWER-UP SEQUENCE!
5789
5790 034630 GEDF EM42A,ERR1 ;REPORT MISSING POWER-UP
; "DEVICE FATAL" ERROR # 76
034630 104455 TRAP C$ERDF
034632 000114 .WORD 76
034634 016746 .WORD EM42A
034636 006212 .WORD ERR1
5791
5792 ;IN EITHER CASE, RESTORE THE VECTOR & STACK AND SEE IF THE DMV GOT RESET
5793
5794 034640 013706 002454 5$: MOV OLDSP,SP ;RESTORE THE STACK
5795 034644 CLRVEC #24 ;RETURN THE VECTOR TO THE SUPERVISOR
034644 012700 000024 MOV #24,R0
034650 104436 TRAP C$CVEC
5796 034652 SETPRI #0 ;MAKE SURE WE'RE BACK RUNNING AT 0 AGAIN!
034652 012700 000000 MOV #0,R0
034656 104441 TRAP C$SPRI
5797 034660 122777 000377 145446 CMPB #377,@BSEL6 ;THIS REGISTER SHOULD NOT HAVE BEEN CLEARED
5798 034666 001404 BEQ 10$;IT ISN'T, ALL IS OK -- EXIT SUBTEST
5799 034670 GEDF EM42C,ERR1 ;IT IS, THEN REPORT THE FAILURE
; "DEVICE FATAL" ERROR # 77
034670 104455 TRAP C$ERDF
034672 000115 .WORD 77
034674 017074 .WORD EM42C
034676 006212 .WORD ERR1
5800
5801 034700 004737 003346 10$: JSR PC,MASCLR ;* AT THIS POINT "DINIT" IS STILL SET *
5802 034704 103001 BCC 11$;MAKE SURE THE DMV IS PROPERLY RESET!
5803 034706 ERROR ;EVERYTHING OK, EXIT SUBR AND TEST.
034706 104460 TRAP C$ERROR ;REPORT MASCLR ERROR
5804 034710 11$: ENDSUB
5805 034710
034710
034710 104403 L10064: TRAP C$ESUB
5806 034712 ENDTST

```

TEST 12 -- DCOK H LO BIT

034712  
034712 104401

L10062: TRAP C\$ETST

SUBTEST 2'S M-CODE -- COMPLETE

5808  
5809  
5810  
5811  
5812 034714  
5813 034714  
5814 034716  
5815 034721  
5816 034723  
5817 034726  
5818 034727  
5819 034730  
5820 034731  
5821 034733  
5822 034736  
5823 034740  
5824 034743  
5825 034745  
5826 034747  
5827 034751  
5828 034753  
5829  
5830 034754  
5831

251 125  
215 004  
251 105  
215 004  
352  
352  
352  
251 107  
215 004  
251 105  
215 004  
251 003  
205 000  
306 000  
320 374  
140

246  
246  
246  
246

.SBTTL SUBTEST 2'S M-CODE -- COMPLETE

;-----  
; 6502 MICROCODE FOR TEST #11/ SUBTEST #2  
;-----

SMCODE:

```
.BYTE 251,125 ;A9 55 LDA #GOBSY1!HALT!PWRUP!DINIT
.BYTE 215,4,246 ;8D 04 A6 STA NPRCTL ;SET DISABLE INIT/HALT
.BYTE 251,105 ;A9 45 LDA #GOBSY1!PWRUP!DINIT
.BYTE 215,4,246 ;8D 04 A6 STA NPRCTL ;CLEAR HALT
.BYTE 352 ;EA NOP ;WAIT A WHILE
.BYTE 352 ;EA NOP
.BYTE 352 ;EA NOP
.BYTE 251,107 ;A9 47 LDA #GOBSY1!PWRUP!SDCOK!DINIT
.BYTE 215,4,246 ;8D 04 A6 STA NPRCTL ;SET DCOK
.BYTE 251,105 ;A9 45 LDA #GOBSY1!PWRUP!DINIT
.BYTE 215,4,246 ;8D 04 A6 STA NPRCTL ;CLEAR DCOK
.BYTE 251,3 ;A9 03 LDA #103 ;DELAY FOR 16.8 USEC
.BYTE 205,0 ;85 00 STA SPO ;BUS INIT IS 10 USEC
.BYTE 306,0 ;C6 00 5:DEC SPO ;
.BYTE 320,374 ;D0 FC BNE 5: ;
.BYTE 140 ;60 RTS ;RETURN TO M-LOOP
.EVEN
```

EMCODE:

;-----

TEST 13 -- HALT MODE VERIFICATION

5996

.SBTTL TEST 13 -- HALT MODE VERIFICATION

```

:*****
:
: TEST 13 -- HALT MODE VERIFICATION
:
: THIS TEST CONTAINS TWO (2) SUBTESTS DESIGNED TO VERIFY THE FUNCTIONALITY
: OF THE "HALT" CONTROL CONTAINED WITHIN THE NPR CONTROL REGISTER. IN EACH
: CASE, MICROCODE IS LOADED INTO THE DMV IN ORDER TO CONTROL THE TESTING
: FROM THERE.
:
:-----
:
: SUBTEST # 1:
:
: HERE WE VERIFY THAT WE CAN CONTROL NPR'S AND DCOK PROPERLY WHILE THE 11 CPU
: IS HALTED.
:
: 11 CPU'S OPERATIONS: DMV-11'S OPERATIONS:
:
: THE MICROCODE IS MOVED INTO THE DMV.
:
: CLEAR TMPO. THIS WILL BE OUR TEST
: LOCATION FOR THE NPR OPERATION.
:
: SETUP FOR POWER-FAIL VECTORING THROUGH
: LOCATION 24.
:
: THE MICROCODE IS INITIATED & BSEL7 IS
: SET TO -1 AS A FLAG.
:
: WAIT FOR BSEL7 TO BE CLEARED CLEAR BSEL7 AND WAIT FOR IT TO GO
: NON-ZERO AGAIN. THIS PUTS THE
: DMV IN SYNC. WITH THE 11 CPU
:
: SAVE R6 IN OLDSP FOR RECOVERY LATER.
: CLEAR TMPC, LOAD INTO SEL4 THE
: ADDRESS OF TMPO, AND SET BSEL7 TO -1.
:
: START LOOPING -- INCREMENTING TMPO GET THE ADDRESS OF TMPO FROM SEL6
: AND SAVE IT FOR LATER
:
: HALT THE 11 CPU.
:
: CONSOLE "ODT" SHOULD BE ENTERED. NPR-IN THE CURRENT CONTENTS OF TMPO
: & PUT IT INTO SEL4 (THE FULL WORD).
:
: DELAY FOR ABOUT 100 MICROSECONDS
: (THE TIME ISN'T CRITICAL).
:
: THE 11 CPU SHOULD NOT BE EXECUTING
: ANYTHING NOW -- NOT EVEN "ODT"
:
: DROP THE "HALT" SIGNAL TO RELEASE
: THE 11 CPU AND SET "DCOK M LO" &
: "DISABL INIT". DROP "DCOK M LO"
:
:
:
:

```

TEST 13 - HALT MODE VERIFICATION

```

; * WE SHOULD GO
; * THROUGH A POWER-UP SEQUENCE. R6 IS
; * RESTORED FROM OLDSP, INTERRUPT
; * PRIORITY LEVEL IS RESTORED TO 0, &
; * INTERRUPT VECTOR 24 IS RETURNED TO
; * THE DIAGNOSTIC SUPERVISOR. SEL4 IS
; * COMPARED AGAINST TMPO -- THEY SHOULD
; * BE EQUAL.

```

```

; * NOW CLEAR BSEL7.

```

```

WAIT FOR BSEL7 TO CLEAR. THEN DROP
"DISABLE INIT" AND EXIT TO M-LOOP.

```

```

; * -----
; * SUBTEST # 2:

```

```

; * HERE THE HALT MODE IS USED IN A WAY WHICH VERY CLOSELY MATCHES THE DMV-11
; * MICROCODE'S UTILIZATION DURING A "MOP BOOT" OPERATION. THE INTERRUPT
; * VECTOR AREA IS COMPLETELY OVERRITTEN BY THE DMV NPR'S AND IS THEREFORE
; * BACKED UP ELSEWHERE IN THE 11 CPU'S MEMORY. THERE IS ALSO THE POSSIBLE
; * CONTENTION WITH THE DIAGNOSTIC SUPERVISOR -- TO HELP HERE, AS MUCH AS
; * POSSIBLE WILL BE DONE AT INTERRUPT LEVEL 7.

```

```

; * 11 CPU'S OPERATIONS:

```

```

DMV-11'S OPERATIONS:

```

```

; * THE MICROCODE IS MOVED INTO THE DMV.

```

```

; * THE INTERRUPT VECTOR AREA IS BACKED-
; * UP IN AN I/O BUFFER FOLLOWING THE
; * PROGRAM

```

```

; * THE MICROCODE IS INITIATED & BSEL7 IS
; * SET TO -1 AS A FLAG.

```

```

; * WAIT FOR BSEL7 TO BE CLEARED

```

```

CLEAR BSEL7 AND WAIT FOR IT TO GO
NON-ZERO AGAIN PUTTING BOTH
PROCESSORS IN SYNC. WITH EACH
OTHER

```

```

; * CLEAR TMPO AND SAVE R6 FOR RECOVERY
; * LATER. SET BSEL7 AGAIN AND WAIT FOR
; * TMPO TO BE SET.

```

```

SET HALT, "DCOK H LO", & "DISABL
INIT" AND PERFORM 2 NOP'S AS A
1 MICROSECOND DELAY

```

```

; * ENTRY INTO THE CONSOLE "ODT" WILL
; * BE INITIATED.

```

```

CLEAR "DCOK H LO", SET "HALT" &
"DISABL INIT"

```

```

NPR-OUT THE FOLLOWING:

```

```

LOC: CONTENTS
 24 000000 VECTOR TO LOC 0
 26 000340 @ PRIORITY 7
 0 012700 MOV @-1,R0
 2 177777
 4 000777 BR .

```

TEST 13 -- HALT MODE VERIFICATION

```

;*
;* THE ENTRY INTO THE CONSOLE "ODT"
;* WILL BE ABORTED!
;*
;*
;*
;* THE ROUTINE NPR'D INTO LOC. 0 WILL
;* BE EXECUTED -- EVENTUALLY HANGING
;* AT THE "BR ." INSTRUCTION @ LOC. 4.

```

```

SET "DCOK H LO" & "DISABL INIT"
AND CLEAR "HALT"

```

```

DELAY FOR A SHORT TIME (ABOUT 1
MICROSECOND)

```

```

CLEAR "DCOK H LO" AND SET "DISABL
INIT"

```

```

NPR-OUT THE FOLLOWING:

```

```

LOC: CONTENTS
6 005001 CLR R1
10 062701 ADD #(.+2),R1
12 062701 ADD #(.+2),R1
. . .
360 062701 ADD #(.+2),R1
362 062701 ADD #(.+2),R1
364 010037 MOV RO,@TMP0
366 [TMP0]
370 013706 MOV @OLDSP,R6
372 [OLDSP]
374 000137 JMP HLTST2
376 [HLTST2]

```

```

THIS IS SYNONYMOUS TO THE DMV-11
LOADING A "MESSAGE" STARTING AT MEM.
LOC. 000006.

```

```

NPR-OUT THE FOLLOWING:

```

```

LOC: CONTENTS
4 000240 NOP

```

```

THIS IS HOW THE DMV-11 WILL TAKE THE
11 CPU OUT OF THE "BR ." CONDITION.

```

```

DROP "DISABL INIT"

```

```

AN EXIT IS TAKEN TO THE M-LOOP

```

```

;* THE ROUTINE JUST LOADED BY THE DMV
;* MICROCODE WILL NOW BE EXECUTED (WE
;* HOPE). WHEN THE SUBROUTINE IS RE-
;* ENTERED (@ HLTST2),

```

- ```

;*
;* 1 THE INTERRUPT VECTORS WILL BE
;* RESTORED;
;* 2 THE PRIORITY LEVEL WILL BE
;* LOWERED BACK TO 0;
;* 3 RO, R1, & R6 WILL ALL BE CHECKED
;* FOR THE PROPER CONTENTS; AND
;* 4 TMP0 WILL BE CHECKED FOR THE
;* PROPER CONTENTS;

```

```

;*****

```

```

BGNTST

```

```

HLTEST = $T

```

```

T13::
;DEFINE TEST # FOR "INIT" SECTION

```

TEST 13 -- HALT MODE VERIFICATION

```

5998 034754 032737 000001 002370      BIT      #PU24,PT.CTL      ;IS POWER-UP STRAPPED FOR OPTION 0?
5999 034762 001002                    BNE      5$              ;YES, THEN WE CAN DO THIS TEST
6000 034764                    EXIT     TST              ;NO, WE CAN'T DO THIS TEST UNLESS IT IS!
                                TRAP      C$EXIT
                                .WORD    L10065-.
6001
6002 034770 032737 000001 002316 5$:  BIT      #BIT0,PFLAG      ;IF BUS RESETS ARE ALLOWED,
6003 034776 001402                    BEQ      2$              ; PERFORM THIS TEST
6004 035000                    EXIT     TST              ;ELSE, BYPASS IT
                                TRAP      C$EXIT
                                .WORD    L10065-.
6005
6006 035004                    2$:  DELAY    40.              ;DELAY TO PREVENT TST # ROACHING
                                MOV       #40.,(PC)+
                                .WORD    0
                                MOV       L$DLY,(PC)+
                                .WORD    0
                                DEC       -6(PC)
                                BNE      -.4
                                DEC       -22(PC)
                                BNE      -.20
6007
6008 035034                    1$:  BGNSUB
                                T13.1:
                                TRAP      C$BSUB
6009 035036 004737 003514      JSR      PC,MSTCLR      ;RESET DMV & ENTER M-LOOP
6010 035042 103003                    BCC     2$              ;IF NO ERROR HERE, CONTINUE
6011 035044                    ERROR
                                ;ELSE, REPORT THE ERROR
                                TRAP      C$ERROR
6012 035046                    ESCAPE  TST              ;
                                ; & EXIT THE TEST
                                TRAP      C$ESCAPE
                                .WORD    L10065-.
6013 035052                    2$:
6014 035052 004537 004166      JSR      R5,MOVLT D     ;MOVE THE MICRO CODE INTO THE DMV
6015 035056 035762                    MC1
                                ; THIS IS WHERE IT STARTS
6016 035060 000114                    MC2-MC1
                                ; THIS IS ITS SIZE IN BYTES
6017
6018 035062 005037 002412      CLR      TMP0           ;INITIALIZE THE COUNTER
6019 035066                    SETVEC #24,#24$,#7      ;SETUP POWER-UP VECTOR
                                MOV       #7,-(SP)
                                MOV       #24$,-(SP)
                                MOV       #24,-(SP)
                                MOV       #3,-(SP)
                                TRAP      C$SVEC
                                ADD      #10,SP
6020 035114 012777 000077 145206      MOV      #77,@SEL4      ;START ADDRESS OF MICROCODE
6021 035122 112777 177777 145206      MOV     #-1,@BSEL7      ;SET FLAG (BSEL7)
6022 035130 012777 000005 145166      MOV     #EXECUT,@SEL2   ;INITIATE M-CODE
6023
6024 035136 005002
6025 035140 105777 145172 3$:  CLR      R2              ;WAIT FOR FLAG TO BE CLEARED
6026 035144 001405                    TSTB   @BSEL7
6027 035146 077204                    BEQ     5$
6028 035150                    SOB     R2,3$
                                GEDF   EM43A,ERR1
                                ;TIMEOUT...M-CODE IS HUNG!
                                ; "DEVICE FATAL" ERROR # 78
                                TRAP      C$ERDF
                                .WORD    78
                                035150 104455
                                035152 000116

```


TEST 13 -- HALT MODE VERIFICATION

```

    035326 000432                                     .WORD  L10065-.
6061 035330
6062 035330 004537 004166      1$:     JSR     R5,MOV,TD      ;MOVE THE MICRO CODE INTO THE DMV
6063 035334 036076            MC2       ;       THIS IS WHERE IT STARTS
6064 035336 000243            MC2END-MC2  ;       THIS IS ITS SIZE IN BYTES
6065
6066 035340 004537 005004      JSR     R5,MOVSW    ;SAVE THE INTERRUPT VECTORS
6067 035344 000000            0
6068 035346 002654            BUFAREA   ; IN THE BUFFER AREA
6069 035350 000200            400/2    ; LOC'S 0 ==> 377 WILL BE SAVED
6070
6071 035352                SETVEC  #376,#0,#7  ;FAKE OUT THE SUPERVISOR, WE'RE JUST
    035352 012746 000007             MOV     #7,-(SP)
    035356 012746 000000             MOV     #0,-(SP)
    035362 012746 000376             MOV     #376,-(SP)
    035366 012746 000003             MOV     #3,-(SP)
    035372 104437             TRAP   C$SVEC
    035374 062706 000010             ADD    #10,SP
6072
6073                                     ; SETTING LOCATION 376 TO ZERO (0)
6074 035400 012777 000077 144722    MOV     #77,@SEL4   ;START ADDRESS OF MICROCODE
6075 035406 112777 177777 144722    MOV     #-1,@BSEL7 ;SET FLAG (BSEL7)
6076 035414 012777 000005 144702    MOV     #EXECUT,@SEL2 ;INITIATE M-CODE
6077
6078 035422 005002
6079 035424 105777 144706      3$:     CLR     R2          ;WAIT FOR FLAG TO BE CLEARED
6080 035430 001406            TSTB   @BSEL7
6081 035432 077204            BEQ    5$
6082 035434                SOB    R2,3$
    035434 104455            GEDF   EM43A,ERR1  ;TIMEOUT...M-CODE IS HUNG!
    035436 000121            ;       "DEVICE FATAL" ERROR # 81
    035440 017153            TRAP   C$ERDF
    035442 006212            .WORD  81
6083 035444 000447            BR     22$          ;EXIT
6084
6085 035446 010637 002454      5$:     MOV     SP,OLDSP  ;SAVE STACK POINTER FOR LATER
6086 035452 005037 002412      CLR     TMPO       ;RESET EXECUTION INDICATOR (TMPO)
6087 035456 112777 177777 144652    MOV     #-1,@BSEL7 ;TELL M-CODE TO PROCEED
6088
6089 035464 005003            CLR     R3         ;WE'LL WAIT THIS LONG FOR THE M-CODE TO
6090
6091 035466 005737 000376      10$:    TST    @#376      ; INTERRUPT OUR SEQUENCE OF OPERATION
6092 035472 001017            BNE   20$          ;LOOK FOR THE M-CODE TO LOAD THIS LOCATION
6093 035474 077304            SOB   R3,10$      ;WE SHOULD NEVER SEE THIS HAPPEN!!!
6094
6095
6096 035476 004537 005004      JSR    R5,MOVSW   ;RESTORE THE INTERRUPT VECTORS
6097 035502 002654            BUFAREA ; FROM THE BUFFER AREA
6098 035504 000000            0
6099 035506 000200            400/2    ; TO LOC'S 0 ==> 377
6100
6101 035510                SETPRI  #0        ;RESTORE PRIORITY LEVEL TO 0
    035510 012700 000000             MOV    #0,R0
    035514 104441             TRAP  C$SPRI
6102
6103 035516                GEDF   EM43A,ERR1 ;BUT WE AREN'T SURE WHAT IT IS!!!

```

TEST 13 -- HALT MODE VERIFICATION

```

; "DEVICE FATAL" ERROR # 82
035516 104455 TRAP C$ERDF
035520 000122 .WORD 82
035522 017153 .WORD EM43A
035524 006212 .WORD ERR1
6104 035526 000240 NOP ; (FOR PATCHING)
6105 035530 000415 BR 22$ ; GO RESET DMV & EXIT
6106
6107 035532 004537 005004 20$: JSR R5,MOVSW ;RESTORE THE INTERRUPT VECTORS
6108 035536 002654 BUFAREA ; FROM THE BUFFER AREA
6109 035540 000000 0
6110 035542 000200 400/2 ; TO LOC'S 0 ==> 377
6111
6112 035544 SETPRI #0 ;RESTORE PRIORITY LEVEL TO 0
035544 012700 000000 MOV #0,R0
035550 104441 TRAP C$SPRI
6113
6114 035552 GEDF EM43C,ERR1 ;POWER-UP INTERRUPT DIDN'T OCCUR
; "DEVICE FATAL" ERROR # 83
035552 104455 TRAP C$ERDF
035554 000123 .WORD 83
035556 017215 .WORD EM43C
035560 006212 .WORD ERR1
6115 035562 000240 NOP ; (FOR PATCHING)
6116 035564 004737 003346 22$: JSR PC,MASCLR ;RESET THE DMV
6117 035570 103001 BCC 23$ ; RESET OK, PROCEED
6118 035572 104460 ERROR ; RESET FAILED, REPORT IT
6119 035574 104460 TRAP C$ERROR
6120 035574 104410 23$: ESCAPE SUB ; & GET THE HECK OUT OF HERE!!!
035574 000160 TRAP C$ESCAPE
035576 000160 .WORD L10067-.
6121 035600 HLTST2:
6122
6123 035600 004537 005004 JSR R5,MOVSW ;RESTORE THE INTERRUPT VECTORS
6124 035604 002654 BUFAREA ; FROM THE BUFFER AREA
6125 035606 000000 0
6126 035610 000200 400/2 ; TO LOC'S 0 ==> 377
6127
6128 035612 010002 MOV R0,R2 ;R0 WILL BE CORRUPTED BY SUPERVISOR CALLS!
6129
6130 035614 SETPRI #0 ;RESTORE PRIORITY LEVEL TO 0
035614 012700 000000 MOV #0,R0
035620 104441 TRAP C$SPRI
6131 035622 022701 071573 CMP #071573,R1 ;CHECK RESULTS OF THE ADDITIONS
6132 035626 001412 BEQ 5$ ;OK -->
6133 035630 010137 002256 MOV R1,BDATA ;WHAT!!! SETUP & REPORT AN
6134 035634 012737 177777 002254 MOV #-1,GDATA ; ADDITION ERROR
6135 035642 GEDF EM43D,ERR1 ;ADDITION DIDN'T WORK PROPERLY
; "DEVICE FATAL" ERROR # 84
035642 104455 TRAP C$ERDF
035644 000124 .WORD 84
035646 017242 .WORD EM43D
035650 006212 .WORD ERR1
6136 035652 000240 NOP ; (FOR PATCHING)
6137 035654 020227 177777 5$: CMP R2,#-1 ;THE LOADED ROUTINE SHOULD HAVE DONE THIS
6138 035660 001003 BNE 8$ ;IT DIDN'T -- IT FAILED

```

TEST 13 -- HALT MODE VERIFICATION

```
6139 035662 023702 002412          CMP    TMO,R2          ;THESE SHOULD BE EQUAL TOO
6140 035666 001412                  BEQ    10$             ;OK -->
6141 035670 010237 002256          MOV    R2,BDATA       ;WHAT!!!  SETUP & REPORT AN ERROR
6142 035674 012737 177777 002254  8$:   MOV    #-1,GDATA
6143 035702                  GEDF   EM43D,ERR1     ;LOADED ROUTINE FAILED
                                           ; "DEVICE FATAL" ERROR # 85
                                           TRAP  C$ERDF
                                           .WORD 85
                                           .WORD EM43D
                                           .WORD ERR1
      035702 104455
      035704 000125
      035706 017242
      035710 006212
6144 035712 000240          NOP
6145 035714 020637 002454          10$:  CMP    SP,OLDSP      ; (FOR PATCHING)
6146 035720 001412                  BEQ    15$             ;THESE SHOULD ALSO BE EQUAL
6147 035722 010637 002256          MOV    SP,BDATA       ;OK -->
6148 035726 013737 002454 002254  15$:  MOV    OLDSP,GDATA    ;WHAT!!!  SETUP & REPORT AN ERROR
6149 035734                  GEDF   EM43D,ERR1     ;LOADED ROUTINE FAILED
                                           ; "DEVICE FATAL" ERROR # 86
                                           TRAP  C$ERDF
                                           .WORD 86
                                           .WORD EM43D
                                           .WORD ERR1
      035734 104455
      035736 000126
      035740 017242
      035742 006212
6150 035744 000240          NOP
6151 035746 004737 003346          15$:  JSR    PC,MASCLR    ; (FOR PATCHING)
6152 035752 103001                  BCC   16$             ;MAKE SURE THE DMV IS RESET
6153 035754                  ERROR                       ; RESET OK, CONTINUE
                                           ;RESET FAILED, REPORT ERROR
                                           TRAP  C$ERROR
      035754 104460
6154 035756          16$:  ENDSUB
6155 035756
                                           L10067: TRAP  C$ESUB
      035756 104403
                                           L10065: TRAP  C$ETST
6156 035760                  ENDTST
      035760 104401
```

SUBTEST 1'S M-CODE -- ASSIGNMENTS

6158
6159
6160
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6164 035762
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```
.SBTTL SUBTEST 1'S M-CODE -- ASSIGNMENTS
;*****
; MICRO-CODE FOR SUBROUTINE # 1
;*****
MC1:
; ASSEMBLED BY: COMPAS MICROSYSTEMS MINMIC (V6A)
; (WITH CHANGES EDITED IN)
;
;LINE# LOC CODE LINE
;0002 0000 *=$0000
;0003 0000
;0004 0000 ;EQUATES FOR BIT DEFINITIONS
;0005 0000 BIT0 =@1
;0006 0000 BIT1 =@2
;0007 0000 BIT2 =@4
;0008 0000 BIT4 =@20
;0009 0000 BIT5 =@40
;0010 0000 BIT6 =@100
;0011 0000
;0012 0000
;0013 0000 ;ADDRESS EQUATES FOR CSR REGISTERS
;0014 0000 BSEL4 =$14
;0015 0000 BSEL5 =BSEL4+1
;0016 0000 BSEL7 =BSEL4+3
;0017 0000
;0018 0000
;0019 0000 ;NPR ADDRESS REGISTER EQUATES
;0020 0000 NPRAIL = $003C ;IN NPR ADRS LO REG
;0021 0000 NPRAIH = NPRAIL+1 ;IN NPR ADRS HI REG
;0022 0000 NPRAIX = NPRAIL+2 ;IN NPR EXTENDED ADRS REG
;0023 0000
;0024 0000
;0025 0000 ;NPR DATA REG EQUATES
;0026 0000 NPRDIL = $A600 ;IN NPR DATA LO REG
;0027 0000 NPRDIH = NPRDIL+1 ;IN NPR DATA HI REG
;0028 0000
;0029 0000
;0030 0000 ;NPR CONTROL REG EQUATES
;0031 0000 NPRCTL = $A604 ;NPR CONTROL REGISTER
;0032 0000 NONPR = BIT6 ;USED TO PREVENT AN NPR
;0033 0000 INOUT = BIT5 ;SET TO 1 FOR INPUT, SET TO 0 FOR OUTPUT NPR
;0034 0000 HALT = BIT4 ;SET DURING MOP MODE ONLY
;0035 0000 PWRUP = BIT2 ;CLEARED BY BUS INIT TO INDICATE PWR UP
;0036 0000 SDCLOW = BIT1 ;SET TO 1 TO RESET LSI-11 FOR MOP BOOT
;0037 0000 DISINI = BIT0 ;SET TO 1 TO DISABLE BUS INIT TO 6502
;0038 0000
;0039 0000
;0040 0000 ;NPR REQUEST FUNCTIONS
;0041 0000 NPRRED = PWRUP ;IN/OUT BIT = 0 FOR READ TO DMV-11
;0042 0000
;0043 0000
;0044 0000 ;MISCELLANEOUS EQUATES
;0045 0000 STARAM = $003F ;STARTING ADRS OF GEN'L PURPOSE RAM TO TEST
;0046 0000
;0047 0000
```

SUBTEST 1'S M-CODE -- ROUTINE

```

6215 .SBTTL SUBTEST 1'S M-CODE -- ROUTINE
6216 ;0048 0000 *=STARAM ;START OF MICROCODE IN RAM
6217
6218 ;LINE# LOC CODE LINE
6219
6220 035762 251 000 .BYTE 251,00
6221 ;0050 003F A9 00 LDA #0 ;CLEAR BSEL7
6222 035764 205 027 .BYTE 205,27
6223 ;0051 0041 85 17 STA BSEL7
6224 ;0052 0043
6225 035766 245 027 .BYTE 245,27
6226 ;0053 0043 A5 17 WAIT1 LDA BSEL7 ;WAIT FOR IT TO GO <> 0
6227 035770 360 374 .BYTE 360,374
6228 ;0054 0045 F0 FC BEQ WAIT1
6229 ;0055 0047
6230 ;0056 0047 ; WE SHOULD NOW BE IN SYNC WITH THE 11 PROCESSOR
6231 ;0057 0047
6232 035772 245 024 .BYTE 245,24
6233 ;0058 0047 A5 14 LDA BSEL4 ;GET & SAVE THE ADDRESS
6234 035774 205 074 .BYTE 205,74
6235 ;0059 0049 85 3C STA NPRAIL ;OF "TMPO" AND USE IT TO
6236 035776 245 025 .BYTE 245,25
6237 ;0060 004B A5 15 LDA BSEL5 ;SETUP FOR AN NPR-IN
6238 036000 205 075 .BYTE 205,75
6239 ;0061 004D 85 3D STA NPRAIH ;OPERATION LATER
6240 ;0062 004F
6241 036002 251 124 .BYTE 251,124
6242 ;0063 004F A9 54 LDA #NONPR!HALT!PWRUP
6243 036004 215 004 246 .BYTE 215,4,246
6244 ;0064 0051 8D 04 A6 STA NPRCTL ;HALT THE 11 CPU
6245 ;0064 0054
6246 ;0064 0054 ; DELAY TO ALLOW "HALT" TO TAKE EFFECT (ABOUT
6247 ;0064 0054 ; 100 MICROSECONDS).
6248 036007 240 041 .BYTE 240,41
6249 ;0064 0054 A0 21 LDY #21 ;INITIAL VALUE OF COUNTER
6250 036011 210 .BYTE 210
6251 ;0064 0056 88 DELAY DEY ;(33. FOR .6 US CYCLE)
6252 036012 320 375 .BYTE 320,375
6253 ;0064 0057 D0 FD BNE DELAY
6254 ;0065 0059
6255 ;0066 0059 ; WE NOW HAVE TO READ THE 11 CPU'S LOCATION WHO'S
6256 ;0067 0059 ; ADDRESS WE PREVIOUSLY READ FROM SEL6
6257 ;0068 0059
6258 036014 251 000 .BYTE 251,00
6259 ;0069 0059 A9 00 LDA #0 ;CLEAR THE EXTENDED-ADDRESS-IN
6260 036016 205 076 .BYTE 205,76
6261 ;0070 005B 85 3E STA NPRAIX
6262 036020 251 024 .BYTE 251,24
6263 ;0071 005D A9 14 LDA #NPRRED!HALT
6264 036022 215 004 246 .BYTE 215,4,246
6265 ;0072 005F 8D 04 A6 STA NPRCTL ;READ ONE WORD FROM THE 11 CPU
6266 ;0073 0062
6267 036025 054 004 246 .BYTE 54,4,246
6268 ;0074 0062 2C 04 A6 NPRWAT BIT NPRCTL ;WAIT FOR IT TO "ALMOST" COMPLETE
6269 036030 160 373 .BYTE 160,373
6270 ;0075 0065 70 FB BVS NPRWAT
6271 036032 352 .BYTE 352

```

SUBTEST 1'S M-CODE -- ROUTINE

```

6272          ;0075 0067 EA                NOP                ; SHOULD COMPLETE HERE
6273          ;0076 0068
6274 036033   255 000 246 .BYTE 255,0,246
6275          ;0077 0068 AD 00 A6        LDA NPRDIL          ; MOVE THE WORD JUST READ INTO
6276 036036   205 024          .BYTE 205,24
6277          ;0078 006B 85 14          STA BSEL4          ; SEL4
6278 036040   255 001 246 .BYTE 255,1,246
6279          ;0079 006D AD 01 A6        LDA NPRDIH
6280 036043   205 025          .BYTE 205,25
6281          ;0080 0070 85 15          STA BSEL5
6282          ;0088 0072
6283          ;0089 0072          ; DROP "HALT" AND SET "DCOK H LO" & "DISABL INIT"
6284          ;0090 0072
6285 036045   251 125          .BYTE 251,125
6286          ;0091 0072 A9 55          LDA #NONPR!PWRUP!DISINI!HALT
6287 036047   215 004 246 .BYTE 215,4,246
6288          ;0092 0074 8D 04 A6        STA NPCRTL
6289 036052   251 107          .BYTE 251,107
6290          ;0093 0077 A9 47          LDA #NONPR!PWRUP!SDCLOW!DISINI
6291 036054   215 004 246 .BYTE 215,4,246
6292          ;0094 0079 8D 04 A6        STA NPRCTL
6293          ;0095 007C
6294          ;0096 007C          ; NOW LET THE 11 CPU GO THROUGH THE POWER-UP SEQUENCE
6295          ;0097 007C
6296 036057   251 105          .BYTE 251,105
6297          ;0098 007C A9 45          LDA #NONPR!PWRUP!DISINI
6298 036061   215 004 246 .BYTE 215,4,246
6299          ;0099 007E 8D 04 A6        STA NPRCTL
6300          ;0100 0081
6301          ;0101 0081          ; WHEN BSEL7 IS CLEARED, CLEAR "DISABL INIT"
6302          ;0102 0081
6303 036064   245 027          .BYTE 245,27
6304          ;0103 0081 A5 17          WAIT2 LDA BSEL7
6305 036066   320 374          .BYTE 320,374
6306          ;0104 0083 D0 FC          BNE WAIT2
6307          ;0105 0085
6308 036070   251 104          .BYTE 251,104
6309          ;0106 0085 A9 44          LDA #NONPR!PWRUP
6310 036072   215 004 246 .BYTE 215,4,246
6311          ;0107 0087 8D 04 A6        STA NPRCTL
6312          ;0108 008A
6313          ;0109 008A          ; USE A STANDARD SUBROUTINE RETURN TO GET BACK INTO
6314          ;0110 008A          ; THE MAINTENANCE LOOP
6315          ;0111 008A
6316 036075   140          .BYTE 140
6317          ;0112 008A 60          RTS
6318          ;0113 008B

```

;ERRORS = 0000

.SBTTL SUBTEST 1'S M-CODE -- SYMBOL TABLE

; BIT0	0001	BIT1	0002	BIT2	0004	BIT4	0010
; BIT5	0020	BIT6	0040	BSEL4	0014	BSEL5	0015
; BSEL7	0017	DELAY	0C6E	DISINI	0001	HALT	0010
; INOUT	0020	NONPR	0040	NPRAIH	003D	NPRAIL	003C

SUBTEST 1'S M-CODE -- SYMBOL TABLE

6329	:	NPRAIX	003E	NPRCTL	A604	NPRDIH	A601	NPRDIL	A600
6330	:	NPRRED	0004	NPRWAT	005D	PWRUP	0004	SDCLOW	0002
6331	:	STARAM	003F	WAIT1	0043	WAIT2	0078		
6332	:	END OF ASSEMBLY(V6A)							
6333	:	SYMBOLS LEFT = 1473 OUT OF 1500							
6334	:								
6335	:	.SBTTL SUBTEST 1'S M-CODE -- CROSS REFERENCE TABLE (CREF V01-05)							
6336	:								
6337	:								
6338	:	BIT0		5#	37				
6339	:	BIT1	6#	36					
6340	:	BIT2	7#	35					
6341	:	BIT4	8#	34					
6342	:	BIT5	9#	33					
6343	:	BIT6	10#	32					
6344	:	BSEL4	14#	58	78				
6345	:	BSEL5	15#	60	80				
6346	:	BSEL7	16#	15	16	51	53	100	
6347	:	DELAY	86#	87					
6348	:	DISINI	37#	91	96				
6349	:	HALT		34#	63	71			
6350	:	INOUT	33#						
6351	:	NONPR	32#	63	91	96	103		
6352	:	NPRAIH	21#	61					
6353	:	NPRAIL	20#	21	22	59			
6354	:	NPRAIX	22#	70					
6355	:	NPRCTL	31#	64	72	74	92	104	
6356	:	NPRDIH	27#	79					
6357	:	NPRDIL	26#	27	77				
6358	:	NPRRED	41#	71					
6359	:	NPRWAT	74#	75					
6360	:	PWRUP	35#	41	63	91	96	103	
6361	:	SDCLOW	36#	91					
6362	:	STARAM	45#	48					
6363	:	WAIT1	53#	54					
6364	:	WAIT2	100#	101					

SUBTEST 1'S M-CODE -- CROSS REFERENCE TABLE (CREF V01-05)

6366
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6372 000010
6373 036076
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6407
6408
6409
6410
6411

```

.EVEN
.SBTTL SUBTEST 2'S M-CODE -- ASSIGNMENTS
;*****
; MICRO-CODE FOR SUBROUTINE # 2
;*****
.RADIX 8.
MC2:
;LINE# LOC CODE LINE
;0002 0000 **$0000
;0003 0000
;0004 0000
;EQUATES FOR BIT DEFINITIONS
BIT0 =@1
BIT1 =@2
BIT2 =@4
BIT4 =@20
BIT6 =@100
;ADDRESS EQUATES FOR CSR REGISTERS
BSEL7 =@17
;NPR ADDRESS REGISTER EQUATES
NPR AOL = $0038 ;OUT NPR ADRS LO REG
NPR AOH = NPR AOL+1 ;OUT NPR ADRS HI REG
NPR AOX = NPR AOL+2 ;OUT NPR EXTENDED ADRS REG
;NPR DATA REG EQUATES
NPR DOL = $A600 ;OUT NPR DATA LO REG
NPR DOH = NPR DOL+1 ;OUT NPR DATA HI REG
;NPR CONTROL REG EQUATES
NPR CTL = $A604 ;NPR CONTROL REGISTER
NONPR = BIT6 ;USED TO PREVENT AN NPR
HALT = BIT4 ;SET DURING MOP MODE ONLY
PWRUP = BIT2 ;CLEARED BY BUS INIT
SDCLOW = BIT1 ;SET TO 1 TO RESET LSI-11 FOR MOP BOOT
DISINI = BIT0 ;SET TO 1 TO DISABLE BUS INIT TO 6502
;MISCELLANEOUS EQUATES
STARAM = $003F ;STARTING ADRS OF GEN'L PURPOSE RAM

```


SUBTEST 2'S M-CODE -- ROUTINE

```

6527 036225 040 305 000 .BYTE 040,305,000
6528 ;0101 0096 20 C5 00 ENDLOP JSR NPR ;MOVE THE LAST 6 WORDS
6529 036230 210 .BYTE 210
6530 ;0102 0099 88 DEY ;IF NOT DONE,
6531 036231 320 372 .BYTE 320,372
6532 ;0103 009A D0 FA BNE ENLDP ; DO IT AGAIN
6533 ;0104 009C ;ELSE, THE SIMULATED "DOWN-
6534 ;0105 009C ; LINE LOAD" IS COMPLETE
6535 ;0106 009C
6536 036233 251 004 .BYTE 251,004
6537 ;0107 009C A9 04 LDA #4 ; AND THE 11'S LOC. 4
6538 036235 205 070 .BYTE 205,070
6539 ;0108 009E 85 38 STA NPRAOL
6540 036237 040 305 000 .BYTE 040,305,000
6541 ;0109 00A0 20 C5 00 JSR NPR ;OVER-WRITE THE "BR ."
6542 ;0110 00A3 ;INSTRUCTION TO LET THE JUST
6543 ;0111 00A3 ;LOADED ROUTINE BE EXECUTED
6544 ;0112 00A3
6545 036242 251 104 .BYTE 251,104
6546 ;0113 00A3 A9 44 LDA #NONPR:PWRUP
6547 036244 215 004 246 .BYTE 215,004,246
6548 ;0114 00A5 8D 04 A6 STA NPRCTL ;LET BINIT RESET US AGAIN
6549 ;0115 00A8
6550 036247 140 .BYTE 140
6551 ;0116 00A8 60 RTS ;RETURN TO MAINTENANCE LOOP
6552 ;0117 00A9
6553 ;0118 00A9
6554 ;0119 00A9
6555 ;0120 00A9
6556 ;0121 00A9
6557 ;0122 00A9
6558 ;0123 00A9
6559 ;0124 00A9
6560 ;0125 00A9
6561 ;0126 00A9
6562 ;0127 00A9
6563 036250 000000 .WORD 000000
6564 ;0128 00A9 00 00
6565 036252 000340 .WORD 000340
6566 ;0128 00AB 00 E0
6567 036254 012700 177777 MOV #-1,R0
6568 ;0129 00AD 15 C0 .DBYTE @012700,-1,@777 ;LOC'S 0 --> 4
6569 ;0129 00AF FF FF
6570 036260 000777 BR
6571 ;0129 00B1 01 FF
6572 036262 005001 CLR R1
6573 ;0130 00B3 0A 01 .DBYTE @005001 ;LOC. 6
6574 036264 062701 .WORD 062701
6575 ;0131 00B5 65 C1 .DBYTE @062701 ;LOC'S 10 --> 362
6576 036266 010037 002412 MOV R0,@TMP0
6577 ;0132 00B7 10 1F .DBYTE @010037 ;LOC 364 "MOV"
6578 ;0133 00B9 00 00 .DBYTE 0 ;LOC 366
6579 036272 013706 002454 MOV @OLDSP,SP
6580 ;0134 00BB 17 C6 .DBYTE @013706 ;LOC 370 "MOV"
6581 ;0135 00BD 00 00 .DBYTE 0 ;LOC 372
6582 036276 000137 035600 JMP @MLTST2
6583 ;0136 00BF 00 5F .DBYTE @000137 ;LOC 374 "JMP"

```

```

;*****
;
; DATABL -- DATA TABLE CONTAINING THE DATA THAT
; IS TO BE NPR'D INTO THE 11'S MEMORY
;*****

```

```

DATABL
.DBYTE 0,@340 ;LOC'S 24 & 26
.DBYTE @012700,-1,@777 ;LOC'S 0 --> 4
.DBYTE @005001 ;LOC. 6
.DBYTE @062701 ;LOC'S 10 --> 362
.DBYTE @010037 ;LOC 364 "MOV"
.DBYTE 0 ;LOC 366
.DBYTE @013706 ;LOC 370 "MOV"
.DBYTE 0 ;LOC 372
.DBYTE @000137 ;LOC 374 "JMP"

```

SUBTEST 2'S M-CODE -- ROUTINE

```

6584                ;0137 00C1 00 00
6585 036302 000240  NOP
6586                ;0138 00C3 00 A0
6587                ;0139 00C5
6588                ;0140 00C5
6589                ;0141 00C5
6590                ;0142 00C5
6591                ;0143 00C5
6592                ;0144 00C5
6593                ;0145 00C5
6594                ;0146 00C5
6595                ;0147 00C5
6596                ;0148 00C5
6597                ;0149 00C5
6598                ;0150 00C5
6599                ;0151 00C5
6600                ;0152 00C5
6601                ;0153 00C5
6602                ;0154 00C5
6603                ;0155 00C5
6604                ;0156 00C5
6605                ;0157 00C5
6606                ;0158 00C5
6607                ;0159 00C5
6608                ;0160 00C5
6609                ;0161 00C5
6610 036304      265      251      .BYTE 265,251
6611                ;0162 00C5 B5 A9
6612 036306      215      000      246  .BYTE 215,000,246
6613                ;0163 00C7 8D 00 A6
6614 036311      265      252      .BYTE 265,252
6615                ;0164 00CA B5 AA
6616 036313      215      001      246  .BYTE 215,001,246
6617                ;0165 00CC 8D 01 A6
6618                ;0166 00CF
6619 036316      255      004      246  .BYTE 255,004,246
6620                ;0167 00CF AD 04 A6
6621 036321      215      004      246  .BYTE 215,004,246
6622                ;0168 00D7 8D 04 A6
6623                ;0169 00D5
6624 036324      350                .BYTE 350
6625                ;0170 00D5 E8
6626 036325      350                .BYTE 350
6627                ;0171 00D6 E8
6628                ;0172 00D7
6629                ;0173 00D7
6630                ;0174 00D7
6631 036326      054      004      246  .BYTE 054,004,246
6632                ;0175 00D7 2C 04 A6
6633 036331      160      373      .BYTE 160,373
6634                ;0176 00DA 70 FB
6635 036333      352                .BYTE 352
6636                ;0177 00DC EA
6637 036334      346      070      .BYTE 346,070
6638                ;0178 00DD E6 38
6639 036336      346      070      .BYTE 346,070
6640                ;0179 00DF E6 38

```

```

.DBYTE 0           ;LOC 376
.DBYTE @000240 ;"NOP" FOR LOC 4
; THE THREE WORDS FOR LOCATIONS 366, 372, & 376 ARE
; ASSEMBLED IN WHEN THIS CODE IS INCLUDED INTO THE
; DIAGNOSTIC
;*****
; "NPR" SUBROUTINE:
; 1 TAKE THE DATA FROM THE DATA TABLE AS INDEXED BY
;   "X" AND PUT IT INTO THE NPR DATA OUT REGISTERS
; 2 GET THE CURRENT SETTING OF THE NPR CONTROL REG.
;   AND WRITE IT BACK TO CAUSE A WORD NPR-OUT
; 3 INCREMENT THE NPR-OUT-ADDRESS-LOW REGISTER
; 4 WAIT FOR "GOBUSY" TO GO LOW
; 5 RETURN TO CALLER
;*****
NPR   LDA   DATABL,X ;LOAD THE DATA-OUT REG'S
      STA   NPRDOL
      LDA   DATABL+1,X
      STA   NPRDOH
      LDA   NPRCTL
      STA   NPRCTL ;KICK OFF A WORD NPR-OUT
      INX                ;POINT TO THE NEXT DATA
      INX                ; WORD
NPRWAT
      BIT   NPRCTL ;WAIT FOR THE NPR TO
      BVS  NPRWAT ; COMPLETE
      NOP
      INC   NPRAOL ;POINT TO THE NEXT WORD
      INC   NPRAOL ;OF THE 11'S MEMORY

```

SUBTEST 2'S M-CODE -- ROUTINE

```

6641      ;0180 00E1
6642 036340      140      .BYTE 140
6643      ;0181 00E1 60      RTS      ;RETURN TO CALLER
6644      ;0182 00E2
6645      ;
6646      ;
6647      ;ERRORS = 0000
6648      ;
6649      ;SBTTL SUBTEST 2'S M-CODE -- SYMBOL TABLE
6650      ;
6651      ; BIT0 0001 BIT1 0002 BIT2 0004 BIT4 0010
6652      ; BIT6 0040 BSEL7 0017 DATABL 009D DISINI 0001
6653      ; ENDLOP 008A FILOOP 007E HALT 0010 NONPR 0040
6654      ; NPR 00B9 NPRAOH 0039 NPRAOL 0038 NPRAOX 003A
6655      ; NPRCTL A604 NPRDOH A601 NPRDOL A600 NPRWAT 00CB
6656      ; PWRUP 0004 SDCLW 0002 STARAM 003F WAIT1 0043
6657      ;
6658      ;END OF ASSEMBLY(V6A)
6659      ;SYMBOLS LEFT = 1476 OUT OF 1500
6660      ;
6661      ;COMPAS MICROSYSTEMS MINMIC CROSS ASSEMBLER PAGE C-1
6662      ;SBTTL SUBTEST 2'S M-CODE -- CROSS REFERENCE TABLE (CREF V01-05 )
6663      ;
6664      ;
6665      ;BIT0 5# 33
6666      ;BIT1 6# 32
6667      ;BIT2 7# 31
6668      ;BIT4 8# 30
6669      ;BIT6 9# 29
6670      ;BSEL7 13# 43 45
6671      ;DATABL 127# 162 164
6672      ;DISINI 33# 55 57 84 86
6673      ;ENDLOP 101# 103
6674      ;FILOOP 93# 97
6675      ;HALT 30# 55 57
6676      ;NONPR 29# 55 57 84 86 113
6677      ;NPR 70 71 75 76 77 91 93
6678      ; 101 109 162#
6679      ;NPRAOH 18# 61
6680      ;NPRAOL 17# 18 19 69 74 108 178
6681      ; 179
6682      ;NPRAOX 19# 62
6683      ;NPRCTL 28# 56 58 85 87 114 167
6684      ; 168 175
6685      ;NPRDOH 24# 165
6686      ;NPRDOL 23# 24 163
6687      ;NPRWAT 174# 176
6688      ;PWRUP 31# 55 57 84 86 113
6689      ;SDCLW 32# 55 84
6690      ;STARAM 37# 40
6691      ;WAIT1 45# 46
6692 036341      MC2END:
6693      .EVEN

```

HARDWARE PARAMETER CODING SECTION

6695
6696
6697
6698
6699
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6726

.SBTTL HARDWARE PARAMETER CODING SECTION

```

: ////////////////////////////////////////////////////////////////////
:/ THE HARDWARE PARAMETER CODING SECTION CONTAINS MACROS
:/ THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES. THE
:/ MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
:/ INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES. THE
:/ MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
:/ WITH THE OPERATOR.
: ////////////////////////////////////////////////////////////////////

```

```

      036342      BGNHRD
      036342      000025
      036344
      036342      .WORD L10070-L$HARD/2
      036344      L$HARD::

      036344      GPRMA   ADDRES,0,0,160020,177776,YES
      036344      .WORD   T$CODE
      036346      036416      .WORD   ADDRES
      036350      160020      .WORD   T$LLOLIM
      036352      177776      .WORD   T$HILIM

      036354      GPRMA   VECTOR,2,0,0,674,YES
      036354      .WORD   T$CODE
      036356      036444      .WORD   VECTOR
      036360      000000      .WORD   T$LLOLIM
      036362      000674      .WORD   T$HILIM

      036364      GPRMD   PRIRTY,4,0,7000,0,7,YES
      036364      .WORD   T$CODE
      036366      036475      .WORD   PRIRTY
      036370      007000      .WORD   7000
      036372      000000      .WORD   T$LLOLIM
      036374      000007      .WORD   T$HILIM

      ;          GPRML   PU24.M,16,100,YES
      ;          GPRMD   BDTY.M,12,0,7,0,2,YES
      ;          ;JB REV A-0

      036376      .WORD   T$CODE
      036400      036526      .WORD   BDTY.M
      036402      000007      .WORD   7
      036404      000000      .WORD   T$LLOLIM
      036406      000002      .WORD   T$HILIM

      036410      GPRML   XMFG.M,16,200,YES
      036410      .WORD   T$CODE
      036412      036673      .WORD   XMFG.M
      036414      000200      .WORD   200

      036416      ENDHRD
      036416      L10070: .EVEN

      .NLIST  BEX
      104     105     126  ADDRES: .ASCIZ  /DEVICE CSR ADDRESS : /
      104     105     126  VECTOR: .ASCIZ  /DEVICE VECTOR ADDRESS : /
      104     105     126  PRIRTY: .ASCIZ  /DEVICE PRIORITY LEVEL : /
      102     117     101  BDTY.M: .ASCIZ  /BOARD TYPE (0=M8064, 1=M8053-V.35, 2=M8053-EIA) : /
      111     123     040  PU24.M: .ASCIZ  /IS THE PROCESSOR STRAPPED TO MODE 0 ON POWER-UP? /
      111     123     040  XMFG.M: .ASCIZ  /IS THIS A MANUFACTURING TEST STAND? /
      .LIST   BEX

```

HARDWARE PARAMETER CODING SECTION

6727

.EVEN

SOFTWARE PARAMETER CODING SECTION

.SBTTL SOFTWARE PARAMETER CODING SECTION

6729
6730
6731
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6736
6737
6738
6739
6740
6741
6742 036740
036740 000000
036742
6743 036742
036742

```
/////////////////////////////////////////  
:/ THE SOFTWARE PARAMETER CODING SECTION CONTAINS MACROS  
:/ THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES. THE  
:/ MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE  
:/ INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES. THE  
:/ MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS  
:/ WITH THE OPERATOR.  
/////////////////////////////////////////
```

BGNSFT

```
          .WORD L10071-L$SOFT/2  
L$SOFT::  
          .EVEN  
L10071:
```

ENDSFT

PATCH AREA FOR DEBUG

6745
 6746 036742
 6747 037042
 6748 037042 000240
 6749 037044 000240
 6750 037046 000240
 6751
 6752
 6753
 6754
 6755 037050
 6756 037050

 037050 000000
 037052 000000
 037054
 6757 000001

.SBTTL PATCH AREA FOR DEBUG

PATCH:

. = . +100
 NOP
 NOP
 NOP

.SBTTL "ENDMOD" & "LASTAD"

ENDMOD
LASTAD

L\$LAST::
.END

.EVEN
 .WORD 0
 .WORD 0

SYMBOL TABLE

VIRTUAL MEMORY USED: 34384 WORDS (135 PAGES)
DYNAMIC MEMORY: 19748 WORDS (75 PAGES)
ELAPSED TIME: 00:24:01
CNDMBA.BIC,CNDMBA.SEQ/CR/-SP=SVC34.MLB/ML,CNDMBA.P11