



PDP-10  
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DC08A/689AG DATA LINE SCANNER FOR PDP-10

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S680I  
DC08A/689AG SCANNER FOR PDP-10

## 1. REQUIREMENTS

### 1.1 Core Storage on the PDP-8/I

S680I is a PDP-8/I program which performs the same functions as a PDP-10 hardware scanner, such as the 630 DCS or the DC10. The program includes the DC08 8-bit character assembly subroutines (described in DEC-8I-F8VA-D), which have been slightly modified. Table space is allocated as described at the beginning of the program listing.

### 1.2 Hardware and Software

The minimum requirement for the system is:

- a. A minimum configuration PDP-8/I
- b. 680 Data Communication System Hardware: DL8I, DC08A, 689AG
- c. DA10 PDP-10 to PDP-8 interface, with PDP-8 IOT codes 35, 36, 37.
- d. PDP-10 with time-sharing monitor, whose scanner service routine is properly interfaced with DA10/CCI equipment. (See below.)

## 2. OPERATION OF THE PDP-8/I S680I PROGRAM

### 2.1 Loading

- a. S680I exists on paper tape in BIN format. Starting from scratch the user must toggle the RIM loader into core starting at location 7756; this process should not have to be repeated unless the PDP-8 is used for some purpose other than scanning lines which would destroy these locations.
- b. The BIN loader is then placed in the reader, the switch register is set at 7756, the operator presses LOAD ADDRESS and then START. The paper tape reader switch must be in the START position. As long as only S680I is used in the PDP-8, this process should only have to be done once.
- c. After the BIN loader is in core, the S680I tape is placed in the reader, the switches are set to 7777, and LOAD ADDRESS is pressed; the switches are then set to 4000 if the slow (Teletype) paper tape reader is used, or to 0000 if the fast reader is used. START is

pressed and the tape is read in. If the accumulator indicator lights are nonzero after the tape has stopped reading in, there is a checksum error.

Steps a, b and c should not have to be repeated as long as the PDP-8 is not used for some other purpose which may destroy the relevant core locations; reloading or reinitializing the PDP-10 monitor has no effect on the S680I program running on the PDP-8.

## 2.2 Initializing and Starting

a. Before starting S680I, it must be ascertained that the highest numbered physical Teletype connected to the 680 hardware is reflected in location 377. The current version assembles with this number  $\bar{m}$ \* equal to  $10_8$ ; if  $m$  for a given installation is generally constant, it would be worthwhile to reassemble S680I to assign a new permanent value  $m^1$ . Otherwise, the operator may load the value  $m$  into location 377 as follows (assume  $m = 5$ ).

- (1) Set switch register to 377.
- (2) Press LOAD ADDRESS.
- (3) Set switch register to 5.
- (4) Elevate DEPOSIT.

S680I will assume physical line numbers 0 through 5 to be on the 680 hardware. The PDP-8/I console Teletype will always be given the line number  $m + 1$  (in this case, line number 6), and will be usable in the same manner as the Teletypes on the 680. 680 line numbers which are less than  $\bar{m}$  but have no Teletypes, will act in the same manner as disconnected Teletypes in a hardware scanner, i.e., as far as the monitor or program is concerned, the Teletype is there and functions properly. Currently the PDP-10 monitor must be built to accept  $m + 1$  physical scanner lines (since the PDP-8/I console Teletype will have line number  $m + 1$  as far as the PDP-10 is concerned).

The starting address for S680I is  $200_8$ . Once started, the program may run indefinitely even if the PDP-10 monitor is stopped and restarted. The starting procedure is as follows:

- (1) Set switches to 200.
- (2) Press LOAD ADDRESS.
- (3) Press START.

The PDP-8/I is now functioning as a line scanner for the PDP-10; the Teletypes connected to the 680 system as well as the PDP-8/I console Teletype are now the TTY's of the PDP-10.

When restarting or reloading the PDP-10 monitor, it may be advisable to restart the PDP-8/I at  $200_8$  in order to clear all buffers of partially transmitted or received characters. The line number in location 377, however, is never altered by the program; it need only be toggled in after loading S680I and then only if the assembled quantity in location 377 differs from the actual highest line number on the 680.

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\*It should be noted that  $\bar{m}$  must be the highest 680 line number which has a Teletype connected to it, which is not necessarily the number of actual Teletypes.

## NOTE

The initialization process which begins at location 200 in the S680I program dynamically creates all line-number dependent portions of the program - both buffers and IOT instructions - from the argument in location 377. Therefore, even if a new PDP-10 monitor with a different amount of line numbers is loaded, or if the physical line number assignments are altered on the 680 hardware, a reload of S680I is not necessary; it is sufficient to toggle the new value of the highest line number into location 377 and then restart at 200.

### 3. PDP-10 PROGRAMMING FOR THE DA10/PDP-8/I

The coding methods described in the following discussion are implemented in that version of the scanner service routine which utilizes the DA10/S680I system as a line scanner. The discussion is included for the benefit of those who may wish to program the DA10/S680I system as if it were a device, or for those who may wish to make any alterations or modifications in the current PDP-10/DA10 interface.

#### 3.1 Setting Initial Conditions

The DA10/S680I system must be initialized to a virgin state where any previously caused conditions are effectively erased, and to where the system is ready to process interrupts from the Teletype lines. The initial conditions are as follows:

- a. All conditions that may cause interrupts on the PDP-10 and the PDP-8/I must be cleared.
- b. A PI channel must be assigned for the PDP-10.
- c. The PDP-8 program, S680I, must be informed that the PDP-10 may receive interrupts. The following instruction sequence will accomplish this:

```
MOVEI    TAC,SCNCHN    ;PI CHANNEL NUMBER.  
CONO     14,2510(TAC)  ;SETS CONDITIONS.
```

This last instruction:

- a. Clears the From-10-Buffer Full flag (bit 25).  
Clears the From-10-Buffer Empty flag (bit 27).  
Clears the To-10-Buffer Full flag (bit 29).
- b. Assigns PI channel number in bits 33 through 35.
- c. Sets to-10-Buffer empty flag (bit 32), which will cause SCN680 to set a flag indicating that information can now be passed to the PDP-10.

### 3.2 Processing Interrupts

Two conditions will cause an interrupt on the PDP-10: The To-10-Buffer Empty flag being set. The From-10-Buffer Empty flag is set whenever the PDP-8 program S680I has input the data in the low-order 12 bits of the From-10-Buffer of the DA10. There is no way for the PDP-10, however, to detect what the contents of the From-10-Buffer were, or whether this interrupt is an indication that the character that was input by S680I has been transmitted - hence, it is not a true "transmit interrupt" and it should be dismissed as follows:

```

CONSO      14,200           ;FROM-10-BUFFER EMPTY?
(see if char. received or transmitted) ;NO
(save TAC)                               ;YES
MOVEI      TAC,SCNCHN      ;CHANNEL NUMBER
CONO       14,400 (TAC)    ;CLEAR FLAG
(restore TAC and dismiss int.)

```

If the To-10-Buffer Full flag caused the interrupt, the following code must be executed.

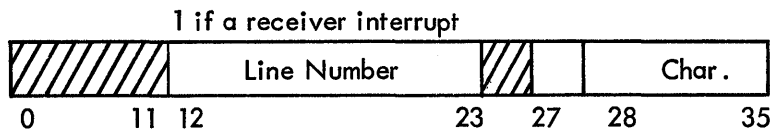
```

...
CONSO      14,40           ;TO-10-BUFFER FULL?
(dismiss int. condition) ;NO
DATAI      14,TAC         ;SETS TO-10-BUFFER EMPTY
...                               ;FLAG CLEARS TO-10-BUFFER FULL
...                               ;FLAG

```

### 3.3 Transmitting and Receiving Characters

The format for data transmitted to or received from the DA10 buffers is as follows:

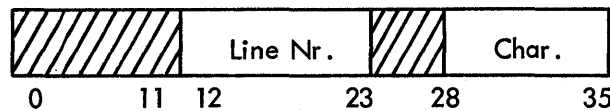


a. To transmit a character on a line, the following instruction is used:

```
DATAO      14,CHAR
```

where

```
CHAR/
```

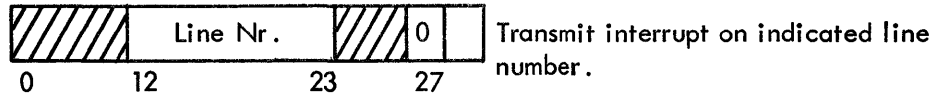


b. If the following sequence is executed,

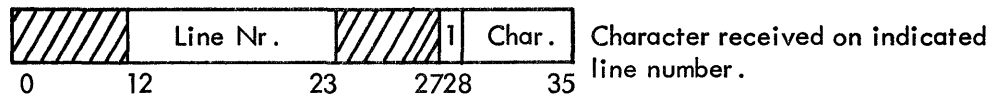
```

CONSO      14,40          ;TO-10-BUFFER FULL FLAG?
(dismiss int.)          ;NO
DATAI      14,CHAR       ;YES
  
```

CHAR may be



or



After determining that the To-10-Buff Full flag caused the interrupt, the following sequence must be performed.

```

DATAI      14,CHAR
TRNN       CHAR,400      ;RECEIVER INTERRUPT?
(transmit interrupt; get next char.) ;NO
(receiver routine)       ;YES
  
```

### ASSEMBLING INSTRUCTIONS

S680I may be assembled for any of 4 possible clocks in the DC08-A hardware. These clocks are called Clock 1, Clock 2, Clock 3 and Clock 4.

In order to assemble S680I for Clock n, the operator must edit the line of the source file defining the clock number as follows:

CLOCK = n

This line is on the first page of the S680I source.

The following commands will create the paper tape and listing of S680I:

```

.RUN DTAN PAL
*PTP: ,LPT:←DTAN:S680I.PAL
  
```