

IDENTIFIER	DEFINED ON LINE	REFERENCES											
xcb_p	8985	9015	9015										
xcb_p	9532	9553	9553										
xcb_p	9533	9553/P	9553/P	9555									
xcb_p	9665	9694	9694										
xcb_p	9665	9695	9695										
xcb_p	9678	9684/P	9693	9694/P	9695/P	9702	9703/M	9712/P	9713				
		9714	9715/M										
xcb_p	13375	13386	13386										
xcb_p	13376	13385	13386/P	13387	13392/S	13393/P	13395	13395	13396/S				
		13403/P											
xcb_p	13428	13464	13464										
xcb_p	13455	13461/M	13462/P	13464/P	13464/P	13465/P	13466/P	13467/P					
xijl_ordinal	7271	7279/M	7282/M	7286									
xijl_ordinal	9665	9686/M	9686/M	9686									
xmcount	9945	10071/M											
xp	5306	8895	9008	9010	9018	9553/P	9555	10900	10905				
		10905	10906	11066	11067	11327	11328/P	11336/M	11336				
		11350	11354	11356	11358	11999/M	13387	13464/P	13465/P				
		13466/P	13467/P										
xp	5492	8772	8826	8936	8962	11510							
xpfti	7687	7702/M											
xpfti	7955	7974/M											
xpfti	9775	9851/M	9852/S	9853/S	9854/S	9870/M	9871/S	9872/S	9873/S				
xpfti	9942	9972/M	9988/M										
xpfti	9942	9976/M	10063/M										
xpfti	11313	11911/M	11912/S	11913/S	11914/P								
xpfti	12026	12317/M											
xrcount	9946	10070/M											
xsfid	7272	7274/M	7275/M	7278/M	7281/M	7285							
xsfid	9665	9686/M	9686/M	9686/M	9686/M	9686							
xsva	8875	8974/M											
xsva	10777	10810											
xsva	11312	11899/M	11901/M	11905	11905	11906/M	11906	11907					
xsva	12058	12107/M	12164	12181/M	12181	12216/M	12218	12237/P	12243/M				
		12243	12265/P	12270	12302/M	12302							

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

```

2 MODULE mmm$pfli_manager;
3
4 {
5 { PURPOSE: Memory_Manager
6 { This module Contains the monitor routines that are used to
7 { locate all page frames bedlonging to a specified range of an SVA.
8 {
9
o 210 { This decks defines compile-time constants to control conditional compilation
o 211 { of debug code in memory manager modules in monitor mode. All constants should
o 212 { be set to FALSE for the transmitted version of this deck.
o 213
o 214 CONST
o 215     mmc$debug = TRUE;
o 216
o 217
o 218 ?VAR
o 219     mmc$debug_check_queues: boolean := TRUE, {Check PQL linkage}
o 220     mmc$debug_relink_swapping_job: boolean := TRUE, {Stop if relink page of swapping job}
o 221     mmc$debug_pt: boolean := TRUE, {check PT - AST linkage}
o 222     mmc$debug_free_asid: boolean := TRUE, {Verify no attempt is made to free an AST
o 223     { entry with pages not in AVAIL queue.
o 224     mmc$debug_aste_p_from_pfli: boolean := TRUE, {Verify aste_p in pfli is correct}
o 225     mmc$debug_rma_list: boolean := TRUE, {Verify RMA list on lock/unlock rma list}
o 226     mmc$debug_esc_alloc: boolean := TRUE, {check for escaped allocation}
o 227     mmc$debug_ast_pfli: boolean := TRUE?; {check pfli.aste_p on reference to AST}
o 228

o 230 {External procedures used by this module.
o 231 { Try to keep them in alphabetical order.
o 232 PROCEDURE [inline] jmp$get_ijle_p (ijl_ordinal: jmt$ijl_ordinal;
o 233     VAR ijle_p: ^jmt$initiated_job_list_entry);
o 234
1173
1174 { PURPOSE: procedure mtp$error_stop
1175 { Prefixes 'ERR=VE0S1000-' to the string and calls mtp$step_unstep_system to write string and step system}
1176
1177 PROCEDURE [XREF] mtp$error_stop (text: string(*<=63) );
1178

```

Global Variable Declarations - XREF and XDCL

```

1180 {Define pointer to Initiated Job List (IJL).
1181
1182   VAR
1183     jmv$ijl_p: [XREF] jmt$ijl_p;
0 1211 {Pointer to the 'PAGE FRAME TABLE' (PFT)
0 1212
0 1213   VAR
0 1214     mmv$pft_p: [XREF] ^mmt$page_frame_table;
0 1215

1219 {Define page table length in words.
1220
1221   VAR
1222     mmv$pt_length: [XREF] integer;
1223

1225 {Pointer to the system PAGE TABLE (PT).
1226
1227   VAR
1228     mmv$pt_p: [XREF] ^ost$page_table;
1229
0 1232 {System page size.}
0 1233
0 1234   VAR
0 1235     osv$page_size: [XREF] ost$page_size;
0 1236
0 1250   VAR
0 1251     mmv$pfti_array_p: [XDCL, #GATE] ^mmt$pfti_array;
0 1252
0 1253

```

SOURCE LIST OF mmm\$pfti_manager

[XDCL] mmp\$initialize_find_next_pfti

```

0 1255 {-----
0 1256 {
0 1257 { The following procedures are used for locating pages belonging to a specified SVA to SVA+LENGTH range.
0 1258 { The procedures use an array (MMV$PFTI_ARRAY) allocated at deadstart time to contain the list of PFTIs.
0 1259 {   mmp$initialize_find_next_pfti - fills the array with the list of PFTIs and returns the first one.
0 1260 {   mmp$find_next_pfti - returns the next PFTI from the array. (0 = end of array)
0 1261 {   mmp$delete_last_pfti_from_array - deletes the most recently returned PFTI from the array. If the
0 1262 {   PFTI array is rescanned the entry will not be returned again;
0 1263 {   mmp$reset_find_next_pfti - resets the array index so that subsequent calls to
0 1264 {   mmp$find_next_pfti will rescan the array and return all PFTIs.
0 1265 {
0 1266 { Depending on initialization options the PFTIs selected by these routines are restricted as follows:
0 1267 {   psc_nominal_queue - only PFTIs of pages that are 'valid' will be returned (eg. working_set, wired,
0 1268 {   fixed, shared, io_error)
0 1269 {   psc_all_except_avail - PFTI's of all pages in memory EXCEPT for pages in the AVAIL queue are returned.
0 1270 {   PROCEDURES THAT CALL THESE ROUTINES MUST BE PREPARED TO HANDLE NOT FINDING
0 1271 {   PAGES THAT ARE IN THE AVAIL QUEUE
0 1272 {   psc_all - the PFTI's of all pages within the range that are in memory are returned. To
0 1273 {   ensure that all pages of the segment are found, THE CALLER MUST SPECIFY THAT
0 1274 {   THE ENTIRE SEGMENT BE SEARCHED.
0 1275 {-----
0 1276 {
0 1277   PROCEDURE [XDCL] mmp$initialize_find_next_pfti
4 1278   (
4 1279     xsva: ost$system_virtual_address;
4 1280     length: ost$segment_length;
4 1281     end_point_option: [include_partial_pages, exclude_partial_pages];
4 1282     page_selection_criteria: mmt$page_selection_criteria;
4 1283     aste_p: ^mmt$active_segment_table_entry;
4 1284     VAR xpfti: mmt$page_frame_index);
4 1285   VAR
4 1286     found: boolean,
4 1287     hcount: 1 .. 32,
4 1288     high_offset: integer,
4 1289     ipti: integer,
4 1290     low_offset: integer,
4 1291     offset: integer,
4 1292     page_count: integer,
4 1293     pages_in_memory: integer,
4 1294     pfti: mmt$page_frame_index,
4 1295     pfti_index: integer,
4 1296     selection_page_queues: mmt$page_queue_set,
4 1297     sva: ost$system_virtual_address;
4 1298
4 1299     mmv$pfti_array_p^.pftis [0] := 0;
4 1300     pfti_index := 0;
4 1301     pages_in_memory := aste_p^.pages_in_memory;
4 1302
4 1303
4 1304 { Set up the parameters used to control the search. This includes the starting offset.
4 1305 { Determine the set of page queues a page can be linked to based on the page_selection_criteria.
4 1306
4 1307   IF (pages_in_memory > 0) AND (length > 0) THEN
28 1308     sva := xsva;
28 1309     offset := sva.offset;
28 1310     IF end_point_option = include_partial_pages THEN

```

[XDCL] mmm\$initialize_find_next_pfli

```

3E 1311.    page_count := (offset + length - 1) DIV osv$page_size + (offset DIV osv$page_size) + 1;
5A 1312    ELSE
5A 1313    page_count := (offset + length) DIV osv$page_size;
5A 1314    offset := ((offset + osv$page_size - 1) DIV osv$page_size) * osv$page_size;
5A 1315    page_count := page_count - offset DIV osv$page_size;
7C 1316    IFEND;
7C 1317    low_offset := offset - offset MOD osv$page_size;
7C 1318    high_offset := low_offset + page_count * osv$page_size - 1;
7C 1319
7C 1320    selection_page_queues := $mmt$page_queue_set [];
7C 1321    IF page_selection_criteria = psc_all THEN
A2 1322    selection_page_queues := -$mmt$page_queue_set [];
AC 1323    ELSEIF page_selection_criteria = psc_all_except_avail THEN
B2 1324    selection_page_queues := -$mmt$page_queue_set [mmc$mq_avail];
CO 1325    ELSE {page_selection_criteria = psc_nominal_queue}
CO 1326    IF aste_p^queue_id = mmc$mq_job_working_set THEN
CC 1327    selection_page_queues := $mmt$page_queue_set [mmc$mq_shared_io_error, mmc$mq_job_io_error,
D4 1328    mmc$mq_job_working_set]; { UNwritable permanent file pages go to the shared_io_error q. }
E4 1329    ELSEIF ((aste_p^queue_id >= mmc$mq_shared_first) AND (aste_p^queue_id <= mmc$mq_shared_last)) THEN
D2 1330    selection_page_queues := -$mmt$page_queue_set [mmc$mq_free, mmc$mq_avail, mmc$mq_avail_modified,
F4 1331    mmc$mq_wired, mmc$mq_job_fixed, mmc$mq_job_working_set, mmc$mq_job_io_error];
F4 1332    ELSEIF aste_p^queue_id = mmc$mq_wired THEN
FE 1333    selection_page_queues := $mmt$page_queue_set [mmc$mq_wired];
10E 1334    ELSEIF aste_p^queue_id = mmc$mq_job_fixed THEN
10E 1335    selection_page_queues := $mmt$page_queue_set [mmc$mq_job_fixed];
114 1336    ELSE
114 1337    mtp$error_stop ('FIND NEXT PFTI -- BAD QUEUE ID');
134 1338    IFEND;
134 1339    IFEND;
134 1340
134 1341 { Search for pages.
134 1342
134 1343    IF (low_offset = 0) AND (length > 7ffff00(16)) THEN
144 1344    pfti := aste_p^.pft_link.fwd;
144 1345    IF (page_selection_criteria = psc_all) THEN
150 1346
150 1347 { The request is for all the pages of a segment that are in memory, so use the search algorithm that uses
150 1348 { the segment/page frame link to find all pages of the segment.
150 1349
150 1350    WHILE pfti <> 0 DO
154 1351    mmv$pfli_array_p^.pftis [pfti_index] := pfti;
154 1352    pfti_index := pfti_index + 1;
154 1353    pfti := mmv$pfli_p^ [pfti].segment_link.fwd;
154 1354    WHILEND;
18A 1355    ELSE
18A 1356
18A 1357 { The request length is for the entire segment, but not all queues should be searched. Use the
18A 1358 { the segment/page frame link to find all pages of the segment and check if each page fits the
18A 1359 { page_selection_criteria.
18A 1360
18A 1361    WHILE (pfti <> 0) DO
192 1362    IF mmv$pfli_p^ [pfti].queue_id IN selection_page_queues THEN
18A 1363    mmv$pfli_array_p^.pftis [pfti_index] := pfti;
18A 1364    pfti_index := pfti_index + 1;
1C8 1365    IFEND;
1C8 1366    pfti := mmv$pfli_p^ [pfti].segment_link.fwd;

```

[XDCL] mmm\$initialize_find_next_pfli

```

1C8 1367    WHILEND;
1E8 1368    IFEND;
1EC 1369
1EC 1370    ELSEIF (page_count < pages_in_memory DIV 4) THEN
1F6 1371
1F6 1372 { If the request is for a small percentage of the pages in memory, use the search algorithm that uses
1F6 1373 { the #HASH instruction to locate pages. This algorithm is NOT efficient for searches that look for
1F6 1374 { a large number of pages, because it does a HASH on each page in the range. If the page is found,
1F6 1375 { it is put in the pfti array only if the page satisfies the page selection criteria.
1F6 1376
1F6 1377    WHILE page_count > 0 DO
1FA 1378    sva.offset := offset;
1FA 1379    #HASH_SVA (sva, ipti, hcount, found);
200 1380    page_count := page_count - 1;
200 1381    offset := offset + osv$page_size; {Must be integer to prevent end case at end of segment (2**31)}
200 1382    IF found THEN
21C 1383    pfti := (mmv$pfli_p^ [ipti].rma * 512) DIV osv$page_size;
21C 1384    IF mmv$pfli_p^ [pfti].queue_id IN selection_page_queues THEN
262 1385    mmv$pfli_array_p^.pftis [pfti_index] := pfti;
262 1386    pfti_index := pfti_index + 1;
270 1387    IFEND;
270 1388    pages_in_memory := pages_in_memory - 1;
270 1389    IF pages_in_memory = 0 THEN
276 1390    page_count := 0;
27A 1391    IFEND;
27A 1392    IFEND;
27A 1393    WHILEND;
282 1394
282 1395    ELSE
282 1396
282 1397 { The request is for a majority of the pages in memory. Again, use the search algorithm that uses the
282 1398 { segment/page frame link to find all pages of the segment. We need to check if the page offset is within
282 1399 { the range requested and decide if the page satisfies the selection criteria.
282 1400
282 1401    pfti := aste_p^.pft_link.fwd;
282 1402    WHILE (pfti <> 0) AND (page_count > 0) DO
292 1403    IF (mmv$pfli_p^ [pfti].sva.offset >= low_offset) AND
28C 1404    (mmv$pfli_p^ [pfti].sva.offset <= high_offset) THEN
28C 1405    page_count := page_count - 1;
28C 1406    IF mmv$pfli_p^ [pfti].queue_id IN selection_page_queues THEN
2CE 1407    mmv$pfli_array_p^.pftis [pfti_index] := pfti;
2CE 1408    pfti_index := pfti_index + 1;
2DC 1409    IFEND;
2DC 1410    IFEND;
2DC 1411    pfti := mmv$pfli_p^ [pfti].segment_link.fwd;
2DC 1412    WHILEND;
300 1413
300 1414    IFEND;
300 1415    IFEND; { pages_in_memory > 0 }
300 1416
300 1417    mmv$pfli_array_p^.pfti_first := 0;
300 1418    mmv$pfli_array_p^.pfti_index := 0;
300 1419    mmv$pfli_array_p^.last_pfti_index := pfti_index;
300 1420    mmv$pfli_array_p^.pftis [pfti_index] := 0;
300 1421    xpfti := mmv$pfli_array_p^.pftis [0];
300 1422

```

[XDCL] mmp\$initialize_find_next_pfti

```
300 1423 PROCEND mmp$initialize_find_next_pfti;
    o 1424
    o 1425
    o 1426
```

SOURCE LIST OF mmm\$pfti_manager

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13:33:34

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INLINE Procedures used in other modules

```
1429
1430 PROCEDURE [INLINE] mmp$delete_last_pfti_from_array;

o 1437
o 1438 PROCEDURE [INLINE] mmp$fetch_pfti_array_size
o 1439 (VAR pfti_size: integer);
o 1440

1447
1448 PROCEDURE [INLINE] mmp$find_next_pfti
1449 (VAR xpfti: mmt$page_frame_index);
1450

o 1469
o 1470 PROCEDURE [INLINE] mmp$reset_find_next_pfti
o 1471 (VAR xpfti: mmt$page_frame_index);
o 1472

1496
1497 PROCEDURE [INLINE] mmp$reset_store_pfti;
1498

o 1506
o 1507 PROCEDURE [INLINE] mmp$store_pfti (pfti: mmt$page_frame_index);
o 1508

1516
1517 PROCEDURE [INLINE] mmp$reset_store_pfti_reverse;
1518

o 1526
o 1527 PROCEDURE [INLINE] mmp$store_pfti_reverse (pfti: mmt$page_frame_index);
o 1528
o 1537 MODEND mmm$pfti_manager
```

**** I=\$05578173AS0102D19890821T183254 L=ZXXLIST B=LGO DA=NONE LD=R RC=NONE OPT=SCHED EL=F LF=CS612 PAD=0

**** NO DIAGNOSTICS

IDENTIFIER	DEFINED ON LINE	REFERENCES
aste_p	1282	1301 1326 1329 1329 1332 1334 1344 1401
block_index	180	236/S
block_number	179	236/S
block_p	1189	236
dfc\$command_record_bytes	356	364
dfc\$division_overwrite_words	343	371
dfc\$esm_command_record_size	364	372
dfc\$esm_header_record_size	365	372
dfc\$esm_maintenance_buf_size	344	375
dfc\$esm_memory_base_shift	350	372 373 373
dfc\$header_record_bytes	355	365
dfc\$max_esm_memory_size	345	374
dfc\$max_number_of_mainframes	352	337
dfc\$min_data_record_bytes	360	371
dfc\$min_esm_division_size	370	374
dft\$mainframe_set	337	287
dmt\$system_file_id	386	319 288 446 447
end_point_option	1280	1310
found	1286	1379 1382
fwd	196	1344 1353 1366 1401 1411
gft\$file_descriptor_index	162	152
gft\$system_file_identifier	151	141 386
gft\$table_residence	165	153
hcount	1287	1379
high_offset	1288	1318/M 1404
ijl_ordinal	232	236/S 236/S
ijle_p	233	236/M
include_partial_pages	1280	1310
index_p	1202	236
iot\$io_error	1131	320 1085
ipti	1289	1379 1383/S
jmc\$highest_service_accumulator	834	835
jmc\$ies_job_swapped	614	623
jmc\$ies_swapin_in_progress	613	622
jmc\$iss_idle_tasks_initiated	629	656
jmc\$iss_swapin_io_complete	654	657
jmc\$iss_swapin_requested	650	657
jmc\$iss_swapout_complete	649	656
jmc\$iss_swapped_io_cannot_init	640	667
jmc\$iss_swapped_no_io	631	666
jmc\$kj1_maximum_entries	407	400 401 786
jmc\$ko1_maximum_entries	417	402
jmc\$max_ajl_ord	389	392
jmc\$max_dispatching_control	573	577
jmc\$max_dispatching_priority	495	455 458 459

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES
jmc\$max_ijl_index_count	188	1200
jmc\$maximum_job_classes	764	767
jmc\$maximum_job_count	414	407
jmc\$maximum_output_count	424	417
jmc\$maximum_service_classes	867	870
jmc\$min_dispatching_control	572	576
jmc\$null_service_class	860	861
jmc\$priority_p1	509	456
jmc\$priority_p10	518	457
jmc\$priority_p14	522	457
jmc\$priority_p8	516	456
jmc\$reserved_ajls	403	398
jmc\$service_accumulator_maximum	826	823
jmc\$system_default_offset	850	851
jmc\$system_supplied_name_size	995	992
jmc\$unlimited_offset	847	836
jmp\$set_ijle_p	232	238
jmt\$ajl_ordinal	392	256
jmt\$delayed_swapin_work	439	286 443
jmt\$dispatching_control_index	576	533 543
jmt\$dispatching_controls	546	544
jmt\$dispatching_priority	455	268 534 535 536 548
jmt\$ijl_block_index	184	180 1202
jmt\$ijl_block_number	183	179 1190 1191
jmt\$ijl_dispatching_control	532	269
jmt\$ijl_entry_status	609	255
jmt\$ijl_ordinal	178	134 232 275 303 1010 1011 1077
jmt\$ijl_p	1188	1183
jmt\$ijl_page_fault_count	683	678 679 680
jmt\$ijl_page_stats	677	673
jmt\$ijl_service_class_stats	671	290
jmt\$ijl_statistics	716	289
jmt\$ijl_swap_count	692	688 689
jmt\$ijl_swap_counts	687	309 674
jmt\$ijl_swap_status	627	258 259 260
jmt\$initiated_job_list_block	1189	1205
jmt\$initiated_job_list_entry	252	233 1035 1202
jmt\$initiated_job_list_p	1205	1189
jmt\$input_file_location	806	801
jmt\$job_abort_disposition	815	799
jmt\$job_class	767	314
jmt\$job_mode	770	271
jmt\$job_priority	775	311 312
jmt\$job_recovery_disposition	818	800
jmt\$kj1_index	786	257
jmt\$queue_file_ijl_information	798	296
jmt\$scheduling_data	302	280
jmt\$service_accumulator	823	304 305 306
jmt\$service_class_index	870	315
jmt\$swap_data	318	282
jmt\$swapout_reasons	873	310
jmt\$swapped_job_entry	888	327 1036
jmt\$system_supplied_name	992	253

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

IDENTIFIER	DEFINED ON LINE	REFERENCES							
jmt\$task_time_slice	586	566	567						
jmt\$time_slice_values	585	550							
jmv\$ijl_p	1183	236							
jsc\$isiq_swapped_io_completed	1015	1017							
jsc\$isiq_swapped_io_not_init	1014	1017							
jst\$changed_asid_entry	1058	1049							
jst\$ijl_swap_queue_id	1014	1009							
jst\$ijl_swap_queue_link	1008	264							
jst\$io_control_information	1022	283							
jst\$swap_file_descriptor	1034	284							
jst\$swapped_page_descriptor	1043	1041							
jst\$swapped_page_descriptors	1040	1037							
last_pfti_index	17	1419/M	1442	1456	1457	1461	1481	1482	1487
length	1279	1500/M	1510/S	1511/M	1511	1520/M			
low_offset	1290	1307	1311	1313	1343				
		1317/M	1318	1343	1403				
mmc\$pg_avail	60	106	1324	1330					
mmc\$pg_avail_modified	61	1330							
mmc\$pg_free	59	118	1330						
mmc\$pg_job_fixed	100	107	119	1331	1334	1335			
mmc\$pg_job_io_error	101	1327	1331						
mmc\$pg_job_working_set	102	119	120	1326	1328	1331			
mmc\$pg_shared_first	108	1329							
mmc\$pg_shared_first_site	110	114							
mmc\$pg_shared_io_error	97	1327							
mmc\$pg_shared_last	115	1329							
mmc\$pg_shared_num_sites	111	114							
mmc\$pg_shared_other	69	109							
mmc\$pg_shared_site_01	71	110							
mmc\$pg_shared_site_25	95	115							
mmc\$pg_shared_task_service	64	108							
mmc\$pg_swapped_io_error	98	118							
mmc\$pg_wired	62	105	1331	1332	1333				
mmp\$delete_last_pfti_from_array	1430	1434							
mmp\$fetch_pfti_array_size	1438	1444							
mmp\$find_next_pfti	1448	1446							
mmp\$initialize_find_next_pfti	1277	1423							
mmp\$reset_find_next_pfti	1470	1493							
mnt\$active_segment_table_entry	131	147	1046	1084	1282				
mnt\$ast_index	1087	326	1061						
mnt\$global_page_queue_index	118	1169							
mnt\$global_page_queue_list_ent	1159	1169							
mnt\$job_page_queue_index	119	890	1170						
mnt\$job_page_queue_list	1170	281							
mnt\$link	194	132	1074	1075	1156				
mnt\$locked_page	1096	1080							
mnt\$memory_reserve_request	1137	274							
mnt\$page_age	1103	1083	1107	1107					
mnt\$page_frame_index	21	18	196	196	1023	1025	1026	1027	1139
		1140	1283	1294	1449	1453	1471	1475	1507
		1527							

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

IDENTIFIER	DEFINED ON LINE	REFERENCES							
mnt\$page_frame_queue_id	120	48	140	1024	1076				
mnt\$page_frame_table	1089	1214							
mnt\$page_frame_table_entry	1073	1044	1089						
mnt\$page_queue_list_entry	1155	1160	1170						
mnt\$page_queue_set	48	1286	1320	1322	1324	1327	1330	1333	1335
mnt\$page_selection_criteria	125	1281							
mnt\$pfti_array	14	1252							
mmv\$pfti_p	1214	1353	1362	1366	1384	1403	1404	1406	1411
mmv\$pfti_array_p	1252	1299/M	1351/M	1363/M	1385/M	1407/M	1417/M	1418/M	1419/M
		1420/M	1421	1432/S	1432/M	1442	1442	1456	1456
		1457	1457	1458/M	1458	1459	1459/S	1461	1461
		1478/M	1478	1479	1479/S	1481	1481	1482	1482
		1483/M	1483	1484	1484/S	1487	1487	1500/M	1501/M
		1502/M	1510/S	1510/M	1511/M	1511	1520/M	1520	1521/M
		1521	1522/M	1522	1530/M	1530	1531/M	1531	1532/S
		1532/M							
mmv\$pft_p	1228	1383							
mnt\$error_stop	1177	1337							
offset	907	1309	1378/M	1403	1404				
offset	1291	1309/M	1311	1311	1313	1314/M	1314	1315	1317
		1317	1378	1381/M	1381				
osc\$free_running_clock_maximum	206	203							
osc\$invalid_ring	924	964							
osc\$max_page_frames	25	15	16	17	21	133	321	322	889
		891	1157	1163					
osc\$max_page_size	1247	1243							
osc\$max_page_table_entries	26	28							
osc\$max_ring	923	964	965						
osc\$max_segment_length	947	970							
osc\$max_tasks	1129	1126							
osc\$maximum_offset	946	947	967	967	968				
osc\$maximum_segment	945	966							
osc\$min_page_size	1246	1243							
osc\$min_ring	922	965							
osc\$task_time_slice_maximum	597	600							
ost\$asid	910	138	265	906	1048	1059	1060		
ost\$cp_time	704	672	717						
ost\$cp_time_value	702	307	705	706					
ost\$free_running_clock	203	137	276	277	278	279	313	323	324
		325	537	549					
ost\$global_task_id	1120	270	299						
ost\$key_lock_value	959	956							
ost\$page_id	31	41							
ost\$page_size	1243	1235							
ost\$page_table	45	1228							
ost\$page_table_entry	36	45	1045						
ost\$page_table_index	29	45	1081						
ost\$paging_statistics	740	718							
ost\$ring	964	976							
ost\$segment	966	977							
ost\$segment_length	970	1279							
ost\$segment_offset	967	907	878						

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES							
ost\$system_virtual_address	905	1086	1278	1297					
ost\$task_index	1126	1114	1115	1121					
ost\$task_time_slice	600	586							
osv\$page_size	1235	1311	1311	1313	1314	1314	1314	1315	1317
		1318	1381	1383					
page_count	1292	1311/M	1313/M	1315/M	1315	1318	1370	1377	1377
		1380/M	1380	1390/M	1402	1402	1405/M	1405	
page_selection_criteria	1281	1321	1323	1345					
pages_in_memory	133	1301							
pages_in_memory	1293	1301/M	1307	1370	1388/M	1388	1388		
pft_link	132	1344	1401						
pfti	1294	1344/M	1350	1350	1351	1353/M	1353/S	1361	1361
		1362/S	1363	1366/M	1366/S	1383/M	1384/S	1385	1401/M
		1402	1402	1403/S	1404/S	1406/S	1407	1411/M	1411/S
pfti	1453	1455/M	1456	1456	1459/M	1462/M	1464		
pfti	1475	1479/M	1481	1481	1484/M	1488/M	1490		
pfti	1507	1510							
pfti	1527	1532							
pfti_first	15	1417/M	1442	1478	1479/S	1502/M	1522/M	1530	1531/M
		1531	1532/S						
pfti_index	16	1418/M	1432/S	1456	1457	1458/M	1458	1459/S	1461
		1478/M	1481	1482	1483/M	1483	1484/S	1487	1501/M
		1521/M	1530/M						
pfti_index	1295	1300/M	1351/S	1352/M	1352	1363/S	1364/M	1364	1385/S
		1386/M	1386	1407/S	1408/M	1408	1419	1420/S	
pfti_size	1439	1442/M							
pftis	18	1299/M	1351/M	1363/M	1385/M	1407/M	1420/M	1421	1432/M
		1459	1479	1484	1510/M	1520	1521	1522	1532/M
pvc_all	125	1321	1345						
pvc_all_except_avail	125	1323							
queue_id	140	1326	1329	1329	1332	1334			
queue_id	1078	1362	1384	1406					
rma	42	1383							
segment_link	1075	1353	1366	1411					
selection_page_queues	1296	1320/M	1322/M	1324/M	1327/M	1330/M	1333/M	1335/M	1362
		1384	1406						
sft\$counter	750	719	720						
sva	1086	1403	1404						
sva	1297	1308/M	1309	1378/M	1379				
tmt\$task_queue_link	1113	1082							
xpfti	1283	1421/M							
xpfti	1449	1464/M							
xpfti	1471	1490/M							
xsva	1278	1308							

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

MMM\$SEGMENT_MANAGER_JOB_TEMP

```

3 MODULE mmm$segment_manager_job_temp;
4
O 4524
O 4525 VAR
O 4526   gfv$null_sfid: [XREF, READ, OSS$MAINFRAME_WIRED_LITERAL] gft$system_file_identifier;
O 4527
O 4528
O 4529 SECTION
O 4530   oss$mainframe_wired_literal: READ;
O 4531
O 4532 VAR
O 4533   mmv$file_allocation_interval: [XREF] integer;
O 4534
O 4535 VAR
O 4536   mmv$shadow_by_segnum: [XREF] boolean;
O 4537 {system page size.}
O 4538
O 4539 VAR
O 4540   osv$page_size: [XREF] ost$page_size;
O 4541
O 4544
O 4545 VAR
O 4546   osv$task_private_heap: [XREF, READ, oss$job_pageable] ^ost$heap;
O 4547
O 4553
O 4554 { Define variables that are global to this module.
O 4555
O 4556 VAR
O 4557   osv$system_privilege_map: [XDCL, #GATE, oss$task_private] ost$system_privilege_map ::
O 4558   [REP (mmc$default_sdt_length + 1) of TRUE];
O 4559

```

DETERMINE_VALIDATING_RING_NUM

```

0 4562
0 4563 PROCEDURE [INLINE] determine_validating_ring_num
0 4564 ( caller_ring: ost$ring;
0 4565 validation_ring_number: ost$valid_ring;
0 4566 VAR validating_ring_num: ost$valid_ring);
0 4567
0 4568 IF validation_ring_number < caller_ring THEN
0 4569 validating_ring_num := caller_ring;
0 4570 ELSE
0 4571 validating_ring_num := validation_ring_number;
0 4572 IFEND;
0 4573
0 4574 PROCEND determine_validating_ring_num;
0 4575

```

UPDATE_PASSIVE_WITH_ACTIVE

```

0 4577
0 4578 PROCEDURE update_passive_with_active
4 4579 ( segment_p: ^cell;
4 4580 VAR status: ost$status);
4 4581
4 4582 TYPE
4 4583 offset_list = array [1 .. *] of ost$segment_offset;
4 4584
4 4585 VAR
4 4586 access_selections: mmt$access_selections,
4 4587 address_list: array [1 .. 100] of dmt$addr_length_pair,
4 4588 addr_returned: integer,
4 4589 dest_p: mmt$segment_pointer,
4 4590 dest: ^cell,
4 4591 dm_element_length: ost$segment_length,
4 4592 dm_element_offset: ost$segment_offset,
4 4593 fd_p: gft$file_desc_entry_p,
4 4594 file_limits_to_enforce: sft$file_space_limit_kind,
4 4595 i: integer,
4 4596 in_memory: boolean,
4 4597 list_overflow: boolean,
4 4598 list_p: ^offset_list,
4 4599 local_status: ost$status,
4 4600 memory_list_index: integer,
4 4601 offset_returned: integer,
4 4602 sdt_p: ^mmt$segment_descriptor,
4 4603 sdtx_p: ^mmt$segment_descriptor_extended,
4 4604 segnum: ost$segment,
4 4605 source: ^cell,
4 4606 starting_addr: ost$segment_offset;
4 4607
4 4608 { Get access selection before change to restore at end of proc.
4 4609
4 4610 mmp$validate_segment_number (#SEGMENT (segment_p), sdt_p, sdtx_p, status);
2E 4611 IF NOT status.normal THEN
36 4612 RETURN;
38 4613 IFEND;
38 4614
38 4615 IF sdtx_p^.software_attribute_set = $mmt$software_attribute_set
46 4616 [mmc$sa_read_transfer_unit, mmc$sa_free_behind] THEN
46 4617 access_selections := mmc$as_sequential;
4C 4618 ELSEIF sdtx_p^.software_attribute_set = $mmt$software_attribute_set [mmc$sa_read_transfer_unit] THEN
52 4619 access_selections := mmc$as_read_tu;
58 4620 ELSE
58 4621 access_selections := mmc$as_random;
5A 4622 IFEND;
5A 4623
5A 4624 { Set read_transfer_unit attribute in ACTIVE segment.
5A 4625
5A 4626 mmp$set_access_selections (segment_p, mmc$as_sequential, status);
74 4627
74 4628 { Obtain access to PASSIVE segment.
74 4629
74 4630 mmp$open_file_segment (sdtx_p^.shadow_info.shadow_sfid, NIL, mmc$cell_pointer, 1, sfc$no_limit, dest_p,
B6 4631 status);
B6 4632 IF NOT status.normal THEN

```


UPDATE_PASSIVE_WITH_ACTIVE

```

BE 4633 RETURN;
CO 4634 IFEND;
CO 4635
CO 4636 offsets_returned := 100;
CO 4637 REPEAT
C8 4638 ALLOCATE list_p: [1 .. offsets_returned] IN osv$task_private_heap^;
10A 4639 mmp$fetch_offset_modified_pages (segment_p, FALSE {return_unallocated_offsets}, list_p^,
13C 4640 offsets_returned, status);
13C 4641 IF NOT status.normal THEN
144 4642 FREE list_p IN osv$task_private_heap^;
16A 4643 RETURN;
172 4644 IFEND;
172 4645 IF offsets_returned > UPPERBOUND (list_p^) THEN
186 4646 FREE list_p IN osv$task_private_heap^;
1AE 4647 IFEND;
1AE 4648 UNTIL offsets_returned <= UPPERBOUND (list_p^);
1BE 4649
1BE 4650 IF offsets_returned < 0 THEN
1C8 4651 { Convert offsets to PVAs and move all modified pages from the ACTIVE segment
1C6 4652 FOR i := 1 TO offsets_returned DO
1CE 4653 source := #ADDRESS [1, #SEGMENT (segment_p), list_p^ [i]];
1CE 4654 dest := #ADDRESS [1, #SEGMENT (dest_p.cell_pointer), list_p^ [i]];
1CE 4655 i#move (source, dest, osv$page_size);
248 4656 mmp$advise_out (dest, osv$page_size, status);
266 4657 FOREND;
270 4658 IFEND;
270 4659
270 4660
270 4661 { Determine whether the segment is assigned to a device.
270 4662 { Get and move all initialized addresses from ACTIVE file.
270 4663 { If the segment is not assigned, no further action is then required.
270 4664
270 4665
270 4666 starting_addr := 0;
270 4667 gfp$get_fde_p (sdtx_p^.sfid, fde_p);
2CA 4668 list_overflow := {fde_p^.media = gfc$fm_mass_storage_file};
2CA 4669
2CA 4670 /get_list_of_addresses/
2CA 4671 WHILE list_overflow DO
2EA 4672 dmp$get_initialized_addresses (sdtx_p^.sfid, starting_addr, address_list, addr_returned, list_overflow,
32C 4673 status);
32C 4674 IF NOT status.normal THEN
334 4675 RETURN;
336 4676 IFEND;
336 4677
336 4678 IF addr_returned < 0 THEN
33E 4679 FOR i := 1 TO addr_returned DO
344 4680
344 4681 { Look for offsets which may have already been written. Specifically, we are looking for pages which are on
344 4682 { disk and are not modified in real memory at the same time.
344 4683
344 4684 dm_element_length := address_list [i].length;
344 4685 dm_element_offset := address_list [i].addr;
344 4686 memory_list_index := 1;
344 4687 WHILE (dm_element_length > 0) AND (memory_list_index > 0) DO
364 4688 memory_list_index := offsets_returned;

```

UPDATE_PASSIVE_WITH_ACTIVE

```

364 4689
364 4690 { Search the list (of offsets of all modified pages for the ACTIVE segment) for the equivalent offset returned
364 4691 { from Device Management. If ANY of the pages in <addr + length> are not found, move the disk pages, starting
364 4692 { at <address_list [i].addr> and continuing through <address_list [i].addr + address_list [i].length.>. In
364 4693 { this case we MAY overwrite some addresses which were processed in the previous update above, but this is OK.
364 4694
364 4695 WHILE (memory_list_index > 0) AND (list_p^ [memory_list_index] <> dm_element_offset) DO
37E 4696 memory_list_index := memory_list_index - 1;
37E 4697 WHILEND;
396 4698 dm_element_offset := dm_element_offset + osv$page_size;
396 4699 dm_element_length := dm_element_length - osv$page_size;
396 4700 WHILEND;
3A6 4701
3A6 4702 IF memory_list_index = 0 THEN
3AA 4703
3AA 4704 { The address offset returned from DM was not written (either partially or completely) in the previous
3AA 4705 { process; write it now.
3AA 4706
3AA 4707 source := #ADDRESS [1, #SEGMENT (segment_p), address_list [i].addr];
3AA 4708 dest := #ADDRESS [1, #SEGMENT (dest_p.cell_pointer), address_list [i].addr];
3AA 4709 i#move (source, dest, address_list [i].length);
41E 4710 mmp$advise_out (source, address_list [i].length, status);
446 4711 mmp$advise_out (dest, address_list [i].length, status);
464 4712 IFEND;
464 4713 FOREND;
468 4714 starting_addr := address_list [addr_returned].addr + address_list [addr_returned].length;
484 4715 IFEND;
484 4716 WHILEND /get_list_of_addresses/;
490 4717
490 4718 mmp$set_access_selections (segment_p, access_selections, status);
4A8 4719 mmp$close_segment (dest_p, 1, local_status);
4C8 4720 FREE list_p IN osv$task_private_heap^;
4F2 4721
4F2 4722 PROCEND update_passive_with_active;

```

MMP\$CHANGE_SEGMENT_INHERITANCE

```

O 4725 {
O 4726 { The purpose of this request is to change the segment inheritance of the
O 4727 { specified segment. Only segments of inheritance mmc$si_none can be modified
O 4728 { with this request.
O 4729 {
O 4730 { MMP$CHANGE_SEGMENT_INHERITANCE (segment_pointer, segment_inheritance, status)
O 4731 {
O 4732 { SEGMENT_POINTER: (input) This parameter specifies the process_virtual_address of
O 4733 { the segment which is to be changed.
O 4734 {
O 4735 { SEGMENT_INHERITANCE: (input) This parameter specifies the new segment inheritance.
O 4736 { The only valid inheritances for this request are mmc$si_share_segment and
O 4737 { mmc$si_transfer_segment.
O 4738 {
O 4739 { STATUS: (output) This parameter specifies the request status.
O 4740 {
O 4741 {
O 4742 { PROCEDURE [XDCL] mmp$change_segment_inheritance
O 4743 { ( pva: ^cell;
O 4744 { segment_inheritance: mmt$segment_inheritance;
O 4745 { VAR status: ost$status);
O 4746 {
O 4747 { VAR
O 4748 { caller_id: ost$caller_identifier,
O 4749 { r1_status: ost$status,
O 4750 { validating_ring_number: ost$valid_ring;
O 4751 {
O 4752 { status.normal := TRUE;
O 4753 { #CALLER_ID (caller_id);
O 4754 { determine_validating_ring_num (caller_id.ring, #RING (pva), validating_ring_number);
O 4755 { mmp$change_seg_inheritance_r1 (#SEGMENT (pva), validating_ring_number, segment_inheritance, r1_status);
O 4756 { IF NOT r1_status.normal THEN
O 4757 { osp$set_status_condition (r1_status.condition, status);
O 4758 { IFEND;
O 4759 {
O 4760 { PROCEND mmp$change_segment_inheritance;
O 4761 {

```

MMP\$CHANGE_SEGMENT_NUMBER

```

O 4764 {
O 4765 { The purpose of this request is to associate a previously opened
O 4766 { file with a different segment number. The old segment is deleted
O 4767 { when the new segment number has been assigned.
O 4768 {
O 4769 { MMP$CHANGE_SEGMENT_NUMBER (segment_pointer, segment_number,
O 4770 { validation_ring_number, new_segment_pointer, status);
O 4771 {
O 4772 { Segment_pointer: (INPUT) This parameter specifies the process virtual
O 4773 { address of the segment.
O 4774 {
O 4775 { Segment_number: (INPUT) This parameter is the new segment number
O 4776 { to be associated with the file.
O 4777 {
O 4778 { Validation_ring_number: (INPUT) This parameter specifies the ring
O 4779 { of execution for whom the segment is being modified. The ring
O 4780 { number used for validation is the maximum of the caller ring
O 4781 { number and the 'validation_ring_number'.
O 4782 {
O 4783 { New_segment_pointer: (OUTPUT) This parameter specifies the process
O 4784 { virtual address of the file with the new segment number.
O 4785 {
O 4786 { Status: (OUTPUT) This parameter specifies the request status.
O 4787 { CONDITIONS:
O 4788 { mme$ring_violation
O 4789 { mme$unsupported_segment_kind
O 4790 { IDENTIFIER: 'MM'
O 4791 {
O 4792 {
O 4793 {
O 4794 { PROCEDURE [XDCL] mmp$change_segment_number
O 4795 { ( segment_pointer: amt$segment_pointer;
O 4796 { segment_number: ost$segment;
O 4797 { validation_ring_number: ost$valid_ring;
O 4798 { VAR new_segment_pointer: amt$segment_pointer;
O 4799 { VAR status: ost$status);
O 4800 {
O 4801 { VAR
O 4802 { caller_id: ost$caller_identifier,
O 4803 { r1_segment_pointer: amt$segment_pointer,
O 4804 { r1_status: ost$status,
O 4805 { validating_ring_number: ost$valid_ring;
O 4806 {
O 4807 { status.normal := TRUE;
O 4808 { #CALLER_ID (caller_id);
O 4809 { determine_validating_ring_num (caller_id.ring, validation_ring_number, validating_ring_number);
O 4810 { mmp$change_segment_number_r1 (#SEGMENT (segment_pointer.cell_pointer), segment_number,
O 4811 { validating_ring_number, r1_status);
O 4812 { IF r1_status.normal THEN
O 4813 { new_segment_pointer := segment_pointer;
O 4814 { new_segment_pointer.cell_pointer := #ADDRESS (#RING (segment_pointer.cell_pointer),
O 4815 { segment_number, #OFFSET (segment_pointer.cell_pointer));
O 4816 { ELSE
O 4817 { osp$set_status_condition (r1_status.condition, status);
O 4818 { IFEND;
O 4819 {

```

MMP\$CHANGE_SEGMENT_NUMBER

```
BO 4820 PROCEND mmp$change_segment_number;
O 4821
```

MMP\$CHANGE_STACK_ATTRIBUTE

```
O 4823
O 4824 { The purpose of this request is to modify the stack attribute
O 4825 { which determines whether or not the pages beyond the current
O 4826 { top-of-stack pointer are freed. Only the stack of the caller ring
O 4827 { can be modified by this request.
O 4828 {
O 4829 { MMP$CHANGE_STACK_ATTRIBUTE (stack_pages_to_be_freed, status);
O 4830 {
O 4831 { STACK_PAGES_TO_BE_FREED: (INPUT, BOOLEAN) This parameter specifies
O 4832 { whether or not the pages beyond the current top-of-stack are freed.
O 4833 {
O 4834 { STATUS: (OUTPUT, OST$STATUS) The request status is returned in this
O 4835 { parameter.
O 4836 {
O 4837
O 4838 PROCEDURE [XDCL, #GATE] mmp$change_stack_attribute
O 4839 { stack_pages_to_be_freed: boolean;
O 4840 VAR status: ost$status;
O 4841
O 4842 VAR
O 4843 caller_id: ost$caller_identifier,
O 4844 r1_status: ost$status;
O 4845
O 4846 status.normal := TRUE;
O 4847 #CALLER_ID (caller_id);
O 4848
O 4849 mmp$change_stack_attribute_r1 (stack_pages_to_be_freed, caller_id.ring, r1_status);
C 4850 IF NOT r1_status.normal THEN
4A 4851 osp$set_status_condition (r1_status.condition, status);
5C 4852 IFEND;
5C 4853
5C 4854 PROCEND mmp$change_stack_attribute;
O 4855
```

MMP\$CLOSE_SEGMENT

```

O 4857 {
O 4858 { The purpose of this request is to close/delete a segment. If the
O 4859 { caller is not within the read bracket of the segment being closed the
O 4860 { call is rejected.
O 4861 {
O 4862 { MMP$CLOSE_SEGMENT (POINTER, VALIDATION_RING_NUMBER, STATUS)
O 4863 {
O 4864 { POINTER: (input/output) This parameter specifies the segment
O 4865 { to be deleted. This pointer will be set to NIL when the
O 4866 { request completes.
O 4867 {
O 4868 { VALIDATION_RING_NUMBER: (input) This parameter specifies the ring of
O 4869 { execution for whom the segment is being closed. The ring
O 4870 { number used for validation is the max of the caller ring number
O 4871 { and 'validation_ring_number'.
O 4872 {
O 4873 { STATUS: (output) This parameter specifies the request status.
O 4874 { The possible error codes are:
O 4875 { dme$file_descriptor_not_deleted
O 4876 { mme$invalid_close_segment_req
O 4877 { mme$invalid_shared_taskid
O 4878 { mme$segment_number_not_in_use
O 4879 { mme$segment_number_too_big
O 4880 {
O 4881 {
O 4882 { PROCEDURE [XDCL, #GATE] mmp$close_segment
O 4883 { (VAR pointer: mmt$segment_pointer;
O 4884 { validation_ring_number: ost$valid_ring;
O 4885 { VAR status: ost$status);
O 4886 {
O 4887 { VAR
O 4888 { caller_id: ost$caller_identifier,
O 4889 { r1_status: ost$status,
O 4890 { validating_ring_number: ost$valid_ring;
O 4891 {
O 4892 { #CALLER_ID (caller_id);
O 4893 { #KEYPOINT (osk$entry, osk$m * #SEGMENT (pointer.cell_pointer), mmk$close);
O 4894 { status.normal := TRUE;
O 4895 { determine_validating_ring_num (caller_id.ring, validation_ring_number, validating_ring_number);
O 4896 { mmp$invalidate_segment [#SEGMENT (pointer.cell_pointer), validating_ring_number,
O 4897 { NIL (shared_taskid_array), r1_status);
O 4898 { IF NOT r1_status.normal THEN
O 4899 { osp$set_status_condition (r1_status.condition, status);
O 4900 { ELSE
O 4901 { pointer.cell_pointer := NIL;
O 4902 { IFEND;
O 4903 { #KEYPOINT (osk$exit, 0, mmk$close);
O 4904 {
O 4905 { PROCEND mmp$close_segment;

```

MMP\$CLOSE_SHARED_STACK

```

O 4907 {
O 4908 { The purpose of this request is to close/delete a stack segment
O 4909 { being used by ADA tasks.
O 4910 {
O 4911 { MMP$CLOSE_SHARED_STACK (POINTER, SHARED_TASKID_ARRAY, STATUS)
O 4912 {
O 4913 { POINTER: (INPUT/OUTPUT) This parameter specifies the segment to
O 4914 { be deleted. This pointer will be set to
O 4915 { NIL when the request completes.
O 4916 {
O 4917 { SHARED_TASKID_ARRAY: (INPUT) This parameter specifies the array
O 4918 { of taskids for the tasks in which
O 4919 { this segment is being closed/deleted.
O 4920 {
O 4921 { STATUS: (OUTPUT) This parameter specifies the request status.
O 4922 {
O 4923 {
O 4924 { PROCEDURE [XDCL] mmp$close_shared_stack
O 4925 { (VAR pointer: mmt$segment_pointer;
O 4926 { shared_taskid_array: ^array [1 .. *] of pmt$task_id;
O 4927 { VAR status: ost$status);
O 4928 {
O 4929 { VAR
O 4930 { caller_id: ost$caller_identifier,
O 4931 { r1_status: ost$status;
O 4932 {
O 4933 { #CALLER_ID (caller_id);
O 4934 { status.normal := TRUE;
O 4935 { mmp$invalidate_segment [#SEGMENT (pointer.cell_pointer), caller_id.ring, shared_taskid_array, r1_status);
O 4936 { IF NOT r1_status.normal THEN
O 4937 { osp$set_status_condition (r1_status.condition, status);
O 4938 { ELSE
O 4939 { pointer.cell_pointer := NIL;
O 4940 { IFEND;
O 4941 {
O 4942 { PROCEND mmp$close_shared_stack;

```

MMP\$CREATE_SCRATCH_SEGMENT

```

O 4944 {
O 4945 { The purpose of this request is to create a scratch segment.
O 4946 { Scratch segments are temporary segments that have no name and
O 4947 { exist only until deleted via the mmp$delete_scratch_segment request,
O 4948 { or until the creating task terminates.
O 4949 {
O 4950 { MMP$CREATE_SCRATCH_SEGMENT (POINTER_KIND, ACCESS_SELECTIONS,
O 4951 { POINTER, STATUS)
O 4952 {
O 4953 { POINTER_KIND: (input) This parameter specifies the type of pointer
O 4954 { to be constructed for the segment.
O 4955 {
O 4956 { ACCESS_SELECTIONS: (input) This parameter specifies the mode of
O 4957 { access to the segment (sequential or random).
O 4958 {
O 4959 { POINTER: (output) This parameter specifies the process virtual
O 4960 { address assigned to the segment. The byte offset in the PVA
O 4961 { is set to zero.
O 4962 {
O 4963 { STATUS: (output) This parameter specifies the request status.
O 4964 { The possible error codes are:
O 4965 { dme$unable_to_locate_fde
O 4966 { dme$unable_to_get_fd_lock
O 4967 { mme$address_not_0_mod_16384
O 4968 { mme$asid_specified
O 4969 { mme$binding_attribute_invalid
O 4970 { mme$contig_mem_seg_violation
O 4971 { mme$execute_global_invalid
O 4972 { mme$invalid_asid_specified
O 4973 { mme$invalid_length_requested
O 4974 { mme$invalid_pva
O 4975 { mme$invalid_ring_brackets
O 4976 { mme$invalid_shadow_segment
O 4977 { mme$invalid_shared_taskid
O 4978 { mme$length_not_0_mod_16384
O 4979 { mme$pages_already_assigned
O 4980 { mme$ref_to_unrecovered_file
O 4981 { mme$ring_violation
O 4982 { mme$segment_number_is_in_use
O 4983 { mme$segment_number_not_in_use
O 4984 { mme$segment_number_too_big
O 4985 { mme$segment_origin_change
O 4986 { mme$segment_origin_invalid
O 4987 { mme$segment_table_is_full
O 4988 { mme$software_attribute_invalid
O 4989 { mme$unable_to_assign_contig_mem
O 4990 { mme$unsupported_keyword
O 4991 {
O 4992 {
O 4993 {
O 4994 { PROCEDURE [XDCL, #GATE] mmp$create_scratch_segment
O 4995 { ( pointer_kind: amt$pointer_kind;
O 4996 { access_selections: mmt$access_selections;
O 4997 { VAR pointer: amt$segment_pointer;
O 4998 { VAR status: ost$status);
O 4999 {

```

MMP\$CREATE_SCRATCH_SEGMENT

```

O 5000 VAR
O 5001 caller_id: ost$caller_identifier,
O 5002 kind: mmt$segment_pointer_kind,
O 5003 p: mmt$segment_pointer,
O 5004 r1_status: ost$status,
O 5005 segment_attributes: mmt$segment_attr_descriptor;
O 5006
O 5007 #CALLER_ID (caller_id);
C 5008 #KEYPOINT (osk$entry, 0, mmk$create_scratch_segment);
18 5009 status.normal := TRUE;
18 5010 IF pointer_kind = amc$cell_pointer THEN
2A 5011 kind := mmc$cell_pointer;
30 5012 ELSEIF pointer_kind = amc$sequence_pointer THEN
3E 5013 kind := mmc$sequence_pointer;
3E 5014 ELSE
3E 5015 kind := mmc$heap_pointer;
42 5016 IFEND;
42 5017 segment_attributes.validating_ring_number := caller_id.ring;
42 5018 segment_attributes.file_limits_to_enforce := sfc$temp_file_space_limit;
42 5019 segment_attributes.pointer_kind := kind;
42 5020 segment_attributes.user_attributes := NIL;
42 5021 segment_attributes.sfid := gfv$null_sfid;
42 5022 mmp$build_segment (segment_attributes, NIL (shared_taskid_array), p, r1_status);
98 5023
98 5024 IF r1_status.normal THEN
A0 5025 IF access_selections = mmc$as_sequential THEN
A8 5026 mmp$set_access_selections (p.cell_pointer, mmc$as_sequential, status);
C4 5027 IFEND;
C4 5028 pointer.kind := pointer_kind;
C4 5029 IF pointer_kind = amc$cell_pointer THEN
D4 5030 pointer.cell_pointer := p.cell_pointer;
E0 5031 ELSEIF pointer_kind = amc$sequence_pointer THEN
E4 5032 pointer.sequence_pointer := p.seq_pointer;
F2 5033 ELSE
F2 5034 pointer.heap_pointer := p.heap_pointer;
FC 5035 IFEND;
100 5036 ELSE
100 5037 osp$set_status_condition (r1_status.condition, status);
112 5038 IFEND;
112 5039 #KEYPOINT (osk$exit, 0, mmk$create_scratch_segment);
116 5040
116 5041 PROCEND mmp$create_scratch_segment;
O 5042

```

MMP\$CREATE_SEGMENT

```

O 5044 [
O 5045 [   The purpose of this request is to create a transient segment.
O 5046 [
O 5047 [   MMP$CREATE_SEGMENT (SEG_ATTRIBUTES_P, POINTER_KIND,
O 5048 [     VALIDATION_RING_NUMBER, POINTER, STATUS)
O 5049 [
O 5050 [   SEG_ATTRIBUTES_P: (input) This parameter is a pointer to the array of segment
O 5051 [     attributes to be assigned to the segment.
O 5052 [
O 5053 [   POINTER_KIND: (input) This parameter specifies the type of pointer
O 5054 [     to be constructed for the segment.
O 5055 [
O 5056 [   VALIDATION_RING_NUMBER: (input) This parameter specifies the ring of
O 5057 [     execution for whom the segment is being created. The ring
O 5058 [     number used for validation is the max of caller ring number
O 5059 [     and 'validation_ring_number'.
O 5060 [
O 5061 [   POINTER: (output) This parameter specifies the process virtual
O 5062 [     address assigned to the segment. The byte offset in the PVA
O 5063 [     is set to zero
O 5064 [
O 5065 [   STATUS: (output) This parameter specifies the request status.
O 5066 [     The possible error codes are:
O 5067 [       dme$unable_to_locate_fde
O 5068 [       dme$unable_to_get_fd_lock
O 5069 [       mme$address_not_0_mod_16384
O 5070 [       mme$asid_specified
O 5071 [       mme$binding_attribute_invalid
O 5072 [       mme$contig_mem_seg_violation
O 5073 [       mme$execute_global_invalid
O 5074 [       mme$invalid_asid_specified
O 5075 [       mme$invalid_length_requested
O 5076 [       mme$invalid_pva
O 5077 [       mme$invalid_ring_brackets
O 5078 [       mme$invalid_shadow_segment
O 5079 [       mme$invalid_shared_taskid
O 5080 [       mme$length_not_0_mod_16384
O 5081 [       mme$pages_already_assigned
O 5082 [       mme$ref_to_unrecovered_file
O 5083 [       mme$ring_violation
O 5084 [       mme$segment_number_is_in_use
O 5085 [       mme$segment_number_not_in_use
O 5086 [       mme$segment_number_too_big
O 5087 [       mme$segment_origin_invalid
O 5088 [       mme$segment_table_is_full
O 5089 [       mme$software_attribute_invalid
O 5090 [       mme$unable_to_assign_contig_mem
O 5091 [       mme$unsupported_keyword
O 5092 [
O 5093 [
O 5094 [
O 5095 [   PROCEDURE [XDCL, #GATE] mmp$create_segment
O 5096 [     (   seg_attributes_p: ^array [ * ] of mmt$attribute_descriptor;
O 5097 [       pointer_kind: mmt$segment_pointer_kind;
O 5098 [       validation_ring_number: ost$valid_ring;
O 5099 [       VAR pointer: mmt$segment_pointer;

```

MMP\$CREATE_SEGMENT

```

O 5100 [   VAR status: ost$status);
O 5101 [
O 5102 [
O 5103 [   VAR
O 5104 [     caller_id: ost$caller_identifier,
O 5105 [     r1_pointer: mmt$segment_pointer,
O 5106 [     r1_status: ost$status,
O 5107 [     segment_attributes: mmt$segment_attr_descriptor,
O 5108 [     validating_ring_number: ost$valid_ring;
O 5109 [
O 5110 [   #CALLER_ID (caller_id);
O 5111 [   #KEYPOINT (osk$entry, 0, mmk$create_segment);
18 5112 [   status.normal := TRUE;
18 5113 [   determine_validating_ring_num (caller_id.ring, validation_ring_number, validating_ring_number);
3A 5114 [   segment_attributes.validating_ring_number := validating_ring_number;
3A 5115 [   segment_attributes.file_limits_to_enforce := sfc$no_limit;
3A 5116 [   segment_attributes.pointer_kind := pointer_kind;
3A 5117 [   segment_attributes.user_attributes := seg_attributes_p;
5E 5118 [   segment_attributes.sfid := gfv$null_sfid;
5E 5119 [   IF segment_attributes.user_attributes <> NIL THEN
78 5120 [     PUSH segment_attributes.user_attributes: [LOWERBOUND (seg_attributes_p^
AE 5121 [       ) .. UPPERBOUND (seg_attributes_p^)];
AE 5122 [     segment_attributes.user_attributes^ := seg_attributes_p^;
EE 5123 [   IFEND;
EE 5124 [   mmp$build_segment (segment_attributes, NIL (shared_taskid_array) , r1_pointer, r1_status);
11E 5125 [   IF NOT r1_status.normal THEN
126 5126 [     osp$set_status_condition (r1_status.condition, status);
13A 5127 [   ELSE
13A 5128 [     pointer := r1_pointer;
148 5129 [   IFEND;
148 5130 [   #KEYPOINT (osk$exit, 0, mmk$create_segment);
14C 5131 [
14C 5132 [   PROCEND mmp$create_segment;

```

MMP\$CREATE_SHADOW_SEGMENT

```

0 5134 {
0 5135 { The purpose of this request is to create a transient segment that uses the
0 5136 { ACTIVE file of an existing segment as a PASSIVE file.
0 5137 {
0 5138 { MMP$CREATE_SHADOW_SEGMENT (shadow_pva, shadow_offset, shadow_length,
0 5139 { pointer_kind, pva, status);
0 5140 {
0 5141 { SHADOW_PVA : (input) This parameter specifies the segment pointer of the
0 5142 { segment to be shadowed.
0 5143 {
0 5144 { SHADOW_OFFSET: (input) This parameter specifies the byte offset within the
0 5145 { segment from which to start shadowing.
0 5146 {
0 5147 { SHADOW_LENGTH: (input) This parameter specifies the length of portion in
0 5148 { segment to be shadowed.
0 5149 {
0 5150 { POINTER_KIND: (input) This parameter specifies the type of pointer to be
0 5151 { constructed for the segment.
0 5152 {
0 5153 { PVA: (output) This parameter returns the process virtual address of the
0 5154 { transient segment.
0 5155 {
0 5156 { STATUS: (output) This parameter specifies the request status returned.
0 5157 {
0 5158 {
0 5159 {
0 5160 {
0 5161 PROCEDURE [XDCL, #GATE] mmp$create_shadow_segment
0 5162 { segment_p: ^cell;
0 5163 { shadow_offset: ost$segment_offset;
0 5164 { shadow_length: ost$segment_length;
0 5165 { pointer_kind: mmt$segment_pointer_kind;
0 5166 { VAR pva: mmt$segment_pointer;
0 5167 { VAR status: ost$status);
0 5168 {
0 5169 { VAR
0 5170 { caller_id: ost$caller_identifier,
0 5171 { conv_ptr: ^cell,
0 5172 { i: pmt$initialization_value,
0 5173 { preset_value: pmt$initialization_value,
0 5174 { r1_pointer: mmt$segment_pointer,
0 5175 { r1_status: ost$status,
0 5176 { segment_attributes: mmt$segment_attrib_descriptor,
0 5177 { segnum: ost$segment;
0 5178 {
0 5179 { #KEYPOINT (osk$entry, #SEGMENT (segment_p) * osk$m, mmk$create_shadow_segment);
1E 5180 { status.normal := TRUE;
1E 5181 {
1E 5182 { conv_ptr := #ADDRESS [#RING (segment_p), #SEGMENT (segment_p), shadow_offset];
1E 5183 {
1E 5184 { Issue request to create ACTIVE segment.
1E 5185 { #CALLER_ID (caller_id);
1E 5186 { segment_attributes.validating_ring_number := caller_id.ring;
1E 5187 { segment_attributes.file_limits_to_enforce := sfc$no_limit;
1E 5188 { segment_attributes.pointer_kind := pointer_kind;
1E 5189 { PUSH segment_attributes.user_attributes: [T .. 1];

```

MMP\$CREATE_SHADOW_SEGMENT

```

72 5190 { segment_attributes.user_attributes^ [1].keyword := mmc$kw_shadow_segment;
72 5191 { segment_attributes.user_attributes^ [1].shadow_p := conv_ptr;
72 5192 { segment_attributes.user_attributes^ [1].shadow_length := shadow_length;
72 5193 { segment_attributes.sfid := gfv$null_sfid;
72 5194 { mmp$build_segment (segment_attributes, NIL [shared_taskid_array], r1_pointer, r1_status);
E6 5195 { IF NOT r1_status.normal THEN
EE 5196 { osp$set_status_condition (r1_status.condition, status);
102 5197 { ELSE
102 5198 { pva := r1_pointer;
110 5199 { IFEND;
110 5200 { #KEYPOINT (osk$exit, 0, mmk$create_shadow_segment);
114 5201 {
114 5202 { PROCEND mmp$create_shadow_segment;
0 5203 {

```

MMP\$CREATE_SHARED_STACK

```

O 5205 {
O 5206 { The purpose of this request is to create a stack segment to be
O 5207 { used by ADA tasks.
O 5208 {
O 5209 { MMP$CREATE_SHARED_STACK (SEG_ATTRIBUTES_P, POINTER_KIND,
O 5210 { SHARED_TASKID_ARRAY, POINTER, STATUS)
O 5211 {
O 5212 { SEG_ATTRIBUTES_P: (INPUT) This parameter is a pointer to the array of segment
O 5213 { attributes to be assigned to the segment.
O 5214 {
O 5215 { POINTER_KIND: (INPUT) This parameter specifies the type of pointer to
O 5216 { be constructed for the segment.
O 5217 {
O 5218 { SHARED_TASKID_ARRAY: (INPUT) This parameter specifies the array of taskids
O 5219 { for the tasks in which this segment is being opened.
O 5220 {
O 5221 { POINTER: (OUTPUT) This parameter specifies the process virtual address assigned
O 5222 { to the segment. The byte offset in the PVA is set to zero.
O 5223 {
O 5224 { STATUS: (OUTPUT) This parameter specifies the request status.
O 5225 {
O 5226 {
O 5227 { PROCEDURE [XDCL] mmp$create_shared_stack
O 5228 { ( seg_attributes_p: ^array [ * ] of mmt$attribute_descriptor;
O 5229 { pointer_kind: mmt$segment_pointer_kind;
O 5230 { shared_taskid_array: ^array [ 1 .. * ] of pmt$task_id;
O 5231 { VAR pointer: mmt$segment_pointer;
O 5232 { VAR status: ost$status);
O 5233 {
O 5234 { VAR
O 5235 { caller_id: ost$caller_identifier,
O 5236 { r1_pointer: mmt$segment_pointer,
O 5237 { r1_status: ost$status,
O 5238 { segment_attributes: mmt$segment_attr_descriptor;
O 5239 {
O 5240 { #CALLER_ID (caller_id);
O 5241 { status.normal := TRUE;
O 5242 { segment_attributes.validating_ring_number := caller_id.ring;
O 5243 { segment_attributes.file_limits_to_enforce := sfc$no_limit;
O 5244 { segment_attributes.pointer_kind := pointer_kind;
O 5245 { segment_attributes.user_attributes := seg_attributes_p;
O 5246 { IF segment_attributes.user_attributes (<) NIL THEN
O 5247 { PUSH segment_attributes.user_attributes: [LOWERBOUND (seg_attributes_p^
O 5248 { ) .. UPPERBOUND (seg_attributes_p^)];
O 5249 { segment_attributes.user_attributes^ := seg_attributes_p^;
O 5250 { IFEND;
O 5251 { segment_attributes.sfid := gfv$null_sfid;
O 5252 { mmp$build_segment (segment_attributes, shared_taskid_array, r1_pointer, r1_status);
O 5253 { IF NOT r1_status.normal THEN
O 5254 { osp$set_status_condition (r1_status.condition, status);
O 5255 { ELSE
O 5256 { pointer := r1_pointer;
O 5257 { IFEND;
O 5258 {
O 5259 { PROCEND mmp$create_shared_stack;

```

MMP\$CREATE_USER_SEGMENT

```

O 5262 {
O 5263 { The purpose of this request is to create a transient segment.
O 5264 {
O 5265 { MMP$CREATE_USER_SEGMENT (segment_attributes_p, pointer_kind, access_selections,
O 5266 { pointer, status)
O 5267 {
O 5268 { SEGMENT_ATTRIBUTES_P: (INPUT) This parameter is a pointer to an array
O 5269 { of segment_attributes to be assigned to the segment.
O 5270 {
O 5271 { POINTER_KIND: (INPUT) This parameter specifies the type of pointer
O 5272 { to be constructed for the segment.
O 5273 {
O 5274 { ACCESS_SELECTIONS: (INPUT) This parameter specifies the mode of access
O 5275 { to the segment (sequential or random).
O 5276 {
O 5277 { POINTER: (OUTPUT) This parameter specifies the process virtual address
O 5278 { assigned to the segment. The byte offset in the PVA is set to zero.
O 5279 {
O 5280 { STATUS: (OUTPUT) This parameter specifies the request status.
O 5281 { The possible error codes are:
O 5282 { dme$unable_to_locate_fde
O 5283 { dme$unable_to_get_fd_lock
O 5284 { mme$address_not_0_mod_16384
O 5285 { mme$asid_specified
O 5286 { mme$binding_attribute_invalid
O 5287 { mme$contig_mem_seg_violation
O 5288 { mme$execute_global_invalid
O 5289 { mme$invalid_asid_specified
O 5290 { mme$invalid_length_requested
O 5291 { mme$invalid_pva
O 5292 { mme$invalid_ring_brackets
O 5293 { mme$invalid_shadow_segment
O 5294 { mme$invalid_shared_taskid
O 5295 { mme$length_not_0_mod_16384
O 5296 { mme$pages_already_assigned
O 5297 { mme$ref_to_unrecovered_file
O 5298 { mme$ring_violation
O 5299 { mme$segment_number_is_in_use
O 5300 { mme$segment_number_not_in_use
O 5301 { mme$segment_number_too_big
O 5302 { mme$segment_origin_change
O 5303 { mme$segment_origin_invalid
O 5304 { mme$segment_table_is_full
O 5305 { mme$software_attribute_invalid
O 5306 { mme$unable_to_assign_contig_mem
O 5307 { mme$unsupported_keyword
O 5308 { mme$wired_seg_length_too_large
O 5309 {
O 5310 {
O 5311 {
O 5312 { PROCEDURE [XDCL, #GATE] mmp$create_user_segment
O 5313 { ( user_attributes_p: ^array [ * ] of mmt$user_attribute_descriptor;
O 5314 { pointer_kind: amt$pointer_kind;
O 5315 { access_selections: mmt$access_selections;
O 5316 { VAR pointer: amt$segment_pointer;
O 5317 { VAR status: ost$status);

```


MMP\$CREATE_USER_SEGMENT

```

0 5318
0 5319 VAR
0 5320 byte: 0 .. 255,
0 5321 caller_id: ost$caller_identifier,
0 5322 contiguous_flag: boolean,
0 5323 contiguous_page_count: integer,
0 5324 file_limits_to_enforce: sft$file_space_limit_kind,
0 5325 i: integer,
0 5326 increments: integer,
0 5327 kind: mmt$segment_pointer_kind,
0 5328 local_status: ost$status,
0 5329 max_length_index: integer,
0 5330 max_length_specified: boolean,
0 5331 page_size: integer,
0 5332 preset_pointer: Array [ * ] of 0 .. 255,
0 5333 r1_status: ost$status,
0 5334 save_index: integer,
0 5335 seg_attrib_p: Array [ * ] of mmt$attribute_descriptor,
0 5336 segment_attributes: mmt$segment_attr_descriptor,
0 5337 segment_pointer: mmt$segment_pointer,
0 5338 seq_p: ^SEQ ( * ),
0 5339 wired_flag: boolean,
0 5340 wired_index: integer;
0 5341
0 5342 contiguous_flag := FALSE;
C 5343 status.normal := TRUE;
C 5344 max_length_specified := FALSE;
C 5345 wired_flag := FALSE;
C 5346
C 5347 IF user_attributes_p <> NIL THEN
30 5348 PUSH seg_attrib_p: [LOWERBOUND (user_attributes_p) .. UPPERBOUND (user_attributes_p)];
62 5349
62 5350 FOR i := LOWERBOUND (user_attributes_p) TO UPPERBOUND (user_attributes_p) DO
70 5351 CASE user_attributes_p [i].keyword OF
BC 5352 = mmc$ua_ring_numbers :
BC 5353 seg_attrib_p [i].keyword := mmc$kw_ring_numbers;
BC 5354 seg_attrib_p [i].r1 := user_attributes_p [i].r1;
BC 5355 seg_attrib_p [i].r2 := user_attributes_p [i].r2;
FC 5356 = mmc$ua_segment_number :
FC 5357 seg_attrib_p [i].keyword := mmc$kw_segment_number;
FC 5358 seg_attrib_p [i].segnum := user_attributes_p [i].segnum;
11E 5359 = mmc$ua_max_segment_length :
11E 5360 seg_attrib_p [i].keyword := mmc$kw_max_segment_length;
11E 5361 seg_attrib_p [i].max_length := user_attributes_p [i].max_length;
11E 5362 max_length_specified := TRUE;
11E 5363 max_length_index := i;
14C 5364 = mmc$ua_preset_value :
14C 5365 seg_attrib_p [i].keyword := mmc$kw_preset_value;
14C 5366 seg_attrib_p [i].preset_value := user_attributes_p [i].preset_value;
170 5367 = mmc$ua_segment_access_control :
170 5368 seg_attrib_p [i].keyword := mmc$kw_segment_access_control;
170 5369 seg_attrib_p [i].access_control := user_attributes_p [i].access_control;
1AA 5370 = mmc$ua_wired_segment :
1AA 5371 seg_attrib_p [i].keyword := mmc$kw_wired_segment;
1AA 5372 IF user_attributes_p [i].wired_segment_length > 85536 THEN
1E4 5373 osp$set_status_abnormal ('MM', mmc$wired_seg_length_too_large, '', status);

```

MMP\$CREATE_USER_SEGMENT

```

214 5374 RETURN;
216 5375 IFEND;
216 5376 seg_attrib_p [i].wired_segment_length := user_attributes_p [i].wired_segment_length;
216 5377 IF user_attributes_p [i].contiguous_real_memory THEN
24E 5378 contiguous_flag := TRUE;
252 5379 IFEND;
252 5380 wired_flag := TRUE;
252 5381 wired_index := i;
280 5382 = mmc$ua_null_keyword :
280 5383 seg_attrib_p [i].keyword := mmc$kw_null_keyword;
27A 5384 ELSE
27A 5385 CASEEND;
27A 5386 FOREND;
286 5387 ELSE
286 5388 seg_attrib_p := NIL;
28A 5389 IFEND;
28A 5390
28A 5391 IF max_length_specified AND wired_flag THEN
292 5392 seg_attrib_p [max_length_index].max_length := seg_attrib_p [wired_index].wired_segment_length;
2C0 5393 IFEND;
2C0 5394
2C0 5395 IF pointer_kind = amc$cell_pointer THEN
2C8 5396 kind := mmc$cell_pointer;
2CE 5397 ELSEIF pointer_kind = amc$sequence_pointer THEN
2D4 5398 kind := mmc$sequence_pointer;
2DC 5399 ELSE
2DC 5400 kind := mmc$heap_pointer;
2E0 5401 IFEND;
2E0 5402 #CALLER_ID (caller_id);
2E0 5403
2E0 5404 segment_attributes.validating_ring_number := caller_id.ring;
2E0 5405 segment_attributes.file_limits_to_enforce := sfc$temp_file_space_limit;
2E0 5406 segment_attributes.pointer_kind := kind;
2E0 5407 segment_attributes.user_attributes := seg_attrib_p;
304 5408 segment_attributes.sfid := gfv$null_sf_id;
304 5409 mmp$buid_segment (segment_attributes, NIL {shared_taskid_array}, segment_pointer, r1_status);
340 5410
340 5411 IF r1_status.normal THEN
348 5412 IF access_selections = mmc$as_sequential THEN
350 5413 mmp$set_access_selections (segment_pointer.cell_pointer, mmc$as_sequential, status);
36C 5414 IFEND;
36C 5415 pointer_kind := pointer_kind;
36C 5416 IF pointer_kind = amc$cell_pointer THEN
37C 5417 seq_p := #SEQ (segment_pointer.cell_pointer);
37C 5418 RESET seq_p;
37C 5419 NEXT preset_pointer: [1 .. #SIZE (segment_pointer.cell_pointer)] IN seq_p;
3CA 5420 pointer.cell_pointer := segment_pointer.cell_pointer;
3D6 5421 ELSEIF pointer_kind = amc$sequence_pointer THEN
3DA 5422 seq_p := #SEQ (segment_pointer.seq_pointer);
3DA 5423 RESET seq_p;
3DA 5424 NEXT preset_pointer: [1 .. #SIZE (segment_pointer.seq_pointer)] IN seq_p;
42C 5425 pointer.sequence_pointer := segment_pointer.seq_pointer;
43A 5426 ELSE
43A 5427 seq_p := #SEQ (segment_pointer.heap_pointer);
43A 5428 RESET seq_p;
43A 5429 NEXT preset_pointer: [1 .. #SIZE (segment_pointer.heap_pointer)] IN seq_p;

```

MMP\$CREATE_USER_SEGMENT

```

48C 5430     pointer.heap_pointer := segment_pointer.heap_pointer;
496 5431     IFEND;
496 5432
496 5433     IF contiguous_flag THEN
49A 5434         mmp$assign_contiguous_memory (pointer.cell_pointer, seg_attrib_p^ [wired_index].wired_segment_length,
4D0 5435             status);
4D0 5436
4D0 5437         { MMP$ASSIGN_CONTIGUOUS_MEMORY will PRESET the pages it assigns to the segment--
4D0 5438             { if it was able to assign contiguous pages.
4D0 5439
4D0 5440             IF NOT status.normal THEN
4D8 5441                 mmp$delete_user_segment (pointer, local_status);
4F0 5442                 RETURN;
4F2 5443             IFEND;
4F6 5444         ELSEIF wired_flag AND NOT contiguous_flag THEN
4FE 5445
4FE 5446             { PRESET the pages of a wired segment-this is accomplished by touching every page in the segment..}
4FE 5447
4FE 5448             mmp$get_page_size (page_size);
504 5449             increment := 1;
504 5450             WHILE increment < UPPERBOUND (preset_pointer^) DO
528 5451                 byte := preset_pointer^ [increment];
528 5452                 increment := increment + page_size;
528 5453             WHILEND;
554 5454             IFEND;
556 5455         ELSE
556 5456             osp$set_status_condition (r1_status.condition, status);
566 5457         IFEND;
566 5458
566 5459     PROCEND mmp$create_user_segment;

```

MMP\$DELETE_SCRATCH_SEGMENT

```

O 5461 {
O 5462 {   The purpose of this request is to delete a scratch segment.
O 5463 {
O 5464 {       MMP$DELETE_SCRATCH_SEGMENT (POINTER, STATUS)
O 5465 {
O 5466 {   POINTER: (input_output) This parameter specifies the segment
O 5467 {   to be deleted. This pointer will be set to NIL when the request
O 5468 {   completes.
O 5469 {
O 5470 {   STATUS: (output) This parameter specifies the request status.
O 5471 {   The possible error codes are:
O 5472 {       dme$file_descriptor_not_deleted
O 5473 {       mme$invalid_close_segment_req
O 5474 {       mme$invalid_shared_taskid
O 5475 {       mme$segment_number_not_in_use
O 5476 {       mme$segment_number_too_big
O 5477 {
O 5478 {
O 5479 {   PROCEDURE [XDCL, #GATE] mmp$delete_scratch_segment
O 5480 {   [VAR pointer: amt$segment_pointer;
O 5481 {       VAR status: ost$status];
O 5482 {
O 5483 {   VAR
O 5484 {       caller_id: ost$caller_identifier,
O 5485 {       p: mmt$segment_pointer,
O 5486 {       r1_status: ost$status;
O 5487 {
O 5488 {   #CALLER_ID (caller_id);
C 5489 {   #KEYPOINT (osk$entry, #SEGMENT (pointer.cell_pointer) * osk$m, mmk$delete_scratch_segment);
2A 5490 {   status.normal := TRUE;
2A 5491 {   p.kind := mmc$cell_pointer;
2A 5492 {   p.cell_pointer := pointer.cell_pointer;
2A 5493 {
2A 5494 {   mmp$invalidate_segment (#SEGMENT (pointer.cell_pointer), caller_id.ring, NIL [shared_taskid_array],
78 5495 {       r1_status);
78 5496 {   IF r1_status.normal THEN
80 5497 {       pointer.cell_pointer := NIL;
88 5498 {   ELSE
88 5499 {       osp$set_status_condition (r1_status.condition, status);
98 5500 {   IFEND;
98 5501 {
98 5502 {   #KEYPOINT (osk$exit, 0, mmk$delete_scratch_segment);
9C 5503 {
9C 5504 {   PROCEND mmp$delete_scratch_segment;
O 5505 {

```

MMP\$DELETE_SEGMENT

```

O 5508 {
O 5509 { The purpose of this request is to delete/close a segment. If the
O 5510 { caller is not within the read bracket of the segment being deleted the
O 5511 { call is rejected.
O 5512 {
O 5513 { MMP$DELETE_SEGMENT (POINTER, VALIDATION_RING_NUMBER, STATUS)
O 5514 {
O 5515 { POINTER: (input/output) This parameter specifies the segment
O 5516 { to be deleted. This pointer will be set to NIL when the request
O 5517 { completes.
O 5518 {
O 5519 { VALIDATION_RING_NUMBER: (input) This parameter specifies the ring of
O 5520 { execution for whom the segment is being deleted. The ring
O 5521 { number used for validation is the max of caller ring number
O 5522 { and 'validation_ring_number'.
O 5523 {
O 5524 { STATUS: (output) This parameter specifies the request status.
O 5525 { The possible error codes are:
O 5526 { dme$file_descriptor_not_deleted
O 5527 { mme$invalid_close_segment_req
O 5528 { mme$invalid_shared_taskid
O 5529 { mme$segment_number_not_in_use
O 5530 { mme$segment_number_too_big
O 5531 {
O 5532 {
O 5533 {
O 5534 {
O 5535 { PROCEDURE [XDCL, #GATE] mmp$delete_segment
O 5536 { (VAR pointer: mmt$segment_pointer;
O 5537 { validation_ring_number: ost$valid_ring;
O 5538 { VAR status: ost$status);
O 5539 {
O 5540 { VAR
O 5541 { caller_id: ost$caller_identifier,
O 5542 { r1_status: ost$status,
O 5543 { validating_ring_number: ost$valid_ring;
O 5544 {
O 5545 { #CALLER_ID (caller_id);
O 5546 { #KEYPOINT (osk$entry, #SEGMENT (pointer.cell_pointer) * osk$m, mmk$delete_segment);
O 5547 { status.normal := TRUE;
2A 5548 { determine_validating_ring_num (caller_id.ring, validation_ring_number, validating_ring_number);
4C 5549 { mmp$invalidate_segment (#SEGMENT (pointer.cell_pointer), validating_ring_number,
86 5550 { NIL (shared_taskid_array), r1_status);
86 5551 { IF r1_status.normal THEN
8E 5552 { pointer.cell_pointer := NIL;
96 5553 { ELSE
96 5554 { osp$set_status_condition (r1_status.condition, status);
A8 5555 { IFEND;
A8 5556 { #KEYPOINT (osk$exit, 0, mmk$delete_segment);
AC 5557 {
AC 5558 { PROCEND mmp$delete_segment;

```

MMP\$DELETE_USER_SEGMENT

```

O 5561 {
O 5562 {
O 5563 { The purpose of this request is to close/delete a segment.
O 5564 {
O 5565 { MMP$DELETE_USER_SEGMENT (pointer, status)
O 5566 {
O 5567 { POINTER: (INPUT/OUTPUT) This parameter specifies the segment
O 5568 { to be deleted. This pointer will be set to NIL when the
O 5569 { request completes.
O 5570 {
O 5571 { STATUS: (OUTPUT) This parameter specifies the request status.
O 5572 { The possible error codes are:
O 5573 { dme$file_descriptor_not_deleted
O 5574 { mme$invalid_close_segment_req
O 5575 { mme$invalid_shared_taskid
O 5576 { mme$segment_number_not_in_use
O 5577 { mme$segment_number_too_big
O 5578 {
O 5579 {
O 5580 {
O 5581 { PROCEDURE [XDCL, #GATE] mmp$delete_user_segment
O 5582 { (VAR pointer: amt$segment_pointer;
O 5583 { VAR status: ost$status);
O 5584 {
O 5585 { VAR
O 5586 { caller_id: ost$caller_identifier,
O 5587 { r1_status: ost$status;
O 5588 {
O 5589 { #CALLER_ID (caller_id);
O 5590 { status.normal := TRUE;
C 5591 { mmp$invalidate_segment (#SEGMENT (pointer.cell_pointer), caller_id.ring, NIL (shared_taskid_array),
5C 5592 { r1_status);
5C 5593 { IF r1_status.normal THEN
64 5594 { pointer.cell_pointer := NIL;
6A 5595 { ELSE
6A 5596 { osp$set_status_condition (r1_status.condition, status);
7C 5597 { IFEND;
7C 5598 {
7C 5599 { PROCEND mmp$delete_user_segment;

```

MMP\$FAILED_ALLOCATION_FLAG_HDL

```

O 5602
O 5603 { The purpose of the procedure is to attempt to complete file
O 5604 [allocation on a file which has previously failed in an
O 5605 [attempt to allocate file space. It only handles cases
O 5606 [where the failure was dme$unable_to_alloc_all_space.
O 5607 { The ring 1 segment manager sets a system flag if
O 5608 [such a failure occurs.
O 5609 { The procedure make a single attempt to perform
O 5610 [file allocation. If allocation is not possible then
O 5611 [the procedure will wait a short time and then return
O 5612 [to the code which caused the allocation to be required.
O 5613 [If any events such as terminate_job or interactive break have
O 5614 [arisen during the wait, they will be processed on the return
O 5615 [if possible. If nothing preempts the original code, it will cause
O 5616 [the same allocation requirement and the process is repeated.
O 5617
O 5618 PROCEDURE [XDCL] mmp$failed_allocation_flag_hdl
O 5619 ( flag_id: ost$system_flag);
O 5620
O 5621 VAR
O 5622 i: integer,
O 5623 new_allocated_length: amt$file_byte_address,
O 5624 previous_allocated_length: amt$file_byte_address,
O 5625 segnum: ost$segment,
O 5626 status: ost$status,
O 5627 wait_time: integer,
O 5628 xcb_p: ^ost$execution_control_block;
O 5629
O 5630 pmp$find_executing_task_xcb (xcb_p);
A 5631 previous_allocated_length := 0;
A 5632
A 5633 WHILE TRUE DO
26 5634 IF pmp$task_state () = pmc$task_terminating THEN
38 5635 RETURN;
3A 5636 IFEND;
3A 5637
3A 5638 wait_time := mmv$file_allocation_interval;
3A 5639 mmp$process_file_alloc (new_allocated_length, status);
5E 5640 IF status.normal THEN
66 5641 RETURN;
6C 5642 ELSEIF NOT (status.normal) AND (status.condition = dme$unable_to_alloc_all_space) THEN
7C 5643 IF (new_allocated_length > 1000000) OR (new_allocated_length > previous_allocated_length) THEN
8C 5644 wait_time := 2000;
90 5645 IFEND;
90 5646 previous_allocated_length := new_allocated_length;
94 5647 IFEND;
94 5648
94 5649 { Any abnormal status which is actually returned to this procedure is an 'OK' status, which
94 5650 [will cause the system to keep attempting to allocate file_space for this task.
94 5651 [In the case of the File_Server, however, we don't want to wait forever because the server
94 5652 [may not EVER come back. Just return; the next time the user references the page the correct
94 5653 [condition will be raised.
94 5654
94 5655 IF (status.condition = dfe$family_not_served) OR (status.condition = dfe$server_not_active) OR
80 5656 (status.condition = dfe$server_has_terminated) THEN
80 5657 RETURN;

```

MMP\$FAILED_ALLOCATION_FLAG_HDL

```

B2 5658 IFEND;
B2 5659
B2 5660 bap$exit_fap_on_condition (dme$unable_to_alloc_all_space);
C6 5661
C6 5662 [Special trap for system disk full in early deadstart
C6 5663 IF NOT dsp$system_committed () THEN
D6 5664 dpp$put_critical_message ('The system disk is full - reeadstart without job recovery', status);
FA 5665 IFEND;
FA 5666
FA 5667 [Raise the appropriate user condition.
FA 5668 osp$wait_on_condition (dme$unable_to_alloc_all_space);
10E 5669
12C 5670 IF (xcb_p^.system_flags * $tmt$system_flags [pmc$sf_terminate_task, jmc$terminate_job_flag,
12C 5671 jmc$logout_flag_id, jmc$kill_job_flag, pmc$kill_task_flag] <> $tmt$system_flags []) AND
12C 5672 (xcb_p^.system_table_lock_count <= 0) THEN
12C 5673 pmp$log (' Job terminated while waiting for file allocation.', status);
150 5674 pmp$exit (status);
17C 5675 IFEND;
17C 5676
17C 5677 IF (xcb_p^.system_flags = $tmt$system_flags []) THEN
188 5678 FOR i := 1 TO tmc$maximum_signals DO
190 5679 IF xcb_p^.signals.present [i] THEN
1A4 5680 RETURN;
1A6 5681 IFEND;
1A6 5682 FOREND;
1B0 5683 ELSE
1B0 5684 RETURN;
1B2 5685 IFEND;
1B2 5686 WHILEND;
1B6 5687
1B6 5688 PROCEND mmp$failed_allocation_flag_hdl;

```

MMP\$FETCH_OFFSET_MODIFIED_PAGES

```

O 5690
O 5691 PROCEDURE [XDCL, #GATE] mmp$fetch_offset_modified_pages
O 5692 (
O 5693   segment_p: ^cell;
O 5694   return_unallocated_offsets: boolean;
O 5695   VAR offset_list: array [ * ] of ost$segment_offset;
O 5696   VAR offsets_returned: integer;
O 5697   VAR status: ost$status);
O 5698
O 5699   VAR
O 5700     i: integer,
O 5701     r1_offset_list_p: ^array [ * ] of ost$segment_offset,
O 5702     r1_offsets_returned: integer,
O 5703     r1_status: ost$status;
O 5704
O 5705   status.normal := TRUE;
O 5706   PUSH r1_offset_list_p: [1 .. UPPERBOUND (offset_list)];
40 5707   mmp$fetch_offset_mod_pages_r1 (#SEGMENT (segment_p), gfv$null_sfid, return_unallocated_offsets,
8A 5708     r1_offset_list_p, r1_offsets_returned, r1_status);
8A 5709   IF NOT r1_status.normal THEN
92 5710     osp$set_status_condition (r1_status.condition, status);
A6 5711   ELSE
A6 5712     offset_list := r1_offset_list_p^;
F2 5713     offsets_returned := r1_offsets_returned;
FE 5714   IFEND;
FE 5715 PROCEND mmp$fetch_offset_modified_pages;
O 5716

```

MMP\$FETCH_SEGMENT_ATTRIBUTES

```

O 5719 {
O 5720 {   The purpose of this request is to fetch one or more attributes
O 5721 { of a segment.
O 5722 {
O 5723 {   MMP$FETCH_SEGMENT_ATTRIBUTES (PVA, SEG_ATTRIBUTES, STATUS)
O 5724 {
O 5725 {   PVA: (input) This parameter specifies the segment number.
O 5726 {
O 5727 {   SEG_ATTRIBUTES: (input_output) This parameter is an adaptable
O 5728 {   array of segment attribute descriptors. Prior to issuing this
O 5729 {   request, the caller stores into this array the attribute keywords
O 5730 {   of the attributes to be fetched.
O 5731 {
O 5732 {   STATUS: (output) This parameter specifies the request status.
O 5733 {   The possible error codes are:
O 5734 {     mme$caller_not_in_read_bracket
O 5735 {     mme$segment_number_not_in_use
O 5736 {     mme$segment_number_too_big
O 5737 {     mme$unsupported_keyword
O 5738 {
O 5739 {
O 5740 PROCEDURE [XDCL, #GATE] mmp$fetch_segment_attributes
O 5741 (
O 5742   pva: ^cell;
O 5743   VAR seg_attributes: array [ * ] of mmt$attribute_descriptor;
O 5744   VAR status: ost$status);
O 5745
O 5746   VAR
O 5747     caller_id: ost$caller_identifier,
O 5748     i: integer,
O 5749     r1_status: ost$status,
O 5750     r1_segment_attributes_p: ^array [ * ] of mmt$attribute_descriptor;
O 5751
O 5752   #KEYPOINT (osk$entry, osk$m * #SEGMENT (pva), mmk$fetch_seg_attributes);
1E 5753
1E 5754   status.normal := TRUE;
1E 5755   #CALLER_ID (caller_id);
1E 5756   PUSH r1_segment_attributes_p: [LOWERBOUND (seg_attributes) .. UPPERBOUND (seg_attributes)];
80 5757   r1_segment_attributes_p^ := seg_attributes;
AC 5758   mmp$fetch_segment_attributes_r1 (#SEGMENT (pva), caller_id.ring, r1_segment_attributes_p, r1_status);
DE 5759   IF NOT r1_status.normal THEN
E6 5760     osp$set_status_condition (r1_status.condition, status);
FC 5761   ELSE
FC 5762     seg_attributes := r1_segment_attributes_p^;
140 5763   IFEND;
140 5764
144 5765   #KEYPOINT (osk$exit, 0, mmk$fetch_seg_attributes);
144 5766 PROCEND mmp$fetch_segment_attributes;

```

MMP\$GET_ALLOCATED_ADDRESSES

```

O 5767
O 5768 PROCEDURE [XDCL, #GATE] mmp$get_allocated_addresses
O 5769 (
O 5770     file: Acell;
O 5771     starting_byte_address: ost$segment_offset;
O 5772     VAR addr_list: array [ * ] of dmt$addr_length_pair;
O 5773     VAR addr_returned: integer;
O 5774     VAR list_overflow: boolean;
O 5775     VAR status: ost$status);
O 5776
O 5777 VAR
O 5778     caller_id: ost$caller_identifier,
O 5779     i: integer,
O 5780     r1_addr_list_p: Array [ * ] of dmt$addr_length_pair,
O 5781     r1_addr_returned: integer,
O 5782     r1_list_overflow: boolean,
O 5783     r1_status: ost$status;
O 5784
O 5785 status.normal := TRUE;
O 5786 #CALLER_ID (caller_id);
O 5787 PUSH r1_addr_list_p: [LOWERBOUND (addr_list) .. UPPERBOUND (addr_list)];
54 5787 mmp$get_allocated_addresses_r1 [#SEGMENT (file), caller_id.ring, starting_byte_address, r1_addr_list_p,
A2 5788     r1_addr_returned, r1_list_overflow, r1_status];
A2 5789 IF NOT r1_status.normal THEN
AA 5790     osp$set_status_condition (r1_status.condition, status);
BE 5791 ELSE
BE 5792     addr_list := r1_addr_list_p^;
10A 5793     list_overflow := r1_list_overflow;
10A 5794     addr_returned := r1_addr_returned;
122 5795 IFEND;
O 5796 PROCEND mmp$get_allocated_addresses;
O 5797

```

MMP\$GET_SEGMENT_LENGTH

```

O 5800 {
O 5801 { The purpose of this procedure is to return the current segment length
O 5802 { to the caller.
O 5803 {
O 5804 { MMP$GET_SEGMENT_LENGTH (PVA, VALIDATION_RING_NUMBER, SEGMENT_LENGTH,
O 5805 { STATUS)
O 5806 {
O 5807 { PVA: (input) This parameter specifies the segment for which the current
O 5808 { segment length is returned.
O 5809 {
O 5810 { VALIDATION_RING_NUMBER: (input) This parameter specifies the ring number
O 5811 { used for validation if it is greater than the caller ring number.
O 5812 {
O 5813 { SEGMENT_LENGTH: (output) This parameter is where the current segment
O 5814 { length is returned.
O 5815 {
O 5816 { STATUS: (output) This parameter is where the request status is returned
O 5817 { to the caller. The possible error codes are:
O 5818 { mme$caller_not_in_read_bracket
O 5819 { mme$invalid_pva
O 5820 { mme$ref_to_Unrecovered_file
O 5821 { mme$segment_number_not_in_use
O 5822 { mme$segment_number_too_big
O 5823 {
O 5824 {
O 5825 {
O 5826 PROCEDURE [XDCL, #GATE] mmp$get_segment_length
O 5827 (
O 5828     pva: Acell;
O 5829     validation_ring_number: ost$valid_ring;
O 5830     VAR segment_length: ost$segment_length;
O 5831     VAR status: ost$status);
O 5832
O 5833 VAR
O 5834     caller_id: ost$caller_identifier,
O 5835     r1_segment_length: ost$segment_length,
O 5836     r1_status: ost$status,
O 5837     validating_ring_number: ost$valid_ring;
O 5838
O 5839 #KEYPOINT (osk$entry, osk$m * #SEGMENT (pva), mmk$get_segment_length);
1E 5839 status.normal := TRUE;
1E 5840 segment_length := 0;
1E 5841 #CALLER_ID (caller_id);
1E 5842 determine_validating_ring_num (caller_id.ring, validation_ring_number, validating_ring_number);
52 5843 mmp$get_segment_length_r1 [#SEGMENT (pva), validating_ring_number, r1_segment_length, r1_status];
84 5844 IF NOT r1_status.normal THEN
8C 5845     osp$set_status_condition (r1_status.condition, status);
AO 5846 ELSE
AO 5847     segment_length := r1_segment_length;
A8 5848 IFEND;
A8 5849 #KEYPOINT (osk$exit, 0, mmk$get_segment_length);
AC 5850
AC 5851 PROCEND mmp$get_segment_length;
O 5852

```

MMP\$INITIATE_DEBUG_SHADOWING

```

O 5855
O 5856 { The purpose of this request is to initiate shadowing on segments for the
O 5857 { Interactive Debugger.
O 5858 {
O 5859 { MMP$INITIATE_DEBUG_SHADOWING (segment_pointer, status)
O 5860 {
O 5861 { SEGMENT_POINTER: (INPUT) This parameter specifies the process virtual
O 5862 { address assigned to the segment.
O 5863 {
O 5864 { STATUS: (OUTPUT) This parameter specifies the request status.
O 5865 {
O 5866 {
O 5867 PROCEDURE [XDCL, #GATE] mmp$initiate_debug_shadowing
O 5868 { ( segment_pointer: Acell;
O 5869 { VAR status: ost$status);
O 5870 {
O 5871 VAR
O 5872 caller_id: ost$caller_identifier,
O 5873 r1_pointer: Acell,
O 5874 r1_status: ost$status,
O 5875 validating_ring: ost$valid_ring;
O 5876
O 5877 status.normal := TRUE;
C 5878 #CALLER_ID (caller_id);
C 5879
C 5880 { Change the segment attributes to WRITE and flush the pages to disk.
C 5881 {
C 5882 mmp$write_modified_pages (segment_pointer, osc$maximum_offset, osc$wait, status);
44 5883 IF NOT status.normal THEN
4C 5884 RETURN;
4E 5885 IFEND;
4E 5886
4E 5887 { Issue ring_1 call to establish ACTIVE segment.
4E 5888
4E 5889 r1_pointer := segment_pointer;
4E 5890 mmp$initiate_shadowing_r1 (r1_pointer, caller_id.ring, mmc$ssk_read_only_file, r1_status);
78 5891 IF NOT r1_status.normal THEN
80 5892 osp$set_status_condition (r1_status.condition, status);
90 5893 IFEND;
90 5894
90 5895 PROCEND mmp$initiate_debug_shadowing;

```

MMP\$INITIATE_SHADOWING

```

O 5898 {
O 5899 { The purpose of this request is to initiate shadowing of files.
O 5900 { Modified pages of the file to be shadowed are written to disk
O 5901 { and the segment for the file to be shadowed is transformed into
O 5902 { an ACTIVE segment to allow continuation of access to files during
O 5903 { an online dump.
O 5904 {
O 5905 { MMP$INITIATE_SHADOWING (POINTER, STATUS)
O 5906 {
O 5907 { POINTER: (input) This parameter specifies the process virtual
O 5908 { address assigned to the segment.
O 5909 {
O 5910 { STATUS: (output) This parameter specifies the request status.
O 5911 {
O 5912 {
O 5913 PROCEDURE [XDCL, #GATE] mmp$initiate_shadowing
O 5914 { ( segment_p: Acell;
O 5915 { VAR status: ost$status);
O 5916 {
O 5917 VAR
O 5918 caller_id: ost$caller_identifier,
O 5919 r1_pointer: Acell,
O 5920 r1_status: ost$status;
O 5921
O 5922 #KEYPOINT (osk$entry, #SEGMENT (segment_p) * osk$m, mmp$initiate_shadowing);
1E 5923 status.normal := TRUE;
1E 5924 #CALLER_ID (caller_id);
1E 5925
1E 5926 { Change the segment attributes to WRITE and flush the pages to disk.
1E 5927 {
1E 5928 mmp$write_modified_pages (segment_p, osc$maximum_offset, osc$wait, status);
52 5929 IF NOT status.normal THEN
5A 5930 RETURN;
5C 5931 IFEND;
5C 5932
5C 5933 { Issue ring_1 call to establish ACTIVE segment.
5C 5934
5C 5935 r1_pointer := segment_p;
5C 5936 mmp$initiate_shadowing_r1 (r1_pointer, #RING (segment_p), mmc$ssk_read_write_file, r1_status);
88 5937 IF NOT r1_status.normal THEN
90 5938 osp$set_status_condition (r1_status.condition, status);
A0 5939 IFEND;
A0 5940 #KEYPOINT (osk$exit, 0, mmp$initiate_shadowing);
A4 5941
A4 5942
A4 5943 PROCEND mmp$initiate_shadowing;

```

MMP\$OPEN_FILE_SEGMENT

```

0 5946 {
0 5947 { The purpose of this request is to open a file for segment
0 5948 { level access.
0 5949 {
0 5950 { MMP$OPEN_FILE_SEGMENT (SFID, SEG_ATTRIBUTES_P, POINTER_KIND,
0 5951 { VALIDATION_RING_NUMBER, CHAPTER_NUMBER, POINTER, STATUS)
0 5952 {
0 5953 { SFID: (input) This parameter specifies the system file identifier of the file
0 5954 { to be opened as a segment.
0 5955 {
0 5956 { SEG_ATTRIBUTES_P: (input) This parameter is a pointer to an array
0 5957 { of segment attributes to be assigned to the segment.
0 5958 {
0 5959 { POINTER_KIND: (input) This parameter specifies the type of
0 5960 { pointer to be constructed for the segment.
0 5961 {
0 5962 { VALIDATION_RING_NUMBER: (input) This parameter specifies the ring of
0 5963 { execution for whom the segment is being opened. The ring
0 5964 { number used for validation is the max of caller ring number
0 5965 { and 'validation_ring_number'.
0 5966 {
0 5967 { POINTER: (output) This parameter is where the segment pointer
0 5968 { is returned. The byte offset in the PVA is set to zero.
0 5969 {
0 5970 { STATUS: (output) This parameter specifies the request status.
0 5971 { The possible error codes are:
0 5972 { dme$unable_to_locate_fde
0 5973 { dme$unable_to_get_fd_lock
0 5974 { mme$address_not_0_mod_16384
0 5975 { mme$asid_specified
0 5976 { mme$binding_attribute_invalid
0 5977 { mme$contig_mem_seg_violation
0 5978 { mme$execute_global_invalid
0 5979 { mme$invalid_asid_specified
0 5980 { mme$invalid_length_requested
0 5981 { mme$invalid_pva
0 5982 { mme$invalid_ring_brackets
0 5983 { mme$invalid_shadow_segment
0 5984 { mme$invalid_shared_taskid
0 5985 { mme$length_not_0_mod_16384
0 5986 { mme$pages_already_assigned
0 5987 { mme$ref_to_unrecovered_file
0 5988 { mme$ring_violation
0 5989 { mme$segment_number_is_in_use
0 5990 { mme$segment_number_not_in_use
0 5991 { mme$segment_number_too_big
0 5992 { mme$segment_origin_invalid
0 5993 { mme$segment_table_is_full
0 5994 { mme$software_attribute_invalid
0 5995 { mme$unable_to_assign_contig_mem
0 5996 { mme$unsupported_keyword
0 5997 {
0 5998 { PROCEDURE [XDCL] mmp$open_file_segment
0 5999 { ( sfid: gft$system_file_identifier,
0 6000 { attr_p: Aarray [ * ] of mmt$attribute_descriptor,
0 6001 { pointer_kind: mmt$segment_pointer_kind;

```

MMP\$OPEN_FILE_SEGMENT

```

0 6002 validation_ring_number: ost$valid_ring;
0 6003 file_limits_to_enforce: sft$file_space_limit_kind;
0 6004 VAR pointer: mmt$segment_pointer;
0 6005 VAR status: ost$status);
0 6006
0 6007 VAR
0 6008 caller_id: ost$caller_identifier,
0 6009 r1_pointer: mmt$segment_pointer,
0 6010 r1_status: ost$status,
0 6011 segment_attributes: mmt$segment_attr_descriptor,
0 6012 validating_ring_number: ost$valid_ring;
0 6013
0 6014 #CALLER_ID (caller_id);
0 6015 #KEYPOINT (osk$entry, 0, mmm$open_file_segment);
0 6016 status.normal := TRUE;
18 6017 determine_validating_ring_num (caller_id,ring, validation_ring_number, validating_ring_number);
3A 6018 segment_attributes.validating_ring_number := validating_ring_number;
3A 6019 segment_attributes.file_limits_to_enforce := file_limits_to_enforce;
3A 6020 segment_attributes.pointer_kind := pointer_kind;
3A 6021 segment_attributes.user_attributes := attr_p;
60 6022 IF segment_attributes.user_attributes <> NIL THEN
6E 6023 PUSH segment_attributes.user_attributes: [LOWERBOUND (attr_p^).. UPPERBOUND (attr_p^)];
A4 6024 segment_attributes.user_attributes^ := attr_p^;
E4 6025 IFEND;
E4 6026 segment_attributes.sfid := sfid;
E4 6027 mmp$build_segment (segment_attributes, NIL [shared_taskid_array], r1_pointer, r1_status);
11C 6028 IF NOT r1_status.normal THEN
124 6029 osp$set_status_condition (r1_status.condition, status);
13A 6030 ELSE
13A 6031 pointer := r1_pointer;
148 6032 IFEND;
148 6033 #KEYPOINT (osk$exit, osk$m # SEGMENT (pointer.cell_pointer), mmm$open_file_segment);
15E 6034
15E 6035 PROCEND mmp$open_file_segment;

```


mmp\$open_segment

```

0 6037
0 6038 PROCEDURE [XDCL, #GATE] mmp$open_segment
0 6039 (
0 6040     file_name: ost$name;
0 6041     seg_attributes: Aarray [ * ] of mmt$attribute_descriptor;
0 6042     pointer_kind: mmt$segment_pointer_kind;
0 6043     VAR pointer: mmt$segment_pointer;
0 6044     VAR status: ost$status);
0 6045
0 6046 VAR
0 6047     caller_id: ost$caller_identifier;
0 6048
0 6049 #CALLER_ID (caller_id);
0 6050 fmp$ln_open_chapter (file_name, 0, caller_id.ring, seg_attributes, pointer_kind, pointer, status);
C 6051
56 6052 PROCEND mmp$open_segment;

```

MMP\$PRESET_PAGE_STREAMING

```

0 6054 {-----
0 6055 { PURPOSE:
0 6056 { Procedure mmp$preset_page_streaming provides the capability of presetting the SDTX of a segment so that
0 6057 { it is already in page streaming mode, with free behind TRUE and the transfer size as specified. It returns
0 6058 { the original values of free behind and transfer size so that the caller can call again later and restore
0 6059 { the original values.
0 6060 {
0 6061 { DESIGN:
0 6062 { There is nothing fancy. The boolean "preset_and_save_ts_fb" indicates the purpose of a call, a value
0 6063 { of TRUE indicates preset and save the original values, a value of FALSE indicates a restore. Nothing is
0 6064 { done to ensure calls are in order or completed. In preset mode, the transfer size is changed if the
0 6065 { specified transfer size is > sdtx.stream.transfer_size. IF sdtx.stream.streaming = TRUE the segment is
0 6066 { already in page streaming mode and nothing else need be done. If sdtx.stream.streaming = FALSE then
0 6067 { the boolean sdtx.stream.preset_streaming is set to indicate that the next fault should stream. To ensure
0 6068 { the next fault enters the page streaming code, the value of sdtx.stream.sequential_accesses is forced
0 6069 { to be >= mmp$page_streaming_prestream.
0 6070 { When restoring the original values, the streaming boolean in the SDTX is left TRUE. The page
0 6071 { fault process will terminate streaming if that is appropriate. If the restore call is not made, the only
0 6072 { result is that free behind is TRUE and Transfer size >64K which may or may not have been original values.
0 6073 {-----
0 6074
0 6075 PROCEDURE [XDCL, #GATE] mmp$preset_page_streaming
0 6076 (
0 6077     pva: Acell;
0 6078     temp_transfer_size: integer;
0 6079     VAR saved_transfer_size: 0 .. 15;
0 6080     VAR saved_free_behind: boolean;
0 6081     VAR status: ost$status);
0 6082
0 6083 VAR
0 6084     caller_id: ost$caller_identifier,
0 6085     r1_free_behind: boolean,
0 6086     r1_transfer_size: 0 .. 15,
0 6087     r1_status: ost$status;
0 6088
0 6089 { Validate the pva and get a pointer to the segment descriptor.
0 6090
0 6091 status.normal := TRUE;
0 6092 #CALLER_ID (caller_id);
0 6093 mmp$preset_page_streaming_r1 (#SEGMENT (pva), caller_id.ring, preset_and_save_ts_fb, temp_transfer_size,
C 6094     r1_transfer_size, r1_free_behind, r1_status);
68 6095 IF NOT r1_status.normal THEN
70 6096     osp$set_status_condition (r1_status.condition, status);
84 6097 ELSE
84 6098     saved_transfer_size := r1_transfer_size;
84 6099     saved_free_behind := r1_free_behind;
9C 6100 IFEND;
9C 6101
9C 6102 PROCEND mmp$preset_page_streaming;

```

MMP\$RESERVE_SEGMENT_NUMBER

```

0 6104
0 6105 PROCEDURE [XDCL, #GATE] mmp$reserve_segment_number
0 6106 ( shared_stack_flag: boolean;
0 6107   VAR segment_num_array: ^array [ * ] of ost$segment;
0 6108   VAR status: ost$status);
0 6109
0 6110   VAR
0 6111     i: integer,
0 6112     r1_num_array_p: ^array [ * ] of ost$segment,
0 6113     r1_status: ost$status;
0 6114
0 6115 { This procedure is the user interface to reserve segments for subsequent explicit assignment.
0 6116
0 6117   status.normal := TRUE;
0 6118   PUSH r1_num_array_p: [LOWERBOUND (segment_num_array^)..UPPERBOUND (segment_num_array^)];
42 6119   FOR i := LOWERBOUND (segment_num_array^) TO UPPERBOUND (segment_num_array^) DO
62 6120     r1_num_array_p^ [i] := 0;
62 6121   FOREND;
7C 6122
7C 6123   mmp$reserve_segment_number_r1 (shared_stack_flag, r1_num_array_p, r1_status);
AO 6124   IF NOT r1_status.normal THEN
A8 6125     osp$set_status_condition (r1_status.condition, status);
BC 6126   ELSE
BC 6127     FOR i := LOWERBOUND (segment_num_array^) TO UPPERBOUND (segment_num_array^) DO
DB 6128       segment_num_array^ [i] := r1_num_array_p^ [i];
DE 6129     FOREND;
10F 6130   IFEND;
106 6131
106 6132 PROCEND mmp$reserve_segment_number;

```

MMP\$REVERIFY_ACCESS

```

0 6135
0 6136 {
0 6137 { This request can be used to determine whether a PVA
0 6138 { can be accessed by a program without causing an access
0 6139 { violation or segment fault.
0 6140 {
0 6141 { This request is similar to MMP$VERIFY_ACCESS but runs
0 6142 { faster. The speed improvement is at a cost of less checking.
0 6143 { The request simply verifies that the requested segment is still
0 6144 { valid in the segment table. Checking of access rights and
0 6145 { read-beyond-eoi is NOT done.
0 6146 {
0 6147 {     MMP$REVERIFY_ACCESS (PVA_P): BOOLEAN
0 6148 {
0 6149 { PVA_P: (input) This parameter is a pointer to the PVA to be
0 6150 { tested.
0 6151 {
0 6152 { BOOLEAN: (output) The boolean result of this function specifies
0 6153 { whether access is valid.
0 6154 {
0 6155
0 6156 FUNCTION [XDCL] mmp$reverify_access
0 6157 ( pva_p: ^^cell): boolean;
0 6158
0 6159   VAR
0 6160     sdt_entry_p: ^mmt$segment_descriptor,
0 6161     sdtx_entry_p: ^mmt$segment_descriptor_extended,
0 6162     segnum: ost$segment,
0 6163     xcb_p: ^ost$execution_control_block;
0 6164
0 6165     segnum := #SEGMENT (pva_p^);
4 6166
4 6167     xcb_p := #ADDRESS (1, osc$segnum_job_fixed_heap, #READ_REGISTER (osc$pr_base_constant));
18 6168     sdt_entry_p := mmp$get_sdt_entry_p (xcb_p, segnum);
18 6169     sdtx_entry_p := mmp$get_sdtx_entry_p (xcb_p, segnum);
18 6170     mmp$reverify_access := [segnum <= xcb_p^ .xp.segment_table_length] AND
A6 6171     [sdt_entry_p^ .ste.v1 <> osc$V1_invalid_entry] AND [sdtx_entry_p^ .access_state <>
A6 6172     mmc$as_terminate_access];
A6 6173   FUNCEND mmp$reverify_access;
0 6174

```

MMP\$SET_ACCESS_SELECTIONS

```

0 6176 {
0 6177 { The purpose of this request is to change the segment access selections
0 6178 { associated with a segment. Three access selections are currently supported
0 6179 { for access to a segment: sequential, random, and read_tu. Sequential access
0 6180 { should be selected if access to the segment is sequential and more than 4
0 6181 { pages will be accessed sequentially. Sequential access should NOT be selected
0 6182 { for segments that are managed via ADVISE requests. Read_tu access should be
0 6183 { selected if the task is randomly accessing large blocks of data in the
0 6184 { segment, or if the task is sequentially accessing multiple parts of the
0 6185 { segment. Random access should be selected otherwise.
0 6186 {
0 6187 { An access selection of sequential causes memory manager to read an entire
0 6188 { transfer unit when a page fault occurs for a page on disk. In addition
0 6189 { pages assigned to the segment may be automatically removed when new
0 6190 { pages are added as a result of a page fault.
0 6191 {
0 6192 { An access selection of read_tu causes memory manager to read multiple pages,
0 6193 { one or more transfer units, when a page fault occurs for a page on disk. In
0 6194 { addition pages assigned to the segment are not automatically removed when new
0 6195 { pages are added as a result of a page fault.
0 6196 {
0 6197 { An access selection of random causes memory manager to read one page for each
0 6198 { page fault for a page on disk. No pages pages are removed as a result of the
0 6199 { page fault.
0 6200 {
0 6201 {           MMP$SET_ACCESS_SELECTIONS (PVA, ACCESS_SELECTIONS, STATUS)
0 6202 {
0 6203 {           PVA: (input) This parameter specifies the segment.
0 6204 {
0 6205 {           ACCESS_SELECTIONS: (input) This parameter specifies the access
0 6206 {           selections for the segment.
0 6207 {
0 6208 {           STATUS: (output) This parameter specifies the request status.
0 6209 {           The possible error codes are:
0 6210 {           dme$unable_to_locate_fde
0 6211 {           dme$unable_to_get_fd_lock
0 6212 {           mme$invalid_ring_brackets
0 6213 {           mme$ring_violation
0 6214 {           mme$segment_number_not_in_use
0 6215 {           mme$segment_number_too_big
0 6216 {           mme$segment_origin_change
0 6217 {
0 6218 {
0 6219 { PROCEDURE [XDCL, #GATE] mmp$set_access_selections
0 6220 {     ( pva: %cell;
0 6221 {       access_selections: mmt$access_selections;
0 6222 {       VAR status: ost$status);
0 6223 {
0 6224 {     VAR
0 6225 {       caller_id: ost$caller_identifier,
0 6226 {       r1_status: ost$status,
0 6227 {       validating_ring_number: ost$valid_ring;
0 6228 {
0 6229 {     #KEYPOINT (osk$entry, osk$m * #SEGMENT (pva), mmk$set_access_selections);
1E 6230
1E 6231 {     status.normal := TRUE;

```

MMP\$SET_ACCESS_SELECTIONS

```

1E 6232 { #CALLER_ID (caller_id);
1E 6233 { determine_validating_ring_num (caller_id.ring, #RING (pva), validating_ring_number);
48 6234 { mmp$set_access_selections_r1 (#SEGMENT (pva), validating_ring_number, access_selections, r1_status);
7A 6235 { IF NOT r1_status.normal THEN
82 6236 {   osp$set_status_condition (r1_status.condition, status);
94 6237 {   IFEND;
94 6238 {
94 6239 {   #KEYPOINT (osk$exit, 0, mmk$set_access_selections);
98 6240 {
98 6241 { PROCEND mmp$set_access_selections;

```

MMP\$SET_SEGMENT_LENGTH

```

0 6244 {
0 6245 { The purpose of this procedure is to explicitly set the segment length.
0 6246 { Segment length is defined as the last byte in the segment that can be
0 6247 { referenced, references beyond segment length are treated the same as a
0 6248 { read beyond EDI.
0 6249 {
0 6250 {     MMP$SET_SEGMENT_LENGTH (PVA, VALIDATION_RING_NUMBER, SEGMENT_LENGTH,
0 6251 {     STATUS)
0 6252 {
0 6253 { PVA: (input) This parameter specifies the segment for which segment length
0 6254 {     is being set.
0 6255 {
0 6256 { VALIDATION_RING_NUMBER: (input) This parameter specifies the ring number
0 6257 {     used for validation if it is greater than the caller ring number.
0 6258 {
0 6259 { SEGMENT_LENGTH: (input) This parameter specifies the new segment length.
0 6260 {
0 6261 { STATUS: (output) This parameter is where the request status is returned
0 6262 {     to the caller. The possible error codes are:
0 6263 {     mme$caller_not_in_write_bracket
0 6264 {     mme$invalid_pva
0 6265 {     mme$no_write_access
0 6266 {     mme$ref_to_unrecovered_file
0 6267 {     mme$segment_number_not_in_use
0 6268 {     mme$segment_number_too_big
0 6269 {
0 6270 {
0 6271 { PROCEDURE [XDCL, #GATE] mmp$set_segment_length
0 6272 {     (
0 6273 {         pva: ^cell;
0 6274 {         validation_ring_number: ost$valid_ring;
0 6275 {         segment_length: ost$segment_length;
0 6276 {         VAR status: ost$status);
0 6277 {
0 6278 {     VAR
0 6279 {         caller_id: ost$caller_identifier,
0 6280 {         r1_status: ost$status,
0 6281 {         segment_pointer: ^cell,
0 6282 {         validating_ring_number: ost$valid_ring;
0 6283 {
0 6284 {     #KEYPOINT (osk$entry, 0, mmk$set_segment_length);
0 6285 {     #CALLER_ID (caller_id);
0 6286 {     status_normal := TRUE;
0 6287 {     determine_validating_ring_num (caller_id.ring, validation_ring_number, validating_ring_number);
0 6288 {     segment_pointer := pva;
0 6289 {     mmp$set_segment_length_r1 [#SEGMENT (segment_pointer), validating_ring_number, segment_length, r1_status];
0 6290 {     IF NOT r1_status.normal THEN
0 6291 {         osp$set_status_condition (r1_status.condition, status);
0 6292 {     IFEND;
0 6293 {     #KEYPOINT (osk$exit, osk$m * #SEGMENT (pva), mmk$set_segment_length);
0 6294 {
0 6295 { PROCEND mmp$set_segment_length;

```

MMP\$STORE_SEGMENT_ATTRIBUTES

```

0 6297 {
0 6298 { The purpose of this request is to allow a user to change
0 6299 { attributes of a segment. Some attribute changes (such as
0 6300 { changing a segment attribute to BINDING) are rejected unless
0 6301 { the calling procedure is running within a certain ring bracket.
0 6302 {
0 6303 {     MMP$STORE_SEGMENT_ATTRIBUTES (PVA, VALIDATION_RING_NUMBER,
0 6304 {     SEG_ATTRIBUTES, STATUS)
0 6305 {
0 6306 { PVA: (input) This parameter specifies the segment to be changed.
0 6307 {
0 6308 { VALIDATION_RING_NUMBER: (input) This parameter specifies the ring
0 6309 {     of execution for whom the change is being made. The
0 6310 {     ring number used for validation is the max of caller ring
0 6311 {     number and 'validation_ring_number'.
0 6312 {
0 6313 { SEG_ATTRIBUTES: (input) This parameter is an adaptable array of
0 6314 {     segment attribute descriptors. Each attribute descriptor specifies
0 6315 {     a new value for a segment attribute.
0 6316 {
0 6317 { STATUS: (output) This parameter specifies the request status.
0 6318 {     The possible error codes are:
0 6319 {     dme$unable_to_get_fd_lock
0 6320 {     dme$unable_to_locate_fde
0 6321 {     mme$address_not_0_mod_16384
0 6322 {     mme$asid_specified
0 6323 {     mme$binding_attribute_invalid
0 6324 {     mme$execute_global_invalid
0 6325 {     mme$execute_local_invalid
0 6326 {     mme$invalid_asid_specified
0 6327 {     mme$invalid_ring_brackets
0 6328 {     mme$invalid_shadow_segment
0 6329 {     mme$length_not_0_mod_16384
0 6330 {     mme$ring_violation
0 6331 {     mme$segment_number_is_in_use
0 6332 {     mme$segment_number_not_in_use
0 6333 {     mme$segment_number_too_big
0 6334 {     mme$segment_origin_change
0 6335 {     mme$segment_origin_invalid
0 6336 {     mme$set_unmodifiable_attribute
0 6337 {     mme$software_attribute_invalid
0 6338 {     mme$unsupported_keyword
0 6339 {
0 6340 {
0 6341 {
0 6342 { PROCEDURE [XDCL, #GATE] mmp$store_segment_attributes
0 6343 {     (
0 6344 {         pva: ^cell;
0 6345 {         validation_ring_number: ost$valid_ring;
0 6346 {         attr: array [ * ] of mmt$attribute_descriptor;
0 6347 {         VAR status: ost$status);
0 6348 {
0 6349 {     VAR
0 6350 {         caller_id: ost$caller_identifier,
0 6351 {         r1_status: ost$status,
0 6352 {         r1_segment_attributes_p: ^array [ * ] of mmt$attribute_descriptor;

```

MMP\$STORE_SEGMENT_ATTRIBUTES

```

0 6353 #KEYPOINT (osk$exit, 0, mmk$store_segment_attributes);
10 6354
10 6355 status.normal := TRUE;
10 6356 #CALLER_ID (caller_id);
10 6357 PUSH r1_segment_attributes_p: [LOWERBOUND (attr) .. UPPERBOUND (attr)];
54 6358 r1_segment_attributes_p^ := attr;
9A 6359 mmp$store_segment_attributes_r1 (#SEGMENT (pva), caller_id.ring, r1_segment_attributes_p, r1_status);
CC 6360 IF NOT r1_status.normal THEN
D4 6361 osp$set_status_condition (r1_status.condition, status);
E6 6362 IFEND;
E6 6363
E6 6364 #KEYPOINT (osk$exit, 0, mmk$store_segment_attributes);
EA 6365
EA 6366 PROCEND mmp$store_segment_attributes;

```

MMP\$TERMINATE_SHADOWING

```

0 6369 {
0 6370 { The purpose of this request is to terminate shadowing of files.
0 6371 { o If required (UPDATE parameter set to true), a request will be issued to
0 6372 { update
0 6373 { the PASSIVE file with the ACTIVE .
0 6374 { o A request to mmp$terminate_shadowing_r1 will be issued to destroy the
0 6375 { ACTIVE
0 6376 { file and to activate the PASSIVE file again.
0 6377 {
0 6378 { MMP$TERMINATE_SHADOWING (POINTER, UPDATE, STATUS)
0 6379 {
0 6380 { POINTER: (input) This parameter specifies the process virtual
0 6381 { address assigned to the ACTIVE segment.
0 6382 {
0 6383 { UPDATE: (input) This boolean parameter specifies if an update
0 6384 { (PASSIVE with ACTIVE) is required or not.
0 6385 {
0 6386 { STATUS: (output) This parameter specifies the request status.
0 6387 {
0 6388 {
0 6389 PROCEDURE [XDCL, #GATE] mmp$terminate_shadowing
0 6390 { segment_p: Acell;
0 6391 { update: boolean;
0 6392 { VAR status: ost$status;
0 6393 {
0 6394 { VAR
0 6395 { access_selections: mmt$access_selections,
0 6396 { dest_p: mmt$segment_pointer,
0 6397 { r1_status: ost$status;
0 6398 {
0 6399 #KEYPOINT (osk$entry, #SEGMENT (segment_p) * osk$m, mmk$terminate_shadowing);
16 6400 status.normal := TRUE;
16 6401
16 6402 { Determine if update (PASSIVE with ACTIVE) is required.
16 6403 {
16 6404 { IF update THEN
28 6405 { update_passive_with_active (segment_p, status);
38 6406 { IFEND;
38 6407 {
38 6408 { mmp$terminate_shadowing_r1 (#SEGMENT (segment_p), r1_status);
5A 6409 { IF NOT r1_status.normal THEN
62 6410 { osp$set_status_condition (r1_status.condition, status);
74 6411 { IFEND;
74 6412 { #KEYPOINT (osk$exit, 0, mmk$terminate_shadowing);
78 6413 {
78 6414 PROCEND mmp$terminate_shadowing;

```

MMP\$VERIFY_ACCESS

```

O 8417 [
O 8418 [ This request can be used to determine whether a PVA
O 8419 [ can be accessed by a program without causing an access
O 8420 [ violation or segment fault.
O 8421 [
O 8422 [ MMP$VERIFY_ACCESS (PVA_P, ACCESS_MODE): BOOLEAN
O 8423 [
O 8424 [ PVA_P: (input) This parameter is a pointer to the PVA to be
O 8425 [ tested.
O 8426 [
O 8427 [ ACCESS_MODE: (input) This parameter specifies the type of
O 8428 [ access to be tested.
O 8429 [
O 8430 [ BOOLEAN: (output) The boolean result of this function specifies
O 8431 [ whether access is valid.
O 8432 [
O 8433 [
O 8434 [ FUNCTION [XDCL, #GATE] mmp$verify_access
O 8435 [ (
O 8436 [ pva_p: ^Acell;
O 8437 [ access_mode: mmt$va_access_mode): boolean;
O 8438 [
O 8439 [ TYPE
O 8440 [ external_code_base_pointer = packed record
O 8441 [ fill: 0 .. 0ff(16),
O 8442 [ vmid: 0 .. 0ff(16),
O 8443 [ xp: boolean,
O 8444 [ fill12: 0 .. 7,
O 8445 [ r3: 0 .. 15,
O 8446 [ code_pva: ost$pva,
O 8447 [ fill13: 0 .. 0fff(16),
O 8448 [ binding_pva: ost$pva,
O 8449 [ recend;
O 8450 [
O 8451 [ VAR
O 8452 [ caller_id: ost$caller_identifier,
O 8453 [ code_pva: ost$pva,
O 8454 [ pointer: record
O 8455 [ case (pva, code_pointer, str) of
O 8456 [ = pva =
O 8457 [ pva: ost$pva,
O 8458 [ = code_pointer =
O 8459 [ cbp_p: ^external_code_base_pointer,
O 8460 [ static_link: ost$pva,
O 8461 [ = str =
O 8462 [ s: string (12),
O 8463 [ casend,
O 8464 [ recend,
O 8465 [ ptr: record
O 8466 [ case 0 .. 1 of
O 8467 [ = 0 =
O 8468 [ pva_p: ^Acell,
O 8469 [ = 1 =
O 8470 [ s_p: ^string (12),
O 8471 [ casend,
O 8472 [ recend,
O 8473 [ ref_r: ost$ring,

```

MMP\$VERIFY_ACCESS

```

O 8473 [ sd_p: ^mmt$segment_descriptor,
O 8474 [ sdtx_p: ^mmt$segment_descriptor_extended,
O 8475 [ segnum: ost$segment,
O 8476 [ status: ost$status,
O 8477 [ xcb_p: ^ost$execution_control_block;
O 8478 [
O 8479 [ mmp$verify_access := TRUE;
O 8480 [ ptr.pva_p := pva_p;
O 8481 [ pointer.s := ptr.s_p;
O 8482 [ segnum := pointer.pva_seg;
O 8483 [ #KEYPOINT (ost$entry, segnum * ost$m, mmt$verify_access);
O 8484 [
O 8485 [ /verify_access/
O 8486 [ BEGIN
O 8487 [ xcb_p := #ADDRESS (1, osc$segnum_job_fixed_heap, #READ_REGISTER (osc$pr_base_constant));
O 8488 [ IF Segnum > xcb.p^xp.segment_table_length THEN
O 8489 [ mmp$verify_access := FALSE;
O 8490 [ EXIT /verify_access/;
O 8491 [ IFEND;
O 8492 [ sd_p := mmp$get_sdt_entry_p (xcb_p, segnum);
O 8493 [ sdtx_p := mmp$get_sdtx_entry_p (xcb_p, segnum);
O 8494 [ IF sd_p^ste.v1 = osc$vl_invalid_entry THEN
O 8495 [ mmp$verify_access := FALSE;
O 8496 [ EXIT /verify_access/;
O 8497 [ IFEND;
O 8498 [ IF (sdtx_p^access_state = mmc$zas_terminate_access) THEN
O 8499 [ mmp$verify_access := FALSE;
O 8500 [ EXIT /verify_access/;
O 8501 [ IFEND;
O 8502 [
O 8503 [
O 8504 [ #CALLER_ID (caller_id);
O 8505 [ ref_r := pointer.pva_ring;
O 8506 [ IF ref_r = 0 THEN
O 8507 [ mmp$verify_access := FALSE;
O 8508 [ EXIT /verify_access/;
O 8509 [ IFEND;
O 8510 [
O 8511 [
O 8512 [ Move pointer being verified to a local variable so that ring number checking will be
O 8513 [ valid on recursive calls.
O 8514 [
O 8515 [
O 8516 [ IF caller_id.ring > ref_r THEN
O 8517 [ ref_r := caller_id.ring;
O 8518 [ pointer.pva_ring := caller_id.ring;
O 8519 [ IFEND;
O 8520 [
O 8521 [ CASE access_mode OF
O 8522 [ = mmc$va_read =
O 8523 [ mmp$verify_access := (ref_r <= sd_p^ste.r2) AND (sd_p^ste.rp <> osc$non_readable);
O 8524 [ = mmc$va_write =
O 8525 [ mmp$verify_access := (ref_r <= sd_p^ste.r1) AND (sd_p^ste.wp <> osc$non_writable);
O 8526 [ = mmc$va_read_write =
O 8527 [ mmp$verify_access := (ref_r <= sd_p^ste.r1) AND (sd_p^ste.wp <> osc$non_writable) AND
O 8528 [ (sd_p^ste.rp <> osc$non_readable);

```

MMP\$VERIFY_ACCESS

```

18C 6529      = mmc$va_execute =
18C 6530      mmp$verify_access := (ref_r >= sd_p^ste.r1) AND (ref_r <= sd_p^ste.r2) AND
1F4 6531      (sd_p^ste.xp <> osc$non_executable);
1F4 6532      = mmc$va_read_execute =
1F4 6533      mmp$verify_access := mmp$verify_access (#LOC (pointer.pva), mmc$va_execute) AND
230 6534      mmp$verify_access (#LOC (pointer.pva), mmc$va_read);
230 6535      = mmc$va_binding =
230 6536      mmp$verify_access := sd_p^ste.rp = osc$binding_segment;
246 6537      = mmc$va_pointer_to_procedure =
246 6538
246 6539 { To verify a pointer to procedure the following must be checked:
246 6540 { . The procedure pointer must be in a segment with read access.
246 6541 { . The static link pointer, code base pointer, code PVA, or binding PVA must not have a ring number
246 6542 { equal to zero.
246 6543 { . The code base pointer must be in a segment with the "binding" attribute and be in a ring
246 6544 { readable segment.
246 6545 { . The caller must be within the call bracket.
246 6546 { . The code PVA in the code base pointer must be in a segment with "execute" privilege.
246 6547 { . The binding PVA in the code base pointer must be in a segment with the "binding" attribute
246 6548 { if this is a two word external code base pointer.
246 6549
246 6550      IF mmp$verify_access (#LOC (pointer.pva), mmc$va_read) = FALSE THEN
266 6551      mmp$verify_access := FALSE;
266 6552      EXIT /verify_access/;
26E 6553      IFEND;
26E 6554
26E 6555      IF pointer.static_link.ring = 0 THEN
27A 6556      mmp$verify_access := FALSE;
27A 6557      EXIT /verify_access/;
282 6558      IFEND;
282 6559
282 6560      IF pointer.cbp_p^code_pva.ring = 0 THEN
292 6561      mmp$verify_access := FALSE;
292 6562      EXIT /verify_access/;
29A 6563      IFEND;
29A 6564
29A 6565      pointer.pva.ring := ref_r;
29A 6566      IF (mmp$verify_access (#LOC (pointer.pva), mmc$va_binding) AND mmp$verify_access (#LOC (pointer.pva),
2D8 6567      mmc$va_read)) = FALSE THEN
2D8 6568      mmp$verify_access := FALSE;
2D8 6569      EXIT /verify_access/;
2E0 6570      IFEND;
2E0 6571
2E0 6572      IF ref_r > pointer.cbp_p^r3 THEN
2F0 6573      mmp$verify_access := FALSE;
2F0 6574      EXIT /verify_access/;
2F8 6575      IFEND;
2F8 6576
2F8 6577 { The caller is within the call bracket so the call is possible. The ring of execution will be r2 from
2F8 6578 { the code pva segment descriptor if it is greater than the caller's ring number, if not the caller's
2F8 6579 { ring number is the ring of execution. The ring of execution is used as the ring number in
2F8 6580 { validating the code pva and the new binding pva if there is one.
2F8 6581
2F8 6582      code_pva := pointer.cbp_p^code_pva;
2F8 6583      IF ref_r > sd_p^ste.r2 THEN
310 6584      code_pva.ring := sd_p^ste.r2;

```

MMP\$VERIFY_ACCESS

```

310 6585      ref_r := sd_p^ste.r2;
328 6586      ELSE
328 6587      code_pva.ring := ref_r;
334 6588      IFEND;
334 6589
334 6590      IF mmp$verify_access (#LOC (code_pva), mmc$va_execute) = FALSE THEN
352 6591      mmp$verify_access := FALSE;
352 6592      EXIT /verify_access/;
35A 6593      IFEND;
35A 6594
35A 6595      IF pointer.cbp_p^xp = TRUE THEN
368 6596      IF pointer.cbp_p^binding_pva.ring = 0 THEN
374 6597      mmp$verify_access := FALSE;
374 6598      EXIT /verify_access/;
37C 6599      IFEND;
37C 6600      ELSE
380 6601      mmp$verify_access := FALSE;
384 6602      CASEND;
384 6603      END /verify_access/;
384 6604
384 6605      #KEYPOINT (osk$exit, 0, mmp$verify_access);
388 6607
388 6608      FUNCEND mmp$verify_access;

```

MMP\$VOLUME_UNAVAILABLE_FLAG_HDL

```
0 6611
0 6612 PROCEDURE [XDCL] mmp$volume_unavailable_flag_hdl
4 6613 ( flag_id: ost$system_flag);
4 6614
4 6615 bap$exit_fap_on_condition (mme$volume_unavailable);
1C 6616 osp$wait_for_unavailable_volume (mme$volume_unavailable);
30 6617
30 6618 PROCEND mmp$volume_unavailable_flag_hdl;
```

MMP\$WAIT_FOR_UNAVAILABLE_VOLUME

```
0 6620
0 6621 PROCEDURE [XDCL, #GATE] osp$wait_for_unavailable_volume
C 6622 ( condition: ost$status_condition_code);
C 6623
C 6624 osp$verify_system_privilege;
42 6625
42 6626 osp$wait_on_condition (condition);
56 6627
56 6628 PROCEND osp$wait_for_unavailable_volume;
0 6630 MODEND mmm$segment_manager_job_temp;
```

**** I=\$05578173AS0102D19890821T183254 L=Z2XKLIST B=LGO DA=NONE LO=R RC=NONE OPT=SCHED EL=F LF=CS612 PAD=0

**** NO DIAGNOSTICS

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
access_control	2185	5369/M
access_control	2376	5369
access_mode	6436	6521
access_selections	4586	4617/M 4619/M 4621/M 4718/P
access_selections	4996	5025
access_selections	5315	5412
access_selections	6221	6234/P
access_state	2931	6171 6498
addr	391	4685 4707 4708 4714
addr_list	5771	5786 5786 5792/M
addr_returned	4588	4672/P 4678 4679 4714/S 4714/S
addr_returned	5772	5794/M
address_list	4567	4672/P 4684 4685 4707 4708 4709/P 4710/P 4711/P
		4714 4714
amc\$cell_pointer	9	510 5010 5029 5395 5416
amc\$file_byte_limit	1185	1188 1190
amc\$heap_pointer	9	15
amc\$sequence_pointer	10	17 5012 5031 5397 5421
amt\$file_byte_address	1188	1149 4133 5623 5624
amt\$file_limit	1190	1153
amt\$local_file_name	3788	3777
amt\$pointer_kind	9	12 4995 5314
amt\$segment_pointer	11	4795 4798 4803 4997 5316 5480 5582
attr	6345	6357 6357 6358
attr_p	6000	6021 6023 6023 6024
bad_offset	4259	4277
bad_offset	6621	6624
bap\$exit_fap_on_condition	3742	5660 6615
binding_pva	6447	6596
byte	5320	5451/M
caller_id	4748	4753 4754/P
caller_id	4802	4808 4809/P
caller_id	4843	4847 4849/P
caller_id	4888	4892 4895/P
caller_id	4930	4933 4935/P
caller_id	5001	5007 5017
caller_id	5104	5110 5113/P
caller_id	5170	5185 5186
caller_id	5235	5240 5242
caller_id	5321	5402 5404
caller_id	5484	5488 5494/P
caller_id	5541	5545 5548/P
caller_id	5586	5589 5591/P
caller_id	5746	5754 5757/P
caller_id	5777	5785 5787/P
caller_id	5833	5841 5842/P
caller_id	5872	5878 5890/P
caller_id	5918	5924
caller_id	6008	6014 6017/P
caller_id	6047	6049 6050/P
caller_id	6084	6092 6093/P

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
caller_id	6225	6232 6233/P
caller_id	6278	6284 6286/P
caller_id	6349	6356 6359/P
caller_id	6451	6504 6516 6517 6518
caller_ring	4564	4568 4569
caller_ring	4742	4754 4754
caller_ring	4794	4809 4809
caller_ring	4882	4895 4895
caller_ring	5095	5113 5113
caller_ring	5535	5548 5548
caller_ring	5826	5842 5842
caller_ring	5998	6017 6017
caller_ring	6219	6233 6233
caller_ring	6271	6286 6286
cbp_p	6458	6560 6572 6582 6595 6596
cell_pointer	14	4810/P 4814/M 4814 4815 5030/M 5420/M 5434/P 5489
cell_pointer	2230	5492 5494/P 5497/M 5591/P 5594/M 5494 4708 4893 4896/P 4901/M 4935/P 4939/M 5026/P 5030 5413/P 5417 5419 5420 5492/M 5546 5549/P
		5552/M 6033
code_pointer	6454	6457
code_pva	6445	6560 6582
code_pva	6452	6582/M 6584/M 6587/M 6590/P
condition	2292	4227/M 4757/P 4817/P 4817/M 4851/P 4851/M 4899/P 4899/M 4937/P 4937/M 5037/P 5037/M 5126/P 5126/M 5196/P 5196/M 5254/P 5254/M 5456/P 5456/M 5499/P 5499/M 5554/P 5554/M 5596/P 5596/M 5642 5655 5655 5656 5709/P 5709/M 5845/P 5845/M 5892/P 5892/M 5939/P 5939/M 6029/P 6029/M 6096/P 6096/M 6125/P 6125/M 6236/P 6236/M 6290/P 6290/M 6361/P 6361/M 6410/P 6410/M
condition	4222	4227
condition	4742	4757
condition	4794	4817
condition	4838	4851
condition	4882	4899
condition	4924	4937
condition	4994	5037
condition	5095	5126
condition	5181	5196
condition	5227	5254
condition	5312	5456
condition	5479	5499
condition	5535	5554
condition	5581	5596
condition	5691	5709
condition	5740	5759
condition	5768	5790
condition	5826	5845
condition	5867	5892
condition	5913	5939
condition	5998	6029
condition	6075	6096

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IDENTIFIER-----	DEFINED-----	REFERENCES										
	ON LINE											
condition	6105	6125										
condition	6219	6236										
condition	6271	6290										
condition	6342	6361										
condition	6389	6410										
condition	6622	6626/P										
contiguous_flag	5322	5342/M	5378/M	5433	5444							
contiguous_real_memory	2379	5377										
conv_ptr	5171	5182/M	5191									
dest	3842	3852										
dest	4578	4655	4709									
dest	4590	4654/M	4655/P	4656/P	4708/M	4709/P	4711/P					
dest_p	4588	4630/P	4654	4708	4719/P							
determine_validating_ring_num	4563	4674	4754	4809	4895	5113	5548	5842	6017			
		6233	6286									
dfc\$min_cdcnet_errors	320	326	328	331	334	337	340	343	346			
		348	352	355	375							
dfc\$min_driver_test_errors	315	363	366	369	372							
dfc\$min_ecc	25	31	34	37	40	43	47	51	54			
		57	60	63	66	69	72	75	78			
		81	84	88	91	94	98	101	104			
		107	110	113	116	119	122	125	128			
		131	134	137	141	145	148	151	155			
		158	161	164	167	170	173	176	179			
		183	186	190	193	196	199	202	205			
		208	212	216	220	223	226	229	233			
		237	240	243	246	249	252	256	259			
		263	266	269	272	275	278	282	285			
		288	292	296	301	304	308	315	317			
		320	322									
dfc\$min_mm_recovery_errors	322	378	382	385								
dfe\$family_not_served	190	5655										
dfe\$server_has_terminated	173	5656										
dfe\$server_not_active	122	5655										
dm_element_length	4591	4684/M	4687	4687	4699/M	4699						
dm_element_offset	4592	4685/M	4695	4695	4698/M	4698						
dmc\$device_manager_error_code	475	476	479	482	485	488	491	494	497			
		500	503	506	509	512	515	518	521			
		524	527	530	533	536	539	542	545			
		548	551	554	557	560	563	566	569			
		572	575	578	581	584	587	590	593			
		596	599	602	605	608	611	614	617			
		620	623	626	629	632	635	638	641			
		644	647	650	653	656	659	662	665			
		668	671	674	677	680	683	686	689			
		692	695	698	701	704	707	710	713			
		716	719	722	725	728	731	734	737			
		740	743	746	749	752	755	758	761			
		764	767	770	773	776	779	782	785			
		788	791	794	797	800	803	806	809			
		812	815	818	821	824	827	830	833			
		836	839	842	845	848	851	854	857			

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES										
	ON LINE											
		860	863	866	869	872	875	878	881			
		884	887	890	893	896	899	902	905			
		908	911	914	917	920	923	926	929			
		932	935	938	941	944	947	950	953			
		956	959	962	965	968	971	974	977			
		980	983	986	989	992	995	998	1001			
		1004	1007	1010	1013	1016	1019	1022	1025			
		1028	1031	1034	1037	1040	1043	1046	1049			
		1055	1058	1061	1064	1067	1070	1073	1076			
		1079	1082	1086	1089	1092	1097	1100	1103			
		1106	1109	1112	1115	1118	1121	1124				
dme\$unable_to_alloc_all_space	962	5642	5660/P	5668/P								
dmp\$get_initialized_addresses	3755	4672										
dmt\$addr_length_pair	390	3757	3973	4587	5771	5779						
dmt\$chapter_number	3796	3778										
dmt\$system_file_id	3769	3755										
dpp\$put_critical_message	3748	5664										
dsp\$system_committed	3773	5663										
external_code_base_pointer	6439	6458										
fde_p	3800	3815/M	3816	3817/M								
fde_p	4578	4667/M	4667	4667/M								
fde_p	4593	4667/P	4668									
file	5768	5787/P										
file_entry_index	1495	3815	4667									
file_hash	1146	3816	4667									
file_hash	1497	3816	4667									
file_limits_to_enforce	2344	5018/M	5115/M	5187/M	5243/M	5405/M	6019/M					
file_limits_to_enforce	6003	6019										
file_name	6039	6050/P										
fmp\$In_open_chapter	3777	6050										
get_list_of_addresses	4670	4670	4716									
gfc\$fde_size	3832	3815	4667									
gfc\$fde_table_base	3830	3815	3831	4667								
gfc\$fk_catalog	1225	1237										
gfc\$fk_job_local_file	1227	1236										
gfc\$fm_mass_storage_file	1240	1162	4668									
gfc\$fm_served_file	1241	1165										
gfc\$tr_job	1505	3809	4667									
gfc\$tr_system	1505	3808	4667									
gfp\$get_fde_p	3799	3820	4667									
gft\$allocation_unit_size	1196	1151										
gft\$attach_count	1201	1142										
gft\$fde_flags	1171	1139	1143									
gft\$file_desc_entry_p	3837	3800	4593									
gft\$file_descriptor_entry	1136	1128	1141	3837								
gft\$file_descriptor_index	1210	1495										
gft\$file_kind	1221	1145	1233									
gft\$file_media	1240	1161										
gft\$locked_file_desc_entry_p	1128	3943										
gft\$open_count	1270	1144	1286									

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
gft\$queue_status	1281	1154
gft\$segment_lock_info	1285	1147
gft\$signature_lock	1246	1137
gft\$system_file_identifier	1494	2346 2535 2932 3024 3769 3799 3951 4526
		5999
gft\$stable_residence	1505	1496
gft\$transfer_unit_size	1207	1152
gfv\$null_sf_id	4526	5021 5118 5193 5251 5408 5706/P
heap_pointer	16	5034/M 5430/M
heap_pointer	2234	5034 5427 5429 5430
i	4595	4652 4653/S 4654/S 4679 4684/S 4685/S 4707/S 4708/S
		4709/S 4710/S 4711/S
i	5325	5350 5351/S 5353/S 5354/S 5354/S 5355/S 5355/S 5357/S
		5358/S 5358/S 5360/S 5361/S 5361/S 5363 5365/S 5366/S
		5366/S 5368/S 5369/S 5369/S 5371/S 5372/S 5376/S 5376/S
		5377/S 5381 5383/S
i	5622	5678 5679/S
i	6111	6119 6120/S 6127 6128/S 6128/S
i#move	3841	3857 4655 4708
id	4262	4269 4273/S
id	6621	6624 6624/S
increment	5326	5448/M 5450 5450 5451/S 5452/M 5452
iot\$transfer_count	3098	3086
jmc\$highest_prio_age_interval	2780	2771 2781
jmc\$highest_service_accumulator	2812	2813
jmc\$highest_service_factor_valu	2864	2857
jmc\$keyword_offset_maximum	2797	2772
jmc\$kill_job_flag	3665	5671
jmc\$kill_maximum_entries	2745	2738 2739
jmc\$kol_maximum_entries	2755	2740
jmc\$logout_flag_id	3665	5671
jmc\$max_active_jobs	2736	2723 2731 2732
jmc\$max_ajl_ord	2737	2736
jmc\$max_dispatching_control	2681	2685
jmc\$max_dispatching_priority	2582	2542 2545 2546
jmc\$maximum_job_count	2752	2745
jmc\$maximum_output_count	2762	2755
jmc\$maximum_service_classes	2830	2833
jmc\$min_dispatching_control	2680	2684
jmc\$null_service_class	2823	2824
jmc\$priority_aging_interval_max	2771	2768
jmc\$priority_p1	2596	2543
jmc\$priority_p10	2605	2544
jmc\$priority_p14	2609	2544
jmc\$priority_p8	2603	2543
jmc\$reserved_ajls	2741	2736
jmc\$service_accumulator_maximum	2804	2801
jmc\$service_factor_value_max	2857	2854
jmc\$system_default_offset	2796	2797
jmc\$terminate_job_flag	3664	5670

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
jmc\$unlimited_offset	2793	2782 2814
jmt\$dispatching_control	2651	2634
jmt\$dispatching_control_index	2684	2651
jmt\$dispatching_controls	2654	2652
jmt\$dispatching_priority	2542	2507 2509 2656
jmt\$job_priority	2712	2643 2644 2645 2646
jmt\$maximum_active_jobs	2723	2628
jmt\$priority_aging_interval	2768	2636
jmt\$scheduling_priority	2642	2635
jmt\$service_accumulator	2801	2801
jmt\$service_class_index	2833	2626 2627
jmt\$service_class_name	2836	2619 2629
jmt\$service_factor_value	2854	2621 2622
jmt\$service_factors	2850	2630
jmt\$task_time_slice	2694	2674 2675
jmt\$time_slice_values	2673	2520 2658
keyword	2161	5190/M 5353/M 5357/M 5360/M 5365/M 5368/M 5371/M 5383/M
keyword	2368	5351
kind	12	5028/M 5415/M
kind	2228	5491/M
kind	5002	5011/M 5013/M 5015/M 5019
kind	5327	5396/M 5398/M 5400/M 5406
length	392	4684 4709/P 4710/P 4711/P 4714
length	3843	3850 3853/M 3853
length	4578	4655 4655/M 4655
list_overflow	4597	4668/M 4671 4671 4672/P
list_overflow	5773	5793/M
list_p	4598	4638 4639/P 4642 4645 4646 4648 4653 4654
		4695 4695 4720
local_status	4598	4719/P
local_status	5328	5441/P
max_length	2170	5361/M 5392/M
max_length	2372	5361
max_length_index	5329	5363/M 5392/S
max_length_specified	5330	5344/M 5362/M 5391
media	1161	4668
memory_list_index	4600	4685/M 4687 4687 4688/M 4695 4695/S 4695 4695/S
		4695/M 4696 4702
mmc\$	1509	1515 1518 1521 1524 1527 1530 1533 1537
		1540 1543 1546 1549 1552 1555 1559 1562
		1565 1568 1571 1574 1578 1581 1584 1587
		1590 1593 1596 1599 1602 1605 1608 1611
		1614 1617 1620 1623 1626 1629 1632 1635
		1638 1642 1645 1648 1651 1654 1657 1660
		1663 1666 1669 1672 1675 1678 1681 1684
		1687 1690 1693 1696 1699 1702 1706 1710
		1713 1717 1720 1723 1726 1729 1732 1735
		1738 1741 1744 1747 1750 1753 1756 1759
		1762 1765 1768 1771 1774 1778 1782 1785
		1788 1791 1794 1797 1800 1803 1806 1809

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IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

IDENTIFIER	DEFINED ON LINE	REFERENCES	REFERENCES	REFERENCES	REFERENCES	REFERENCES	REFERENCES	REFERENCES
		1812	1815	1818	1821	1824	1827	1830 1833
mmc\$as_random	2133	1836	1840	1843	1846			
mmc\$as_read_tu	2134	4621						
mmc\$as_sequential	2133	4617	4626/P	5025	5026/P	5412	5413/P	
mmc\$asign_active_null	2972	2973						
mmc\$cell_pointer	2224	2229	4630/P	5011	5396	5491		
mmc\$default_sdt_length	3737	3730						
mmc\$heap_pointer	2225	2233	5015	5400				
mmc\$kw_asid	2150	2186						
mmc\$kw_clear_space	2148	2173						
mmc\$kw_current_segment_length	2147	2167						
mmc\$kw_error_exit_procedure	2149	2177						
mmc\$kw_gl_key	2149	2171						
mmc\$kw_hardware_attributes	2151	2180						
mmc\$kw_inheritance	2151	2188						
mmc\$kw_max_segment_length	2148	2169	5360					
mmc\$kw_null_keyword	2146	5383						
mmc\$kw_preset_value	2150	2175	5365					
mmc\$kw_ps_transfer_size	2152	2196						
mmc\$kw_ring_numbers	2146	2162	5353					
mmc\$kw_segment_access_control	2150	2184	5368					
mmc\$kw_segment_number	2147	2165	5357					
mmc\$kw_shadow_segment	2152	2190	5190					
mmc\$kw_software_attributes	2149	2182						
mmc\$kw_wired_segment	2152	2193	5371					
mmc\$sa_free_behind	2212	4616						
mmc\$sa_read_transfer_unit	2212	4616	4618					
mmc\$sa_terminate_access	3011	6172	6498					
mmc\$segment_fault_processor_id	3441	3495						
mmc\$sequence_pointer	2224	2231	5013	5398				
mmc\$ssk_none	3051	3028						
mmc\$ssk_read_only_file	3051	5890/P						
mmc\$ssk_read_write_file	3051	5937/P						
mmc\$ssk_segment_number	3052	3026						
mmc\$ua_max_segment_length	2363	2371	5359					
mmc\$ua_null_keyword	2365	5382						
mmc\$ua_preset_value	2363	2373	5364					
mmc\$ua_ring_numbers	2365	2380	5352					
mmc\$ua_segment_access_control	2364	2375	5367					
mmc\$ua_segment_number	2362	2369	5356					
mmc\$ua_wired_segment	2364	2377	5370					
mmc\$va_binding	2391	6535	6566/P					
mmc\$va_execute	2390	6529	6533/P	6590/P				
mmc\$va_pointer_to_procedure	2390	6537						
mmc\$va_read	2389	6522	6534/P	6550/P	6567/P			
mmc\$va_read_execute	2390	6532						
mmc\$va_read_write	2389	6526						
mmc\$va_write	2389	6524						
mmc\$volume_unavailable	1778	6615/P	6616/P					
mmk\$wired_seg_length_too_large	1785	5373/P						
mmk\$close	1889	4893	4903					
mmk\$create_scratch_segment	1967	5008	5039					

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IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

IDENTIFIER	DEFINED ON LINE	REFERENCES	REFERENCES	REFERENCES	REFERENCES	REFERENCES	REFERENCES	REFERENCES
mmk\$create_segment	1922	5111	5130					
mmk\$create_shadow_segment	2011	5179	5200					
mmk\$delete_scratch_segment	1971	5489	5502					
mmk\$delete_segment	1918	5546	5556					
mmk\$fetch_seg_attributes	1900	5751	5764					
mmk\$get_segment_length	1955	5838	5849					
mmk\$initiate_shadowing	1987	5922	5941					
mmk\$job_base	2053	1854	1858	1862	1866	1870	1874	1878 1882
		1886	1889	1893	1897	1900	1904	1907 1910
		1914	1918	1922	1926	1929	1932	1935 1938
		1941	1944	1947	1951	1955	1959	1963 1967
		1971	1975	1979	1983	1987	1991	1995 1999
		2003	2007	2011	2015	2019	2023	
mmk\$open_file_segment	1893	6015	6033					
mmk\$set_access_selections	1963	6229	6239					
mmk\$set_segment_length	1959	6283	6292					
mmk\$store_segment_attributes	1910	6353	6364					
mmk\$terminate_shadowing	1991	6399	6412					
mmk\$verify_access	1914	6483	6606					
mmp\$advise_out	3859	4656	4710	4711				
mmp\$assign_contiguous_memory	3866	5434						
mmp\$build_segment	3874	5022	5124	5194	5252	5409	6027	
mmp\$change_seg_inheritance_r1	3883	4755						
mmp\$change_segment_inheritance	4742	4760						
mmp\$change_segment_number	4794	4820						
mmp\$change_segment_number_r1	3892	4810						
mmp\$change_stack_attribute	4838	4854						
mmp\$change_stack_attribute_r1	3902	4849						
mmp\$close_segment	4882	4719	4905					
mmp\$close_shared_stack	4924	4942						
mmp\$convert_ps_transfer_size	3912	3937						
mmp\$create_scratch_segment	4994	5041						
mmp\$create_segment	5095	5132						
mmp\$create_shadow_segment	5161	5202						
mmp\$create_shared_stack	5227	5259						
mmp\$create_user_segment	5312	5459						
mmp\$delete_scratch_segment	5479	5504						
mmp\$delete_segment	5535	5558						
mmp\$delete_user_segment	5581	5441	5599					
mmp\$failed_allocation_flag_hd1	5618	5688						
mmp\$fetch_offset_mod_pages_r1	3949	5706						
mmp\$fetch_offset_modified_pages	5691	4639	5715					
mmp\$fetch_segment_attributes	5740	5765						
mmp\$fetch_segment_attributes_r1	3960	5757						
mmp\$get_allocated_addresses	5768	5796						
mmp\$get_allocated_addresses_r1	3969	5787						
mmp\$get_page_size	3980	3985	5448					
mmp\$get_sdt_entry_p	4050	6168	6492					
mmp\$get_sdt_entry_p	4052	4054/M	6168/M	6492/M				
mmp\$get_sdt_x_entry_p	4089	6169	6493					
mmp\$get_sdt_x_entry_p	4091	4093/M	6169/M	6493/M				
mmp\$get_segment_length	5826	5851						
mmp\$get_segment_length_r1	4041	5843						

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IDENTIFIER	DEFINED	REFERENCES
	ON LINE	
mmp\$initiate_debug_shadowing	5867	5895
mmp\$initiate_shadowing	5913	5943
mmp\$initiate_shadowing_r1	4102	5890
mmp\$invalidate_segment	4111	5937 4896 4935 5494 5549 5591
mmp\$open_file_segment	5998	4630 6035
mmp\$open_segment	6038	6052
mmp\$preset_page_streaming	6075	6102
mmp\$preset_page_streaming_r1	4120	6093
mmp\$process_file_alloc	4132	5839
mmp\$reserve_segment_number	6105	6132
mmp\$reserve_segment_number_r1	4139	6123
mmp\$verify_access	6156	6173
mmp\$verify_access	6157	6170/M
mmp\$set_access_selections	6219	4626 4718 5026 5413 6241
mmp\$set_access_selections_r1	4148	6234
mmp\$set_segment_length	6271	6294
mmp\$set_segment_length_r1	4157	6288
mmp\$store_segment_attributes	6342	6366
mmp\$store_segment_attributes_r1	4166	6359
mmp\$terminate_shadowing	6389	6414
mmp\$terminate_shadowing_r1	4175	6408
mmp\$validate_segment_number	4183	4610
mmp\$verify_access	6434	6533 6534 6550 6566 6566 6590 6608
mmp\$verify_access	6436	6479/M 6489/M 6495/M 6499/M 6507/M 6523/M 6525/M 6527/M
		6530/M 6533/M 6536/M 6551/M 6556/M 6561/M 6568/M 6573/M
		6591/M 6597/M 6602/M
mmp\$volume_unavailable_flag_hd1	6612	6618
mmp\$write_modified_pages	4192	5882
mmt\$access_selections	2133	4151 4586 4996 5315 6221 6395
mmt\$ast_index	1303	1148 2883
mmt\$attribute_descriptor	2160	2347 3780 3963 4169 5096 5228 5335 5742
		5749 6000 6040 6345 6351
mmt\$attribute_keyword	2146	2161
mmt\$aoi_state	1312	1150
mmt\$hardware_attribute_set	2215	2181
mmt\$hardware_attributes	2203	2215
mmt\$lock_segment_status	2337	2937
mmt\$max_sdt	2893	2897
mmt\$max_sdtx	2961	2965
mmt\$sdtX_stream_data	2944	2940
mmt\$segment_access_condition	3468	3496
mmt\$segment_access_rights	3000	2936
mmt\$segment_access_state	3006	2931
mmt\$segment_attrib_descriptor	2342	3875 5005 5107 5176 5238 5336 6011
mmt\$segment_descriptor	2880	2890 2894 3941 4052 4185 4602 6160 6473
mmt\$segment_descriptor_extended	2929	2958 2962 3942 4091 4094 4186 4603 6161
		6169 6474 6493
mmt\$segment_inheritance	2242	2189 2933 3886 4744
mmt\$segment_pointer	2227	3782 3877 4589 4883 4925 5003 5099 5105
		5166 5174 5231 5236 5337 5485 5536 6004
		6009 6042 6396
mmt\$segment_pointer_kind	2224	2228 2345 3781 5002 5097 5165 5229 5327
		6001 6041

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IDENTIFIER	DEFINED	REFERENCES
	ON LINE	
mmt\$segment_reservation_state	3041	2934
mmt\$shadow_info	3021	2938
mmt\$shadow_reference_info	3055	2533
mmt\$shadow_segment_kind	3051	3025 4105
mmt\$software_attribute_set	2217	2183 2935 4615 4618
mmt\$software_attributes	2211	2217
mmt\$user_attribute_descriptor	2367	5313
mmt\$user_attribute_keyword	2362	2368
mmt\$va_access_mode	2389	6436
mmt\$xcb_page_wait_info	3066	2519
mmv\$file_allocation_interval	4533	5638
mtt\$monitor_interlock	1319	1138
nat\$received_message_descriptor	3082	3075 3084
nat\$received_message_list	3074	2501
new_allocated_length	5623	5639/P 5643 5643 5646
new_segment_pointer	4798	4813/M 4814/M
nlc\$cc_connect_confirm	3114	3105
nlc\$cc_connect_request	3113	3103
nlc\$cc_expedited_data	3119	3105
nlc\$cc_max_pdu_kind	3121	3124
nlc\$channel_connection_pdu	3137	3089
nlc\$channelnet_pdu	3137	3091
nltscc_pdu_kind	3124	3102
nltscc_seq#_or_connect_time	3101	3090
nltscc_sequence_number	3127	3106
nltsdevice_identifier	3134	3085
nlts pdu_type	3137	3088
normal	2290	4226/M 4611 4632 4641 4674 4752/M 4756 4757/M
		4807/M 4812 4817/M 4846/M 4850 4851/M 4894/M 4898
		4899/M 4934/M 4936 4937/M 5009/M 5024 5037/M 5112/M
		5125 5126/M 5180/M 5195 5196/M 5241/M 5253 5254/M
		5343/M 5411 5440 5456/M 5490/M 5496 5499/M 5647/M
		5551 5554/M 5590/M 5593 5596/M 5640 5642 5704/M
		5708 5709/M 5753/M 5758 5759/M 5784/M 5789 5790/M
		5839/M 5844 5845/M 5877/M 5883 5891 5892/M 5923/M
		5928 5938 6016/M 6028 6029/M 6091/M 6095
		6096/M 6117/M 6124 6125/M 6231/M 6235 6236/M 6285/M
		6289 6290/M 6355/M 6360 6361/M 6400/M 6409 6410/M
offset_list	4583	4598
offset_list	5694	5705
offsets_returned	4601	5711/M 4638 4640/P 4645 4648 4650 4652 4688
offsets_returned	5695	5712/M
osc\$base_exception	469	475
osc\$binding_segment	2283	6536
osc\$call_instruction	3332	3340
osc\$data_read	3331	3340
osc\$free_running_clock_maximum	1437	1434
osc\$invalid_ring	407	447
osc\$max_fault_contents	3508	3502
osc\$max_name_size	2840	2844
osc\$max_page_size	1425	1421 2847

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IDENTIFIER-----	DEFINED-----	REFERENCES
	ON LINE	
osc\$max_ring	406	447 448
osc\$max_segment_length	430	453 2941 2972
osc\$max_status_condition_code	2305	2301 2317
osc\$max_string_size	2321	2324 2327 2332 4211
osc\$max_tasks	1265	1262
osc\$maximum_offset	429	430 450 450 451 5882/P 5928/P
osc\$maximum_processor_id	3380	3376
osc\$maximum_segment	428	449
osc\$min_ecc	468	469
osc\$min_page_size	1424	1421
osc\$min_ring	405	448
osc\$non_executable	2279	6531
osc\$non_readable	2282	6523 6528
osc\$non_writable	2285	6525 6527
osc\$pr_base_constant	4003	4312 5630 6167 6487
osc\$pr_page_size_mask	4006	3983 5448
osc\$segnum_job_fixed_heap	4068	4311 5630 6167 6487
osc\$task_time_slice_maximum	2705	2708
osc\$vl_invalid_entry	2908	2708
osc\$wait	4205	6171 6494 5882/P 5928/P
osk\$base	2058	2395 2399 2403 2407 2411 2415 2419 2423 2427 2431 2435 2439 2443 2447 2452 2455 2458
osk\$entry	2090	4893 5008 5111 5179 5489 5546 5751 5838 5922 6015 6229 6283 6399 6483
osk\$exit	2091	4903 5039 5130 5200 5502 5556 5764 5849 5941 6033 6239 6292 6353 6364 6412 6606
osk\$m	2129	4893 5179 5489 5546 5751 5838 5922 6033 6229 6292 6399 6483
osk\$system_class	2104	2088 2089 2090 2091 2092 2093 2094
osp\$set_status_abnormal	4208	5373
osp\$set_status_condition	4221	4229 4757 4817 4851 4899 4937 5037 5126 5196 5254 5456 5499 5554 5596 5709 5759 5790 5845 5892 5939 6029 6096 6125 6236 6290 6361 6410
osp\$verify_system_privilege	4243	4285 6624
osp\$wait_for_unavailable_volume	6621	6616 6628
osp\$wait_on_condition	4287	5668 6626
ost\$asid	2260	2187 2256 2914
ost\$binary_unique_name	1333	1140
ost\$byte_count	2250	3660 4193
ost\$caller_identifier	2489	4262 4748 4802 4843 4888 4930 5001 5104 5170 5235 5321 5484 5541 5586 5746 5777 5833 5872 5918 6008 6047 6084 6225 6278 6349 6451
ost\$cp_time	3170	2518
ost\$cp_time_value	3168	2531 3171 3172
ost\$cs_lock	1444	2499
ost\$debug_code	3331	3319
ost\$debug_list	3327	3231
ost\$debug_list_entry	3318	3327
ost\$debug_mask	3337	3230
ost\$exchange_package	3180	2486

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IDENTIFIER-----	DEFINED-----	REFERENCES
	ON LINE	
ost\$execute_privilege	2279	2274 2909
ost\$execution_control_block	2485	2511 4051 4090 4299 5628 6163 6477
ost\$flags	3237	3187
ost\$frame_descriptor	3295	3310
ost\$free_running_clock	1434	1156 2517 2657
ost\$global_task_id	1256	1158 1249 2496
ost\$heap	4085	4546
ost\$key_lock	436	2172 2915
ost\$key_lock_value	442	439 2471 2473 3254 3256
ost\$keypoint_class	3269	3200 3271
ost\$keypoint_mask	3271	3203
ost\$minimum_save_area	3305	3192 3280 3489
ost\$monitor_condition	3141	3148
ost\$monitor_conditions	3148	3193 3197 3285 3564 3578
ost\$monitor_fault	3485	3434
ost\$monitor_fault_contents	3502	3498
ost\$name	2847	2620 2836 3527 3788 6039
ost\$register	3252	3181 3306 3556 3562
ost\$page_size	1421	1402 4540
ost\$paging_statistics	3358	2526
ost\$processor_id	3376	2489 3370
ost\$processor_id_set	3370	2488
ost\$processor_model_number	1351	1335
ost\$processor_serial_number	1429	1334
ost\$pva	458	3225 3243 3257 3486 3579 6445 6447 6452 6456 6459
ost\$read_privilege	2282	2275 2910
ost\$register_number	3248	3222 3291 3299 3300 3301 3256
ost\$string	447	459 2163 2164 2381 2382 2474 2912 2913 2930 3242 4564 6472
ost\$string_termination_reason	3388	2522
ost\$segment	449	460 1157 2166 2370 2475 3027 3220 3321 3884 3893 3894 3940 3950 3961 3970 4042 4052 4091 4112 4121 4141 4149 4158 4167 4175 4184 4604 4796 5177 5625 6107 6112 6162 6475
ost\$segment_access_control	2272	2185 2376
ost\$segment_descriptor	2907	2881
ost\$segment_length	453	392 2168 2170 2192 2194 2197 2372 2378 3668 4044 4160 4591 5164 5829 5834 6274
ost\$segment_offset	450	391 461 2257 2945 3322 3324 3756 3953 3972 4583 4592 4606 5163 5694 5700 5770
ost\$stack_frame_save_area	3279	3313 3522
ost\$status	2289	2179 3557 3750 3760 3783 3861 3869 3878 3887 3896 3905 3944 3955 3964 3976 4045 4106 4115 4127 4134 4142 4152 4161 4170 4176 4187 4195 4212 4223 4293 4318 4580 4589 4745 4749 4799 4804 4840 4844 4885 4889 4927 4931 4988 5004 5100 5106 5167 5175 5232 5237 5317 5328 5333 5481 5486 5538 5542 5583 5587 5626 5686 5702 5743 5748 5774 5782 5830 5835 5869 5874 5915 5920 6005 6010 6043 6081 6087 6108 6113

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IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

		6222	6226	6275	6279	6346	6350	6392	6397
		6476							
ost\$status_condition_code	2317	2282	2313	3743	4210	4222	4288	6622	
ost\$status_identifer	4217	4209							
ost\$string	2330	2293							
ost\$string_size	2324	2331							
ost\$system_flag	3663	3659	5619	6613					
ost\$system_privilege_map	3730	4255	4557	4557					
ost\$task_index	1262	1257	1296	1297					
ost\$task_time_slice	2708	2694							
ost\$top_of_stack_pointer	3240	3232							
ost\$strap_enable	3274	3189	3553						
ost\$user_condition	3151	3158							
ost\$user_conditions	3158	3191	3195	3283	3312	3525	3565		
ost\$valid_relative_pointer	456	1163	1166	2515	2516				
ost\$valid_ring	448	2343	3232	3779	3885	3895	3904	3962	3971
		4043	4104	4113	4122	4150	4159	4168	4565
		4566	4750	4797	4805	4884	4890	5098	5108
		5537	5543	5828	5836	5875	6002	6012	6227
		6273	6281	6344					
ost\$virtual_machine_identifer	3262	3183	3185	3307					
ost\$wait	4205	4194							
ost\$write_privilege	2285	2276	2911						
ost\$x_register	3249	3222	3291						
osv\$page_size	4540	3925	3925	4655/P	4656/P	4698	4699		
osv\$system_privilege_map	4255	4273							
osv\$system_privilege_map	6621	6624							
osv\$task_private_heap	4546	4638	4642	4646	4720				
p	4263	4277/M	4281/M						
p	5003	5022/P	5026/P	5030	5032	5034			
p	5485	5491/M	5492/M						
p	6621	6624/M	6624/M						
page_size	3980	3983/M							
page_size	5312	5448/M							
page_size	5331	5448/P	5452						
pmc\$account_log	4332	4340	4342						
pmc\$external_log_base_exception	4375	4378	4381	4385	4388	4392	4395	4398	4401
		4405	4409	4412	4416	4419	4423	4427	4430
		4434	4438	4441	4444	4447	4450	4454	4457
		4460	4463	4466	4469	4472	4475	4478	4481
		4484	4487	4490	4493	4496	4499	4502	4505
		4508							
pmc\$job_account_log	4332	4338	4344						
pmc\$job_log	4334	4336							
pmc\$job_statistic_log	4332	4344							
pmc\$kill_task_flag	3663	3679	5671						
pmc\$max_signal_contents	3646	3640							
pmc\$max_task_id	3401	3398							
pmc\$min_ecc	4365	4361							
pmc\$pc_base_exception	4361	4375							
pmc\$program_aborting	4519	4521							
pmc\$program_exiting	4518	4521							

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

pmc\$sf_terminate_task	3664	5670							
pmc\$statistic_log	4333	4338	4342						
pmc\$system_log	4334	4336	4340						
pmc\$task_terminating	4519	5634							
pmp\$exit	4293	5674							
pmp\$find_executing_task_xcb	4298	4313	5630						
pmp\$log	4317	5673							
pmp\$task_state	4514	5634							
pmt\$ascii_logs	4336	4348							
pmt\$binary_logs	4338	4350							
pmt\$condition_identifer	3475	3469							
pmt\$cpu_model_number	1411	1400	1407						
pmt\$cpu_serial_number	1414	1401	1406						
pmt\$global_binary_logs	4342	4354							
pmt\$global_logs	4340	4352							
pmt\$initialization_value	1488	1155	2176	2374	5172	5173			
pmt\$local_binary_logs	4344	4356							
pmt\$log_msg_text	4326	4317							
pmt\$log	4332	4346							
pmt\$signal	3602	3596							
pmt\$signal_contents	3640	3604							
pmt\$signal_id	3607	3603							
pmt\$task_id	3398	2513	3393	3876	4114	4926	5230		
pmt\$task_state	4518	4514							
pointer	4883	4893	4896/P	4901/M					
pointer	4925	4935/P	4939/M						
pointer	4997	5028/M	5030/M	5032/M	5034/M				
pointer	5099	5128/M							
pointer	5231	5256/M							
pointer	5316	5415/M							
pointer	5480	5489	5420/M	5425/M	5430/M	5434/P	5441/P		
pointer	5536	5492	5494/P	5497/M					
pointer	5552	5546	5549/P	5552/M					
pointer	5582	5591/P	5594/M						
pointer	6004	6031/M	6033						
pointer	6042	6050/P							
pointer	6453	6481/M	6482	6505	6518/M	6533/P	6534/P	6550/P	6555
		6560	6565/M	6566/P	6572	6582	6595	6596	
pointer_kind	2345	5019/M	5116/M	5188/M	5244/M	5406/M	6020/M		
pointer_kind	4995	5010	5012	5028	5029	5031			
pointer_kind	5097	5116							
pointer_kind	5165	5188							
pointer_kind	5229	5244							
pointer_kind	5314	5395	5397	5415	5416	5421			
pointer_kind	6001	6020							
pointer_kind	6041	6050/P							
power	3917	3926/M	3928/M	3928	3931	3934			
present	3587	5679							
preset_and_save_ts_fb	6076	6093/P							
preset_pointer	5332	5419	5424	5429	5450	5450	5451		
preset_value	2176	5366/M							
preset_value	2374	5366							
previous_allocated_length	5624	5631/M	5643	5646/M					
ps_transfer_size	3912	3925							

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES
	ON LINE	
ps_transfer_size_power	3913	3931 3932/M 3932 3934/M
ptr	6464	6480/M 6481
pva	4743	4754/P 4755/P
pva	5166	5198/M
pva	5741	5751 5757/P
pva	5827	5838 5843/P
pva	6077	6093/P
pva	6220	6229 6233/P 6234/P
pva	6272	6287 6292
pva	6343	6359/P
pva	6454	6455
pva	6456	6482 6505 6518/M 6533/P 6534/P 6550/P 6565/M 6566/P
pva_p	6157	6165
pva_p	6435	6480
pva_p	6467	6480/M
r1	2163	5354/M
r1	2381	5354
r1	2912	6525 6527 6530
r1_addr_list_p	5779	5786 5787/P 5792
r1_addr_returned	5780	5788/P 5794
r1_free_behind	6085	6094/P 6099
r1_list_overflow	5781	5788/P 5793
r1_num_array_p	6112	6118 6120/M 6123/P 6128
r1_offset_list_p	5700	5705 5707/P 5711
r1_offsets_returned	5701	5707/P 5712
r1_pointer	5105	5124/P 5128
r1_pointer	5174	5194/P 5198
r1_pointer	5236	5252/P 5256
r1_pointer	5873	5889/M 5890/P
r1_pointer	5919	5936/M 5937/P
r1_pointer	6009	6027/P 6031
r1_segment_attributes_p	5749	5755 5756/M 5757/P 5761
r1_segment_attributes_p	6351	6357 6358/M 6359/P
r1_segment_length	5834	5843/P 5847
r1_status	4749	4755/P 4756
r1_status	4804	4811/P 4812 4817/P
r1_status	4844	4849/P 4850 4851/P
r1_status	4889	4897/P 4898 4899/P
r1_status	4931	4935/P 4936 4937/P
r1_status	5004	5022/P 5024 5037/P
r1_status	5106	5124/P 5125 5126/P
r1_status	5175	5194/P 5195 5196/P
r1_status	5237	5252/P 5253 5254/P
r1_status	5333	5409/P 5411 5456/P
r1_status	5486	5495/P 5496 5499/P
r1_status	5542	5550/P 5551 5554/P
r1_status	5587	5592/P 5593 5596/P
r1_status	5702	5707/P 5708 5709/P
r1_status	5748	5757/P 5758 5759/P
r1_status	5782	5788/P 5789 5790/P
r1_status	5835	5843/P 5844 5845/P

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES
	ON LINE	
r1_status	5874	5890/P 5891 5892/P
r1_status	5920	5937/P 5938 5939/P
r1_status	6010	6027/P 6028 6029/P
r1_status	6087	6094/P 6095 6096/P
r1_status	6113	6123/P 6124 6125/P
r1_status	6226	6234/P 6235 6236/P
r1_status	6279	6288/P 6289 6290/P
r1_status	6350	6359/P 6360 6361/P
r1_status	6397	6408/P 6409 6410/P
r1_transfer_size	6086	6094/P 6098
r2	2164	5355/M
r2	2382	5355
r2	2913	6523 6530 6583 6584 6585
r3	6444	6523
ref_r	6472	6505/M 6506 6516 6517/M 6523 6525 6527 6530
residence	1496	6530 6585 6572 6583 6585/M 6587
return_unallocated_offsets	5693	5706/P 4667
ring	459	6505 6518/M 6555 6560 6565/M 6584/M 6587/M 6596
ring	2474	4754/P 4809/P 4849/P 4895/P 4935/P 5017 5113/P 5186
		5242 5404 5494/P 5548/P 5591/P 5757/P 5787/P 5842/P
		5890/P 6017/P 6050/P 6093/P 6233/P 6286/P 6359/P 6516
rp	2910	6517 6518 6523 6528 6536
s	6461	6481/M
s_p	6469	6481
saved_free_behind	6080	6099/M
saved_transfer_size	6079	6098/M
sd_p	6473	6492/M 6494 6523 6523 6525 6525 6527 6527
		6528 6530 6530 6531 6536 6583 6584 6585
sdt_entry_p	6160	6168/M 6171
sdt_offset	2515	4055 6168 6492
sdt_p	4602	4610/P
sdtx_entry_p	6161	6169/M 6171
sdtx_offset	2516	4094 6169 6493
sdtx_p	4603	4610/P 4615 4618 4630/P 4667/P 4672/P
sdtx_p	6474	6493/M 6498
seg	480	6482
seg	3805	3807/M 3810/M 3812/M 3815 3816
seg	4578	4667/M 4667/M 4667/M 4667 4667
seg_attrib_p	5335	5348 5352/M 5354/M 5355/M 5357/M 5358/M 5360/M 5361/M
		5365/M 5366/M 5368/M 5369/M 5371/M 5376/M 5383/M 5388/M
		5392/M 5392 5407 5434/P
seg_attributes	5742	5755 5755 5756 5761/M
seg_attributes	6040	6050/P
seg_attributes_p	5096	5117 5120 5121 5122
seg_attributes_p	5228	5245 5247 5248 5249
segment_attributes	5005	5017/M 5018/M 5019/M 5020/M 5021/M 5022/P
segment_attributes	5107	5114/M 5115/M 5116/M 5118/M 5119 5120 5122/M
		5124/P
segment_attributes	5176	5186/M 5187/M 5188/M 5189 5190/M 5191/M 5192/M 5193/M
		5194/P

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
segment_attributes	5238	5242/M 5243/M 5244/M 5245/M 5246 5247 5249/M 5251/M
segment_attributes	5336	5252/P 5404/M 5405/M 5406/M 5407/M 5408/M 5409/P
segment_attributes	6011	6018/M 6019/M 6020/M 6021/M 6022 6023 6024/M 6026/M
segment_inheritance	4744	6027/P 4755/P
segment_length	5828	5840/M 5847/M
segment_length	6274	6288/P
segment_num_array	6107	6118 6119 6119 6127 6127 6128/M
segment_number	4786	4810/P 4815
segment_p	4579	4610/P 4626/P 4639/P 4653 4707 4718/P
segment_p	5162	5179 5182 5182
segment_p	5692	5706/P
segment_p	5914	5922 5928/P 5936 5937/P
segment_p	6390	6398 6405/P 6408/P
segment_pointer	4795	4810/P 4813 4814 4815
segment_pointer	5337	5409/P 5413/P 5417 5419 5420 5422 5424 5425
segment_pointer	5868	5427 5429 5430
segment_pointer	6280	5882/P 5889
segment_table_length	3220	6287/M 6288/P
segnum	2166	6170 6488
segnum	2370	5358/M
segnum	2475	5358
segnum	4052	4273/S 6624/S
segnum	4091	4055
segnum	6156	4094
segnum	6156	6168
segnum	6156	6169
segnum	6162	6165/M 6168/P 6169/P 6170
segnum	6434	6482
segnum	6434	6483
segnum	6475	6482/M 6483 6488 6492/P 6493/P
seq_p	5338	5417/M 5418 5419 5422/M 5423 5424 5427/M 5428
seq_p	5429	
seq_pointer	2232	5032 5422 5424 5425
sequence_pointer	18	5032/M 5425/M
sfc\$no_limit	2357	4630/P 5115 5187 5243
sfc\$step_file_space_limit	2358	5018 5405
sfid	2346	5021/M 5118/M 5193/M 5251/M 5408/M 6026/M
sfid	2932	4667/P
sfid	3799	3808 3809 3815 3816
sfid	4578	4667 4667 4667
sfid	5999	6026
sft\$file_space_limit_kind	2357	2344 2939 4594 5324 6003
shadow_info	2938	4630/P
shadow_length	2192	5192/M
shadow_length	5164	5192
shadow_offset	5163	5182
shadow_p	2191	5191/M
shadow_sfid	3024	4630/P
shared_stack_flag	6106	6123/P
shared_taskid_array	4926	4935/P
shared_taskid_array	5230	5252/P

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
signals	2525	5679
size	2331	4228/M 4757/M 4817/M 4851/M 4899/M 4937/M 5037/M 5126/M
software_attribute_set	2935	5196/M 5254/M 5456/M 5499/M 5554/M 5596/M 5709/M 5759/M
source	3841	5790/M 5845/M 5892/M 5939/M 6029/M 6096/M 6125/M 6236/M
source	4578	6290/M 6361/M 6410/M
source	4605	4615 4618
stack_pages_to_be_freed	4839	3851 4709
starting_addr	4605	4653/M 4655/P 4707/M 4709/P 4710/P
starting_byte_address	5770	4849/P
static_link	6459	4666/M 4672/P 4714/M
status	4223	5787/P
status	4580	6555
status	4742	4226/M 4227/M 4228/M
status	4745	4610/P 4611 4626/P 4631/P 4632 4640/P 4641 4656/P
status	4794	4673/P 4674 4710/P 4711/P 4718/P
status	4799	4757/M 4757/M 4757/M
status	4838	4752/M 4757/P
status	4840	4817/M 4817/M 4817/M
status	4882	4807/M 4817/P
status	4885	4851/M 4851/M 4851/M
status	4924	4846/M 4851/P
status	4927	4899/M 4899/M 4899/M
status	4994	4894/M 4899/P
status	4998	4937/M 4937/M 4937/M
status	5095	4934/M 4937/P
status	5100	5037/M 5037/M 5037/M
status	5161	5009/M 5026/P 5037/P
status	5167	5126/M 5126/M 5126/M
status	5227	5112/M 5126/P
status	5232	5196/M 5196/M 5196/M
status	5312	5180/M 5196/P
status	5317	5180/M 5254/M 5254/M
status	5479	5254/M 5254/P
status	5481	5241/M 5254/P
status	5535	5456/M 5456/M 5456/M
status	5538	5343/M 5373/P 5413/P 5435/P 5440 5456/P
status	5581	5498/M 5499/M 5499/M
status	5583	5490/M 5499/P
status	5626	5554/M 5554/M 5554/M
status	5691	5547/M 5554/P
status	5696	5596/M 5596/M 5596/M
status	5740	5590/M 5596/P
status	5743	5639/P 5640 5642 5642 5655 5655 5656 5664/P
status	5768	5673/P 5674/P
status	5774	5709/M 5709/M 5709/M
status	5826	5704/M 5709/P
status	5830	5759/M 5759/M 5759/M
status	5867	5753/M 5759/P
status		5790/M 5790/M 5790/M
status		5784/M 5790/P
status		5845/M 5845/M 5845/M
status		5839/M 5845/P
status		5892/M 5892/M 5892/M

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES							
status	5869	5877/M	5882/P	5883	5892/P				
status	5913	5939/M	5939/M	5939/M					
status	5915	5923/M	5928/P	5929	5939/P				
status	5998	6029/M	6029/M	6029/M					
status	6005	6016/M	6029/P						
status	6043	6050/P							
status	6075	6096/M	6096/M	6096/M					
status	6081	6091/M	6096/P						
status	6105	6125/M	6125/M	6125/M					
status	6108	6117/M	6125/P						
status	6219	6236/M	6236/M	6236/M					
status	6222	6231/M	6236/P						
status	6271	6290/M	6290/M	6290/M					
status	6275	6285/M	6290/P						
status	6342	6361/M	6361/M	6361/M					
status	6346	6355/M	6361/P						
status	6389	6410/M	6410/M	6410/M					
status	6392	6400/M	6405/P	6410/P					
ste	2881	6171	6494	6523	6523	6525	6525	6527	6527
		6528	6530	6530	6531	6536	6583	6584	6585
str	6454	6460							
str1	3846	3851/M	3853						
str1	4578	4655/M	4655	4709/M	4709				
str2	3847	3852/M	3853/M	3854					
str2	4578	4655/M	4655/M	4655	4709/M	4709/M	4709		
sync\$ucr_condition	3513	3524							
sync\$user_defined_condition	3514	3526							
system_flags	2500	5670	5677						
system_table_lock_count	2499	5672							
syts\$monitor_flag	3420	3405							
syts\$monitor_flags	3405	2487							
temp_transfer_size	6078	6093/P							
text	2293	4228/M	4757/M	4817/M	4851/M	4899/M	4937/M	5037/M	5126/M
		5196/M	5254/M	5456/M	5499/M	5554/M	5596/M	5709/M	5759/M
		5790/M	5845/M	5892/M	5939/M	6029/M	6096/M	6125/M	6236/M
		6290/M	6361/M	6410/M					
tmc\$broken_task_fault_id	3441	3481							
tmc\$btc_invalid_a0	3539	3560							
tmc\$btc_invalid_p	3539	3560							
tmc\$btc_mcr_traps_disabled	3540	3561							
tmc\$btc_mf_traps_disabled	3539	3559							
tmc\$btc_mntr_fault_buffer_full	3538	3559							
tmc\$btc_system_error	3541	3555							
tmc\$btc_ucr_traps_disabled	3540	3561							
tmc\$dummy_fault	3442	3497							
tmc\$flag_available_31	3676	3680							
tmc\$maximum_monitor_faults	3446	3437							
tmc\$maximum_signals	3656	3653	5678						
tmc\$maximum_system_task_id	3689	3692							
tmc\$mc_r_fault	3441	3493							
tmc\$signal_available_63	3638	3649							
tmc\$stid_null_task	3695	3692							

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IDENTIFIER	DEFINED ON LINE	REFERENCES							
tmt\$broken_task_condition	3538	3554							
tmt\$broken_task_monitor_fault	3552	3492							
tmt\$mcr_faults	3577	3494							
tmt\$monitor_fault_buffer	3431	2524							
tmt\$monitor_fault_buffers	3437	3432	3433	3434					
tmt\$monitor_fault_identifiers	3440	3490	3566						
tmt\$signal	3594	3589							
tmt\$signal_buffer	3586	2525							
tmt\$signal_buffers	3653	3587	3588	3589					
tmt\$system_flags	3659	2500	5670	5671	5677				
tmt\$system_task_id	3692	2491							
tmt\$task_queue_link	1295	1288							
ts	3918	3925/M	3927	3927	3929/M	3929			
update	6391	6404							
update_passive_with_active	4578	4722	6405						
user_attributes	2347	5020/M	5117/M	5119	5120	5122/M	5189	5190/M	5191/M
		5192/M	5245/M	5246	5247	5249/M	5407/M	6021/M	6022
		6023	6024/M						
user_attributes_p	5313	5347	5348	5348	5350	5350	5351	5354	5355
		5358	5361	5366	5369	5372	5376	5377	
validating_ring_num	4566	4569/M	4571/M						
validating_ring_num	4742	4754/M	4754/M						
validating_ring_num	4794	4809/M	4809/M						
validating_ring_num	4882	4895/M	4895/M						
validating_ring_num	5095	5113/M	5113/M						
validating_ring_num	5535	5548/M	5548/M						
validating_ring_num	5826	5842/M	5842/M						
validating_ring_num	5998	6017/M	6017/M						
validating_ring_num	6219	6233/M	6233/M						
validating_ring_num	6271	6286/M	6286/M						
validating_ring_number	2343	5017/M	5114/M	5186/M	5242/M	5404/M	6018/M		
validating_ring_number	4750	4754/P	4755/P						
validating_ring_number	4805	4809/P	4811/P						
validating_ring_number	4890	4895/P	4896/P						
validating_ring_number	5108	5113/P	5114						
validating_ring_number	5543	5548/P	5549/P						
validating_ring_number	5836	5842/P	5843/P						
validating_ring_number	6012	6017/P	6018						
validating_ring_number	6227	6233/P	6234/P						
validating_ring_number	6281	6286/P	6288/P						
validation_ring_number	4565	4568	4571						
validation_ring_number	4742	4754	4754						
validation_ring_number	4794	4809	4809						
validation_ring_number	4797	4809/P							
validation_ring_number	4882	4895	4895						
validation_ring_number	4884	4895/P							
validation_ring_number	5095	5113	5113						
validation_ring_number	5098	5113/P							
validation_ring_number	5535	5548	5548						
validation_ring_number	5537	5548/P							
validation_ring_number	5826	5842	5842						

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----							
	ON LINE								
validation_ring_number	5828	5842/P							
validation_ring_number	5998	6017	6017						
validation_ring_number	6002	6017/P							
validation_ring_number	6219	6233	6233						
validation_ring_number	6271	6286	6286						
validation_ring_number	6273	6286/P							
verify_access	6485	6485	6480	6496	6500	6508	6552	6557	6562
		6589	6574	6592	6598	6604			
v1	2908	6171	6494						
wait_time	5627	5638/M	5644/M						
wired_flag	5339	5345/M	5380/M	5391	5444				
wired_index	5340	5381/M	5392/S	5434/S					
wired_segment_length	2194	5376/M	5392	5434/P					
wired_segment_length	2378	5372	5376						
wp	2811	6525	6527						
xcb	4298	4311/M							
xcb	5618	5630/M							
xcb_p	4051	4054	4055						
xcb_p	4090	4093	4094						
xcb_p	5628	5630/P	5670	5672	5677	5679			
xcb_p	6156	6168	6168						
xcb_p	6156	6169	6169						
xcb_p	6163	6167/M	6168/P	6169/P	6170				
xcb_p	6434	6492	6492						
xcb_p	6434	6493	6493						
xcb_p	6477	6487/M	6488	6492/P	6493/P				
xp	2486	6170	6488						
xp	2909	6531							
xp	6442	6595							

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE

3 MODULE mmm\$segment_manager_system_core;

NDS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE

External Procedures referenced in this module

```

O 5350
O 5351 PROCEDURE [XREF] dmp$set_server_eoi
O 5352 ( sfid: gft$system_file_identifier;
O 5353 segment_length: ost$segment_length;
O 5354 VAR status: ost$status);
O 5355
O 5356
O 5357 PROCEDURE [XREF] dmp$allocate_file_space_rl ALIAS 'dmxaspa' (system_file_id:
O 5358 dmt$system_file_id;
O 5359 byte_address: amt$file_byte_address;
O 5360 number_bytes_to_allocate: amt$file_byte_address;
O 5361 chapter_number: dmt$chapter_number;
O 5362 wait_option: ost$wait;
O 5363 file_space_limit: sft$file_space_limit_kind;
O 5364 VAR status: ost$status);
O 5365
O 5366 PROCEDURE [XREF] dmp$create_disk_file
O 5367 ( fde_p: gft$file_desc_entry_p;
O 5368 file_attributes_p: ^array [ * ] of dmt$file_attribute;
O 5369 allocation_length: amt$file_byte_address;
O 5370 sfid: gft$system_file_identifier;
O 5371 VAR status: ost$status);
O 5372
O 5373 PROCEDURE [XREF] dmp$destroy_file ALIAS 'dmxfdes' (VAR system_file_id:
O 5374 dmt$system_file_id;
O 5375 file_space_limit: sft$file_space_limit_kind;
O 5376 VAR status: ost$status);
O 5377
O 5378 PROCEDURE [XREF] dmp$fetch_eoi ALIAS 'dmxfdoi' (system_file_id:
O 5379 dmt$system_file_id;
O 5380 VAR eoi: amt$file_byte_address;
O 5381 VAR status: ost$status);
O 5382
O 5383 PROCEDURE [INLINE] dmp$get_disk_file_descriptor_p (fde_p: gft$file_desc_entry_p;
O 5384 VAR dfd_p: ^dmt$disk_file_descriptor);
O 5385
O 5386
O 5387 PROCEDURE [XREF] dmp$get_initialized_addresses (sfid: dmt$system_file_id;
O 5388 starting_byte_address: ost$segment_offset;
O 5389 VAR addr_list: ^array [ * ] of dmt$addr_length_pair;
O 5390 VAR addr_returned: integer;
O 5391 VAR list_overflow: boolean;
O 5392 VAR status: ost$status);
O 5393
O 5394
O 5395 PROCEDURE [XREF] dmp$get_total_allocated_length
O 5396 ( fde_p: gft$locked_file_desc_entry_p;
O 5397 VAR allocated_length: amt$file_byte_address);
O 5398
O 5399
O 5400 PROCEDURE [XREF] dmp$mfh_for_sfid (system_file_id: dmt$system_file_id;
O 5401 offset_requiring_allocation: amt$file_byte_address;
O 5402 file_space_limit: sft$file_space_limit_kind;
O 5403 VAR status: ost$status);
O 5404
O 5405

```

NDS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE

External Procedures referenced in this module

```

O 5476 VAR status: ost$status);
O 5477
O 5480 PROCEDURE [XREF] dmp$reallocate_file_space (sfid: dmt$system_file_id;
O 5481 copy_pages: boolean;
O 5482 VAR status: ost$status);
O 5483
O 5485 PROCEDURE [XREF] dmp$sparse_allocate
O 5486 ( sfid: gft$system_file_identifier;
O 5487 offset_requiring_allocation: amt$file_byte_address;
O 5488 file_space_limit: sft$file_space_limit_kind;
O 5489 VAR status: ost$status);
O 5490
O 5491
O 5492 PROCEDURE [XREF] gfp$assign_fde
O 5493 ( residence: gft$table_residence;
O 5494 segment_number: ost$segment;
O 5495 VAR sfid: gft$system_file_identifier;
O 5496 VAR fde_p: gft$file_desc_entry_p);
O 5497
O 5498
O 5499 PROCEDURE [XREF] gfp$free_fde
O 5500 ( fde_p: gft$file_desc_entry_p);
O 5501
O 5502
O 5503 FUNCTION [UNSAFE, INLINE] gfp$get_eoi_from_fde
O 5504 ( fde_p: gft$file_desc_entry_p): amt$file_byte_address;
O 5505
O 5506
O 5507 PROCEDURE [INLINE] gfp$get_fde_p (sfid: gft$system_file_identifier;
O 5508 VAR fde_p: gft$file_desc_entry_p);
O 5509
O 5510
O 5511
O 5512 PROCEDURE [INLINE] gfp$lock_fde (fde_p: gft$file_desc_entry_p);
O 5513
O 5514
O 5515
O 5516 PROCEDURE [INLINE] gfp$unlock_fde_p (fde_p: gft$locked_file_desc_entry_p);
O 5517
O 5518
O 5519
O 5520 PROCEDURE [INLINE] gfp$get_locked_fde_p (sfid: gft$system_file_identifier;
O 5521 VAR fde_p: gft$file_desc_entry_p);
O 5522
O 5523
O 5524 PROCEDURE [XREF] i#build_adaptable_heap_pointer (ring: 0 .. 15;
O 5525 segment: 0 .. 4095;
O 5526 offset: ^-8000000(16) .. ^7fffffff(16);
O 5527 heap_length: 0 .. ^7fffffff(16);
O 5528 VAR heap_p: ^HEAP ( * ));
O 5529
O 5530
O 5531 PROCEDURE [XREF] i#build_adaptable_seq_pointer (ring: 0 .. 15;
O 5532 segment: 0 .. 4095;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE

External Procedures referenced in this module

```

0 5812      offset: -8000000(16) .. 7fffffff(16);
0 5813      sequence_length: 0 .. 8000000(16);
0 5814      next_entry: 0 .. 7fffffff(16);
0 5815      VAR seq_p: ^ASEQ ( * );
0 5816
0 5817
0 5818      PROCEDURE [INLINE] i#move (source: ^cell;
0 5819      dest: ^cell;
0 5820      length: 0 .. 7fffffff(16));
0 5821
0 5822      VAR
0 5823      str1: ^string (65535);
0 5824      str2: ^string (65535);
0 5825
0 5826      IF length < 0 THEN
0 5827      str1 := source;
0 5828      str2 := dest;
0 5829      str2^ [1, length] := str1^ [1, length];
0 5830      #SPOIL (str2^);
0 5831      IFEND;
0 5832      PROCEND i#move;
0 5833
0 5834      PROCEDURE [XREF] i#real_memory_address (p: ^cell;
0 5835      VAR rma: integer);
0 5836
0 5837      PROCEDURE [XREF] mmp$advise_in (pva: ^cell;
0 5838      length: ost$byte_count;
0 5839      VAR status: ost$status);
0 5840
0 5841      PROCEDURE [XREF] mmp$advise_out (pva: ^cell;
0 5842      length: ost$byte_count;
0 5843      VAR status: ost$status);
0 5844
0 5845      PROCEDURE [XREF] mmp$assign_contiguous_memory
0 5846      ( process_virtual_address: ^cell;
0 5847      contiguous_mem_length: ost$segment_length;
0 5848      VAR status: ost$status);
0 5849
0 5850
0 5851      PROCEDURE [XREF] mmp$assign_contiguous_memory
0 5852      ( process_virtual_address: ^cell;
0 5853      contiguous_mem_length: ost$segment_length;
0 5854      VAR status: ost$status);
0 5855
0 5856      PROCEDURE [XREF] mmp$assign_contiguous_memory
0 5857      ( process_virtual_address: ^cell;
0 5858      contiguous_mem_length: ost$segment_length;
0 5859      VAR status: ost$status);
0 5860
0 5861      { Convert page streaming transfer size from bytes number of pages expressed as a power of 2
0 5862
0 5863      PROCEDURE [INLINE] mmp$convert_ps_transfer_size (ps_transfer_size: integer;
0 5864      VAR ps_transfer_size_power: 0..15);
0 5865
0 5866
0 5867
0 5868
0 5869
0 5870
0 5871
0 5872
0 5873
0 5874
0 5875
0 5876
0 5877
0 5878
0 5879
0 5880
0 5881
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```

SOURCE LIST OF mmm\$segment_manager_system_core NOS/VE CYBIL/II 1.0 89102

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NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE

External Procedures referenced in this module

```

5902      VAR sdtx_p: mmt$max_sdtx_p;
5903
0 5912
0 5913      FUNCTION [INLINE] mmp$get_sdt_entry_p
0 5914      ( xcb_p: ^ost$execution_control_block;
0 5915      segnum: ost$segment): ^mmt$segment_descriptor;
0 5916
0 5917      mmp$get_sdt_entry_p := #address (1, #segment (xcb_p),
0 5918      8 * segnum + xcb_p^.sdt_offset);
0 5919
0 5920      FUNCEND;
0 5921
0 5922
0 5923
0 5924
0 5925      FUNCTION [INLINE] mmp$get_sdtx_entry_p
0 5926      ( xcb_p: ^ost$execution_control_block;
0 5927      segnum: ost$segment): ^mmt$segment_descriptor_extended;
0 5928
0 5929      mmp$get_sdtx_entry_p := #address (1, #segment (xcb_p),
0 5930      #SIZE (mmt$segment_descriptor_extended) * segnum + xcb_p^.sdtx_offset);
0 5931
0 5932      FUNCEND;
0 5933
0 5934
0 5935
0 5936
0 5937
0 5938      PROCEDURE [INLINE] mmp$touch_all_pages
0 5939      ( pages_to_touch: ^cell;
0 5940      length: ost$segment_length);
0 5941
0 5942      VAR
0 5943      temp_pages_to_touch: ^array [1 .. 7fffffff(16)] of 0 .. Off(16);
0 5944      page_size: ost$page_size;
0 5945      referenced_byte: 0 .. Off(16);
0 5946      size_offset: integer;
0 5947
0 5948      page_size := 512 * (128 - #READ_REGISTER (osc$pr_page_size_mask));
0 5949      temp_pages_to_touch := pages_to_touch;
0 5950      size_offset := 1;
0 5951
0 5952      WHILE size_offset < length DO
0 5953      referenced_byte := temp_pages_to_touch^ [size_offset];
0 5954      size_offset := size_offset + page_size;
0 5955      WHILEND;
0 5956
0 5957      referenced_byte := temp_pages_to_touch^ [length];
0 5958
0 5959      PROCEND mmp$touch_all_pages;
0 5960
0 5961
0 5962      PROCEDURE [XREF] mmp$unlock_segment (p: ^cell;
0 5963      page_disposition: mmt$lus_page_disposition;
0 5964      wait: ost$wait;
0 5965      VAR status: ost$status);
0 5966
0 5967
0 5968
0 5969
0 5970      PROCEDURE [XREF] mmp$write_modified_pages (pva: ^cell;

```

NDS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
 External Procedures referenced in this module

```

5971     length: ost$byte_count;
5972     waitopt: ost$wait;
5973     VAR status: ost$status);
5974
O 5977
O 5978 PROCEDURE [XREF] osp$begin_system_activity;
O 5979
O 5980 PROCEDURE [XREF] osp$end_system_activity;
O 5981
O 5982 PROCEDURE [XREF] osp$set_status_abnormal ALIAS 'ospssa'
O 5983   (
O 5984     identifier: ost$status_identifier;
O 5985     condition: ost$status_condition_code;
O 5986     text: string (* <= osc$max_string_size);
O 5987     VAR status: ost$status);
5994
5995 PROCEDURE [XREF] pmp$cycle
5996   (VAR status: ost$status);
5997
O 6000
O 6001
O 6002 PROCEDURE [XREF] pmp$delay
O 6003   (
O 6004     milliseconds: integer;
O 6005     VAR status: ost$status);
O 6008
O 6009
O 6010 PROCEDURE [INLINE] pmp$find_executing_task_xcb (VAR xcb:
O 6011   ^ost$execution_control_block);
O 6027
O 6028 PROCEDURE [XREF] pmp$find_task_xcb (task_id: pmt$task_id;
O 6029   VAR xcb: ^ost$execution_control_block);
O 6032
O 6033 PROCEDURE [INLINE] pmp$get_executing_task_gid (VAR global_task_id:
O 6034   ost$global_task_id);
O 6053
O 6054 PROCEDURE [XREF] pmp$set_system_flag (flag_id: ost$system_flag;
O 6055   recipient: ost$global_task_id;
O 6056   VAR status: ost$status);
O 6057
O 6101 PROCEDURE [XREF] pmp$zero_out_table
O 6102   (
O 6103     p: ^cell;
O 6104     len: ost$byte_count);
O 6107
O 6108 PROCEDURE [XREF] syp$cause_condition (name: ost$name);
6111
6112 PROCEDURE [XREF] syp$mfh_for_hang_task;
6113 PROCEDURE [INLINE] syp$set_status_from_mtr_status (monitor_status:
6114   syp$monitor_status;
6115   VAR status: ost$status);
6116
O 6125
O 6126 PROCEDURE [XREF] syp$return_jobs_rl_resources;
O 6127

```

NDS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
 External Procedures referenced in this module

```

O 6128 PROCEDURE [XREF] syp$terminate_task
O 6129   (
O 6130     terminate_reason: ost$ring1_termination_reason);

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
External Variables referenced in this module

```

6134 VAR
6135   dmv$idle_system: [XREF] boolean;
6136
6137   VAR
6138     gfv$null_sfid: [XREF, READ, OSS$MAINFRAME_WIRED_LITERAL] gft$system_file_identifier;
6139
6140
6141   SECTION
6142     oss$mainframe_wired_literal: READ;
6143
6144   VAR
6145     jmv$executing_within_system_job: [XREF, oss$job_fixed] boolean;
6146
o 6152 {Job Control Block (JCB).}
o 6153
o 6154   VAR
o 6155     jmv$jcb: [XREF] jmt$job_control_block;
6985   VAR
6986     jmv$system_ijn_ordinal: [XREF] jmt$ijn_ordinal;
6987
o 6970 VAR
o 6971     jmv$task_private_temp1_p: [XREF] ^pmt$task_template;
6986 {Pointer to the Active Segment Table - (AST).}
6987
6988   VAR
6989     mmv$ast_p: [XREF] ^mmt$active_segment_table;
6990
o 6993 {This deck contains XREFs to variables used by Memory Manager in
o 6994 {monitor mode. The variables are initialized by a job mode routine
o 6995 {during system deadstart.
o 6996
o 6997   VAR
o 6998     mmv$a_divisor: [XREF] 0 .. 10000(16),
o 6999     mmv$a_mult: [XREF] 0 .. 10000(16),
o 7000     mmv$number_free_astes: [XREF] integer;
o 7001
o 7002
o 7003
o 7004 { Define variable that contains the SDT index for the first transient segment.
o 7005
o 7006   VAR
o 7007     mmv$first_transient_seg_index: [XREF] ost$segment;
o 7008
7011 {Maximum number of pages that will be assigned to a segment that does not
7012 {have a backing file. A signal is sent to the task to assign a file when
7013 {the number of assigned pages exceeds this value.
7014
7015   VAR
7016     mmv$max_pages_no_file: [XREF] integer;
7017 VAR
7018     mmv$page_map_offsets: [XREF] mmt$page_map_offsets;
7019
o 7043
o 7044 { Define the number of page faults that trigger prestreaming mode
o 7045

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
External Variables referenced in this module

```

o 7046   VAR
o 7047     mmv$page_streaming_prestream: [XREF] 0 .. 255;
o 7048
o 7049 { Define the number of bytes used to override the transfer size
o 7050
o 7051   VAR
o 7052     mmv$page_streaming_transfer: [XREF] integer;
o 7053
o 7054   VAR
o 7055     mmv$preset_conversion_table: [XREF, READ] array [pmt$initialization_value]
o 7056     of integer;
o 7057
o 7058
o 7059   VAR
o 7060     mmv$shadow_by_segnum: [XREF] boolean;
o 7061
o 7062   VAR
o 7063     osv$job_fixed_heap: [XREF, READ, oss$job_fixed] ^ost$heap;
o 7064
o 7065
o 7066   VAR
o 7067     osv$cpus_logically_on: [XREF] 0 .. osc$max_number_of_processors;
o 7068
o 7069
o 7070
o 7080   VAR
o 7081     osv$multiple_cpus_possible: [XREF] boolean;
o 7082
o 7083
o 7084 {System page size.}
o 7085
o 7086   VAR
o 7087     osv$page_size: [XREF] ost$page_size;
o 7088
o 7091   VAR
o 7092     syv$job_initialization_complete: [XREF] boolean;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
Global Declarations defined and used in this module

```

0 7094
0 7095 CONST
0 7096 max_specified_transfer_size = 1048576; { Limit user specified transfer size to 1MB (1,048,576)
0 7097
0 7098 { Define global variables used by this module.
0 7099
0 7100 VAR
0 7101 mmv$sfid_match: [XDCL] integer;
0 7102 mmv$sfid_mismatch: [XDCL] integer;
0 7103 mmv$sparse_threshold: [XDCL] integer := 30 * 4096, {Arbitrary number}
0 7104
0 7105 mmv$default_sdt_entry: [READ, oss$mainframe_paged_literal] mmt$segment_descriptor :=
0 7106 [[osc$vl_regular_segment, osc$non_executable, osc$read_uncontrolled, osc$write_uncontrolled, 1, 1,
0 7107 0, [FALSE, FALSE, 0]], 0, 0],
0 7108 mmv$default_sdtx_entry: [XDCL, #GATE, READ, oss$mainframe_paged_literal]
0 7109 mmt$segment_descriptor_extended := [1, mmc$sas_allow_access, *, mmc$si_none,
0 7110 mmc$srfs_not_reserved, []], mmc$sar_write_extend, mmc$lss_none, [0, 0, *, mmc$ssk_none, FALSE],
0 7111 sfc$no_limit, [0, 0, 2, 0, FALSE, FALSE, FALSE], osc$max_segment_length];
0 7112

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
ASID FUNCTIONS - {from common decks}

```

0 7114
0 7115 {-----
0 7116 {Name:
0 7117 { mmp$ast_index
0 7118 {Purpose:
0 7119 {These functions convert AST indexes into an ASID and vice-versa.
0 7120 {Input:
0 7121 { AST_index or ASID
0 7122 {Output:
0 7123 { astid or ast_index
0 7124 {-----
0 7125
0 7126 VAR
0 7127 bits: array [0 .. 15] of 0 .. 255 := [0, 8, 4, 12, 2, 10, 6, 14, 1, 9, 5, 13, 3, 11, 7, 15];
0 7128
0 7129 PROCEDURE [INLINE] mmp$asti (xasid: ost$asid;
0 7130 VAR xasti: mmt$ast_index);
0 7131
0 7132 VAR
0 7133 asid: ost$asid;
0 7134 i: integer;
0 7135 asti: integer;
0 7136
0 7137 asid := xasid DIV mmv$a_mult + (xasid MOD mmv$a_mult) * mmv$a_divisor;
0 7138 asti := 0;
0 7139 FOR i := 1 TO 4 DO
0 7140 asti := asti * 16 + bits [asid MOD 16];
0 7141 asid := asid DIV 16;
0 7142 FOREND;
0 7143 xasti := asti;
0 7144 PROCEND mmp$asti;
0 7145
0 7146 PROCEDURE [INLINE] mmp$asid (xasti: mmt$ast_index;
0 7147 VAR asid: ost$asid);
0 7148
0 7149 VAR
0 7150 asti: mmt$ast_index;
0 7151
0 7152 asti := xasti;
0 7153 asid := (bits [asti MOD 16] * 4096) + (bits [(asti DIV 16) MOD 16] * 256) + (bits [(asti DIV 256) MOD 16]
0 7154 * 16) + bits [(asti DIV 4096) MOD 16];
0 7155 asid := asid DIV mmv$a_divisor + (asid MOD mmv$a_divisor) * mmv$a_mult;
0 7156
0 7157 PROCEND mmp$asid;
0 7158
0 7159

```


NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
DESTROY_SEGMENT

```

0 7161 PROCEDURE destroy_segment
4 7162 (   xsfid: gft$system_file_identifier;
4 7163   fde_entry_p: gft$file_desc_entry_p;
4 7164   file_limits_enforced: sft$file_space_limit_kind;
4 7165   VAR status: ost$status);
4 7166
4 7167   VAR
4 7168   rb: mmt$rb_ring1_segment_request,
4 7169   sfid: gft$system_file_identifier;
4 7170
4 7171   sfid := xsfid;
4 7172   IF fde_entry_p^.media = gfc$fm_transient_segment THEN
10 7173     IF fde_entry_p^.astl <> 0 THEN
18 7174       rb.reqcode := syc$src_ring1_segment_request;
18 7175       rb.request := mmc$sr1_delete_seg_sfid;
18 7176       rb.sfid := sfid;
18 7177       i$call_monitor (#LOC (rb), #SIZE (rb));
4A 7178     IFEND;
4A 7179     gfp$free_fde (fde_entry_p);
5C 7180   ELSE
5C 7181     dmp$destroy_file (sfid, file_limits_enforced, status);
78 7182   IFEND;
78 7183
78 7184 PROCEND destroy_segment;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
EXPAND_SEGMENT_TABLE

```

0 7186
0 7187 PROCEDURE expand_segment_table
0 7188 (   xcb_p: ^ost$execution_control_block;
0 7189   VAR status: ost$status);
0 7190
0 7191 {
0 7192 { The purpose of this procedure is to expand the SDT and SDTX when a segment table full
0 7193 { situation is encountered when adding a new segment. Currently, the maximum number of
0 7194 { segments a task can have open/attached is 4095.
0 7195 {
0 7196
0 7197   CONST
0 7198   segment_table_size_increase = 32;
0 7199
0 7200   VAR
0 7201   new_sdt_length: integer, {must be an integer variable}
0 7202   new_table_size: ost$segment_length,
0 7203   old_sdt_length: ost$segment,
0 7204   old_sdt_offset: 0 .. Offffffff(16),
0 7205   old_sdtX_offset: 0 .. Offffffff(16),
0 7206   old_sdt_p: Acell,
0 7207   old_sdtX_p: Acell,
0 7208   new_sdt_p: ^mmt$segment_descriptor_table,
0 7209   new_sdtX_p: ^mmt$segment_descriptor_table_ex,
0 7210   request_block: mmt$rb_change_segment_table;
0 7211
0 7212   status.normal := TRUE;
4 7213
4 7214 { Save the following values, so that the old SDT and SDTX can be freed after
4 7215 { the new ones are successssfully allocated.
4 7216
4 7217   old_sdt_offset := xcb_p^.sdt_offset;
4 7218   old_sdtX_offset := xcb_p^.sdtX_offset;
4 7219   old_sdt_length := xcb_p^.xp.segment_table_length;
4 7220
4 7221   new_sdt_length := xcb_p^.xp.segment_table_length + segment_table_size_increase;
4 7222
4 7223   IF ((new_sdt_length + 1) * 8) > osv$page_size THEN
32 7224     new_sdt_length := (((new_sdt_length + 1) * 8) + osv$page_size) DIV osv$page_size;
32 7225     new_table_size := new_sdt_length * osv$page_size;
32 7226     new_sdt_length := ((new_sdt_length + osv$page_size) DIV 8) - 1;
4C 7227   IFEND;
4C 7228
4C 7229   IF new_sdt_length >= 4096 THEN
54 7230     new_sdt_length := 4095;
58 7231   IFEND;
58 7232
58 7233   IF new_sdt_length = xcb_p^.xp.segment_table_length THEN
64 7234     osp$set_status_abnormal ('MM', mme$segment_table_is_full, '', status);
96 7235     RETURN;
98 7236   IFEND;
98 7237
98 7238   ALLOCATE new_sdt_p: [0 .. new_sdt_length] IN osv$job_fixed_heap^;
D4 7239
D4 7240   IF ((new_sdt_length + 1) * 8) > osv$page_size THEN

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
EXPAND_SEGMENT_TABLE

```

DC 7241      mmp$free_pages (new_sdt_p, new_table_size, osc$nowait, status);
100 7242      mmp$assign_contiguous_memory (new_sdt_p, new_table_size, status);
11C 7243      IF NOT status.normal THEN
124 7244          RETURN;
126 7245          IFEND;
12A 7246      ELSE
12A 7247          pmp$zero_out_table (#LOC (new_sdt_p^), #SIZE (new_sdt_p^));
146 7248          IFEND;
146 7249      [ Allocate and zero out the SDTX.
146 7250      [
146 7251          ALLOCATE new_sdtx_p: [0 .. new_sdt_length] IN osv$job_fixed_heap^;
186 7252          pmp$zero_out_table (#LOC (new_sdtx_p^), #SIZE (new_sdtx_p^));
1A2 7253      [
1A2 7254      [ Issue monitor request to move old segment table to new segment table, update segment table
1A2 7255      [ address and segment table length in task's exchange package.
1A2 7256      [
1A2 7257      [
1A2 7258      [
1A2 7259      [ request_block.request_code := syc$rc_change_segment_table;
1A2 7260      [ request_block.new_sdt_offset := #OFFSET (new_sdt_p);
1A2 7261      [ request_block.new_sdtx_offset := #OFFSET (new_sdtx_p);
1A2 7262      [ request_block.new_sdt_length := new_sdt_length;
1A2 7263      [ i$call_monitor (#LOC (request_block), #SIZE (request_block));
1E0 7264      [ syp$set_status_from_mtr_status (request_block.status, status);
220 7265      [ IF NOT status.normal THEN
228 7266      [     osp$system_error ('Error in change segment table monitor request.', ^status);
24C 7267      [     RETURN;
24E 7268      [     IFEND;
24E 7269      [
24E 7270      [ Free the old SDT and SDTX tables.
24E 7271      [ NOTE: Job monitor's SDT and SDTX are not allocated, hence they can not be freed unless they
24E 7272      [     have been expanded once.
24E 7273      [
24E 7274      [ IF (old_sdt_length * 8) > osv$page_size THEN
25A 7275      [     mmp$free_pages (#ADDRESS (1, osc$segnum_job_fixed_heap, old_sdtx_offset),
280 7276      [         [old_sdt_length * #SIZE (mmt$segment_descriptor_extended)], osc$wait, status);
280 7277      [     IFEND;
280 7278      [ IF old_sdt_offset >= #OFFSET (osv$job_fixed_heap) THEN
29C 7279      [     old_sdt_p := #ADDRESS (1, osc$segnum_job_fixed_heap, old_sdt_offset);
29C 7280      [     old_sdtx_p := #ADDRESS (1, osc$segnum_job_fixed_heap, old_sdtx_offset);
29C 7281      [     FREE old_sdt_p IN osv$job_fixed_heap^;
2D4 7282      [     FREE old_sdtx_p IN osv$job_fixed_heap^;
2F6 7283      [     IFEND;
2F6 7284      [
2F6 7285      [ PROCEND expand_segment_table;
0 7286      [

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
FIND_AVAILABLE_SEGMENT_NUMBER

```

0 7288      PROCEDURE find_available_segment_number
0 7289      (
0 7290          [ xcb_p: ^ost$execution_control_block;
0 7291          [ segment_res_state: mmt$segment_reservation_state;
0 7292          [ VAR segnum: ost$segment;
0 7293          [ VAR status: ost$status];
0 7294      [
0 7295      [ VAR
0 7296          [ sdt_p: mmt$max_sdt_p,
0 7297          [ sdtx_p: mmt$max_sdtx_p,
0 7298          [ segment_table_length: integer;
0 7299      [
0 7300      [
0 7301      [ { Find an available segment number.
0 7302      [
0 7303      [ status.normal := TRUE;
4 7304      [ segnum := mmv$first_transient_seg_index - 1;
4 7305      [ segment_table_length := xcb_p^.xp.segment_table_length;
4 7306      [ mmp$get_max_sdt_sdtx_pointer (xcb_p, sdt_p, sdtx_p);
4 7307      [
4 7308      [ REPEAT
54 7309      [     segnum := segnum + 1;
54 7310      [     IF segnum > segment_table_length THEN
62 7311      [         expand_segment_table (xcb_p, status);
76 7312      [         IF NOT status.normal THEN
7E 7313      [             RETURN;
80 7314      [             IFEND;
80 7315      [             segment_table_length := xcb_p^.xp.segment_table_length;
8E 7316      [             IFEND;
8E 7317      [             UNTIL (sdt_p^.st [segnum].ste.v1 = osc$vl_invalid_entry) AND
CO 7318      [                 (sdtx_p^.sdtx_table [segnum].segment_reservation_state = segment_res_state);
CO 7319      [
CO 7320      [ PROCEND find_available_segment_number;
0 7321      [

```

NDS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
STORE_STE_IN_SEGMENT_TABLE

```

0 7323
0 7324 PROCEDURE store_ste_in_segment_table
0 7325 ( xsdt_entry: mmt$segment_descriptor;
0 7326 sfid: gft$system_file_identifier;
0 7327 ste_p: ^mmt$segment_descriptor;
0 7328 fde_entry_p: gft$locked_file_desc_entry_p;
0 7329 segnum: ost$segment);
0 7330
0 7331 { This routine is tricky so make sure you understand it before changing it!!!!!!!!!!!!
0 7332 { This routine stores a STE entry into the segment table. If the ASID in the new ste
0 7333 { entry is zero then the procedure is straightforward.
0 7334 { If, however, the STE already has an ASID then things get more complicated since monitor may be
0 7335 { changing the ASID asynchronously while this routine is running.
0 7336 { NEVER let a STE entry get into the segment table if the ASID/ASTI dont match
0 7337 { after putting an entry in with a non-zero ASID, check the AST to see if the AST.SFID
0 7338 { agrees with the SDTX.SFID. If they agree all is well. Otherwise, clear the ASID/ASTI to zero;
0 7339 { the assumption is that Memory Manager changed the ASID. The correct ASID will be fetched
0 7340 { on the first page fault. (this should not happen very much.)
0 7341
0 7342 VAR
0 7343 aste_p: ^mmt$active_segment_table_entry,
0 7344 asid: ost$asid,
0 7345 asti: mmt$ast_index,
0 7346 rb: mmt$rb_ring1_segment_request,
0 7347 sdt_entry: mmt$segment_descriptor;
0 7348
0 7349 sdt_entry := xsdt_entry;
4 7350 asti := fde_entry_p^.asti;
4 7351 IF (asti = 0) OR (sdt_entry.ste.asid = OFFF(16)) OR (mmv$ast_p = NIL) THEN
2C 7352 ste_p^ := sdt_entry;
32 7353 ELSE
32 7354 sdt_entry.asti := asti;
32 7355 mmp$asid (asti, asid);
32 7356 sdt_entry.ste.asid := asid;
32 7357 ste_p^ := sdt_entry;
32 7358 aste_p := ^mmv$ast_p^ [sdt_entry.asti];
32 7359
32 7360 IF (aste_p^.in_use) AND (aste_p^.sfid = sfid) AND
172 7361 (((sfid.residence = gfc$tr_job) AND (aste_p^.ijl_ordinal = jmv$jcb.ijl_ordinal)) OR
172 7362 ((sfid.residence = gfc$tr_system) AND
172 7363 (((aste_p^.queue_id = mmc$pr_job_working_set) AND (aste_p^.ijl_ordinal = jmv$jcb.ijl_ordinal) AND
172 7364 ((fde_entry_p^.queue_status = gfc$qs_job_working_set) OR
172 7365 ((fde_entry_p^.queue_status = gfc$qs_job_shared) AND (fde_entry_p^.attach_count = 1)))) OR
172 7366 ((aste_p^.queue_id = mmc$pq_shared_first) AND (aste_p^.queue_id <= mmc$pq_shared_last)) AND
172 7367 (aste_p^.ijl_ordinal = jmv$system_ijl_ordinal) AND
172 7368 ((fde_entry_p^.queue_status = gfc$qs_global_shared) OR
172 7369 ((fde_entry_p^.queue_status = gfc$qs_job_shared) AND (fde_entry_p^.attach_count > 1)))))) THEN
172 7370
172 7371 mmv$sfid_match := mmv$sfid_match + 1;
17E 7372 ELSE
17E 7373 mmv$sfid_mismatch := mmv$sfid_mismatch + 1;
17E 7374 ste_p^ := xsdt_entry;
17E 7375
17E 7376 { If the file is job shared and there is more that one user (attach) of the file, the above tests which
17E 7377 { would have allowed the asid to be stored may have failed because pages of the file are being kept in

```

NDS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
STORE_STE_IN_SEGMENT_TABLE

```

17E 7378 { the working set and should now be kept in the shared queue. (Or they may have failed because the
17E 7379 { asid changed--we can't be sure by looking at the ast.queue_id.) Issue a monitor request to straighten
17E 7380 { out job shared files. If there is more than one user, pages of job shared files must be removed
17E 7381 { from the jws of the original job and moved to the shared queue before a second user can reference
17E 7382 { any of the pages.
17E 7383
17E 7384 IF (fde_entry_p^.queue_status = gfc$qs_job_shared) AND (fde_entry_p^.attach_count > 1) THEN
19E 7385 rb.reqcode := sys$rc_ring1_segment_request;
19E 7386 rb.request := mmc$sr1_remove_job_shared_pages;
19E 7387 rb.system_file_id := sfid;
19E 7388 rb.segment_number := segnum;
19E 7389 rb.server_file := FALSE;
19E 7390 i$call_monitor [#LOC (rb), #SIZE (rb)];
1D6 7391 IFEND;
1D6 7392 IFEND;
1D6 7393 IFEND;
1D6 7394 PROCEND store_ste_in_segment_table;
0 7395

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
ADD_SDT_SDTX_ENTRY

```

0 7397
0 7398 {
0 7399 { The purpose of this procedure is to add the SDT and SDTX entries to the
0 7400 { SDT and the SDTX tables respectively. The caller can specify a segment
0 7401 { number to use or this procedure will use the first available one.
0 7402 {
0 7403 {
0 7404 { MPM$ADD_SDT_SDTX_ENTRY (SDT_ENTRY, SDTX_ENTRY, FDE_ENTRY,
0 7405 { SHARED_TASKID_ARRAY, SEGMENT_NUMBER, STATUS)
0 7406 {
0 7407 { SDT_ENTRY: (input) This parameter is the SDT entry to be added to the
0 7408 { SDT table.
0 7409 { SDTX_ENTRY: (input) This parameter is the SDTX entry to be added to the
0 7410 { SDTX table.
0 7411 {
0 7412 { FDE_ENTRY: (input) This parameter is the FDE entry for the segment being
0 7413 { added.
0 7414 {
0 7415 { SHARED_TASKID_ARRAY: (input) This parameter specifies the list of task-ids
0 7416 { in which this segment should be added. Currently, the only user of
0 7417 { this capability is ADA-TASKING. All other callers should pass a NIL
0 7418 { pointer for this parameter.
0 7419 {
0 7420 { SEGMENT_NUMBER: (input) This parameter specifies the segment number which
0 7421 { the user passed or the system assigned for this instance of open.
0 7422 {
0 7423 { STATUS: (output) This parameter is where the request status is returned
0 7424 { to the caller.
0 7425 {
0 7426 { dme$unable_to_locate_fde
0 7427 { dme$unable_to_get_fd_lock
0 7428 { mme$contig_mem_seg_violation
0 7429 { mme$invalid_length_requested
0 7430 { mme$invalid_pva
0 7431 { mme$invalid_ring_brackets
0 7432 { mme$invalid_shared_taskid
0 7433 { mme$pages_already_assigned
0 7434 { mme$ref_to_unrecovered_file
0 7435 { mme$ring_violation
0 7436 { mme$segment_number_is_in_use
0 7437 { mme$segment_number_not_in_use
0 7438 { mme$segment_number_too_big
0 7439 { mme$segment_origin_invalid
0 7440 { mme$segment_table_is_full
0 7441 {
0 7442 {
0 7443 {
0 7444 {
0 7445 {
0 7446 {
0 7447 PROCEDURE add_sdt_sdtx_entry
0 7448 { sdt_entry: mmt$segment_descriptor;
0 7449 { sdtx_entry: mmt$segment_descriptor_extended;
0 7450 { fde_entry_p: gft$locked_file_desc_entry_p;
0 7451 { shared_taskid_array: ^array [1..*] of pmt$task_id;

```

SOURCE LIST OF mmm\$segment_manager_system_core NOS/VE CYBIL/II 1.0 89102

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NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
ADD_SDT_SDTX_ENTRY

```

0 7452 segment_number: ost$segment);
0 7453
0 7454 VAR
0 7455 cache_bypass: boolean,
0 7456 cell_p: ^cell,
0 7457 i: integer,
0 7458 local_sdt: mmt$segment_descriptor,
0 7459 local_sdtx: mmt$segment_descriptor_extended,
0 7460 pva_p: ^ost$pva,
0 7461 sdt_entry_p: ^mmt$segment_descriptor,
0 7462 shadow_fde_p: gft$locked_file_desc_entry_p,
0 7463 task_sdt_p: ^mmt$segment_descriptor,
0 7464 task_sdtx_p: ^mmt$segment_descriptor_extended,
0 7465 task_xcb: ^ost$execution_control_block,
0 7466 xcb_p: ^ost$execution_control_block;
0 7467
0 7468
0 7469 local_sdt := sdt_entry;
0 7470 local_sdtx := sdtx_entry;
E 7471
E 7472 pmp$find_executing_task_xcb (xcb_p);
14 7473
14 7474 { Set cache bypass if required for multiprocessing.
14 7475
14 7476 cache_bypass := FALSE;
14 7477 IF osv$multiple_cpus_possible THEN
3A 7478 IF (local_sdt.ste.xp = osc$non_executable) OR ((local_sdt.ste.xp <> osc$non_executable) AND
4A 7479 (local_sdt.ste.wp <> osc$non_writable)) THEN
4A 7480
4A 7481 { I dont think we need a clause for global_unnamed files because it appears
4A 7482 { that they would drop out anyway.
4A 7483
4A 7484 IF [fde_entry_p^.file_kind <> gfc$fk_unnamed_file] THEN
54 7485 IF [fde_entry_p^.queue_status = gfc$qs_job_working_set] THEN
5E 7486 IF [jmv$job.i.jle_p^.multiprocessing_allowed] THEN
5E 7487 cache_bypass := TRUE;
5E 7488 local_sdt.ste.v1 := osc$v1_cache_bypass;
7B 7489 IFEND;
7C 7490 ELSE
7C 7491 IF [fde_entry_p^.queue_status = gfc$qs_global_shared] THEN
80 7492 cache_bypass := TRUE;
80 7493 local_sdt.ste.v1 := osc$v1_cache_bypass;
8A 7494 IFEND;
8A 7495 IFEND;
8A 7496 IFEND;
8A 7497 IFEND;
8A 7498 IFEND;
8A 7499
8A 7500
8A 7501 { Add sdt_entry to the task's segment descriptor table (SDT) and the sdtx_entry to the
8A 7502 { segment descriptor table extended (SDTX).
8A 7503
8A 7504 sdt_entry_p := mmp$get_sdt_entry_p (xcb_p, segment_number);
8A 7505 sdtx_entry_p := mmp$get_sdtx_entry_p (xcb_p, segment_number);
8A 7506 local_sdtx.segment_reservation_state := sdtx_entry_p^.segment_reservation_state;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
ADD_SDT_SDTX_ENTRY

```

8A 7507 mmp$set_segment_access_rights (local_sdt, local_sdtx);
10A 7508
10A 7509 sdtx_entry_p^ := local_sdtx;
*WARN+ 7510 store_ste_in_segment_table [local_sdt, local_sdtx.sfid, sdt_entry_p, fde_entry_p, segment_number];
13A 7511 fde_entry_p^.open_count := fde_entry_p^.open_count + 1;
13A 7512
13A 7513 IF local_sdtx.shadow_info.shadow_segment_kind <> mmc$ssk_none THEN
14E 7514 gfp$get_locked_fde_p (local_sdtx.shadow_info.shadow_sfId, shadow_fde_p);
28A 7515 shadow_fde_p^.open_count := shadow_fde_p^.open_count + 1;
28A 7516 gfp$unlock_fde_p (shadow_fde_p);
38A 7517 IFEND;
38A 7518
38A 7519 {If a stack segment was just created, update the TOS pointer.
38A 7520
38A 7521 IF mmc$sa_stack IN sdtx_entry.software_attribute_set THEN
392 7522 cell_p := #ADDRESS [local_sdt.ste.r1, segment_number,
392 7523 (mmv$page_map_offsets [mmc$pmo_user_stack] * osv$page_size) + mmc$ring_crossing_offset];
392 7524 pva_p := #LOC [cell_p];
3CA 7525 xcb_p^ .xp.tos_registers [local_sdt.ste.r1].pva := pva_p;
3CA 7526 fde_entry_p^.stack_for_ring := local_sdt.ste.r1;
3E6 7527 IFEND;
3E6 7528
3E6 7529 IF shared_taskid_array <> NIL THEN
3F4 7530
3F4 7531 { Mmc$sa_stack is removed from the software attribute set before the sdtx entry of the segment
3F4 7532 { is propagated to all of the other tasks. The task which opens the stack segment will be
3F4 7533 { the only task with mmc$sa_stack in its sdtx entry.
3F4 7534
3F4 7535 local_sdtx.software_attribute_set := local_sdtx.software_attribute_set -
3F4 7536 $mmt$software_attribute_set [mmc$sa_stack];
3F4 7537 FOR i := LOWERBOUND [shared_taskid_array^] TO UPPERBOUND [shared_taskid_array^] DO
410 7538 pmp$find_task_xcb [shared_taskid_array^ [i], task_xcb];
436 7539 IF task_xcb <> xcb_p THEN
446 7540 task_sdt_p := mmp$get_sdt_entry_p (task_xcb, segment_number);
446 7541 task_sdtx_p := mmp$get_sdtx_entry_p (task_xcb, segment_number);
446 7542 task_sdtx_p^ := local_sdtx;
4A2 7543 mmp$set_segment_access_rights [local_sdt, task_sdtx_p^];
*WARN+ 7544 store_ste_in_segment_table [local_sdt, local_sdtx.sfid, task_sdt_p, fde_entry_p, segment_number];
508 7545 fde_entry_p^.open_count := fde_entry_p^.open_count + 1;
51A 7546 IFEND;
51A 7547 FOREND;
520 7548 IFEND;
520 7549
520 7550 IF (osv$cpus_logically_on > 1) AND (NOT cache_bypass) THEN
530 7551 cell_p := #ADDRESS [1, segment_number, 0];
530 7552 #PURGE_BUFFER (osc$pva_purge_segment_cache, cell_p);
548 7553 IFEND;
548 7554
548 7555 PROCEND add_sdt_sdtx_entry;
O 7556

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$ASSIGN_MASS_STORAGE

```

O 7558 {
O 7559 { Purpose:
O 7560 { This procedure assigns disk space for all pages currently assigned to a segment/file.
O 7561 { If necessary, it converts a transient file into a disk file.
O 7562 {
O 7563 { segment_number: If non zero, this specifies the segment number
O 7564 { sfid: If segment number is zero, this is the SFID of the file
O 7565 { min_allocation_length: Normally zero. If zero disk space is assigned to all
O 7566 { pages that exist in the file. If non-zero, contiguous disk space is assign for
O 7567 { offset zero thru this offset. NOTE if
O 7568 {
O 7569 {
O 7570 {
O 7571 {
O 7572 PROCEDURE [XDCL, #GATE] mmp$assign_mass_storage
O 7573 { segment_number: ost$segment;
O 7574 { xsfid: gft$system_file_identifier;
O 7575 { min_allocation_length: ost$segment_length;
O 7576 { VAR status: ost$status;
O 7577 {
O 7578 { VAR
O 7579 { assign_active: amt$file_byte_address,
O 7580 { fde_p: gft$locked_file_desc_entry_p,
O 7581 { file_attributes_p: ^array [1..*] of dmt$file_attribute,
O 7582 { file_limits: sft$file_space_limit_kind,
O 7583 { length_to_allocate: ost$segment_length,
O 7584 { max_pages_no_file: integer,
O 7585 { page_streaming_ts_shift: 0..15,
O 7586 { sdt_entry_p: ^mmt$segment_descriptor,
O 7587 { sdtx_entry_p: ^mmt$segment_descriptor_extended,
O 7588 { sfid: gft$system_file_identifier,
O 7589 { xcb_p: ^ost$execution_control_block;
O 7590 {
O 7591 {
O 7592 status.normal := TRUE;
A 7593
A 7594 pmp$find_executing_task_xcb (xcb_p);
1A 7595 IF segment_number <> 0 THEN
36 7596 sdtx_entry_p := mmp$get_sdtx_entry_p (xcb_p, segment_number);
36 7597 file_limits := sdtx_entry_p^.file_limits_enforced;
36 7598 assign_active := sdtx_entry_p^.assign_active;
36 7599 sfid := sdtx_entry_p^.sfid;
36 7600 IF (mmc$sa_wired IN sdtx_entry_p^.software_attribute_set) OR
7C 7601 (mmc$sa_fixed IN sdtx_entry_p^.software_attribute_set) THEN
7C 7602 osp$set_status_abnormal ('MM', mme$segment_not_pageable, '', status);
AE 7603 RETURN;
BO 7604 IFEND;
B4 7605 ELSE
B4 7606 file_limits := sfc$no_limit;
B4 7607 assign_active := mmc$assign_active_escaped;
B4 7608 sfid := xsfid;
C8 7609 IFEND;
C8 7610
C8 7611 gfp$get_locked_fde_p (sfid, fde_p);
C8 7612

```

NDS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$ASSIGN_MASS_STORAGE

```

1F2 7613
1F2 7614 IF NOT dmV$idle_system THEN
1FE 7615 IF fde_p^media = gfc$fm_transient_segment THEN
20A 7616 PUSH file_attributes_p: [1 .. 1];
224 7617 file_attributes_p^ [1].keyword := dmc$class;
224 7618 IF jmv$executing_within_system_job THEN
23A 7619 file_attributes_p^ [1].class := rmc$msc_system_critical_files;
246 7620 ELSE
246 7621 file_attributes_p^ [1].class := rmc$msc_user_temporary_files;
24E 7622 IFEND;
24E 7623 dmp$create_disk_file (fde_p, file_attributes_p, 0, sfid, status);
27C 7624 IF (assign_active <> mmc$assign_active_escaped) AND
292 7625 NOT (sdtX_entry_p^stream.transfer_size_specified) THEN
292 7626 mmp$convert_ps_transfer_size (fde_p^transfer_unit_size, page_streaming_ts_shift);
2D2 7627 sdtX_entry_p^stream.transfer_size := page_streaming_ts_shift;
2E2 7628 IFEND;
2E2 7629 IFEND;
2E2 7630
2E2 7631 IF status.normal THEN
2EE 7632 length_to_allocate := fde_p^eoi_byte_address;
2EE 7633 IF (min_allocation_length > 0) OR (length_to_allocate < mmv$sparse_threshold) OR
314 7634 (fde_p^media = gfc$fm_served_file) THEN
314 7635 IF length_to_allocate < min_allocation_length THEN
31C 7636 length_to_allocate := min_allocation_length;
320 7637 IFEND;
320 7638 dmp$allocate_file_space_r1 (sfid, 0, length_to_allocate, 0, osc$nowait, file_limits, status);
34E 7639 ELSE
34E 7640 dmp$sparse_allocate (sfid, assign_active, file_limits, status);
36C 7641 IFEND;
36C 7642 IFEND;
36C 7643 IFEND;
36C 7644
36C 7645 gfp$unlock_fde_p (fde_p);
460 7646
460 7647 { If everything worked OK, clear the assign active flag in the SDTX or XCB since there cannot
460 7648 { be any more escaped allocation or allocation required.
460 7649
460 7650 IF status.normal THEN
46C 7651 IF segment_number <> 0 THEN
474 7652 sdtX_entry_p^assign_active := mmc$assign_active_null;
47E 7653 ELSE
47E 7654 xcb_p^assign_active_sfid := gfv$null_sfid;
48E 7655 IFEND;
48E 7656 IFEND;
48E 7657
48E 7658 PROCEND mmp$assign_mass_storage;
O 7659

```

NDS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$BUILD_SEGMENT

```

O 7661
O 7662 PROCEDURE [XDCL, #GATE] mmp$build_segment
O 7663 ( attrib: mmt$segment_attrib_descriptor;
O 7664 shared_taskid_array: ^array [1 .. *] of pmt$task_id;
O 7665 VAR segment_pointer: mmt$segment_pointer;
O 7666 VAR status: ost$status);
O 7667
O 7668 VAR
O 7669 i: integer;
O 7670 fde_entry_p: gft$file_desc_entry_p;
O 7671 file_hash: 0 .. 255;
O 7672 locked_fde_entry_p: gft$locked_file_desc_entry_p;
O 7673 page_streaming_ts_shift: 0 .. 15;
O 7674 page_streaming_transfer_size: integer;
O 7675 sdt_entry: mmt$segment_descriptor;
O 7676 sdtX_entry: mmt$segment_descriptor_extended;
O 7677 sdt_entry_p: ^mmt$segment_descriptor;
O 7678 sdtX_entry_p: ^mmt$segment_descriptor_extended;
O 7679 segment_length: ost$segment_length;
O 7680 segment_number: ost$segment;
O 7681 segment_res_state: mmt$segment_reservation_state;
O 7682 sfid: gft$system_file_identifier;
O 7683 shadow_fde_p: gft$file_desc_entry_p;
O 7684 shadow_sdt_p: ^mmt$segment_descriptor;
O 7685 shadow_sdtX_p: ^mmt$segment_descriptor_extended;
O 7686 task_xcb: ^ost$execution_control_block;
O 7687 xcb_p: ^ost$execution_control_block;
O 7688
O 7689 status.normal := TRUE;
4 7690 sdt_entry := mmv$default_sdt_entry;
4 7691 sdtX_entry := mmv$default_sdtX_entry;
20 7692 sdt_entry.ste.r1 := attrib.validating_ring_number;
20 7693 sdt_entry.ste.r2 := attrib.validating_ring_number;
20 7694 sdtX_entry.open_validating_ring_number := attrib.validating_ring_number;
20 7695 sdtX_entry.file_limits_enforced := attrib.file_limits_to_enforce;
20 7696 segment_number := 0;
20 7697 IF shared_taskid_array <> NIL THEN
54 7698 FOR i := LOWERBOUND (shared_taskid_array) TO UPPERBOUND (shared_taskid_array) DO
52 7699 pmp$find_task_xcb (shared_taskid_array^ [i], task_xcb);
88 7700 IF task_xcb = NIL THEN
92 7701 osp$set_status_abnormal ('MM', mme$invalid_shared_taskid, '', status);
C2 7702 RETURN;
C4 7703 IFEND;
C4 7704 FOREND;
CA 7705 IFEND;
CA 7706
CA 7706 IF attrib.sfid = gfv$null_sfid THEN
DE 7707 gfp$assign_fde (gfc$tr_job, segment_number, sfid, fde_entry_p);
108 7708 IF fde_entry_p = NIL THEN
112 7709 osp$set_status_abnormal ('MM', mme$unable_to_assign_fde, '', status);
142 7710 RETURN;
144 7711 IFEND;
144 7712 gfp$get_locked_fde_p (sfid, locked_fde_entry_p);
274 7713 locked_fde_entry_p^queue_status := gfc$qs_job_working_set;
274 7714 locked_fde_entry_p^file_kind := gfc$fk_unnamed_file;
286 7715 ELSE

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$BUILD_SEGMENT

```

286 7716      sfd := attrib.sfid;
286 7717      gfp$get_locked_fde_p (sfid, locked_fde_entry_p);
3B8 7718      IF locked_fde_entry_p = NIL THEN
3C2 7719          osp$set_status_abnormal ('MM', mme$invalid_sfid, '', status);
3F6 7720      RETURN;
3F8 7721      IFEND;
3FA 7722      IFEND;
3FA 7723
3FA 7724      sdtx_entry.sfid := sfid;
3FA 7725
3FA 7726      /user_attributes/
3FA 7727      BEGIN
3FA 7728          IF attrib.user_attributes <> NIL THEN
410 7729              FOR i := LOWERBOUND (attrib.user_attributes^ ) TO UPPERBOUND (attrib.user_attributes^ ) DO
42A 7730                  CASE attrib.user_attributes^ [i].keyword OF
48E 7731                      = mmc$kw_null_keyword :
4C2 7732                      = mmc$kw_ring_numbers :
4C2 7733                          IF (attrib.user_attributes^ [i].r1 > attrib.user_attributes^ [i].r2) OR
4D2 7734                              (attrib.user_attributes^ [i].r2 = 0) THEN
4D2 7735                              osp$set_status_abnormal ('MM', mme$invalid_ring_brackets, '', status);
506 7736                              EXIT /user_attributes/;
50A 7737                          IFEND;
50A 7738                              sdt_entry.ste.r1 := attrib.user_attributes^ [i].r1;
50A 7739                              sdt_entry.ste.r2 := attrib.user_attributes^ [i].r2;
516 7740                              = mmc$kw_segment_number :
516 7741                              segment_number := attrib.user_attributes^ [i].segnum;
522 7742                              = mmc$kw_hardware_attributes :
522 7743                              IF mmc$ha_read IN attrib.user_attributes^ [i].hardware_attri_set THEN
540 7744                                  sdt_entry.ste.rp := osc$read_uncontrolled;
548 7745                              ELSE
548 7746                                  sdt_entry.ste.rp := osc$non_readable;
54C 7747                              IFEND;
54C 7748                              IF mmc$ha_binding IN attrib.user_attributes^ [i].hardware_attri_set THEN
56A 7749                                  IF attrib.validating_ring_number <= 3 THEN
574 7750                                      sdt_entry.ste.rp := osc$binding_segment;
57C 7751                                  ELSE
57C 7752                                      osp$set_status_abnormal ('MM', mme$binding_attribute_invalid, '', status);
580 7753                                      EXIT /user_attributes/;
584 7754                                  IFEND;
584 7755                                  IF mmc$ha_write IN attrib.user_attributes^ [i].hardware_attri_set THEN
584 7756                                      sdt_entry.ste.wp := osc$write_uncontrolled;
5D2 7757                                  ELSE
5DA 7758                                      sdt_entry.ste.wp := osc$non_writable;
5DA 7759                                  IFEND;
5DE 7760                                  IF mmc$ha_cache_bypass IN attrib.user_attributes^ [i].hardware_attri_set THEN
5FC 7762                                      sdt_entry.ste.v1 := osc$sv1_cache_bypass;
606 7763                                  ELSE
606 7764                                      sdt_entry.ste.v1 := osc$sv1_regular_segment;
60A 7765                                  IFEND;
60A 7766                                  IF mmc$ha_execute IN attrib.user_attributes^ [i].hardware_attri_set THEN
628 7767                                      sdt_entry.ste.xp := osc$non_privileged;
630 7768                                  ELSE
630 7769                                      IF mmc$ha_execute_local IN attrib.user_attributes^ [i].hardware_attri_set THEN
638 7770                                          sdt_entry.ste.xp := osc$local_privilege;

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MMP\$BUILD_SEGMENT

```

640 7771      ELSE
640 7772          IF (mmc$ha_execute_global IN attrib.user_attributes^ [i].hardware_attri_set) AND
650 7773              (attrib.validating_ring_number > 1) THEN
650 7774              osp$set_status_abnormal ('MM', mme$execute_global_invalid, '', status);
684 7775              EXIT /user_attributes/;
68C 7776          ELSE
68C 7777              sdt_entry.ste.xp := osc$non_executable;
690 7778          IFEND;
690 7779          IFEND;
690 7780          IFEND;
694 7781          = mmc$kw_software_attributes :
694 7782          IF attrib.validating_ring_number <= 3 THEN
69E 7783              sdtx_entry.software_attribute_set := attrib.user_attributes^ [i].software_attri_set;
6BC 7784          ELSEIF ((attrib.validating_ring_number <= 6) AND (([mmt$software_attribute_set [mmc$sa_stack] *
70A 7785              attrib.user_attributes^ [i].software_attri_set) < [mmt$software_attribute_set [i]] OR
70A 7786              ((attrib.validating_ring_number > 6) AND ([mmt$software_attribute_set
70A 7787              [mmc$sa_wired, mmc$sa_fixed, mmc$sa_stack] * attrib.user_attributes^ [i].
70A 7788              software_attri_set) < [mmt$software_attribute_set [i]])) THEN
70A 7789              osp$set_status_abnormal ('MM', mme$software_attribute_invalid, '', status);
73E 7790              EXIT /user_attributes/;
746 7791          ELSE
746 7792              sdtx_entry.software_attribute_set := attrib.user_attributes^ [i].software_attri_set;
760 7793          IFEND;
764 7794          = mmc$kw_error_exit_procedure :
764 7795          osp$set_status_abnormal ('MM', mme$unsupported_keyword, '', status);
798 7796          EXIT /user_attributes/;
7A0 7797          = mmc$kw_max_segment_length :
7A0 7798          locked_fde_entry_p^ .file_limit := attrib.user_attributes^ [i].max_length;
7B0 7799          = mmc$kw_gl_key :
7B0 7800          sdt_entry.ste.key_lock := attrib.user_attributes^ [i].gl_key;
7CE 7801          = mmc$kw_clear_space :
7D2 7802          = mmc$kw_preset_value :
7D2 7803          locked_fde_entry_p^ .preset_value := attrib.user_attributes^ [i].preset_value;
7F4 7804          = mmc$kw_segment_access_control :
7F4 7805          IF attrib.user_attributes^ [i].access_control.cache_bypass = TRUE THEN
80E 7806              sdt_entry.ste.v1 := osc$sv1_cache_bypass;
818 7807          ELSE
818 7808              sdt_entry.ste.v1 := osc$sv1_regular_segment;
81C 7809          IFEND;
81C 7810          IF (attrib.user_attributes^ [i].access_control.execute_privilege = osc$global_privilege) AND
840 7812              (attrib.validating_ring_number > 1) THEN
840 7813              osp$set_status_abnormal ('MM', mme$execute_global_invalid, '', status);
874 7814              EXIT /user_attributes/;
87C 7815          ELSE
87C 7816              sdt_entry.ste.xp := attrib.user_attributes^ [i].access_control.execute_privilege;
896 7817          IFEND;
896 7818
896 7819          IF (attrib.user_attributes^ [i].access_control.read_privilege = osc$binding_segment) AND
8BA 7820              (attrib.validating_ring_number > 3) THEN
8BA 7821              osp$set_status_abnormal ('MM', mme$binding_attribute_invalid, '', status);
8EE 7822              EXIT /user_attributes/;
8F2 7823          IFEND;
8F2 7824          sdt_entry.ste.rp := attrib.user_attributes^ [i].access_control.read_privilege;
8F2 7825          sdt_entry.ste.wp := attrib.user_attributes^ [i].access_control.write_privilege;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$BUILD_SEGMENT

```

918 7826      = mmc$kw_asid =
918 7827      IF attrib.validating_ring_number <> 1 THEN
920 7828          osp$set_status_abnormal ('MM', mme$asid_specified, '', status);
954 7829          EXIT /user_attributes/;
958 7830      IFEND;
958 7831
958 7832      IF (attrib.user_attributes^ [i].asid = osc$asid_ei) OR
98A 7833          (attrib.user_attributes^ [i].asid = osc$asid_eie) OR
98A 7834          (attrib.user_attributes^ [i].asid = osc$asid_nos) THEN
98A 7835          sdt_entry.ste.asid := attrib.user_attributes^ [i].asid;
98A 7836      ELSE
98B 7837          osp$set_status_abnormal ('MM', mme$invalid_asid_specified, '', status);
98B 7838          EXIT /user_attributes/;
98C 7839      IFEND;
98C 7840      = mmc$kw_wired_segment =
98C 7841          sdt_entry.software_attribute_set := sdt_entry.software_attribute_set +
98C 7842              $mmt$software_attribute_set [mmc$sa_wired];
98C 7843          locked_fde_entry_p^.file_limit := attrib.user_attributes^ [i].wired_segment_length;
A10 7844      = mmc$kw_inheritance =
A10 7845          sdt_entry.inheritance := attrib.user_attributes^ [i].inheritance;
A2E 7846      = mmc$kw_shadow_segment =
A2E 7847          IF ((attrib.user_attributes^ [i].shadow_length MOD mmc$shadow_allocation_size) <> 0) THEN
A4C 7848              osp$set_status_abnormal ('MM', mme$length_not_0_mod_16384, '', status);
A80 7849              EXIT /user_attributes/;
A84 7850          IFEND;
A84 7851          IF (#OFFSET (attrib.user_attributes^ [i].shadow_p) MOD mmc$shadow_allocation_size) <> 0) THEN
AAC 7852              osp$set_status_abnormal ('MM', mme$address_not_0_mod_16384, '', status);
AD4 7853              EXIT /user_attributes/;
AD8 7854          IFEND;
AD8 7855          sdt_entry.shadow_info.shadow_length_page_count :=
AD8 7856              attrib.user_attributes^ [i].shadow_length DIV osv$page_size;
AD8 7857          sdt_entry.shadow_info.shadow_start_page_number := #OFFSET (attrib.user_attributes^ [i].
AD8 7858              shadow_p) DIV osv$page_size;
AD8 7859          mmp$validate_segment_number [#SEGMENT (attrib.user_attributes^ [i].shadow_p),
B1A 7860              shadow_sdt_p, shadow_sdtx_p, status];
B1A 7861          IF NOT status.normal THEN
B22 7862              EXIT /user_attributes/;
B26 7863          IFEND;
B26 7864          IF shadow_sdtx_p^.shadow_info.shadow_segment_kind <> mmc$ssk_none THEN
B32 7865              osp$set_status_abnormal ('MM', mme$invalid_shadow_segment, '', status);
B62 7866              EXIT /user_attributes/;
B66 7867          IFEND;
B66 7868          sdt_entry.shadow_info.shadow_segment_kind := mmc$ssk_segment_number;
B66 7869          sdt_entry.shadow_info.shadow_segment_number := #SEGMENT (attrib.user_attributes^ [i].shadow_p);
B66 7870          sdt_entry.shadow_info.shadow_sfid := shadow_sdtx_p^.sfid;
B66 7871          shadow_sdtx_p^.shadow_info.passive_for_shadow_by_segnum := TRUE;
B9C 7872      = mmc$kw_ps_transfer_size =
B9C 7873          page_streaming_transfer_size := attrib.user_attributes^ [i].ps_transfer_size;
B9C 7874          IF page_streaming_transfer_size > max_specified_transfer_size THEN
BBA 7875              page_streaming_transfer_size := max_specified_transfer_size;
BBE 7876          IFEND;
BBE 7877          mmp$convert_ps_transfer_size (page_streaming_transfer_size, page_streaming_ts_shift);
BF8 7878          sdt_entry.stream.transfer_size := page_streaming_ts_shift;
BF8 7879          sdt_entry.stream.transfer_size_specified := TRUE;
C12 7880      ELSE

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NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$BUILD_SEGMENT

```

C12 7881          EXIT /user_attributes/;
C16 7882          CASEND;
C16 7883          FOREND;
C1E 7884          IFEND;
C1E 7885
C1E 7886      { Find an available segment number if the caller did not supply one.
C1E 7887
C1E 7888          pmp$find_executing_task_xcb (xcb_p);
C24 7889
C24 7890          IF segment_number = 0 THEN
C40 7891              IF shared_taskid_array = NIL THEN
C4E 7892                  segment_res_state := mmc$srs_not_reserved;
C54 7893              ELSE
C54 7894                  segment_res_state := mmc$srs_reserved_shared_stack;
C56 7895              IFEND;
C56 7896              find_available_segment_number (xcb_p, segment_res_state, segment_number, status);
C78 7897          ELSE
C78 7898              mmp$validate_segment_number (segment_number, sdt_entry_p, sdtx_entry_p, status);
CA0 7899              IF NOT status.normal AND (status.condition = mme$segment_number_not_in_use) THEN
CB8 7900                  status.normal := TRUE;
CB8 7901                  WHILE (segment_number > xcb_p^.xp.segment_table_length) DO
CDO 7902                      expand_segment_table (xcb_p, status);
CE2 7903                  IF NOT status.normal THEN
CEA 7904                      EXIT /user_attributes/;
CF4 7905                  IFEND;
CF4 7906                  WHILEND;
D16 7907              ELSEIF status.normal THEN
D1E 7908                  osp$set_status_abnormal ('MM', mme$segment_number_is_in_use, '', status);
D4E 7909                  IFEND;
D50 7910          IFEND;
D50 7911          END /user_attributes/;
D52 7912
D52 7913          IF NOT status.normal THEN
D5E 7914              gfp$unlock_fde_p (locked_fde_entry_p);
E54 7915              IF attrib.sfid = gfv$null_sfid THEN
E68 7916                  gfp$free_fde (fde_entry_p);
E7C 7917              IFEND;
E7C 7918              RETURN;
E7E 7919          IFEND;
E7E 7920
E7E 7921          locked_fde_entry_p^.last_segment_number := segment_number;
E7E 7922          locked_fde_entry_p^.global_task_id := xcb_p^.global_task_id;
E7E 7923
E7E 7924          IF (mmc$sa_read_transfer_unit IN sdt_entry.software_attribute_set) THEN
EA2 7925              sdt_entry.stream.sequential_accesses := mmv$page_streaming_prestream;
EAE 7926          IFEND;
EAE 7927
EAE 7928          IF NOT sdt_entry.stream.transfer_size_specified THEN
EB8 7929              mmp$convert_ps_transfer_size (locked_fde_entry_p^.transfer_unit_size, page_streaming_ts_shift);
EF8 7930              IF mmv$page_streaming_transfer > 0 THEN (override transfer size with mmv$page_streaming_transfer
FO4 7931                  mmp$convert_ps_transfer_size (mmv$page_streaming_transfer, page_streaming_ts_shift);
F3A 7932              IFEND;
F3A 7933              sdt_entry.stream.transfer_size := page_streaming_ts_shift;
F4A 7934          IFEND;
F4A 7935

```


NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$BUILD_SEGMENT

```

F4A 7936 add_sdt_sdtx_entry (sdt_entry, sdtx_entry, locked_fde_entry_p, shared_taskid_array, segment_number);
F6A 7937
F6A 7938 IF locked_fde_entry_p^.file_limit < osc$maximum_offset THEN
F7A 7939 segment_length := locked_fde_entry_p^.file_limit;
F7E 7940 ELSE
F7E 7941 segment_length := osc$maximum_offset;
F84 7942 IFEND;
F84 7943
F84 7944 CASE attrib.pointer_kind OF
F98 7945 = mmc$sequence_pointer :
F98 7946 i#build_adaptable_seq_pointer (sdt_entry.ste.r1, segment_number, 0 {offset}, segment_length, 0,
FDO 7947 segment_pointer.seq_pointer);
FDO 7948 = mmc$heap_pointer :
FDO 7949 i#build_adaptable_heap_pointer (sdt_entry.ste.r1, segment_number, 0 {offset}, segment_length,
1004 7950 segment_pointer.heap_pointer);
1004 7951 ELSE
1004 7952 segment_pointer.cell_pointer := #ADDRESS (sdt_entry.ste.r1, segment_number, 0 {offset} );
1022 7953 CASEND;
1022 7954
1022 7955 gfp$unlock_fde_p (locked_fde_entry_p);
1116 7956
1116 7957 PROCEND mmp$build_segment;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$CHANGE_SEG_INHERITANCE_R1

```

O 7959
O 7960 PROCEDURE [XDCL, #GATE] mmp$change_seg_inheritance_r1
O 7961 (
O 7962 segment_number: ost$segment;
O 7963 validating_ring: ost$valid_ring;
O 7964 segment_inheritance: mmt$segment_inheritance;
O 7965 VAR status: ost$status);
O 7966
O 7967 VAR
O 7968 sdt_entry_p: ^mmt$segment_descriptor,
O 7969 sdtx_entry_p: ^mmt$segment_descriptor_extended;
O 7970
O 7971 mmp$validate_segment_number (segment_number, sdt_entry_p, sdtx_entry_p, status);
30 7971 IF NOT status.normal THEN
38 7972 RETURN;
3A 7973 IFEND;
3A 7974
3A 7975 IF (validating_ring > sdt_entry_p^.ste.r2) THEN
4E 7976 osp$set_status_abnormal ('MM', mme$ring_violation, '', status);
80 7977 RETURN;
82 7978 IFEND;
82 7979
82 7980 IF (segment_inheritance = mmc$si_transfer_segment) AND (sdtx_entry_p^.inheritance = mmc$si_none) THEN
98 7981 sdtx_entry_p^.inheritance := mmc$si_transfer_segment;
9E 7982 ELSEIF (segment_inheritance = mmc$si_share_segment) AND (sdtx_entry_p^.inheritance = mmc$si_none) THEN
B4 7983 sdtx_entry_p^.inheritance := mmc$si_share_segment;
BA 7984 ELSE
BA 7985 osp$set_status_abnormal ('MM', mme$illegal_segment_origin_chg, '', status);
EC 7986 IFEND;
EC 7987
EC 7988 PROCEND mmp$change_seg_inheritance_r1;
O 7989

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$CHANGE_SEGMENT_NUMBER_R1

```

0 7991
0 7992 PROCEDURE [XDCL, #GATE] mmp$change_segment_number_r1
0 7993   (   old_segment_number: ost$segment;
0 7994     new_segment_number: ost$segment;
0 7995     validating_ring_number: ost$valid_ring;
0 7996     VAR status: ost$status);
0 7997
0 7998   VAR
0 7999     fde_entry_p: gft$locked_file_desc_entry_p,
0 8000     new_sdt_entry: mmt$segment_descriptor,
0 8001     new_sdtX_entry: mmt$segment_descriptor_extended,
0 8002     old_sdt_p: Ammt$segment_descriptor,
0 8003     old_sdtX_p: Ammt$segment_descriptor_extended;
0 8004
0 8005   mmp$fetch_sdt_sdtX_locked_fde (old_segment_number, old_sdt_p, old_sdtX_p, fde_entry_p, status);
38 8006   IF NOT status.normal THEN
40 8007     RETURN;
42 8008   IFEND;
42 8009
42 8010   IF (validating_ring_number > old_sdtX_p^.open_validating_ring_number) THEN
52 8011     osp$set_status_abnormal ('MM', mme$ring_violation, '', status);
84 8012     RETURN;
86 8013   IFEND;
86 8014
86 8015   new_sdt_entry := old_sdt_p^;
86 8016   new_sdtX_entry := old_sdtX_p^;
9C 8017
9C 8018   add_sdt_sdtX_entry (new_sdt_entry, new_sdtX_entry, fde_entry_p, NIL, new_segment_number);
C4 8019
C4 8020   mmp$invalidate_segment (old_segment_number, 1, NIL {shared_taskid}, status);
EE 8021
EE 8022   gfp$unlock_fde_p (fde_entry_p);
1EO 8023
1EO 8024 PROCEND mmp$change_segment_number_r1;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$CHANGE_STACK_ATTRIBUTE_R1

```

0 8026
0 8027 PROCEDURE [XDCL, #GATE] mmp$change_stack_attribute_r1
0 8028   (   stack_pages_to_be_freed: boolean;
0 8029     caller_ring: ost$valid_ring;
0 8030     VAR status: ost$status);
0 8031
0 8032   VAR
0 8033     xcb_p: ^ost$execution_control_block;
0 8034
0 8035     status.normal := TRUE;
4 8036     pmp$find_executing_task_xcb (xcb_p);
14 8037     xcb_p^.stack_pages_saved [caller_ring] := NOT (stack_pages_to_be_freed);
14 8038
14 8039 PROCEND mmp$change_stack_attribute_r1;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$CLOSE_ASID_BASED_SEGMENT

```

0 8041
0 8042 PROCEDURE [XDCL] mmp$close_asid_based_segment
0 8043 {
0 8044   segment_number: ost$segment;
0 8045   VAR status: ost$status);
0 8046
0 8047   VAR
0 8048     pva: ^cell,
0 8049     sdt_entry_p: ^mmt$segment_descriptor,
0 8050     xcb_p: ^ost$execution_control_block;
0 8051
0 8052     status.normal := TRUE;
14 8053     pmp$find_executing_task_xcb {xcb_p};
14 8054
38 8055     IF segment_number > xcb_p^.xp.segment_table_length THEN
6A 8056       osp$set_status_abnormal ('MM', mme$segment_number_too_big, '', status);
6C 8057     RETURN;
6C 8058   IFEND;
6C 8059   sdt_entry_p := mmp$get_sdt_entry_p {xcb_p, segment_number};
6C 8060   IF sdt_entry_p^.ste.v1 = osc$v1_invalid_entry THEN
9E 8061     osp$set_status_abnormal ('MM', mme$segment_number_not_in_use, '', status);
D0 8062   RETURN;
D2 8063   IFEND;
D2 8064
D2 8065 { Delete the Segment table entry.
D2 8066
D2 8067   pva := #ADDRESS (1, segment_number, 0);
D2 8068   #PURGE_BUFFER {osc$pva_purge_all_page_seg_map, pva};
E6 8069   sdt_entry_p^.ste.v1 := osc$v1_invalid_entry;
E6 8070
E6 8071 PROCEND mmp$close_asid_based_segment;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$CLOSE_DEVICE_FILE

```

0 8073
0 8074 PROCEDURE [XDCL, #GATE] mmp$close_device_file
0 8075 {
0 8076   segment_number: ost$segment;
0 8077   VAR status: ost$status);
0 8078
0 8079   VAR
0 8080     fde_entry_p: gft$locked_file_desc_entry_p,
0 8081     pva: ^cell,
0 8082     sdt_entry_p: ^mmt$segment_descriptor,
0 8083     sdtx_entry_p: ^mmt$segment_descriptor_extended;
0 8084
0 8085     status.normal := TRUE;
4 8086     mmp$fetch_sdt_sdtx_locked_fde {segment_number, sdt_entry_p, sdtx_entry_p, fde_entry_p, status};
3E 8087
3E 8088 { Delete the Segment table entry.
3E 8089
3E 8090     pva := #ADDRESS (1, segment_number, 0);
3E 8091     #PURGE_BUFFER {osc$pva_purge_all_page_seg_map, pva};
4E 8092     sdt_entry_p^.ste.v1 := osc$v1_invalid_entry;
4E 8093     fde_entry_p^.open_count := fde_entry_p^.open_count - 1;
4E 8094     gfp$unlock_fde_p {fde_entry_p};
160 8095
160 8096 PROCEND mmp$close_device_file;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$CREATE_INHERITED_SDT

```

0 8098 [
0 8099 [ The purpose of this request is to create the SDT and SDTX for a
0 8100 [ new task. All valid segments with an SDTX.INHERITANCE of mmc$si_share_segment
0 8101 [ in the parent's SDTX are inherited by the new task. For segments in the parent's
0 8102 [ SDTX with an inheritance of mmc$si_new_segment, a new segment is created in the
0 8103 [ new task (these are primarily the task template segments). All segments in the
0 8104 [ parent's SDTX with a segment reservation of mmc$srs_reserved_shared_stack
0 8105 [ are inherited, regardless, of whether or not the segment is valid.
0 8106 [
0 8107 [ The ASID and ASTI of inherited segments are zeroed out. These values
0 8108 [ may be copied into the child's segment table during a later phase of
0 8109 [ task initiation, in the procedure MMP$CREATE_TASK.
0 8110 [
0 8111 [ MMP$CREATE_INHERITED_SDT (TASK_ID, STATUS)
0 8112 [
0 8113 [ TASK_ID: (input) This parameter identifies the task being created.
0 8114 [
0 8115 [ STATUS: (output) The request status is returned in this parameter.
0 8116 [ The possible error codes are:
0 8117 [ mme$invalid_task_id
0 8118 [ mme$sdt_or_sdtx_exists
0 8119 [
0 8120 [
0 8121 [ PROCEDURE [XDCL, #GATE] mmp$create_inherited_sdt
0 8122 [ ( task_id: pmt$task_id;
0 8123 [ VAR status: ost$status);
0 8124 [
0 8125 [ VAR
0 8126 [ caller_id: ost$caller_identifier,
0 8127 [ child_sdt_entry_p: ^mmt$segment_descriptor,
0 8128 [ child_sdtx_entry_p: ^mmt$segment_descriptor_extended,
0 8129 [ child_xcb_p {new task xcb pointer} : ^ost$execution_control_block,
0 8130 [ fde_entry_p: gft$locked_file_desc_entry_p,
0 8131 [ local_sdt_p: ^mmt$segment_descriptor_table,
0 8132 [ local_sdtxp: ^mmt$segment_descriptor_table_ex,
0 8133 [ local_status: ost$status,
0 8134 [ new_sdt_length: integer,
0 8135 [ new_table_size: ost$segment_length,
0 8136 [ parent_sdt_p: mmt$max_sdt_p,
0 8137 [ parent_sdtx_p: mmt$max_sdtx_p,
0 8138 [ parent_xcb_p {current task xcb pointer} : ^ost$execution_control_block,
0 8139 [ pva: ^ocb1;
0 8140 [ rma: integer,
0 8141 [ sdt_entry: mmt$segment_descriptor,
0 8142 [ sdtx_entry: mmt$segment_descriptor_extended,
0 8143 [ segnum: ost$segment,
0 8144 [ sfid: gft$system_file_identifier,
0 8145 [ software_attribute_set: mmt$software_attribute_set,
0 8146 [ st1 {segment table length} : ost$segment;
0 8147 [
0 8148 [ status.normal := TRUE;
4 8149 [
4 8150 [ pmp$find_task_xcb (task_id, child_xcb_p);
2A 8151 [ IF child_xcb_p = NIL THEN
38 8152 [ osp$set_status_abnormal ('MM', mme$invalid_task_id, '', status);

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$CREATE_INHERITED_SDT

```

6A 8153 [ RETURN;
6C 8154 [ IFEND;
6C 8155 [
6C 8156 [ pmp$find_executing_task_xcb (parent_xcb_p);
72 8157 [
72 8158 [ st1 := parent_xcb_p^.xp.segment_table_length;
72 8159 [ mmp$get_max_sdt_sdtx_pointer (parent_xcb_p, parent_sdt_p, parent_sdtx_p);
72 8160 [ WHILE ((parent_sdtx_p^.sdtx_table [st1].inheritance = mmc$si_none) OR
104 8161 [ (parent_sdt_p^.st [st1].ste.v1 = osc$v1_invalid_entry)) AND
104 8162 [ (parent_sdtx_p^.sdtx_table [st1].segment_reservation_state <> mmc$srs_reserved_shared_stack) AND
104 8163 [ (st1 > mmc$default_sdt_length) DO
104 8164 [ st1 := st1 - 1;
104 8165 [ WHILEND;
14E 8166 [
14E 8167 [ ALLOCATE local_sdt_p: [0 .. st1] IN osv$job_fixed_heap^;
18C 8168 [
18C 8169 [ IF ((st1 + 1) * 8) > osv$page_size THEN
198 8170 [ new_sdt_length := (((st1 + 1) * 8) + osv$page_size) DIV osv$page_size;
198 8171 [ new_table_size := new_sdt_length * osv$page_size;
1A4 8172 [ IFEND;
1A4 8173 [
1A4 8174 [ Allocate and initialize the SDT.
1A4 8175 [ IF ((st1 + 1) * 8) > osv$page_size THEN
1B2 8176 [ mmp$free_pages [local_sdt_p, new_table_size, osc$nowait, status];
1D6 8177 [ mmp$assign_contiguous_memory (local_sdt_p, new_table_size, status);
1F2 8178 [ IF NOT status.normal THEN
1FA 8179 [ RETURN;
1FC 8180 [ IFEND;
200 8181 [ ELSE
200 8182 [ pmp$zero_out_table [#LOC (local_sdt_p^), #SIZE (local_sdt_p^)];
21C 8183 [ IFEND;
21C 8184 [
21C 8185 [
21C 8186 [ Allocate and zero out the SDTX.
21C 8187 [
21C 8188 [ ALLOCATE local_sdtxp: [0 .. st1] IN osv$job_fixed_heap^;
25C 8189 [ pmp$zero_out_table [#LOC (local_sdtxp^), #SIZE (local_sdtxp^)];
278 8190 [ i#real_memory_address (local_sdt_p, rma);
294 8191 [ child_xcb_p^.xp.segment_table_address_1 := rma DIV 10000[16];
294 8192 [ child_xcb_p^.xp.segment_table_address_2 := rma MOD 10000[16];
294 8193 [ child_xcb_p^.xp.segment_table_length := st1;
294 8194 [ child_xcb_p^.sdt_offset := #OFFSET (local_sdt_p);
294 8195 [ child_xcb_p^.sdtx_offset := #OFFSET (local_sdtxp);
294 8196 [
294 8197 [ Create the SDT and SDTX in the new task.
294 8198 [
294 8199 [ /create_sdt_and_sdtx/
294 8200 [ FOR segnum := 0 TO st1 DO
2E6 8201 [ IF (parent_sdt_p^.st [segnum].ste.v1 <> osc$v1_invalid_entry) AND
31A 8202 [ ((parent_sdtx_p^.sdtx_table [segnum].inheritance <> mmc$si_none) OR
31A 8203 [ (parent_sdtx_p^.sdtx_table [segnum].segment_reservation_state =
31A 8204 [ mmc$srs_reserved_shared_stack)) THEN
31A 8205 [ sdt_entry := parent_sdt_p^.st [segnum];
31A 8206 [ sdt_entry.ste.asid := 0;
31A 8207 [ sdt_entry.asti := 0;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$CREATE_INHERITED_SDT

```

31A 8208      sdtx_entry := parent_sdtx_p^.sdtx_table [segnum];
348 8209      IF (sdtx_entry.shadow_info.shadow_segment_kind <> mmc$ssk_none) AND
356 8210          (sdtx_entry.shadow_info.shadow_segment_kind <> mmc$ssk_segment_number) THEN
356 8211          sdtx_entry.shadow_info.shadow_segment_kind := mmc$ssk_none;
356 8212          sdtx_entry.shadow_info.passive_for_shadow_by_segnum := FALSE;
356 8213          sdtx_entry.sfid := sdtx_entry.shadow_info.shadow_sfid;
366 8214      IFEND;
366 8215
366 8216      sdtx_entry.assign_active := mmc$assign_active_null;
366 8217
366 8218 { New FDE entries must be created for the task template segments.
366 8219
366 8220      IF sdtx_entry.inheritance = mmc$si_new_segment THEN
378 8221          gfp$assign_fde (gfc$tr_job, 0 [segment_number], sfid, fde_entry_p);
39E 8222          IF fde_entry_p <> NIL THEN
3A8 8223              sfid.file_hash := segnum;
3A8 8224              fde_entry_p^.open_count := 1;
3A8 8225              fde_entry_p^.attach_count := 1;
3A8 8226              fde_entry_p^.file_kind := gfc$fk_unnamed_file;
3A8 8227              fde_entry_p^.file_hash := segnum;
3A8 8228              fde_entry_p^.last_segment_number := segnum;
3A8 8229              IF mmc$sa_stack IN sdtx_entry.software_attribute_set THEN
3CE 8230                  fde_entry_p^.stack_for_ring := sdt_entry.ste.r1;
3D6 8231              IFEND;
3D6 8232              sdtx_entry.sfid := sfid;
3E2 8233          ELSE
3E2 8234              osp$set_status_abnormal ('MM', mme$unable_to_assign_fde, '', status);
412 8235          RETURN;
414 8236          IFEND;
418 8237      ELSEIF parent_sdtx_p^.sdtx_table [segnum].inheritance = mmc$si_transfer_segment THEN
430 8238          sdtx_entry.inheritance := mmc$si_none;
430 8239          pva := #ADDRESS (1, segnum, 0);
430 8240          #PURGE_BUFFER (osc$pva_purge_all_page_seg_map, pva);
442 8241          parent_sdt_p^.st [segnum].ste.v1 := osc$vi_invalid_entry;
45C 8242      ELSEIF sdtx_entry.open_validating_ring_number > 1 THEN
464 8243          gfp$get_locked_fde_p (sdtx_entry.sfid, fde_entry_p);
58E 8244          fde_entry_p^.open_count := fde_entry_p^.open_count + 1;
58E 8245          gfp$unlock_fde_p (fde_entry_p);
68E 8246          sdtx_entry.software_attribute_set := sdtx_entry.software_attribute_set -
69E 8247              $mmt$software_attribute_set [mmc$sa_stack];
69E 8248          IFEND;
69E 8249          child_sdt_entry_p := mmp$get_sdt_entry_p (child_xcb_p, segnum);
69E 8250          child_sdtx_entry_p := mmp$get_sdtx_entry_p (child_xcb_p, segnum);
69E 8251          child_sdt_entry_p^. := sdt_entry;
69E 8252          child_sdtx_entry_p^. := sdtx_entry;
6FC 8253
6FC 8254      ELSEIF (parent_sdtx_p^.sdtx_table [segnum].segment_reservation_state =
728 8255          mmc$srs_reserved_shared_stack) AND
728 8256          (parent_sdt_p^.st [segnum].ste.v1 = osc$vi_invalid_entry) THEN
728 8257          sdtx_entry := parent_sdtx_p^.sdtx_table [segnum];
734 8258          sdtx_entry.segment_reservation_state := mmc$srs_reserved_shared_stack;
734 8259          child_sdtx_entry_p := mmp$get_sdtx_entry_p (child_xcb_p, segnum);
734 8260          child_sdtx_entry_p^. := sdtx_entry;
76E 8261      IFEND;
76E 8262  FOREND /create_sdt_and_sdtx/;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$CREATE_INHERITED_SDT

```

772 8263
772 8264     PROCEND mmp$create_inherited_sdt;
   0 8265

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$DELETE_NON_INHERITED_SEGS

```

0 8267 {
0 8268 { The purpose of this request is to close all segments in the task's SDT
0 8269 { that do not have the inherited attribute. Segments which have a reservation
0 8270 { state of mmc$srp_reserved_shared_stack are not closed.
0 8271 {
0 8272 { MMP$DELETE_NON_INHERITED_SEGS (STATUS)
0 8273 {
0 8274 { STATUS: (output) The request status is returned in this parameter.
0 8275 { The possible error codes are:
0 8276 { mme$invalid_close_segment_req
0 8277 { mme$invalid_shared_taskid
0 8278 { mme$segment_number_not_in_use
0 8279 { mme$segment_number_too_big
0 8280 {
0 8281 {
0 8282 PROCEDURE [XDCL, #GATE] mmp$delete_non_inherited_segs
0 8283 { (VAR status: ost$status);
0 8284 {
0 8285 VAR
0 8286 pointer: mmt$segment_pointer,
0 8287 sdt_p: mmt$max_sdt_p,
0 8288 sdtx_p: mmt$max_sdtx_p,
0 8289 segnum: ost$segment,
0 8290 xcb_p: ^ost$execution_control_block;
0 8291 {
0 8292 status.normal := TRUE;
4 8293 pmp$find_executing_task_xcb (xcb_p);
14 8294 {
14 8295 { Close all user segments.
14 8296 {
14 8297 mmp$get_max_sdt_sdtx_pointer (xcb_p, sdt_p, sdtx_p);
14 8298 FOR segnum := 0 TO xcb_p^.xp.segment_table_length DO
6A 8299 IF ((sdt_p^.st [segnum].ste.v1 <> oscsv1_invalid_entry) AND
94 8300 (sdtx_p^.sdtx_table [segnum].open_validating_ring_number > 1)) THEN
94 8301 mmp$invalidate_segment (segnum, 1, NIL [shared_taskid_array], status);
BC 8302 IFEND;
BC 8303 FOREND;
CO 8304 {
CO 8305 PROCEND mmp$delete_non_inherited_segs;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$FETCH_OFFSET_MOD_PAGES_R1

```

0 8307 {
0 8308 PROCEDURE [XDCL, #GATE] mmp$fetch_offset_mod_pages_r1
0 8309 { ( segment_number: ost$segment;
0 8310 xsfid: gft$system_file_identifier;
0 8311 return_unallocated_offsets: boolean;
0 8312 VAR offset_list: ^array [ * ] of ost$segment_offset;
0 8313 VAR offsets_returned: integer;
0 8314 VAR status: ost$status);
0 8315 {
0 8316 TYPE
0 8317 offset_array = array [1 .. * ] of ost$segment_offset;
0 8318 {
0 8319 VAR
0 8320 i: integer,
0 8321 offset_array_index: integer,
0 8322 offset_count: integer,
0 8323 offset_p: ^offset_array,
0 8324 request_block: mmt$rb_fetch_offset_mod_pages,
0 8325 sdt_entry_p: ^mmt$segment_descriptor,
0 8326 sdtx_entry_p: ^mmt$segment_descriptor_extended,
0 8327 sfid: gft$system_file_identifier;
0 8328 {
0 8329 status.normal := TRUE;
4 8330 {
4 8331 { Allocate array to hold all the offsets for the modified pages.
4 8332 {
4 8333 ALLOCATE offset_p: [1 .. UPPERBOUND (offset_list^)] IN osv$job_fixed_heap^;
66 8334 {
66 8335 { Touch all of the pages allocated so that referencing them in
66 8336 { monitor will not cause a page fault.
66 8337 {
66 8338 mmp$touch_all_pages (offset_p, #SIZE (offset_p^));
98 8339 {
98 8340 { Issue monitor function to return offsets for modified pages.
98 8341 {
98 8342 { If the segment number is non-zero, then the sfid passed into the request
98 8343 { is invalid. The correct sfid must be set from the segment's SDTX entry.
98 8344 {
98 8345 IF segment_number <> 0 THEN
A6 8346 mmp$validate_segment_number (segment_number, sdt_entry_p, sdtx_entry_p, status);
CA 8347 IF NOT status.normal THEN
D2 8348 RETURN;
D4 8349 IFEND;
D4 8350 sfid := sdtx_entry_p^.sfid;
EO 8351 ELSE
EO 8352 sfid := xsfid;
E4 8353 IFEND;
E4 8354 {
E4 8355 request_block.reqcode := syc$rc_fetch_offset_mod_pages;
E4 8356 request_block.sfid := sfid;
E4 8357 request_block.offsets_returned := UPPERBOUND (offset_list^);
E4 8358 request_block.offset_list := offset_p;
112 8359 request_block.return_unallocated_offsets := return_unallocated_offsets;
112 8360 {
112 8361 #call_monitor (#LOC (request_block), #SIZE (request_block));

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$FETCH_OFFSET_MOD_PAGES_R1

```

136 8362      syp$set_status_from_mtr_status (request_block.status, status);
172 8363      IF NOT status.normal THEN
17A 8364          FREE offset_p IN osv$job_fixed_heap^;
1A0 8365          RETURN;
1AC 8366          IFEND;
1AC 8367
1AC 8368 {      If array not large enough to hold all offsets, create list_overflow condition.
1AC 8369
1AC 8370          IF (request_block.offsets_returned > UPPERBOUND (offset_list^)) OR
1CC 8371             (request_block.offsets_returned = 0) THEN
1CC 8372              FREE offset_p IN osv$job_fixed_heap^;
1F2 8373              offsets_returned := request_block.offsets_returned;
1F2 8374              RETURN;
20A 8375              IFEND;
20A 8376
20A 8377 {      Move offsets to caller's offset list.
20A 8378
20A 8379          FOR i := 1 TO request_block.offsets_returned DO
214 8380              offset_list^ [i] := offset_p^ [i];
214 8381          FOREND;
23C 8382
23C 8383          offsets_returned := request_block.offsets_returned;
23C 8384          FREE offset_p IN osv$job_fixed_heap^;
26E 8385
26E 8386      PROCEND mmp$fetch_offset_mod_pages_r1;
O 8387

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$FETCH_SDT_SDTX_LOCKED_FDE

```

O 8389
O 8390      PROCEDURE [XDCL, #GATE] mmp$fetch_sdt_sdtx_locked_fde
O 8391      (      segment_number: ost$segment;
O 8392          VAR sdt_entry_p: ^amnt$segment_descriptor;
O 8393          VAR sdtx_entry_p: ^amnt$segment_descriptor_extended;
O 8394          VAR locked_fde_p: gft$locked_file_desc_entry_p;
O 8395          VAR status: ost$status);
O 8396
O 8397      VAR
O 8398          xcb_p: ^ost$execution_control_block;
O 8399
O 8400          status.normal := TRUE;
4 8401          pmp$find_executing_task_xcb (xcb_p);
14 8402
14 8403          IF segment_number > xcb_p^.xp.segment_table_length THEN
38 8404              IF segment_number > 4085 THEN
40 8405                  osp$set_status_abnormal ('MM', mme$segment_number_too_big, '', status);
74 8406              ELSE
74 8407                  osp$set_status_abnormal ('MM', mme$segment_number_not_in_use, '', status);
A6 8408                  IFEND;
A6 8409                  RETURN;
A8 8410              IFEND;
A8 8411
A8 8412          sdt_entry_p := mmp$get_sdt_entry_p (xcb_p, segment_number);
A8 8413          sdtx_entry_p := mmp$get_sdtx_entry_p (xcb_p, segment_number);
A8 8414          IF sdt_entry_p^.ste.v1 = oscsv1_invalid_entry THEN
116 8415              osp$set_status_abnormal ('MM', mme$segment_number_not_in_use, '', status);
14A 8416          ELSE
14A 8417              gfp$get_locked_fde_p (sdtx_entry_p^.sfid, locked_fde_p);
27A 8418          IFEND;
27A 8419
27A 8420      PROCEND mmp$fetch_sdt_sdtx_locked_fde;
O 8421

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$FETCH_SEGMENT_ATTRIBUTES_R1

```

0 8423
0 8424 PROCEDURE [XDCL, #GATE] mmp$fetch_segment_attributes_r1
0 8425 (
0 8426   segment_number: ost$segment;
0 8427   validating_ring: ost$valid_ring;
0 8428   VAR seg_attributes: ^array [ * ] of mmt$attribute_descriptor;
0 8429   VAR status: ost$status);
0 8430
0 8431 VAR
0 8432   fde_entry_p: gft$locked_file_desc_entry_p,
0 8433   i: integer,
0 8434   sdt_entry_p: ^mmt$segment_descriptor,
0 8435   sdtX_entry_p: ^mmt$segment_descriptor_extended;
0 8436
0 8437 status.normal := TRUE;
4 8437 mmp$fetch_sdt_sdtX_locked_fde (segment_number, sdt_entry_p, sdtX_entry_p, fde_entry_p, status);
3E 8438 FDR i := LOWERBOUND (seg_attributes^ ) TO UPPERBOUND (seg_attributes^ ) DO
5C 8439   CASE seg_attributes^ [i].keyword OF
DA 8440     = mmc$kw_ring_numbers :
DA 8441       seg_attributes^ [i].r1 := sdt_entry_p^.ste.r1;
DA 8442       seg_attributes^ [i].r2 := sdt_entry_p^.ste.r2;
10C 8443     = mmc$kw_segment_number :
10C 8444       seg_attributes^ [i].segnum := segment_number;
118 8445     = mmc$kw_current_segment_length :
118 8446       seg_attributes^ [i].current_length := gfp$get_eoi_from_fde (fde_entry_p);
15C 8447     = mmc$kw_max_segment_length :
15C 8448       IF fde_entry_p^.file_limit < UPPERVALUE (seg_attributes^ [i].max_length) THEN
17E 8449         seg_attributes^ [i].max_length := fde_entry_p^.file_limit;
18E 8450       ELSE
18E 8451         seg_attributes^ [i].max_length := UPPERVALUE (seg_attributes^ [i].max_length);
18E 8452     IFEND;
182 8453     = mmc$kw_gl_key :
182 8454       seg_attributes^ [i].gl_key := sdt_entry_p^.ste.key_lock;
184 8455     = mmc$kw_hardware_attributes :
184 8456       seg_attributes^ [i].hardware_attri_set := $mmt$hardware_attribute_set [];
184 8457     CASE sdt_entry_p^.ste.rp OF
1EC 8458       = osc$read_uncontrolled :
210 8459         seg_attributes^ [i].hardware_attri_set := seg_attributes^ [i].hardware_attri_set +
210 8460           $mmt$hardware_attribute_set [mmc$ha_read];
210 8461       = osc$read_key_lock_controlled :
210 8462         seg_attributes^ [i].hardware_attri_set := seg_attributes^ [i].hardware_attri_set +
220 8463           $mmt$hardware_attribute_set [mmc$ha_read_key_lock];
220 8464       = osc$binding_segment :
220 8465         seg_attributes^ [i].hardware_attri_set := seg_attributes^ [i].hardware_attri_set +
22E 8466           $mmt$hardware_attribute_set [mmc$ha_binding];
22E 8467     ELSE
22E 8468     CASEND;
22E 8469     CASE sdt_entry_p^.ste.wp OF
24E 8470       = osc$write_uncontrolled :
24E 8471         seg_attributes^ [i].hardware_attri_set := seg_attributes^ [i].hardware_attri_set +
26A 8472           $mmt$hardware_attribute_set [mmc$ha_write];
26A 8473       = osc$write_key_lock_controlled :
26A 8474         seg_attributes^ [i].hardware_attri_set := seg_attributes^ [i].hardware_attri_set +
28E 8475           $mmt$hardware_attribute_set [mmc$ha_write_key_lock];
28E 8476     ELSE
28E 8477     CASEND;

```

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NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$FETCH_SEGMENT_ATTRIBUTES_R1

```

28E 8478 CASE sdt_entry_p^.ste.xp OF
2AC 8479   = osc$non_privileged :
2AC 8480     seg_attributes^ [i].hardware_attri_set := seg_attributes^ [i].hardware_attri_set +
2CE 8481       $mmt$hardware_attribute_set [mmc$ha_execute];
2CE 8482   = osc$local_privilege :
2CE 8483     seg_attributes^ [i].hardware_attri_set := seg_attributes^ [i].hardware_attri_set +
2FO 8484       $mmt$hardware_attribute_set [mmc$ha_execute_local];
2FO 8485   = osc$global_privilege :
2FO 8486     seg_attributes^ [i].hardware_attri_set := seg_attributes^ [i].hardware_attri_set +
310 8487       $mmt$hardware_attribute_set [mmc$ha_execute_global];
310 8488   ELSE
310 8489   CASEND;
32E 8490 CASE sdt_entry_p^.ste.v1 OF
32E 8491   = osc$v1_cache_bypass :
32E 8492     seg_attributes^ [i].hardware_attri_set := seg_attributes^ [i].hardware_attri_set +
34E 8493       $mmt$hardware_attribute_set [mmc$ha_cache_bypass];
34E 8494   ELSE
34E 8495   CASEND;
34E 8496   = mmc$kw_software_attributes :
34E 8497     seg_attributes^ [i].software_attri_set := sdtX_entry_p^.software_attribute_set;
36E 8498   = mmc$kw_error_exit_procedure :
36E 8499     seg_attributes^ [i].err_exit_proc := NIL;
38E 8500   = mmc$kw_preset_value :
38E 8501     seg_attributes^ [i].preset_value := fde_entry_p^.preset_value;
380 8502   = mmc$kw_inheritance :
380 8503     seg_attributes^ [i].inheritance := sdtX_entry_p^.inheritance;
3D2 8504   = mmc$kw_clear_space :
3D2 8505     seg_attributes^ [i].clear_space := FALSE;
3EE 8506   = mmc$kw_segment_access_control :
3EE 8507     IF sdt_entry_p^.ste.v1 = osc$v1_cache_bypass THEN
400 8508       seg_attributes^ [i].access_control.cache_bypass := TRUE;
41A 8509     ELSE
41A 8510       seg_attributes^ [i].access_control.cache_bypass := FALSE;
43E 8511     IFEND;
43E 8512     seg_attributes^ [i].access_control.execute_privilege := sdt_entry_p^.ste.xp;
43E 8513     seg_attributes^ [i].access_control.read_privilege := sdt_entry_p^.ste.rp;
43E 8514     seg_attributes^ [i].access_control.write_privilege := sdt_entry_p^.ste.wp;
46A 8515   ELSE
46A 8516     osp$set_status_abnormal ('MMM', mme$unsupported_keyword, '', status);
49C 8517   CASEND;
49C 8518   FOREND;
4A0 8519
4A0 8520   gfp$unlock_fde_p (fde_entry_p);
59E 8521
59E 8522 PROCEND mmp$fetch_segment_attributes_r1;
0 8523

```


NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$GET_ALLOCATED_ADDRESSES_R1

```

0 8525
0 8526 PROCEDURE [XDCL, #GATE] mmp$get_allocated_addresses_r1
0 8527 (
0 8528   segment_number: ost$segment;
0 8529   validating_ring: ost$valid_ring;
0 8530   starting_byte_address: ost$segment_offset;
0 8531   VAR addr_list: ^array [ * ] of dmt$addr_length_pair;
0 8532   VAR addr_returned: integer;
0 8533   VAR list_overflow: boolean;
0 8534   VAR status: ost$status);
0 8535
0 8536   VAR
0 8537     allocated_length: amt$file_byte_address,
0 8538     dfd_p: ^dmt$disk_file_descriptor,
0 8539     fde_entry_p: gft$locked_file_desc_entry_p,
0 8540     sdt_entry_p: ^mmt$segment_descriptor,
0 8541     sdtx_entry_p: ^mmt$segment_descriptor_extended;
0 8542   mmp$validate_segment_number (segment_number, sdt_entry_p, sdtx_entry_p, status);
30 8543   IF NOT status.normal THEN
38 8544     RETURN;
3A 8545   IFEND;
3A 8546   IF (sdt_entry_p^.ste.rp = osc$non_readable) OR (validating_ring > sdt_entry_p^.ste.r2) THEN
5A 8547     osp$set_status_abnormal ('MM', mme$ring_violation, '', status);
8C 8548     RETURN;
8E 8549   IFEND;
8E 8550
8E 8551   IF starting_byte_address = 0 THEN
96 8552     gfp$get_locked_fde_p (sdtx_entry_p^.sfid, fde_entry_p);
1D0 8553     dmp$get_disk_file_descriptor_p (fde_entry_p, dfd_p);
1E4 8554     dmp$get_total_allocated_length (fde_entry_p, allocated_length);
214 8555     gfp$unlock_fde_p (fde_entry_p);
308 8556     IF allocated_length = dfd_p^.highest_offset_allocated THEN
318 8557       {Return 1 address pair if not sparsely allocated
318 8558       addr_returned := 1;
318 8559       list_overflow := FALSE;
318 8560       addr_list [LOWERBOUND (addr_list^)].addr := 0;
318 8561       addr_list [LOWERBOUND (addr_list^)].length := fde_entry_p^.eoi_byte_address;
318 8562       RETURN;
35E 8563     IFEND;
35E 8564   IFEND;
35E 8565
35E 8566   dmp$get_initialized_addresses (sdtx_entry_p^.sfid, starting_byte_address, addr_list^, addr_returned,
3AC 8567     list_overflow, status);
3AC 8568
3AC 8569 PROCEND mmp$get_allocated_addresses_r1;
0 8570

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$GET_SDT_FOR_JOB_TEMPLATE

```

0 8572
0 8573 PROCEDURE [XDCL] mmp$get_sdt_for_job_template
0 8574 (
0 8575   pva: ^cell;
0 8576   VAR sdt_entry: mmt$segment_descriptor;
0 8577   VAR sdtx_entry: mmt$segment_descriptor_extended;
0 8578   VAR status: ost$status);
0 8579
0 8580   VAR
0 8581     fde_entry_p: gft$locked_file_desc_entry_p,
0 8582     sdt_entry_p: ^mmt$segment_descriptor,
0 8583     sdtx_entry_p: ^mmt$segment_descriptor_extended,
0 8584     segnum: ost$segment;
0 8585
0 8586   segnum := #SEGMENT (pva);
4 8587   mmp$fetch_sdt_sdtx_locked_fde (segnum, sdt_entry_p, sdtx_entry_p, fde_entry_p, status);
3E 8588   IF status.normal = FALSE THEN
46 8589     RETURN;
48 8590   IFEND;
48 8591
48 8592   fde_entry_p^.flags.global_template_file := TRUE;
48 8593   sdtx_entry_p^.inheritance := mmc$si_share_segment;
48 8594   sdtx_entry_p^.open_validating_ring_number := 0;
48 8595   sdtx_entry_p^.file_limits_enforced := sfc$no_limit;
48 8596   sdtx_entry := sdtx_entry_p;
74 8597   sdt_entry := sdt_entry_p;
74 8598   sdt_entry.ste.asid := 0;
74 8599   gfp$unlock_fde_p (fde_entry_p);
176 8600
176 8601 PROCEND mmp$get_sdt_for_job_template;

```

NDS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$GET_SEGMENT_LENGTH_R1

```

0 8603
0 8604 PROCEDURE [XDCL, #GATE] mmp$get_segment_length_r1
0 8605 (
0 8606   segment_number: ost$segment;
0 8607   validating_ring_number: ost$valid_ring;
0 8608   VAR segment_length: ost$segment_length;
0 8609   VAR status: ost$status);
0 8610
0 8611 VAR
0 8612   fde_p: gft$file_desc_entry_p,
0 8613   sdt_entry_p: ^mmt$segment_descriptor,
0 8614   sdtx_entry_p: ^mmt$segment_descriptor_extended;
0 8615
0 8616 mmp$validate_segment_number (segment_number, sdt_entry_p, sdtx_entry_p, status);
30 8617 IF status.normal = FALSE THEN
38 8618   RETURN;
3A 8619 IFEND;
3A 8620 [ Verify that caller is within read bracket of the segment.
3A 8621
3A 8622 IF validating_ring_number > sdt_entry_p^.ste.r2 THEN
4E 8623   osp$set_status_abnormal ('MM', mme$caller_not_in_read_bracket, '', status);
80 8624   RETURN;
82 8625 IFEND;
82 8626
82 8627 gfp$get_fde_p (sdtx_entry_p^.sfid, fde_p);
DE 8628 segment_length := gfp$get_eoi_from_fde (fde_p);
118 8629
118 8630 PROCEND mmp$get_segment_length_r1;

```

NDS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$INITIATE_SHADOWING_R1

```

0 8632 [
0 8633 [ The purpose of this request is to execute ring 1 code for MMP$INITIATE_SHADOWING.
0 8634 [ Segment attributes are updated to transform that segment into an ACTIVE (shadow)
0 8635 [ segment.
0 8636 [
0 8637 [ MMP$INITIATE_SHADOWING_R1 (SEGMENT_POINTER, SEGMENT_LENGTH, STATUS);
0 8638 [
0 8639 [ SEGMENT_POINTER: (input) This parameter specifies the process virtual
0 8640 [ address assigned to the segment.
0 8641 [
0 8642 [ SEGMENT_LENGTH: (input) This parameter specifies the segment length
0 8643 [ for the segment that is to be shadowed.
0 8644 [
0 8645 [ STATUS: (output) This parameter specifies the request status.
0 8646 [
0 8647 [
0 8648 [
0 8649 PROCEDURE [XDCL, #GATE] mmp$initiate_shadowing_r1
0 8650 (
0 8651   segment_pointer: Acell;
0 8652   validating_ring_number: ost$valid_ring;
0 8653   shadow_segment_kind: mmt$shadow_segment_kind;
0 8654   VAR status: ost$status);
0 8655
0 8656 VAR
0 8657   fde_p: gft$locked_file_desc_entry_p,
0 8658   new_fde_p: gft$file_desc_entry_p,
0 8659   new_sfids: gft$system_file_identifier,
0 8660   segment_length: ost$segment_length,
0 8661   sdt_entry_p: ^mmt$segment_descriptor,
0 8662   sdtx_entry_p: ^mmt$segment_descriptor_extended;
0 8663
0 8664 mmp$fetch_sdt_sdtx_locked_fde (#SEGMENT (segment_pointer), sdt_entry_p, sdtx_entry_p, fde_p, status);
3E 8665 IF NOT status.normal THEN
4E 8666   RETURN;
48 8667 IFEND;
48 8668
48 8669 [!!!## why was there a check for R1 = R2 ???]
48 8670 [!!!## when ADA task crates a child task, what happens to debug-shadowed code segments??]
48 8671
48 8672 /fde_locked/
48 8673 BEGIN
48 8674 IF (sdt_entry_p^.ste.r1 < validating_ring_number) OR (sdtx_entry_p^.open_validating_ring_number <= 1) OR
76 8675 (mmc$sa_stack IN sdtx_entry_p^.software_attribute_set) THEN
76 8676   osp$set_status_abnormal ('MM', mme$init_shadow_improper_seg, '', status);
A8 8677   EXIT /fde_locked/;
AC 8678 IFEND;
AC 8679
AC 8680 gfp$assign_fde (gfc$tr_job, 0, new_sfids, new_fde_p);
D4 8681 IF new_fde_p = NIL THEN
E2 8682   osp$set_status_abnormal ('MM', mme$unable_to_assign_fde, '', status);
112 8683   EXIT /fde_locked/;
118 8684 IFEND;
118 8685 new_fde_p^.allocation_unit_size := mmc$shadow_allocation_size;
118 8686 new_fde_p^.open_count := 1;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$INITIATE_SHADOWING_R1

```

118 8687     new_fde_p^.attach_count := 1;
118 8688
118 8689     IF shadow_segment_kind = mmc$ssk_read_only_file THEN
130 8690         sdt_entry_p^.ste.wp := osc$write_uncontrolled;
130 8691         sdtX_entry_p^.access_rights := mmc$sar_write_extend;
14A 8692     IFEND;
14A 8693
14A 8694     mmp$get_segment_length_r1 (#SEGMENT (segment_pointer), 1, segment_length, status);
174 8695
174 8696     sdtX_entry_p^.shadow_info.shadow_segment_kind := shadow_segment_kind;
174 8697     sdtX_entry_p^.shadow_info.shadow_sfid := sdtX_entry_p^.sfid;
174 8698     sdtX_entry_p^.shadow_info.shadow_start_page_number := 0;
174 8699     sdtX_entry_p^.shadow_info.shadow_length_page_count := (((segment_length + 16384 - 1) DIV 16384) *
174 8700         16384) DIV osv$page_size;
174 8701     new_fde_p^.file_limit := fde_p^.file_limit;
174 8702     sdtX_entry_p^.sfid := new_sfid;
174 8703
174 8704     { Purge buffer space for PASSIVE segment and set ASID to zero.
174 8705
174 8706     sdt_entry_p^.ste.asid := 0;
174 8707     #PURGE_BUFFER (osc$pva_purge_all_page_seg_map, segment_pointer);
1CE 8708
1CE 8709     END /fde_locked/;
1DO 8710
1DO 8711     gfp$unlock_fde_p (fde_p);
2C6 8712
2C6 8713     PROCEND mmp$initiate_shadowing_r1;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$INIT_SYSTEM_PRIVILEGE_MAP

```

0 8715 {
0 8716 { This procedure initializes the system privilege bit map. It first
0 8717 { clears the map, located in the mainframe pageable segment, then decides
0 8718 { which segments have system privilege, and sets the corresponding bits
0 8719 { in the map. Before this procedure is called, the bit map must have
0 8720 { been moved from the task private segment static area into mainframe
0 8721 { pageable. After this procedure is called, each new task will pick up
0 8722 { the initialized map from mainframe pageable into its task private segment.
0 8723 {
0 8724 {     MMP$INIT_SYSTEM_PRIVILEGE_MAP (offset);
0 8725 {
0 8726 {     OFFSET: (input) This parameter specifies the offset of the privilege map.
0 8727 {
0 8728 {
0 8729     PROCEDURE [XDCL, #GATE] mmp$init_system_privilege_map
0 8730     (
0 8731         offset: ost$segment_offset);
0 8732     VAR
0 8733         i: ost$segment,
0 8734         leftover: boolean,
0 8735         mapend: ost$segment,
0 8736         mp_p: ^ost$system_privilege_map,
0 8737         sdt_entry_p: ^amnt$segment_descriptor,
0 8738         ste: ost$segment_descriptor,
0 8739         xcb_p: ^ost$execution_control_block;
0 8740
0 8741 { Calculate where the map is in mainframe pageable.
0 8742 { Note - hard-coded [i] must be changed if multiple task private segments exist.
0 8743
0 8744     mp_p := #ADDRESS (1, #SEGMENT (jmv$task_private_temp1_p^.segment [i].content),
4 8745         #OFFSET (jmv$task_private_temp1_p^.segment [i].content) + offset);
4 8746
4 8747 { Find the system job XCB.
4 8748
4 8749     pmp$find_executing_task_xcb (xcb_p);
34 8750
34 8751 { Capture the system privilege segments.
34 8752 { Insure no array bounds errors.
34 8753
34 8754     IF xcb_p^.xp.segment_table_length < UPPERBOUND (ost$system_privilege_map) THEN
58 8755         leftover := TRUE;
58 8756         mapend := xcb_p^.xp.segment_table_length;
5E 8757     ELSE
5E 8758         leftover := FALSE;
5E 8759         mapend := UPPERBOUND (ost$system_privilege_map);
66 8760     IFEND;
66 8761
66 8762 { Compute the bits for which both segment table entries and map entries exist.
66 8763
66 8764     FOR i := 0 TO mapend DO
70 8765         sdt_entry_p := mmp$get_sdt_entry_p (xcb_p, i);
70 8766         ste := sdt_entry_p^.ste;
70 8767         mp_p [i] := (ste.v1 <> osc$v1_invalid_entry) AND (ste.xp <> osc$non_executable);
80 8768     FOREND;
8A 8769

```

NDS/VE - MMSSEGMENT_MANAGER_SYSTEM_CORE
MMP\$INIT_SYSTEM_PRIVILEGE_MAP

```

BA 8770 { Clear any leftover bits.
BA 8771
BA 8772   IF leftover THEN
BE 8773     FOR i := mapend + 1 TO UPPERBOUND (ost$system_privilege_map) DO
C8 8774       mp_p^ [i] := FALSE;
C8 8775     FOREND;
D4 8776   IFEND;
D4 8777
D4 8778   PROCEND mmp$init_system_privilege_map;

```

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NDS/VE - MMSSEGMENT_MANAGER_SYSTEM_CORE
MMP\$ISSUE_RING1_SEGMENT_REQUEST

```

O 8780
O 8781   PROCEDURE [XDCL] mmp$issue_ring1_segment_request
O 8782     {VAR rb: mmt$rb_ring1_segment_request};
O 8783
O 8784     VAR
O 8785       count: integer;
O 8786       status: ost$status;
O 8787       sfid: gft$system_file_identifier;
O 8788
O 8789 { Mmp$process_wmp_status (mtr) will set init_new_io to FALSE if the call is reissued for the wait option.
O 8790
O 8791   rb.init_new_io := TRUE;
4 8792   FOR count := 1 TO 4 DO
16 8793     i#call_monitor (#LOC (rb), #SIZE (rb));
2E 8794     IF NOT rb.status.normal THEN
36 8795       IF rb.status.condition = mme$io_write_error THEN
46 8796         CASE rb.request OF
60 8797           = mmc$sri_detach_file, mmc$sri_flush_delete_seg_sfid, mmc$sri_flush_seg_segnum =
60 8798             {Only attempt reallocate for these requests
60 8799             sfid := rb.sfid;
68 8800           ELSE
68 8801             RETURN;
6A 8802           CASEEND;
6E 8803           ELSE
6E 8804             RETURN;
70 8805           IFEND;
74 8806           ELSE
74 8807             RETURN;
76 8808           IFEND;
76 8809           IF count = 4 THEN
7C 8810             RETURN;
7E 8811           IFEND;
7E 8812           dmp$reallocate_file_space (sfid, TRUE, status);
9A 8813           IF NOT status.normal THEN
A2 8814             RETURN;
A4 8815           IFEND;
A4 8816           rb.init_new_io := TRUE;
A4 8817           FOREND;
AC 8818
AC 8819   PROCEND mmp$issue_ring1_segment_request;
O 8820

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$INVALIDATE_SEGMENT

```

0 8822
0 8823 {
0 8824 { The purpose of this procedure is to invalidate the specified segment
0 8825 { in the SDT table. The backing store file is returned if one is assigned,
0 8826 { and the open_count in the FDE is zero.
0 8827 {
0 8828 {     MMP$INVALIDATE_SEGMENT (SEGMENT_NUMBER, VALIDATING_RING_NUMBER,
0 8829 {         SHARED_TASKID_ARRAY, STATUS)
0 8830 {
0 8831 {     SEGMENT_NUMBER: (input) This parameter specifies the segment number to
0 8832 {         be invalidated.
0 8833 {
0 8834 {     VALIDATING_RING_NUMBER: (input) This parameter specifies the validating
0 8835 {         ring_number of the request.
0 8836 {
0 8837 {     SHARED_TASKID_ARRAY: (input) This parameter is a pointer to the list of
0 8838 {         taskids of tasks in which this segment is to be invalidated.
0 8839 {
0 8840 {     STATUS: (output) This parameter is where the request status is returned
0 8841 {         to the caller. The possible error codes are:
0 8842 {         dme$file_descriptor_not_deleted
0 8843 {         mme$invalid_close_segment_req
0 8844 {         mme$invalid_shared_taskid
0 8845 {         mme$segment_number_not_in_use
0 8846 {         mme$segment_number_too_big
0 8847 {
0 8848 {
0 8849 PROCEDURE [XDCL, #GATE] mmp$invalidate_segment
0 8850 {     segment_number: ost$segment;
0 8851 {     validating_ring_number: ost$valid_ring;
0 8852 {     shared_taskid_array: ^array [1..*] of pmt$task_id;
0 8853 {     VAR status: ost$status;
0 8854 {
0 8855 {     VAR
0 8856 {     caller_id: ost$caller_identifier,
0 8857 {     fde_entry_p: gft$locked_file_desc_entry_p,
0 8858 {     i: integer,
0 8859 {     open_count: integer, {must be integer}
0 8860 {     pva: ^cell,
0 8861 {     rb: mmt$rb_ring1_segment_request,
0 8862 {     sdt_entry_p: ^mmt$segment_descriptor,
0 8863 {     sdtX_entry_p: ^mmt$segment_descriptor_extended,
0 8864 {     sdt_p: mmt$max_sdt_p,
0 8865 {     sdtX_p: mmt$max_sdtX_p,
0 8866 {     segnum: ost$segment,
0 8867 {     shadow_fde_p: gft$locked_file_desc_entry_p,
0 8868 {     shadow_open_count: gft$open_count,
0 8869 {     shadow_sfid: gft$system_file_identifier,
0 8870 {     task_xcb: ^ost$execution_control_block,
0 8871 {     task_sdt_entry_p: ^mmt$segment_descriptor,
0 8872 {     xcb_p: ^ost$execution_control_block;
0 8873 {
0 8874 {     IF shared_taskid_array <> NIL THEN
1E 8875 {     FOR i := LOWERBOUND (shared_taskid_array) TO UPPERBOUND (shared_taskid_array) DO
2E 8876 {     pmp$find_task_xcb (shared_taskid_array[i], task_xcb);

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$INVALIDATE_SEGMENT

```

54 8877     IF task_xcb = NIL THEN
5E 8878         osp$set_status_abnormal ('MM', mme$invalid_shared_taskid, '', status);
94 8879     RETURN;
96 8880     IFEND;
9E 8881     FOREND;
9C 8882     IFEND;
9C 8883
9C 8884     #CALLER_ID (caller_id);
9C 8885     mmp$fetch_sdt_sdtX_locked_fde (segment_number, sdt_entry_p, sdtX_entry_p, fde_entry_p, status);
D8 8886     IF status.normal = FALSE THEN
E0 8887         RETURN;
E2 8888     IFEND;
E2 8889
E2 8890     pva := #ADDRESS (caller_id.ring, segment_number, 0);
E2 8891
E2 8892 {***KLUDGE- the first conditional is necessary to delete task private in
E2 8893 {     job termination.
E2 8894     IF validating_ring_number > 2 THEN
102 8895     IF (sdtX_entry_p.inheritance <> mmc$si_none) THEN
10E 8896         gfp$unlock_fde_p (fde_entry_p);
202 8897         osp$set_status_abnormal ('MM', mme$invalid_close_segment_req, '', status);
232 8898         RETURN;
234 8899     IFEND;
234 8900     IFEND; {***KLUDGE***}
234 8901
234 8902 { Clear any segment locks left by the user.
234 8903
234 8904     IF sdtX_entry_p.segment_lock <> mmc$lsn_none THEN
240 8905         mmp$unlock_segment (pva, mmc$lus_free, osc$nowait, status);
262 8906     IF NOT status.normal THEN
26A 8907         gfp$unlock_fde_p (fde_entry_p);
35C 8908         osp$system_error ('Unexpected mmp$unlock_segment error', ^status);
380 8909     IFEND;
380 8910     IFEND;
380 8911
380 8912     pmp$find_executing_task_xcb (xcb_p);
38E 8913     IF shared_taskid_array <> NIL THEN
3A6 8914         FOR i := LOWERBOUND (shared_taskid_array) TO UPPERBOUND (shared_taskid_array) DO
384 8915             pmp$find_task_xcb (shared_taskid_array[i], task_xcb);
3DA 8916             IF xcb_p <> task_xcb THEN
3EA 8917                 task_sdt_entry_p := mmp$get_sdt_entry_p (task_xcb, segment_number);
3EA 8918                 task_sdt_entry_p.ste.v1 := osc$sv1_invalid_entry;
3EA 8919                 fde_entry_p.open_count := fde_entry_p.open_count - 1;
42A 8920             IFEND;
42A 8921         FOREND;
434 8922     IFEND;
434 8923
434 8924     open_count := 1;
434 8925     IF sdtX_entry_p.shadow_info.shadow_segment_kind <> mmc$ssk_none THEN
442 8926         gfp$get_locked_fde_p (sdtX_entry_p.shadow_info.shadow_sfid, shadow_fde_p);
570 8927         shadow_fde_p.open_count := shadow_fde_p.open_count - 1;
570 8928         shadow_open_count := shadow_fde_p.open_count;
570 8929         gfp$unlock_fde_p (shadow_fde_p);
86E 8930         IF (shadow_open_count = 0) AND
88E 8931             ((shadow_fde_p.file_kind = gfc$fk_unnamed_file) OR

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$INVALIDATE_SEGMENT

```

886 8932      (shadow_fde_p^file_kind = gfc$fk_global_unnamed) THEN
886 8933      destroy_segment (sdtx_entry_p^.shadow_info.shadow_sfid, shadow_fde_p,
886 8934      sfc$temp_file_space_limit, status); {Can only destroy temp files}
886 8935      IFEND;
886 8936      ELSEIF sdtx_entry_p^.shadow_info.passive_for_shadow_by_segnum THEN
886 8937      pmp$find_executing_task_xcb (xcb_p);
886 8938      mmp$get_max_sdt_sdtx_pointer (xcb_p, sdt_p, sdtx_p);
886 8939      FOR segnum := 0 TO xcb_p^.xp.segment_table_length DO
714 8940      IF (sdt_p^.st [segnum].ste.v1 <> osc$vl_invalid_entry) AND
748 8941      (sdtx_p^.sdtx_table [segnum].shadow_info.shadow_segment_number = segnum) AND
748 8942      (sdtx_p^.sdtx_table [segnum].shadow_info.shadow_segment_kind = mmc$ssk_segment_number) THEN
748 8943      sdtx_p^.sdtx_table [segnum].shadow_info.shadow_segment_kind := mmc$ssk_none;
748 8944      sdtx_p^.sdtx_table [segnum].shadow_info.passive_for_shadow_by_segnum := FALSE;
748 8945      open_count := open_count + 1;
752 8946      IFEND;
752 8947      FOREND;
756 8948      IFEND;
756 8949
756 8950      open_count := fde_entry_p^.open_count - open_count;
756 8951      fde_entry_p^.open_count := open_count;
756 8952      gfp$unlock_fde_p (fde_entry_p);
85A 8953      IF open_count < 0 THEN
85E 8954      osp$system_error ('MM - neg open count in invalidate', NIL);
882 8955      IFEND;
882 8956
882 8957      IF (open_count = 0) AND ((fde_entry_p^.file_kind = gfc$fk_unnamed_file) OR
89A 8958      (fde_entry_p^.file_kind = gfc$fk_global_unnamed)) THEN
89A 8959      destroy_segment (sdtx_entry_p^.sfid, fde_entry_p, sdtx_entry_p^.file_limits_enforced, status);
8C0 8960      IFEND;
8C0 8961
8C0 8962      #PURGE_BUFFER (osc$pva_purge_all_page_seg_map, pva);
8C2 8963      sdt_entry_p^.ste.v1 := osc$vl_invalid_entry;
8C2 8964
8C2 8965      PROCEND mmp$invalidate_segment;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$JOB_DELETE_INHERITED_SDT

```

0 8967 {
0 8968 { The purpose of this request is to delete all segments unique to this
0 8969 { job. The ring 1 and 2 stack segments and the job fixed segment are
0 8970 { not deleted. These will be deleted in another phase of job termination.
0 8971 {
0 8972 { NOTE: This procedure can not be called from above ring 2.
0 8973 {
0 8974 { MMP$JOB_DELETE_INHERITED_SDT (STATUS)
0 8975 {
0 8976 { STATUS: (output) This parameter is where the request status is returned.
0 8977 { No error codes are returned. Osp$system_error is called if segments
0 8978 { cannot be deleted.
0 8979 {
0 8980 {
0 8981 {
0 8982 {
0 8983 {
0 8984 {
0 8985 {
0 8986 {
0 8987 {
0 8988 {
0 8989 {
0 8990 {
0 8991 {
0 8992 {
0 8993 {
0 8994 {
0 8995 {
4 8996 {
4 8997 {
10 8998 {
10 8999 {
10 9000 {
3C 9001 {
3C 9002 {
6E 9003 {
6E 9004 {
A8 9005 {
A8 9006 {
FE 9007 {
FE 9008 {
FE 9009 {
FE 9010 {
FE 9011 {
10A 9012 {
112 9013 {
112 9014 {
112 9015 {
112 9016 {
13E 9017 {
142 9018 {
142 9019 {
142 9020 {
16A 9021 {

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$JOB_DELETE_INHERITED_SDT

```

176 9022         IFEND;
176 9023
176 9024         IFEND;
176 9025         IFEND;
176 9026         FOREND /scan_sdt_for_inherited_segs/;
17A 9027
17A 9028         syp$return_jobs_r1_resources;
182 9029
182 9030         PROCEND mmp$job_delete_inherited_sdt;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$JOB_MULTIPROCESSING_CONTROL

```

O 9032
O 9033     PROCEDURE [XDCL] mmp$job_multiprocessing_control
O 9034     (
O 9035       enable: boolean;
O 9036       VAR status: ost$status);
O 9037     VAR
O 9038       sdt_p: mmt$max_sdt_p,
O 9039       sdtX_p: mmt$max_sdtX_p,
O 9040       segnum: ost$segment,
O 9041       xcb_p: ^ost$execution_control_block;
O 9042
O 9043     status.normal := TRUE;
4 9044     pmp$find_executing_task_xcb (xcb_p);
14 9045
14 9046     mmp$get_max_sdt_sdtX_pointer (xcb_p, sdt_p, sdtX_p);
14 9047     FOR segnum := 0 TO xcb_p^.xp.segment_table_length DO
6A 9048       IF (sdt_p^.st [segnum].ste.v1 <> osc$v1_invalid_entry) AND (segnum <> osc$segnum_job_fixed_heap) THEN
84 9049         IF (sdtX_p^.sdtX_table [segnum].inheritance = mmc$si_share_segment) AND
A2 9050           (sdtX_p^.sdtX_table [segnum].open_validating_ring_number <> 0) THEN
A2 9051           IF enable THEN
AA 9052             sdt_p^.st [segnum].ste.v1 := osc$v1_cache_bypass;
B8 9053           ELSE
B8 9054             sdt_p^.st [segnum].ste.v1 := osc$v1_regular_segment;
C2 9055           IFEND;
C2 9056           IFEND;
C2 9057           IFEND;
C2 9058         FOREND;
C6 9059
C6 9060     PROCEND mmp$job_multiprocessing_control;
O 9061

```

NOS/VE - MMSSEGMENT_MANAGER_SYSTEM_CORE
MMP\$MFH_FOR_SEGMENT_MANAGER

```

0 9063
0 9064 {
0 9065 [ The purpose of this procedure is to assign a device to a segment
0 9066 [ if memory manager needs it done. This procedure is called by the ring 1
0 9067 [ trap handler when a specified system flag is set.
0 9068 {
0 9069 [     MMP$MFH_FOR_SEGMENT_MANAGER
0 9070 {
0 9071
0 9072     PROCEDURE [XDCL, #GATE] mmp$mfh_for_segment_manager;
0 9073
0 9074     VAR
0 9075         allocated_length: amt$file_byte_address,
0 9076         ctime: 0 .. 0xffffffff{T6},
0 9077         gtid: ost$global_task_id,
0 9078         status: ost$status,
0 9079         xcb_p: ^ost$execution_control_block;
0 9080
0 9081 [ Allow escaped allocation if the task has system tables locked.
0 9082
0 9083     pmp$find_executing_task_xcb [xcb_p];
0 9084     IF xcb_p^.system_table_lock_count > 255 THEN
2C 9085         xcb_p^.stlc_allocation := TRUE;
2C 9086         RETURN;
32 9087     IFEND;
32 9088
32 9089     mmp$process_file_alloc [allocated_length, status];
4E 9090     IF NOT status.normal THEN
56 9091         IF [status.condition <> dfe$family_not_served] AND [status.condition <> dfe$server_not_active] AND
76 9092             [status.condition <> dfe$server_has_terminated] THEN
76 9093             ctime := #FREE_RUNNING_CLOCK (0);
7E 9094             REPEAT
7E 9095                 pmp$delay [1000, status];
9A 9096                 mmp$process_file_alloc [allocated_length, status];
B2 9097                 UNTIL (status.normal) OR ((#FREE_RUNNING_CLOCK (0) - ctime) > 1000000);
CA 9098                 IF NOT status.normal THEN
D2 9099                     pmp$get_executing_task_gtid [gtid];
D8 9100                     pmp$set_system_flag [mmc$failed_file_alloc_flag, gtid, status];
11E 9101                     IF NOT status.normal THEN
11E 9102                         osp$system_error ('Error setting system flag-MMSMC', NIL);
142 9103                         IFEND;
142 9104                     IFEND;
142 9105                 IFEND;
142 9106             IFEND;
142 9107
142 9108     PROCEND mmp$mfh_for_segment_manager;

```

NOS/VE - MMSSEGMENT_MANAGER_SYSTEM_CORE
MMP\$MFH_SHADOW_FILE_REFERENCE

```

0 9110
0 9111     PROCEDURE [XDCL] mmp$mfh_shadow_file_reference;
0 9112
0 9113 [ The purpose of this procedure is to move data from the shadowed file to the active
0 9114 [ file. A page fault had occurred and the page was found to reside on the shadowed
0 9115 [ file. Mmp$page_pull filled in the necessary information in the XCB and set the
0 9116 [ monitor flag, mmc$mf_shadow_file_reference. The ring 1 trap handler called this
0 9117 [ procedure.
0 9118
0 9119     VAR
0 9120         i: integer,
0 9121         in_memory: boolean,
0 9122         rma: integer,
0 9123         status: ost$status,
0 9124         xcb_p: ^ost$execution_control_block;
0 9125
0 9126     pmp$find_executing_task_xcb [xcb_p];
A 9127     in_memory := TRUE;
A 9128
A 9129     /memory_check/
A 9130     FOR i := 0 TO xcb_p^.shadow_reference_info.page_count - 1 DO
30 9131         i#real_memory_address [#ADDRESS (1, #SEGMENT [xcb_p^.shadow_reference_info.source_pva),
74 9132             #OFFSET [xcb_p^.shadow_reference_info.source_pva] + i * osv$page_size), rma];
74 9133         IF rma < 0 THEN
7C 9134             in_memory := FALSE;
7C 9135             EXIT /memory_check/;
84 9136         IFEND;
84 9137     FOREND /memory_check/;
88 9138
88 9139     IF NOT in_memory THEN
8C 9140         mmp$advise_in [xcb_p^.shadow_reference_info.source_pva,
C2 9141             xcb_p^.shadow_reference_info.page_count * osv$page_size, status];
C2 9142         IFEND;
C2 9143         i#move [xcb_p^.shadow_reference_info.source_pva, xcb_p^.shadow_reference_info.destination_pva,
C2 9144             xcb_p^.shadow_reference_info.page_count * osv$page_size];
108 9145
108 9146     PROCEND mmp$mfh_shadow_file_reference;

```


NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$MFH_VOLUME_UNAVAILABLE

```

O 9148
O 9149 PROCEDURE [XDCL] mmp$mfh_volume_unavailable;
O 9150
O 9151 VAR
O 9152     gtid: ost$global_task_id,
O 9153     mmv$pf_system_core,
O 9154     mmv$pf_job_template: [XDCL] integer := 0,
O 9155     psa: ^ost$minimum_save_area,
O 9156     status: ost$status,
O 9157     str1: integer,
O 9158     str: string (80),
O 9159     xcb: ^ost$execution_control_block;
O 9160
O 9161     psa := #PREVIOUS_SAVE_AREA ();
O 9162     {This code assumes:
O 9163     [ Page fault for bad disk; trap to TH; TH calls this procedure
O 9164     IF #RING (psa^a2_previous_save_area) = 1 THEN
O 9165
O 9166     mmv$pf_system_core := mmv$pf_system_core + 1;
O 9167     {We have interrupted the system core
O 9168     [Allow rollback - then check for system tables locked
O 9169     syp$cause_condition (syc$volume_unavailable);
O 9170
O 9171     {We still have control, so we must wait
O 9172     [If there are system resources tied up, we will be in trouble
O 9173     pmp$delay (30000, status);
O 9174
O 9175     [Return and attempt page fault again
O 9176     ELSE
O 9177
O 9178     mmv$pf_job_template := mmv$pf_job_template + 1;
O 9179     {We have interrupted the job template
O 9180     pmp$get_executing_task_gtid (gtid);
O 9181     pmp$set_system_flag (mmc$volume_unavailable_flag, gtid, status);
O 9182     IF NOT status.normal THEN
O 9183     osp$system_error ('Error setting system flag-MMSMSC', NIL);
O 9184     IFEND;
O 9185     IFEND;
O 9186
O 9187 PROCEND mmp$mfh_volume_unavailable;
O 9188

```

SOURCE LIST OF mmm\$segment_manager_system_core NOS/VE CYBIL/II 1.0 89102

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13:33:34

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NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$MM_MOVE_MOD_SERVER_PAGE

```

O 9190
O 9191 {
O 9192 { NAME:
O 9193 {   MMP$MM_MOVE_MOD_SERVER_PAGE
O 9194 {
O 9195 { PURPOSE:
O 9196 {   This procedure makes a request to move a single modified page from a source
O 9197 { file to a destination file. Only modified pages of the server file are moved;
O 9198 { non-modified pages are discarded. It is used to update the server image file
O 9199 { on the client in the case of a server crash. The procedure executes in job
O 9200 { mode in a system task which writes the server image file with pages from all
O 9201 { of the currently attached server files.
O 9202 {
O 9203 { ASSUMPTIONS:
O 9204 {   The following assumptions are made in designing this interface:
O 9205 {     . The state of the server will not change while this request is
O 9206 {       removing pages
O 9207 {     . The destination file (as specified by "destination_pva") is not on
O 9208 {       the server
O 9209 {     . All space for the destination file must be preallocated
O 9210 {     . A page has not already been assigned to the destination pva
O 9211 {     . Access to the server file whose page is being moved has been
O 9212 {       inhibited by the SDTX access state
O 9213 {     . The "destination_pva" is on a page boundary
O 9214 {     . All server IO on the file has been dequeued and pages associated with
O 9215 {       server IO have been unlocked
O 9216 {     . This procedure is called only in the system job
O 9217 {     . The File Descriptor for the file is expected to be locked and will
O 9218 {       remain so during the monitor call
O 9219 {
O 9220 { PROCEDURE [XDCL] mmp$mmove_mod_server_page
O 9221 {   ( sfid: gft$system_file_identifier;
O 9222 {     destination_pva: ^cell;
O 9223 {     VAR byte_offset: ost$segment_offset;
O 9224 {     VAR status: ost$status);
O 9225 {
O 9226 { SFID: {INPUT} Specifies the System File ID of the file whose pages must be
O 9227 { moved to the destination file.
O 9228 { DESTINATION_PVA: {INPUT} Specifies the location within the server image file
O 9229 { to which a located modified page will be written.
O 9230 { BYTE_OFFSET: {OUTPUT} Specifies the beginning offset of the located page
O 9231 { which has been moved.
O 9232 { STATUS: {OUTPUT} Status processing. Conditions which can be returned are:
O 9233 {   mme$io_active_on_move_page      (retry)
O 9234 {   mme$no_pages_found_for_move     (normal exit condition)
O 9235 {   mme$page_table_full             (retry)
O 9236 {
O 9237
O 9238 PROCEDURE [XDCL] mmp$mmove_mod_server_page
O 9239 {   system_file_id: gft$system_file_identifier;
O 9240 {     destination_pva: ^cell;
O 9241 {     VAR byte_offset: ost$segment_offset;
O 9242 {     VAR status: ost$status);
O 9243
O 9244 VAR

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$MM_MOVE_MOD_SERVER_PAGE

```

0 9245      rb_ring1_server_seg_request: mmt$rb_ring1_server_seg_request;
0 9246
0 9247      status.normal := TRUE;
4 9248
4 9249      rb_ring1_server_seg_request.reqcode := syc$rc_ring1_server_seg_request;
4 9250      rb_ring1_server_seg_request.sfid := system_file_id;
4 9251      rb_ring1_server_seg_request.request := mmc$sr1_move_modified_df_page;
4 9252      rb_ring1_server_seg_request.destination_pva := destination_pva;
4 9253      rb_ring1_server_seg_request.byte_offset := 07fffffff(16); {dummy initialization}
4 9254
4 9255      i#call_monitor (#LOC (rb_ring1_server_seg_request), #SIZE (rb_ring1_server_seg_request));
50 9256
50 9257      byte_offset := rb_ring1_server_seg_request.byte_offset;
50 9258      syp$set_status_from_mtr_status (rb_ring1_server_seg_request.status, status);
9E 9259
9E 9260      PROCEND mmp$mm_move_mod_server_page;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$OPEN_ASID_BASED_SEGMENT

```

0 9262
0 9263      PROCEDURE [XDCL] mmp$open_asid_based_segment
0 9264      (
0 9265          sdt_entry: mmt$segment_descriptor;
0 9266          sdtx_entry: mmt$segment_descriptor_extended;
0 9267          VAR segment_number: ost$segment;
0 9268          VAR status: ost$status);
0 9269
0 9270      VAR
0 9271          sdt_entry_p: ^mmt$segment_descriptor,
0 9272          sdtx_entry_p: ^mmt$segment_descriptor_extended,
0 9273          segnum: ost$segment,
0 9274          xcb_p: ^ost$execution_control_block;
0 9275
0 9276      status.normal := TRUE;
4 9277      pmp$find_executing_task_xcb (xcb_p);
14 9278
14 9279      find_available_segment_number (xcb_p, mmc$srs_not_reserved, segnum, status);
40 9280      IF NOT status.normal THEN
48 9281          RETURN;
4A 9282      IFEND;
4A 9283
4A 9284      { Add sdt_entry to the task's segment descriptor table (SDT)
4A 9285      sdt_entry_p := mmp$get_sdt_entry_p (xcb_p, segnum);
4A 9286      sdt_entry_p^ := sdt_entry;
4A 9287      sdtx_entry_p := mmp$get_sdtx_entry_p (xcb_p, segnum);
4A 9288      sdtx_entry_p^ := sdtx_entry;
AE 9289
AE 9290      segment_number := segnum;
AE 9291
AE 9292      PROCEND mmp$open_asid_based_segment;
0 9293

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$OPEN_FILE_BY_SFID

```

0 9294
0 9295 PROCEDURE [XDCL, #GATE] mmp$open_file_by_sfid
0 9296 (
0 9297   sfid: gft$system_file_identifier;
0 9298   r1: ost$valid_ring;
0 9299   r2: ost$valid_ring;
0 9300   sequential_random_selection: mmt$access_selections;
0 9301   read_write_access_selection: mmt$segment_access_rights;
0 9302   VAR segment_number: ost$segment;
0 9303   VAR status: ost$status);
0 9304
0 9305   VAR
0 9306     fde_entry_p: gft$locked_file_desc_entry_p,
0 9307     page_streaming_ts_shift: 0 .. 15,
0 9308     sdt_entry: mmt$segment_descriptor,
0 9309     sdt_entry_p: ^mmt$segment_descriptor,
0 9310     sdt_x_entry_p: ^mmt$segment_descriptor_extended,
0 9311     segment_res_state: mmt$segment_reservation_state,
0 9312     segnum: ost$segment,
0 9313     ste: mmt$segment_descriptor,
0 9314     xcb_p: ^ost$execution_control_block;
0 9315
0 9316   status.normal := TRUE;
0 9317   pmp$find_executing_task_xcb (xcb_p);
14 9318 {
14 9319   Find an available segment number if the caller did not supply one.
14 9320
14 9321   segment_res_state := mmc$srs_not_reserved,
14 9322   find_available_segment_number (xcb_p, segment_res_state, segnum, status);
40 9323   IF NOT status.normal THEN
48 9324     RETURN;
4A 9325   IFEND;
4A 9326 {
4A 9327   Add sdt_entry to the task's segment descriptor table (SDT) and the sdt_x_entry to the
4A 9328   segment descriptor table extended (SDTX).
4A 9329
4A 9330   sdt_entry_p := mmp$get_sdt_entry_p (xcb_p, segnum);
4A 9331   sdt_x_entry_p := mmp$get_sdt_x_entry_p (xcb_p, segnum);
4A 9332   { THIS SDTX ENTRY SHOULD BE THE DEV FILE
4A 9333
4A 9334   sdt_x_entry_p^ := mmv$default_sdt_x_entry;
4A 9335   sdt_x_entry_p^sfid := sfid;
4A 9336   sdt_entry := mmv$default_sdt_entry;
4A 9337   sdt_entry.ste.r1 := r1;
4A 9338   sdt_entry.ste.r2 := r2;
1F4 9339   gfp$get_locked_fde_p (sfid, fde_entry_p);
1F4 9340   fde_entry_p^.open_count := fde_entry_p^.open_count + 1;
2F4 9341   gfp$unlock_fde_p (fde_entry_p);
300 9342   IF osv$multiple_cpus_possible THEN
306 9343     sdt_entry.ste.v1 := osc$v1_cache_bypass;
306 9344     IFEND;
30E 9345   IF read_write_access_selection = mmc$sar_read THEN
312 9346     sdt_entry.ste.wp := osc$non_writable;
31A 9347     IFEND;
31A 9348   IF sequential_random_selection = mmc$as_sequential THEN
     sdt_x_entry_p^.software_attribute_set := $mmt$software_attribute_set

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$OPEN_FILE_BY_SFID

```

31A 9349   [mmc$sa_read_transfer_unit, mmc$sa_free_behind];
31A 9350   sdt_x_entry_p^.stream.sequential_accesses := mmv$page_streaming_prestream;
32C 9351   IFEND;
32C 9352   mmp$convert_ps_transfer_size (16384, page_streaming_ts_shift); {force transfer size of 16384
362 9353   sdt_x_entry_p^.stream.transfer_size := page_streaming_ts_shift;
362 9354   mmp$set_segment_access_rights (sdt_entry, sdt_x_entry_p^);
3A2 9355
3A2 9356   store_ste_in_segment_table (sdt_entry, sfid, sdt_entry_p, fde_entry_p, segnum);
38C 9357
38C 9358   segment_number := segnum;
38C 9359
38C 9360 PROCEND mmp$open_file_by_sfid;

```

NDS/VE - MMSSEGMENT_MANAGER_SYSTEM_CORE
MMP\$OS_PREALLOCATE_FILE_SPACE

```

0 9362 [
0 9363 [ The purpose of this procedure is to preallocate file space. The request will
0 9364 [ attempt to allocate the number of bytes up to the length which is input by the user.
0 9365 [
0 9366 [           MMP$OS_PREALLOCATE_FILE_SPACE (PROCESS_VIRTUAL_ADDRESS, LENGTH, STATUS)
0 9367 [
0 9368 [ PROCESS_VIRTUAL_ADDRESS: (input) This parameter specifies the address of
0 9369 [ the segment to which space is to be preallocated.
0 9370 [
0 9371 [ LENGTH: (input) This parameter specifies the desired length of the segment
0 9372 [ after preallocation. This request will allocate (length - segment.eoi)
0 9373 [ number of bytes.
0 9374 [
0 9375 [ STATUS: (output) This parameter will return the request status to the user.
0 9376 [
0 9377 [
0 9378 [ PROCEDURE [XDCL, #GATE] mmp$os_preallocate_file_space
0 9379 [ ( process_virtual_address: Acell;
0 9380 [ length: ost$segment_length;
0 9381 [ VAR status: ost$status);
0 9382 [
0 9383 [ VAR
0 9384 [ bytes_to_allocate: integer,
0 9385 [ current_time: 0 .. 0fffffffffff(16),
0 9386 [ delay_status: ost$status,
0 9387 [ dfd_p: ^dmt$disk_file_descriptor,
0 9388 [ eoi: amt$file_byte_address,
0 9389 [ segment_number: ost$segment,
0 9390 [ sd_p: ^amt$segment_descriptor,
0 9391 [ sdtx_p: ^amt$segment_descriptor_extended;
0 9392 [
0 9393 [ segment_number := #SEGMENT (process_virtual_address);
4 9394 [ mmp$validate_segment_number (segment_number, sd_p, sdtx_p, status);
3E 9395 [ IF NOT status.normal THEN
3E 9396 [ RETURN;
40 9397 [ IFEND;
40 9398 [
40 9399 [ dmp$fetch_eoi (sdtx_p^sfid, eoi, status);
64 9400 [ IF NOT status.normal THEN
6C 9401 [ RETURN;
6E 9402 [ IFEND;
6E 9403 [
6E 9404 [ bytes_to_allocate := length - eoi;
6E 9405 [ IF bytes_to_allocate <= 0 THEN
7C 9406 [ RETURN;
7E 9407 [ IFEND;
7E 9408 [
7E 9409 [ current_time := #FREE_RUNNING_CLOCK (0);
86 9410 [ REPEAT
86 9411 [ mmp$assign_mass_storage (segment_number, sdtx_p^sfid, length, status);
AE 9412 [ IF NOT status.normal AND (status.condition = dma$unable_to_alloc_all_space) THEN
C6 9413 [ mmp$delay (1000, delay_status);
E2 9414 [ IFEND;
E2 9415 [ UNTIL (status.normal) OR ((#FREE_RUNNING_CLOCK (0) - current_time) > 60000000);
FE 9416 [

```

NDS/VE - MMSSEGMENT_MANAGER_SYSTEM_CORE
MMP\$OS_PREALLOCATE_FILE_SPACE

```

FE 9417 PROCEND mmp$os_preallocate_file_space;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$PRESET_PAGE_STREAMING

```

O 9419
O 9420 PROCEDURE [XDCL, #GATE] mmp$preset_page_streaming_r1
O 9421 {
O 9422   segment_number: ost$segment;
O 9423   validating_ring_number: ost$valid_ring;
O 9424   preset_and_save_fb_and_ts: boolean;
O 9425   temp_transfer_size: integer;
O 9426   VAR saved_transfer_size: 0 .. 15;
O 9427   VAR saved_free_behind: boolean;
O 9428   VAR status: ost$status;
O 9429 { Validate the pva and get a pointer to the segment descriptor.
O 9430
O 9431   VAR
O 9432     page_streaming_ts_shift: 0 .. 15,
O 9433     sdt_entry_p: ^mmt$segment_descriptor,
O 9434     sdtx_entry_p: ^mmt$segment_descriptor_extended;
O 9435
O 9436   status.normal := TRUE;
O 9437   mmp$validate_segment_number (segment_number, sdt_entry_p, sdtx_entry_p, status);
36 9438   IF NOT status.normal THEN
3E 9439     RETURN;
40 9440   IFEND;
40 9441
40 9442 { Verify that the pointer is in the read bracket of the segment.
40 9443
40 9444   IF (validating_ring_number > sdt_entry_p^.st.e.r2) OR (validating_ring_number > sdt_entry_p^.st.e.r2) THEN
58 9445     osp$set_status_abnormal ('MM', mme$ring_violation, '', status);
8A 9446     RETURN;
8C 9447   IFEND;
8C 9448
8C 9449   IF preset_and_save_fb_and_ts THEN
94 9450
94 9451 { Save the current setting of transfer size and free behind. Then set transfer size as specified,
94 9452 { and if the streaming boolean is false, set the preset_streaming boolean = TRUE.
94 9453
94 9454   saved_free_behind := (mmc$sa_free_behind IN sdtx_entry_p^.software_attribute_set);
94 9455   IF NOT saved_free_behind THEN
AC 9456     sdtx_entry_p^.software_attribute_set := sdtx_entry_p^.software_attribute_set +
B4 9457     $mmt$software_attribute_set [mmc$sa_free_behind];
B4 9458   IFEND;
B4 9459   saved_transfer_size := sdtx_entry_p^.stream.transfer_size;
B4 9460   mmp$convert_ps_transfer_size (temp_transfer_size, page_streaming_ts_shift);
106 9461
106 9462   IF sdtx_entry_p^.stream.transfer_size < page_streaming_ts_shift THEN
11A 9463     sdtx_entry_p^.stream.transfer_size := page_streaming_ts_shift;
122 9464   IFEND;
122 9465   IF NOT sdtx_entry_p^.stream.streaming THEN
130 9466     sdtx_entry_p^.stream.preset_streaming := TRUE;
130 9467   IF sdtx_entry_p^.stream.sequential_accesses < mmv$page_streaming_prestream THEN
148 9468     sdtx_entry_p^.stream.sequential_accesses := mmv$page_streaming_prestream;
14C 9469   IFEND;
14C 9470   IFEND;
14E 9471
14E 9472   ELSE
14E 9473

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$PRESET_PAGE_STREAMING

```

14E 9474 { reset SDTX with the saved transfer size and free behind from a previous call to mmp$preset_page_streaming
14E 9475
14E 9476   sdtx_entry_p^.stream.preset_streaming := FALSE;
14E 9477   IF NOT saved_free_behind THEN
168 9478     sdtx_entry_p^.software_attribute_set := sdtx_entry_p^.software_attribute_set +
17A 9479     {$mmt$software_attribute_set [mmc$sa_free_behind]};
17A 9480   IFEND;
17A 9481   IF sdtx_entry_p^.stream.transfer_size > saved_transfer_size THEN
192 9482     sdtx_entry_p^.stream.transfer_size := saved_transfer_size;
19A 9483   IFEND;
19A 9484   IFEND;
19A 9485
19A 9486   PROCEND mmp$preset_page_streaming_r1;
O 9487

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$PROCESS_FILE_ALLOC

```

0 9489
0 9490 { The purpose of this procedure is to process file allocation
0 9491 { if memory management has set the assign active flag in the SDTX.
0 9492 { This procedure is either called as a result of a trap by the
0 9493 { ring 1 trap handler or because a previous attempt to expand
0 9494 { a file failed.
0 9495 { In the case of a previous failure, this procedure is called periodically
0 9496 { from the ring 3 segment manager until it successfully expands the
0 9497 { file, or the user terminates the task.
0 9498 {
0 9499 {     MMP$PROCESS_FILE_ALLOC (allocation_length, status)
0 9500 {
0 9501 { ALLOCATION_LENGTH: (output) This parameter returns the total amount of space
0 9502 { that was allocated by this request.
0 9503 {
0 9504 { STATUS: (output) This parameter returns the request status.
0 9505 {
0 9506 {
0 9507 PROCEDURE [XDCL, #GATE] mmp$process_file_alloc
0 9508 (VAR allocated_length: amt$file_byte_address;
0 9509 VAR status: ost$status);
0 9510
0 9511 VAR
0 9512 accumulated_allocated_length: amt$file_byte_address,
0 9513 fde_p: gft$locked_file_desc_entry_p,
0 9514 flush_pages: boolean,
0 9515 rb: mmt$rb_ring1_segment_request,
0 9516 segnum: integer,
0 9517 sdt_p: mmt$max_sdt_p,
0 9518 sdt_x_p: mmt$max_sdt_x_p,
0 9519 tstatus: ost$status,
0 9520 xcb_p: ^ost$execution_control_block;
0 9521
0 9522 status.normal := TRUE;
0 9523 osp$begin_system_activity;
16 9524 accumulated_allocated_length := 0;
16 9525 rb.reqcode := syc$rc_ring1_segment_request;
16 9526 rb.request := mmc$sr1_flush_avail_modified;
16 9527 pmp$find_executing_task_xcb {xcb_p};
2E 9528 mmp$get_max_sdt_sdt_x_pointer {xcb_p, sdt_p, sdt_x_p};
2E 9529
2E 9530 /allocate_loop/
2E 9531 FOR segnum := 0 TO xcb_p^.xp.segment_table_length DO
84 9532
84 9533 IF segnum = 0 THEN
88 9534 IF xcb_p^.assign_active_sfid <> gfv$null_sfid THEN
A0 9535 gfp$get_locked_fde_p {xcb_p^.assign_active_sfid, fde_p};
1CE 9536 IF fde_p <> NIL THEN
1D8 9537 mmp$assign_mass_storage (0, xcb_p^.assign_active_sfid, 0, tstatus);
200 9538 gfp$unlock_fde_p {fde_p};
2F2 9539 IFEND;
2F2 9540 IFEND;
2F6 9541
2F6 9542 ELSEIF {sdt_p^.st [segnum].stev1 <> osc$vl_invalid_entry} AND
324 9543 {sdt_x_p^.sdt_x_table [segnum].assign_active <> mmc$assign_active_null} THEN

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$PROCESS_FILE_ALLOC

```

324 9544 rb.sfid := sdt_x_p^.sdt_x_table [segnum].sfid;
324 9545 gfp$get_fde_p {rb.sfid, fde_p};
380 9546 flush_pages := {fde_p^.media = gfc$fm_transient_segment};
380 9547 mmp$assign_mass_storage {segnum, gfv$null_sfid, 0, tstatus};
3B8 9548 IF tstatus.normal THEN
3C0 9549 IF flush_pages THEN
3C4 9550 i#call_monitor [#LOC {rb}, #SIZE {rb}];
3E0 9551 IFEND;
3E4 9552 ELSEIF {tstatus.condition = dme$unable_to_alloc_all_space} OR
414 9553 {tstatus.condition = dme$unable_to_get_fd_lock} OR
414 9554 {tstatus.condition = dfe$family_not_served} OR {tstatus.condition = dfe$server_not_active} OR
414 9555 {tstatus.condition = dfe$server_has_terminated} THEN
414 9556 IF {tstatus.condition = dme$unable_to_alloc_all_space} THEN
420 9557 gfp$get_fde_p {rb.sfid, fde_p};
476 9558 dmp$get_total_allocated_length {fde_p, allocated_length};
492 9559 accumulated_allocated_length := accumulated_allocated_length + allocated_length;
498 9560 IFEND;
498 9561 status := tstatus;
484 9562 ELSEIF tstatus.condition = dme$unable_to_create_fdt_entry THEN
48C 9563 syp$terminate_task {osc$rtr_sft_full};
4CC 9564 EXIT /allocate_loop/;
4D4 9565 ELSE
4D4 9566 osp$send_system_activity;
4DC 9567 syp$mfn_for_hang_task;
4E4 9568 IFEND;
4E4 9569 IFEND;
4E4 9570 FOREND /allocate_loop/;
4E8 9571
4E8 9572 allocated_length := accumulated_allocated_length;
4E8 9573
4E8 9574 osp$send_system_activity;
4F8 9575 PROCEND mmp$process_file_alloc;
0 9576

```

NOS/VE - MMSSEGMENT_MANAGER_SYSTEM_CORE
MMP\$RESERVE_SEGMENT_NUMBER

```

O 9578 {
O 9579 {   The purpose of this request is to reserve a segment number within the
O 9580 {   requesting task's
O 9581 {   address space for subsequent explicit assignment. The reserved segment does
O 9582 {   not have any
O 9583 {   file or segment associated with it as a result of this request; the segment
O 9584 {   number is simply
O 9585 {   not chosen by memory management on any subsequent requests for an empty
O 9586 {   segment.
O 9587 {   The segment can be used in subsequent requests that explicitly pass
O 9588 {   segment management the
O 9589 {   segment number to be used.
O 9590 {
O 9591 {       MMP$RESERVE_SEGMENT_NUMBER (SEGMENT_NUMBER, STATUS)
O 9592 {
O 9593 {   SEGMENT_NUMBER: (output) This parameter specifies the segment number that
O 9594 {   has been reserved.
O 9595 {
O 9596 {   STATUS: (output) This parameter specifies the request status.
O 9597 {
O 9598 {
O 9599 {
O 9600 {   PROCEDURE [XDCL, #GATE] mmp$reserve_segment_r1
O 9601 {   (
O 9602 {     VAR segment_num_list: ^array [ * ] of ost$segment;
O 9603 {     VAR status: ost$status);
O 9604 {
O 9605 {   VAR
O 9606 {     segment_table_length: integer,
O 9607 {     i: integer,
O 9608 {     sdt_entry_p: ^mmt$segment_descriptor,
O 9609 {     sdtX_entry_p: ^mmt$segment_descriptor_extended,
O 9610 {     xcb_p: ^ost$execution_control_block,
O 9611 {     segnum: ost$segment;
O 9612 {
O 9613 {     pmp$find_executing_task_xcb (xcb_p);
A 9614 {
A 9615 {     status.normal := TRUE;
A 9616 {     segnum := mmv$first_transient_seg_index - 1;
A 9617 {     segment_table_length := xcb_p^.xp.segment_table_length;
A 9618 {
A 9619 {     FOR i := LOWERBOUND (segment_num_list^)^ TO UPPERBOUND (segment_num_list^)^ DO
58 9620 {       REPEAT
58 9621 {         segnum := segnum + 1;
58 9622 {         IF segnum > segment_table_length THEN
5E 9623 {           expand_segment_table (xcb_p, status);
70 9624 {           IF NOT status.normal THEN
78 9625 {             RETURN;
7A 9626 {           IFEND;
7A 9627 {           segment_table_length := xcb_p^.xp.segment_table_length;
86 9628 {           IFEND;
86 9629 {           sdt_entry_p := mmp$get_sdt_entry_p (xcb_p, segnum);
86 9630 {           sdtX_entry_p := mmp$get_sdtX_entry_p (xcb_p, segnum);
86 9631 {           UNTIL (sdt_entry_p^.ste.v1 = osc$v1_invalid_entry) AND
E6 9632 {             (sdtX_entry_p^.segment_reservation_state = mmc$srs_not_reserved);

```

NOS/VE - MMSSEGMENT_MANAGER_SYSTEM_CORE
MMP\$RESERVE_SEGMENT_NUMBER

```

E6 9633 {   IF ada_stack_flag THEN
EE 9634 {     sdtX_entry_p^.segment_reservation_state := mmc$srs_reserved_shared_stack;
F8 9635 {   ELSE
F8 9636 {     sdtX_entry_p^.segment_reservation_state := mmc$srs_reserved;
FE 9637 {   IFEND;
FE 9638 {   segment_num_list^ [i] := segnum;
FE 9639 {   FOREND;
118 9640 {
118 9641 {   PROCEND mmp$reserve_segment_r1;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$SET_ACCESS_MODE

```

O 9643
O 9644 {
O 9645 { The purpose of this procedure is to set the access mode of a segment
O 9646 { based on its hardware attributes.
O 9647 {
O 9648 { MMP$SET_ACCESS_MODE (SEGMENT_DESCRIPTOR, ACCESS_MODE)
O 9649 {
O 9650 { SEGMENT_DESCRIPTOR: (input) This parameter is the segment descriptor
O 9651 { from the segment table.
O 9652 {
O 9653 { ACCESS_MODE: (output) This parameter is where the access mode for the
O 9654 { specified segment descriptor is returned.
O 9655 {
O 9656 {
O 9657 PROCEDURE [XDCL, #GATE] mmp$set_access_mode
4 9658 { segment_descriptor: ost$segment_descriptor;
4 9659 VAR access_mode: pft$susage_selections);
4 9660
4 9661 access_mode := $pft$susage_selections [];
4 9662
4 9663 IF segment_descriptor.xp <> osc$non_executable THEN
1A 9664 access_mode := access_mode + $pft$susage_selections [pfc$execute];
22 9665 IFEND;
22 9666
22 9667 IF segment_descriptor.rp <> osc$non_readable THEN
2E 9668 access_mode := access_mode + $pft$susage_selections [pfc$read];
3C 9669 IFEND;
3C 9670
3C 9671 IF segment_descriptor.wp <> osc$non_writable THEN
48 9672 access_mode := access_mode + $pft$susage_selections [pfc$shorten] + $pft$susage_selections [pfc$append] +
5C 9673 $pft$susage_selections [pfc$modify];
5C 9674 IFEND;
5C 9675
5C 9676 PROCEND mmp$set_access_mode;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$SET_ACCESS_SELECTIONS_R1

```

O 9678
O 9679 PROCEDURE [XDCL, #GATE] mmp$set_access_selections_r1
O 9680 { segment_number: ost$segment;
O 9681 validating_ring_number: ost$valid_ring;
O 9682 access_selections: mmt$access_selections;
O 9683 VAR status: ost$status);
O 9684
O 9685 VAR
O 9686 sd_p: ^mmt$segment_descriptor;
O 9687 sdtx_p: ^mmt$segment_descriptor_extended;
O 9688
O 9689 { Validate the pva and get a pointer to the segment descriptor.
O 9690
O 9691 mmp$validate_segment_number (segment_number, sd_p, sdtx_p, status);
30 9692 IF NOT status.normal THEN
38 9693 RETURN;
3A 9694 IFEND;
3A 9695
3A 9696 { Verify that the pointer is in the read bracket of the segment.
3A 9697
3A 9698 IF (validating_ring_number > sd_p^.ste.r2) THEN
4E 9699 osp$set_status_abnormal ('MM', mme$ring_violation, '', status);
80 9700 RETURN;
82 9701 IFEND;
82 9702 IF access_selections = mmc$as_sequential THEN
8C 9703 sdtx_p^.software_attribute_set := sdtx_p^.software_attribute_set +
AO 9704 $mmt$software_attribute_set [mmc$sa_read_transfer_unit, mmc$sa_free_behind];
AO 9705 ELSEIF access_selections = mmc$as_random THEN
A6 9706 sdtx_p^.software_attribute_set := sdtx_p^.software_attribute_set -
BA 9707 $mmt$software_attribute_set [mmc$sa_read_transfer_unit, mmc$sa_free_behind];
BA 9708 ELSEIF access_selections = mmc$as_read_tu THEN
CO 9709 sdtx_p^.software_attribute_set := sdtx_p^.software_attribute_set +
D4 9710 $mmt$software_attribute_set [mmc$sa_read_transfer_unit] -
D4 9711 $mmt$software_attribute_set [mmc$sa_free_behind];
D4 9712 IFEND;
D4 9713
D4 9714 IF (mmc$sa_read_transfer_unit IN sdtx_p^.software_attribute_set) THEN
E4 9715 IF sdtx_p^.stream.sequential_accesses < mmv$page_streaming_prestream THEN
F4 9716 sdtx_p^.stream.sequential_accesses := mmv$page_streaming_prestream;
F8 9717 IFEND;
F8 9718 IFEND;
F8 9719 PROCEND mmp$set_access_selections_r1;
O 9720

```


NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$SET_SEGMENT_ACCESS_RIGHTS

```

0 9722
0 9723 PROCEDURE [XDCL, INLINE] mmp$set_segment_access_rights
4 9724 ( sd: mmt$segment_descriptor;
4 9725   VAR sdtx: mmt$segment_descriptor_extended);
4 9726
4 9727 IF sd.ste.wp = osc$non_writable THEN
10 9728   sdtx.access_rights := mmc$sar_read;
1C 9729 ELSEIF mmc$sa_no_append IN sdtx.software_attribute_set THEN
2C 9730   sdtx.access_rights := mmc$sar_modify;
3A 9731 ELSE
34 9732   sdtx.access_rights := mmc$sar_write_extend;
3A 9733 IFEND;
3A 9734
3A 9735 PROCEND mmp$set_segment_access_rights;

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$SET_SEGMENT_LENGTH_R1

```

0 9737
0 9738 PROCEDURE [XDCL, #GATE] mmp$set_segment_length_r1
0 9739 ( segment_number: ost$segment;
0 9740   validating_ring_number: ost$valid_ring;
0 9741   segment_length: ost$segment_length;
0 9742   VAR status: ost$status);
0 9743
0 9744 {
0 9745 { The purpose of this procedure is to set or get the segment length for the
0 9746 { specified segment. Whether to set or get segment length is based on the
0 9747 { 'set_or_get_segment_length' parameter.
0 9748 {
0 9749 {
0 9750 VAR
0 9751   request_block: mmt$rb_set_get_segment_length,
0 9752   sdt_entry_p: ^mmt$segment_descriptor,
0 9753   sdtx_entry_p: ^mmt$segment_descriptor_extended;
0 9754
0 9755 mmp$validate_segment_number (segment_number, sdt_entry_p, sdtx_entry_p, status);
30 9756 IF status.normal = FALSE THEN
38 9757   RETURN;
3A 9758 IFEND;
3A 9759
3A 9760 { Verify that caller is within write bracket and has write access.
3A 9761
3A 9762 IF validating_ring_number > sdt_entry_p.ste.r1 THEN
4E 9763   osp$set_status_abnormal ('MM', mme$caller_not_in_write_bracket, '', status);
80 9764   RETURN;
86 9765 ELSEIF sdt_entry_p.ste.wp = osc$non_writable THEN
92 9766   osp$set_status_abnormal ('MM', mme$no_write_access, '', status);
C4 9767   RETURN;
C8 9768 IFEND;
C8 9769
C8 9770 gfp$get_fde_p (sdtx_entry_p.sfid, request_block.fde_p);
128 9771 request_block.request_code := syc$rc_set_get_segment_length;
128 9772 request_block.subfunction_code := mmc$sf_set_segment_length_fde_p;
128 9773 request_block.segment_length := segment_length;
128 9774
128 9775 i#call_monitor (#LDC (request_block), #SIZE (request_block));
156 9776
156 9777 PROCEND mmp$set_segment_length_r1;
0 9778

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$STORE_SEGMENT_ATTRIBUTES_R1

```

0 9780
0 9781 PROCEDURE [XDCL, #GATE] mmp$store_segment_attributes_r1
0 9782 (
0 9783   segment_number: ost$segment;
0 9784   validating_ring: ost$validating_ring;
0 9785   segment_attributes: ^array [ * ] of mmt$attribute_descriptor;
0 9786   VAR status: ost$status);
0 9787
0 9788 VAR
0 9789   access_mode: pft$usage_selections,
0 9790   scratch_segment_number: ost$segment,
0 9791   fde_entry_p: gft$locked_file_desc_entry_p,
0 9792   i: integer,
0 9793   pva: Ace11,
0 9794   sdt_entry_p: ^mmt$segment_descriptor,
0 9795   sdt_x_entry_p: ^mmt$segment_descriptor_extended;
0 9796 [ Validate the pva and get a pointer to the segment descriptor.
0 9797
0 9798 mmp$validate_segment_number (segment_number, sdt_entry_p, sdt_x_entry_p, status);
30 9799 IF NOT status.normal THEN
38 9800   RETURN;
3A 9801 IFEND;
3A 9802
3A 9803 IF (validating_ring > sdt_x_entry_p.open_validating_ring_number) THEN
4A 9804   osp$set_status_abnormal ('MM', mme$ring_violation, '', status);
7C 9805   RETURN;
7E 9806 IFEND;
7E 9807
7E 9808 [ Validate that attributes can be modified.
7E 9809
7E 9810 FOR i := LOWERBOUND (segment_attributes) TO UPPERBOUND (segment_attributes) DO
9E 9811   CASE segment_attributes^ [i].keyword OF
E2 9812     = mmc$kw_null_keyword :
E6 9813     = mmc$kw_ring_numbers :
E6 9814     = mmc$kw_max_segment_length :
10A 9815     = mmc$kw_max_segment_length :
10A 9816     = mmc$kw_max_segment_length :
240 9817     = mmc$kw_max_segment_length :
240 9818     = mmc$kw_max_segment_length :
240 9819     = mmc$kw_max_segment_length :
356 9820     = mmc$kw_max_segment_length :
356 9821     = mmc$kw_max_segment_length :
388 9822     = mmc$kw_max_segment_length :
38E 9823     = mmc$kw_max_segment_length :
38E 9824     = mmc$kw_max_segment_length :
38E 9825     = mmc$kw_max_segment_length :
3E8 9826     = mmc$kw_max_segment_length :
3EA 9827     = mmc$kw_max_segment_length :
3EA 9828     = mmc$kw_max_segment_length :
3EA 9829     = mmc$kw_max_segment_length :
408 9830     = mmc$kw_max_segment_length :
41E 9831     = mmc$kw_max_segment_length :
41E 9832     = mmc$kw_max_segment_length :
432 9833     = mmc$kw_max_segment_length :
432 9834     = mmc$kw_max_segment_length :

```

SOURCE LIST OF mmm\$segment_manager_system_core NOS/VE CYBIL/II 1.0 89102

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$STORE_SEGMENT_ATTRIBUTES_R1

```

450 9835   IF validating_ring <= 3 THEN
45A 9836     sdt_entry_p.ste.rp := osc$binding_segment;
46E 9837   ELSE
46E 9838     osp$set_status_abnormal ('MM', mme$binding_attribute_invalid, '', status);
4A0 9839   RETURN;
4A2 9840   IFEND;
4A2 9841   IFEND;
4A2 9842   IF mmc$ha_write IN segment_attributes^ [i].hardware_attri_set THEN
4C0 9843     sdt_entry_p.ste.wp := osc$write_uncontrolled;
4D4 9844   ELSE
4D4 9845     sdt_entry_p.ste.wp := osc$non_writable;
4E6 9846   IFEND;
4E6 9847   IF mmc$ha_cache_bypass IN segment_attributes^ [i].hardware_attri_set THEN
504 9848     sdt_entry_p.ste.v1 := osc$vl_cache_bypass;
51A 9849   ELSE
51A 9850     sdt_entry_p.ste.v1 := osc$vl_regular_segment;
52A 9851   IFEND;
52A 9852   IF mmc$ha_execute IN segment_attributes^ [i].hardware_attri_set THEN
548 9853     sdt_entry_p.ste.xp := osc$non_privileged;
55C 9854   ELSE
55C 9855     IF mmc$ha_execute_local IN segment_attributes^ [i].hardware_attri_set THEN
564 9856       sdt_entry_p.ste.xp := osc$local_privilege;
578 9857     ELSE
578 9858       IF (mmc$ha_execute_global IN segment_attributes^ [i].hardware_attri_set) AND
588 9859         (validating_ring > 1) THEN
588 9860         osp$set_status_abnormal ('MM', mme$execute_global_invalid, '', status);
58A 9861       RETURN;
5C0 9862     ELSE
5C0 9863       sdt_entry_p.ste.xp := osc$non_executable;
5D2 9864     IFEND;
5D2 9865   IFEND;
5D2 9866   = mmc$kw_segment_access_control :
5D6 9867   IF segment_attributes^ [i].access_control.execute_privilege = osc$local_privilege THEN
5F2 9868     osp$set_status_abnormal ('MM', mme$execute_local_invalid, '', status);
624 9869   IFEND;
624 9870
624 9871   IF segment_attributes^ [i].access_control.cache_bypass = TRUE THEN
63E 9872     sdt_entry_p.ste.v1 := osc$vl_cache_bypass;
654 9873   ELSE
654 9874     sdt_entry_p.ste.v1 := osc$vl_regular_segment;
664 9875   IFEND;
664 9876
664 9877   IF (segment_attributes^ [i].access_control.execute_privilege = osc$global_privilege) AND
688 9878     (validating_ring > 1) THEN
688 9879     osp$set_status_abnormal ('MM', mme$execute_global_invalid, '', status);
68A 9880   RETURN;
6C0 9881   ELSE
6C0 9882     sdt_entry_p.ste.xp := segment_attributes^ [i].access_control.execute_privilege;
6E8 9883   IFEND;
6E8 9884
6E8 9885   IF (segment_attributes^ [i].access_control.read_privilege = osc$binding_segment) AND
70C 9886     (validating_ring > 3) THEN
70C 9887     osp$set_status_abnormal ('MM', mme$binding_attribute_invalid, '', status);
73C 9888   RETURN;
73C 9889

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$STORE_SEGMENT_ATTRIBUTES_R1

```

73E 9890         IFEND;
73E 9891         sdt_entry_p^.ste.rp := segment_attributes^ [i].access_control.read_privilege;
73E 9892         sdt_entry_p^.ste.wp := segment_attributes^ [i].access_control.write_privilege;
776 9893         ELSE
776 9894         osp$set_status_abnormal ('MM', mme$set_unmodifiable_attribute, '', status);
7A8 9895         RETURN;
7AA 9896         CASEND;
7AA 9897         FOREND;
7AE 9898
7AE 9899         pva := #ADDRESS (1, segment_number, 0);
7AE 9900         #PURGE_BUFFER (osc$pva_purge_all_page_seg_map, pva);
7C4 9901
7C4 9902         PROCEND mmp$store_segment_attributes_r1;

```

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NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$TASK_DELETE_INHERITED_SDT

```

O 9904 {
O 9905 {   The purpose of this request is to delete all segments with the
O 9906 { inheritance attribute of mmc$si_new_segment. These segments are primarily
O 9907 { the task template segments. The SDT and SDTX tables are deallocated.
O 9908 { This request is used during task termination.
O 9909 {
O 9910 {           MMP$TASK_DELETE_INHERITED_SDT (TASK_ID, STATUS)
O 9911 {
O 9912 { TASK_ID: (input) This parameter identifies the task being terminated.
O 9913 {
O 9914 { STATUS: (output) This parameter is where the request status is returned.
O 9915 {           The possible error codes are:
O 9916 {                 mme$invalid_pva
O 9917 {                 mme$invalid_task_id
O 9918 {                 mme$ref_to_unrecovered_file
O 9919 {
O 9920 PROCEDURE [XDCL, #GATE] mmp$task_delete_inherited_sdt
O 9921 {   task_id: pmt$task_id;
O 9922   VAR status: ost$status);
O 9923
O 9924   VAR
O 9925   fde_entry_p: gft$file_desc_entry_p,
O 9926   local_sdt_p: ^cell,
O 9927   local_sdtx_p: ^cell,
O 9928   rb: mmt$rb_ring1_segment_request,
O 9929   sdt_p: mmt$max_sdt_p,
O 9930   sdtx_p: mmt$max_sdtx_p,
O 9931   segnum: ost$segment,
O 9932   xcb_p: ^ost$execution_control_block;
O 9933
O 9934   status.normal := TRUE;
O 9935   pmp$find_task_xcb (task_id, xcb_p);
2A 9936   IF xcb_p = NIL THEN
38 9937     osp$set_status_abnormal ('MM', mme$invalid_task_id, '', status);
6A 9938     RETURN;
6C 9939   IFEND;
6C 9940
6C 9941   mmp$get_max_sdt_sdtx_pointer (xcb_p, sdt_p, sdtx_p);
6C 9942
6C 9943   /scan_sdt_for_inherited_segs/
6C 9944   FOR segnum := 0 TO xcb_p^.xp.segment_table_length DO
6C 9945     IF (sdt_p^.st [segnum].ste.v1 <> osc$vl_invalid_entry) AND
6C 9946     (sdtx_p^.sdtx_table [segnum].inheritance = mmc$si_new_segment) THEN
6C 9947
6C 9948 { Close or delete the segment based on whether it is assigned to a file or not.
6C 9949   gfp$get_fde_p (sdtx_p^.sdtx_table [segnum].sfid, fde_entry_p);
13A 9950   IF fde_entry_p^.open_count <> 1 THEN
140 9951     osp$system_error ('FDE.OPEN_COUNT incorrect', NIL);
164 9952   IFEND;
164 9953   fde_entry_p^.open_count := fde_entry_p^.open_count - 1;
164 9954   destroy_segment (sdtx_p^.sdtx_table [segnum].sfid, fde_entry_p,
1A0 9955     sdtx_p^.sdtx_table [segnum].file_limits_enforced, status);
1A0 9956   IFEND;
1A0 9957   FOREND /scan_sdt_for_inherited_segs/;
1A4 9958

```

NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$TASK_DELETE_INHERITED_SDT

```

1A4 9959 { Release the SDT and SDTX table space.
1A4 9960
1A4 9961 local_sdt_p := #ADDRESS (1, osc$segnum_job_fixed_heap, xcb_p^.sdt_offset);
1A4 9962 local_sdtx_p := #ADDRESS (1, osc$segnum_job_fixed_heap, xcb_p^.sdtx_offset);
1A4 9963 IF #SIZE (local_sdt_p) > osv$page_size THEN
1D6 9964 mmp$free_pages (#ADDRESS (1, #SEGMENT (local_sdt_p), #OFFSET (local_sdt_p)), #SIZE (local_sdt_p),
208 9965 osc$wait, status);
208 9966 IFEND;
208 9967 FREE local_sdtx_p IN osv$job_fixed_heap^;
22E 9968 FREE local_sdt_p IN osv$job_fixed_heap^;
250 9969
250 9970 PROCEND mmp$task_delete_inherited_sdt;

```

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NOS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$TERMINATE_SHADOWING_R1

```

O 9972 {
O 9973 { The purpose of this request is to invalidate an ACTIVE segment in the SDT.
O 9974 { The backing store file is destroyed and returned if one is assigned.
O 9975 {
O 9976 { MMP$TERMINATE_SHADOWING_R1 (SEGMENT_NUMBER, STATUS);
O 9977 {
O 9978 { SEGMENT_NUMBER: (input) This parameter specifies the SEGMENT NUMBER
O 9979 { to be invalidated.
O 9980 {
O 9981 { STATUS: (output) This parameter specifies the request status returned.
O 9982 {
O 9983 {
O 9984 {
O 9985 { PROCEDURE [XDCL, #GATE] mmp$terminate_shadowing_r1
O 9986 { ( segment_number: ost$segment;
O 9987 { VAR status: ost$status);
O 9988 {
O 9989 { VAR
O 9990 { fde_entry_p: gft$locked_file_desc_entry_p,
O 9991 { open_count: gft$open_count,
O 9992 { pva: ^cell,
O 9993 { rb: mmt$rb_ring1_segment_request,
O 9994 { sdt_entry_p: ^mmt$segment_descriptor,
O 9995 { sdtx_entry_p: ^mmt$segment_descriptor_extended;
O 9996 {
O 9997 {
O 9998 { pva := #ADDRESS (1, segment_number, 0);
4 9999 mmp$fetch_sdt_sdtx_locked_fde (segment_number, sdt_entry_p, sdtx_entry_p, fde_entry_p, status);
48 10000 IF status.normal = FALSE THEN
50 10001 RETURN;
52 10002 IFEND;
52 10003
52 10004
52 10005 { Verify if shadow active. Only READ_WRITE shadowed segments can be terminated
52 10006
52 10007 IF sdtx_entry_p^.shadow_info.shadow_segment_kind <> mmc$ssk_read_write_file THEN
5E 10008 osp$set_status_abnormal ('MM', mme$invalid_shadow_segment, '', status);
8E 10009 gfp$unlock_fde_p (fde_entry_p);
17A 10010 RETURN;
17C 10011 IFEND;
17C 10012
17C 10013
17C 10014 { Clear any segment locks left by the user.
17C 10015
17C 10016 IF sdtx_entry_p^.segment_lock <> mmc$lss_none THEN
188 10017 mmp$unlock_segment (pva, mmc$lus_free, osc$nowait, status);
1A8 10018 IF NOT status.normal THEN
1B0 10019 gfp$unlock_fde_p (fde_entry_p);
2A6 10020 osp$system_error ('Unexpected mmp$unlock_segment error', ^status);
2CA 10021 IFEND;
2CA 10022 IFEND;
2CA 10023
2CA 10024 { Decrement active file open count. Delete the active FDE if open count is now zero.
2CA 10025
2CA 10026 fde_entry_p^.open_count := fde_entry_p^.open_count - 1;

```

NDS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$TERMINATE_SHADOWING_R1

```

2CA 10027      open_count := fde_entry_p^.open_count;
2CA 10028      gfp$unlock_fde_p (fde_entry_p);
3CC 10029
3CC 10030      IF open_count = 0 THEN
3DO 10031          destroy_segment (sdtx_entry_p^.sfid, fde_entry_p, sdtx_entry_p^.file_limits_enforced, status);
3EE 10032      IFEND;
3EE 10033
3EE 10034 { Change segment table entry to be unshadowed.
3EE 10035
3EE 10036      sdt_entry_p^.ste.asid := 0;
3EE 10037      sdtx_entry_p^.sfid := sdtx_entry_p^.shadow_info.shadow_sfid;
3EE 10038      sdtx_entry_p^.shadow_info.shadow_segment_kind := mmc$sk_none;
3EE 10039      sdtx_entry_p^.shadow_info.passive_for_shadow_by_segnum := FALSE;
3EE 10040
3EE 10041      #PURGE_BUFFER (osc$pva_purge_all_page_seg_map, pva);
40C 10042
40C 10043      PROCEND mmp$terminate_shadowing_r1;

```

NDS/VE - MMM\$SEGMENT_MANAGER_SYSTEM_CORE
MMP\$VALIDATE_SEGMENT_NUMBER

```

0 10045
0 10046 {
0 10047 { The purpose of this procedure is to validate a segment. Pointers to the
0 10048 { segment's SDT and SDTX entries are returned. The segment number is
0 10049 { checked to see if it is within the bounds of the segment table and whether the
0 10050 { valid bit is set.
0 10051 {
0 10052 {     MMP$VALIDATE_SEGMENT_NUMBER (SEGMENT_NUMBER, SDT_ENTRY_P,
0 10053 {     SDTX_ENTRY_P, STATUS)
0 10054 {
0 10055 { SEGMENT_NUMBER: (input) This parameter specifies the segment to be
0 10056 {     Validated.
0 10057 {
0 10058 { SDT_ENTRY_P: (output) This parameter is where a pointer to the SDT entry
0 10059 {     is returned.
0 10060 {
0 10061 { SDTX_ENTRY_P: (output) This parameter is where a pointer to the SDTX entry
0 10062 {     is returned.
0 10063 {
0 10064 { STATUS: (output) This parameter is where the request status is returned
0 10065 {     to the caller. The possible error codes are:
0 10066 {         mme$segment_number_not_in_use
0 10067 {         mme$segment_number_too_big
0 10068 {
0 10069 {
0 10070 {
0 10071 { PROCEDURE [XDCL, #GATE] mmp$validate_segment_number
0 10072 {     (
0 10073 {         segment_number: ost$segment;
0 10074 {         VAR sdt_entry_p: ^amnt$segment_descriptor;
0 10075 {         VAR sdtx_entry_p: ^amnt$segment_descriptor_extended;
0 10076 {         VAR status: ost$status);
0 10077 {
0 10078 {     VAR
0 10079 {         xcb_p: ^ost$execution_control_block;
0 10080 {
0 10081 {     status.normal := TRUE;
0 10082 {     pmp$find_executing_task_xcb (xcb_p);
14 10083
14 10083     IF segment_number > xcb_p^.xp.segment_table_length THEN
38 10084         IF segment_number > 4095 THEN
40 10085             osp$set_status_abnormal ('MM', mme$segment_number_too_big, '', status);
74 10086         ELSE
74 10087             osp$set_status_abnormal ('MM', mme$segment_number_not_in_use, '', status);
A6 10088         IFEND;
A6 10089         RETURN;
A8 10090     IFEND;
A8 10091
A8 10092     sdt_entry_p := mmp$get_sdt_entry_p (xcb_p, segment_number);
A8 10093     sdtx_entry_p := mmp$get_sdtx_entry_p (xcb_p, segment_number);
A8 10094     IF sdt_entry_p^.ste.v1 = osc$vl_invalid_entry THEN
116 10095         osp$set_status_abnormal ('MM', mme$segment_number_not_in_use, '', status);
148 10096     IFEND;
148 10097
148 10098     PROCEND mmp$validate_segment_number;
0 10100 MODEND mmm$segment_manager_system_core;

```

**** I:\$05578173AS0102D19890821T183254 L:ZZXLIST B:LGD DA:NONE LO=R RC=NONE OPT=SCHED EL=F LF=CS612 PAD=0

	ERROR	LINE	TEXT
WARNING	CY 821	7510	Code scheduling abandoned for this block due to register jamming.
WARNING	CY 821	7544	Code scheduling abandoned for this block due to register jamming.

LEVEL SUMMARY

**** 2 warning diagnostics

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
a2_previous_save_area	4877	9164
access_control	3880	7805 7811 7816 7819 7824 7825 8508/M 8510/M
		8512/M 8513/M 8514/M 9868 9872 9878 9883 9886
		9891 9892
access_mode	9659	9661/M 9664/M 9664 9668/M 9668 9672/M 9672
access_rights	4495	7507/M 7507/M 7507/M 7543/M 7543/M 7543/M 8691/M 9354/M
		9354/M 9354/M 9728/M 9730/M 9732/M
access_selections	9682	9702 9705 9708
accumulated_allocated_length	9512	9524/M 9559/M 9559 9572
ada_stack_flag	9801	9633
add_sdt_flag	7447	7447
add_sdt_sdt_entry	5458	7555 7936 8018
addr	5458	8560/M
addr_list	8530	8560/S 8560/M 8561/S 8561/M 8566/P
addr_returned	8531	8568/M 8566/P
allocate_loop	9530	9530 9564 9570
allocated_length	8536	8554/P 8556
allocated_length	9075	9089/P 9096/P
allocated_length	9508	9558/P 9559 9572/M
allocation_unit_size	2009	8685/M
already_locked	5572	5577 5577
already_locked	5588	5612 5613
already_locked	5789	5797 5797
already_locked	7447	7514 7514
already_locked	7572	7612 7612
already_locked	7662	7712 7712 7717 7717
already_locked	8121	8243 8243
already_locked	8390	8417 8417
already_locked	8526	8552 8552
already_locked	8849	8926 8926
already_locked	9295	9338 9338
already_locked	9507	9535 9535
already_locked	9781	9817 9817
amc\$access_mode	1134	611
amc\$average_record_length	1136	686
amc\$block_type	1137	613
amc\$cell_pointer	16	20
amc\$character_conversion	1138	615
amc\$clear_space	1139	617
amc\$collate_table_name	1141	688
amc\$compression_procedure_name	1194	690
amc\$data_padding	1142	693
amc\$dynamic_home_block_space	1195	695
amc\$embedded_key	1143	697
amc\$error_exit_name	1144	619
amc\$error_limit	1146	699
amc\$error_options	1147	621
amc\$estimated_record_count	1148	701
amc\$file_access_procedure	1149	623
amc\$file_byte_limit	9	12 438 895 933 969 1248 1310
amc\$file_contents	1150	625
amc\$file_limit	1152	627
amc\$file_organization	1153	629
amc\$file_processor	1154	631

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
amc\$file_structure	1155	633
amc\$forced_write	1156	635
amc\$hashing_procedure_name	1196	703
amc\$heap_pointer	16	22
amc\$index_levels	1162	705
amc\$index_padding	1163	707
amc\$initial_home_block_count	1197	709
amc\$internal_code	1164	637
amc\$key_length	1165	711
amc\$key_position	1166	713
amc\$key_type	1167	715
amc\$label_exit_name	1168	639
amc\$label_options	1170	641
amc\$label_type	1171	643
amc\$line_number	1172	645
amc\$loading_factor	1198	717
amc\$lock_expiration_time	1199	719
amc\$log_residence	1201	723
amc\$logging_options	1200	721
amc\$max_attribute	1242	1246
amc\$max_block_length	1173	647
amc\$max_block_number	755	758
amc\$max_error_count	1126	1129
amc\$max_file_id_ordinal	1063	1070
amc\$max_home_blocks	933	936
amc\$max_index_level	928	931
amc\$max_key_length	1267	1271
amc\$max_key_position	1276	1273
amc\$max_line_number	946	949
amc\$max_lines_per_inch	1334	1331
amc\$max_page_width	898	901
amc\$max_path_name_size	912	915
amc\$max_record_length	1174	649
amc\$max_records_per_block	1318	1322
amc\$max_statement_id_length	993	996
amc\$max_user_info	1329	1325
amc\$maxium_block	763	760 1301
amc\$maximum_keyed_record	1279	1276
amc\$maximum_record	869	872 1044 1305
amc\$message_control	1175	725
amc\$min_block_length	1176	651
amc\$min_record_length	1177	653
amc\$null_attribute	1178	655
amc\$open_position	1179	657
amc\$padding_character	1180	659
amc\$page_format	1181	661
amc\$page_length	1182	663
amc\$page_width	1183	665
amc\$preset_value	1185	667
amc\$record_limit	1186	727
amc\$record_type	1187	669
amc\$records_per_block	1188	729
amc\$return_option	1189	671

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----								
	ON LINE									
amc\$ring_attributes	1190	673								
amc\$sequence_pointer	17	24								
amc\$statement_identifier	1191	675								
amc\$user_info	1192	677								
amt\$vertical_print_density	1193	679								
amt\$access_selection	600	598								
amt\$average_record_length	1044	687								
amt\$block_header_type	736	739	745							
amt\$block_number	758	741	748							
amt\$block_status	737	750								
amt\$block_type	1047	614								
amt\$collation_value	1052	1049								
amt\$compression_procedure_name	903	692								
amt\$data_padding	1055	694								
amt\$dynamic_home_block_space	922	696								
amt\$entry_point_reference	906	903	924							
amt\$error_limit	1122	700								
amt\$estimated_record_count	1131	702								
amt\$file_attribute_keys	1246	605								
amt\$file_byte_address	12	410	411	451	455	467	469	485	1741	
		1789	1790	1816	1840	2007	3760	5361	5362	
		5388	5403	5467	5474	5487	5512	5512	7579	
		8536	9075	9388	9508	9512				
amt\$file_contents	781	626								
amt\$file_id_ordinal	1070	1067								
amt\$file_id_sequence	1071	1068								
amt\$file_identifier	1066	1058	1285							
amt\$file_item	604	600	603							
amt\$file_limit	438	473	628	1663	2011	2888				
amt\$file_organization	1252	630								
amt\$file_position	1255	591								
amt\$file_processor	842	632								
amt\$file_structure	888	634								
amt\$forced_write	1258	636								
amt\$hashing_procedure_name	824	704								
amt\$index_levels	931	706								
amt\$index_padding	1262	708								
amt\$initial_home_block_count	936	710								
amt\$internal_code	1264	638								
amt\$key_length	1271	712								
amt\$key_position	1273	714								
amt\$key_type	1282	716								
amt\$label_options	593	642								
amt\$label_type	1289	644								
amt\$line_number	940	646								
amt\$line_number_length	949	941								
amt\$line_number_location	951	942								
amt\$loading_factor	954	718								
amt\$lock_expiration_time	956	720								
amt\$log_residence	958	724								
amt\$logging_options	961	722								
amt\$logging_possibilities	964	961								
amt\$max_block_length	760	648	740	746	747					

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----								
	ON LINE									
amt\$max_record_length	972	650								
amt\$message_control	1299	726								
amt\$min_block_length	1301	652								
amt\$min_record_length	1305	654								
amt\$open_position	976	658								
amt\$padding_character	1308	660								
amt\$page_format	891	662								
amt\$page_length	895	664								
amt\$page_width	901	666	951	998						
amt\$pathname	915	908	958							
amt\$pointer_kind	16	19								
amt\$preset_value	442	493	668	1661	2898					
amt\$record_limit	1310	728								
amt\$record_type	1314	670								
amt\$records_per_block	1322	730								
amt\$return_option	594	672								
amt\$ring_attributes	980	674								
amt\$statement_id_length	996	988								
amt\$statement_id_location	998	989								
amt\$statement_identifier	987	676								
amt\$stape_error_action	1008	1003								
amt\$stape_error_options	1001	622								
amt\$unused_bit_count	788	742	749							
amt\$user_info	1325	678								
amt\$vertical_print_density	1331	680								
asid	3862	7832	7833	7834	7835					
asid	4473	7351	7356/M	7835/M	8206/M	8598/M	8706/M	10036/M		
asid	7133	7138/M	7141/S	7142/M	7142					
asid	7149	7155/M	7157/M	7157	7157					
asid	7324	7355/M	7355/M	7355	7355					
asid	7344	7355/P	7356							
assign_active	4500	7598	7652/M	8216/M	9543					
assign_active	7579	7598/M	7607/M	7624	7640/P					
assign_active_sfid	4103	7654/M	9534	9535/P	9537/P					
aste_p	7343	7358/M	7360	7361	7361	7363	7363	7366	7366	
		7367								
asti	2006	7173	7350	9012						
asti	4442	7354/M	7358/S	8207/M						
asti	7135	7139/M	7141/M	7141	7144					
asti	7152	7154/M	7155/S	7155/S	7156/S	7156/S				
asti	7324	7355/M	7355/S	7355/S	7355/S	7355/S				
asti	7345	7350/M	7351	7354	7355/P					
attach_count	2001	7365	7369	7384	8225/M	8687/M				
attrib	7663	7692	7693	7694	7695	7706	7716	7728	7729	
		7729	7730	7733	7733	7734	7738	7739	7741	
		7743	7748	7749	7756	7761	7766	7769	7772	
		7773	7782	7783	7784	7785	7786	7787	7792	
		7798	7800	7803	7805	7811	7812	7816	7819	
		7820	7824	7825	7827	7832	7833	7834	7835	
		7843	7845	7847	7851	7856	7857	7859/P	7869	
		7873	7815	7844						
bits	7127	7141	7155	7155	7155	7156	7355	7355	7355	

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table with columns: IDENTIFIER, DEFINED ON LINE, REFERENCES. Rows include identifiers like byte_offset, cache_bypass, class, clear_space, cnv, code, etc., with their corresponding defined line numbers and reference codes.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table with columns: IDENTIFIER, DEFINED ON LINE, REFERENCES. Rows include identifiers like code, condition, count, create_sdt_and_sdtx, ctime, current_length, current_time, cycle_rb, etc., with their corresponding defined line numbers and reference codes.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----						
	ON LINE							
dmc\$eof_byte_address	514	2854	2857	2860	2863	2866	2869	2872
dmc\$eoi_byte_address	515	466						
dmc\$file_hash	515	468						
dmc\$file_kind	516	470						
dmc\$file_limit	515	476	1547					
dmc\$file_status	515	472	2887					
dmc\$global_file_name	516	474						
dmc\$internal_vsn	516	478						
dmc\$level_1_table_size	1768	480	1843					
dmc\$locked_file	516	1772	1775					
dmc\$logical_length	517	482	2889					
dmc\$master_volume_required	517	484						
dmc\$max_bytes_per_allocation	1347	486	1549	2891				
dmc\$max_bytes_per_dau	1881	406	1340	1342	1343			
dmc\$max_bytes_per_mau	1917	1873						
dmc\$max_class_ordinal	1372	1903						
dmc\$max_dau_address	1883	1369						
dmc\$max_daus_allocation	1885	1877	1925					
dmc\$max_daus_position	1887	1874						
dmc\$max_daus_transfer	1889	1875	1927					
dmc\$max_device_file_list_index	1390	1889						
dmc\$max_fau_entries	1985	1387						
dmc\$max_file_hash	1400	1985						
dmc\$max_mau_address	1925	432	434	1397				
dmc\$max_maus_per_allocation	1919	1913						
dmc\$max_maus_per_dau	1921	1905	1906					
dmc\$max_maus_per_transfer	1923	1907	1908	1925	1927			
dmc\$max_maus_position	1927	1910	1911					
dmc\$max_transfer_size	1689	1912						
dmc\$min_bytes_per_allocation	1346	1684						
dmc\$min_bytes_per_dau	1880	1341						
dmc\$min_dau_address	1882	1873						
dmc\$min_mau_address	1924	1877						
dmc\$min_maus_per_allocation	1918	1913						
dmc\$min_maus_per_dau	1920	1904						
dmc\$min_maus_per_transfer	1922	1907						
dmc\$overflow	517	1909						
dmc\$owner	518	488	2893					
dmc\$preset_value	518	490	2895					
dmc\$queue_status	520	492	2897					
dmc\$record_vsn	518	508						
dmc\$requested_allocation_size	518	496	1545	1845				
dmc\$requested_transfer_size	518	498	1551	2899				
dmc\$requested_volume	518	498	2901					
dmc\$setname	518	500	2903					
dmc\$write_mode	520	502	1553					
dme\$unable_to_alloc_all_space	2710	506						
dme\$unable_to_create_fdt_entry	2308	9412	9552	9556				
dme\$unable_to_get_fd_lock	2332	9562						
dmp\$allocate_file_space_r1	5359	2332						
dmp\$create_disk_file	5385	7638						
dmp\$destroy_file	5395	7623	9020					

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----						
	ON LINE							
dmp\$fetch_eoi	5401	9399						
dmp\$get_disk_file_descriptor_p	5408	8553						
dmp\$get_initialized_addresses	5447	8566						
dmp\$get_total_allocated_length	5465	8554	9558					
dmp\$reallocate_file_space	5480	8812						
dmp\$sparse_allocate	5485	7640						
dmt\$access_kind	1521	1527						
dmt\$active_volume_table_index	1858	1812						
dmt\$addr_length_pair	5457	5449	8530					
dmt\$allocation_size	1340	414	457	497	1552	1667	2900	
dmt\$allocation_styles	1351	1824						
dmt\$bytes_per_mau	1903	1817						
dmt\$chapter_number	5374	5363						
dmt\$class_member	1364	415	459	1363	1542	2882		
dmt\$class_ordinal	1369	416	461	1544	2884			
dmt\$dau_address	1877	1971						
dmt\$dau_per_allocation	1874	1819						
dmt\$dau_per_position	1875	1818						
dmt\$delete_count	427	403						
dmt\$delete_logging_count	1828	1814						
dmt\$device_file_list_index	1387	465	1813	1842				
dmt\$disk_file_descriptor	401	5409	8537	9387				
dmt\$fau_states	1979	1972						
dmt\$file_allocation_unit	1970	1800	1801	1968				
dmt\$file_attribute	448	5387	7581					
dmt\$file_attribute_keywords	512	449	1540	1838	2880			
dmt\$file_hash	1397	471						
dmt\$file_hash_thread	430	432						
dmt\$file_medium_descriptor	1809	420	1823					
dmt\$fmd_attribute	1837	1833						
dmt\$fmd_index	1897	409	419	1832	1973			
dmt\$global_file_name	1403	479	1666					
dmt\$internal_vsn	1514	481	1820	1844				
dmt\$level_1_index	1775	408	1789					
dmt\$level_1_table	1789	407						
dmt\$level_2_index	1780	1800						
dmt\$locked_file	1524	483	2890					
dmt\$maus_per_dau	1907	1821						
dmt\$maus_per_transfer	1909	1822						
dmt\$ms_overflow_allowed	1738	2894						
dmt\$queue_status	1677	1664						
dmt\$requested_volume	1557	418	501	2904				
dmt\$system_file_id	1942	1811	3742	3746	3747	3755	3759	5360
		5402	5447	5473	5480	6308		5396
dmt\$transfer_size	1684	417	499	1668	2902			
dmt\$usage_count	1686							
dmt\$write_lock	1523	1529						
dmp\$idle_system	6135	7614						
enable	9034	9051						
eoi	9388	9399/P	9404					
eoi_byte_address	2007	5524	7632	8446	8561	8628		
eoi_state	2008	5518	8446	8628				

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED	REFERENCES
	ON LINE	
err_exit_proc	3853	8499/M
execute_privilege	3924	7816 7816 8512/M 9888 9878 9883
expand_segment_table	7187	7285 7311 7902 9623
fde_entry_p	7163	7172 7173 7179/P
fde_entry_p	7328	7350 7364 7365 7368 7369 7384
fde_entry_p	7450	7484 7485 7491 7510/P 7511/M 7511 7526/M 7544/P
fde_entry_p	7670	7707/P 7708 7916/P
fde_entry_p	7999	8005/P 8018/P 8022/P
fde_entry_p	8079	8086/P 8093/M 8093 8094/P
fde_entry_p	8130	8221/P 8222 8224/M 8225/M 8226/M 8227/M 8228/M 8230/M
fde_entry_p	8431	8243/P 8244/M 8244 8245/P
fde_entry_p	8538	8437/P 8446/P 8448 8449 8501 8520/P
fde_entry_p	8581	8552/P 8553/P 8554/P 8555/P 8561
fde_entry_p	8857	8587/P 8592/M 8599/P
fde_entry_p	8985	8885/P 8896/P 8907/P 8919/M 8919 8950 8951/M 8952/P
fde_entry_p	9305	8957 8958 8959/P
fde_entry_p	9790	9006/P 9011 9012 9019/M
fde_entry_p	9925	9338/P 9339/M 9339 9340/P 9356/P
fde_entry_p	9990	9817/P 9818/M 9819/P
fde_locked	8672	9949/P 9950 9953/M 9953 9954/P
fde_p	3792	9999/P 10009/P 10019/P 10026/M 10026 10027 10028/P 10031/P
fde_p	5408	8672 8677 8683 8709 9770/P
fde_p	5512	5521/M 8446/M 8628/M
fde_p	5534	5417 5420
fde_p	5572	5518 5521 5524
fde_p	5706	5549/M 5550 5551/M
fde_p	5789	5777/P
fde_p	5790	5710/P
fde_p	7447	5795/M 5795 5795/M
fde_p	7447	5795/P 5796 5797/P
fde_p	7447	7514/P 7514 7514/P
fde_p	7447	7514/M 7514 7514/M
fde_p	7572	7516/P
fde_p	7572	7612/P 7612 7612/P
fde_p	7572	7612/M 7612 7612/M
fde_p	7580	7645/P
fde_p	7662	7612/P 7615 7623/P 7626/P 7632 7634 7645/P
fde_p	7662	7712/P 7712 7712/P 7717 7717/P
fde_p	7662	7712/M 7712 7712/M 7717/M 7717 7717/M
fde_p	7662	7914/P 7955/P
fde_p	7992	8022/P
fde_p	8074	8094/P
fde_p	8121	8243/P 8243 8243/P
fde_p	8121	8243/M 8243 8243/M
fde_p	8121	8245/P
fde_p	8390	8417/P 8417 8417/P
fde_p	8390	8417/M 8417 8417/M
fde_p	8424	8446 8446 8446
fde_p	8424	8520/P
fde_p	8526	8552/P 8552 8552/P

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED	REFERENCES
	ON LINE	
fde_p	8526	8552/M 8552 8552/M
fde_p	8526	8553 8553
fde_p	8526	8555/P
fde_p	8573	8599/P
fde_p	8604	8627/M 8627 8627/M
fde_p	8604	8628 8628 8628
fde_p	8611	8627/P 8628/P
fde_p	8649	8711/P
fde_p	8656	8664/P 8701 8711/P
fde_p	8849	8998/P 8907/P 8929/P 8952/P
fde_p	8849	8926/P 8926 8926/P
fde_p	8849	8926/M 8926 8926/M
fde_p	8982	9006/M 9006 9006/M
fde_p	9295	9338/P 9338 9338/P
fde_p	9295	9338/M 9338 9338/M
fde_p	9295	9340/P
fde_p	9507	9535/P 9535 9535/P
fde_p	9507	9535/M 9535 9535/M 9545/M 9545 9545/M 9557/M 9557
fde_p	9507	9557/M
fde_p	9507	9538/P
fde_p	9513	9535/P 9536 9538/P 9545/P 9546 9557/P 9558/P
fde_p	9738	9770/M 9770 9770/M
fde_p	9781	9817/P 9817 9817/P
fde_p	9781	9817/M 9817 9817/M
fde_p	9781	9819/P
fde_p	9920	9949/M 9949 9949/M
fde_p	9985	10009/P 10019/P 10028/P
file_attributes_p	7581	7616 7617/M 7619/M 7621/M 7623/P
file_entry_index	1947	5549 5795 7514 7612 7712 7717 8243 8417
file_hash	1949	8552 8627 8926 9006 9338 9535 9545 9557
file_hash	2004	9770 9817 9949
file_kind	2003	5550 5795 7514 7612 7712 7717 8223/M 8243
file_limit	2011	8417 8552 8627 8926 9006 9338 9535 9545
file_limits	7582	9557 9770 9817 9949
file_limits_enforced	4498	5550 5795 7514 7612 7712 7717 8227/M 8243
file_limits_enforced	7164	8417 8552 8627 8926 9006 9338 9535 9545
file_limits_to_enforce	3804	8417 8552 8627 8926 9006 9338 9535 9545
find_available_segment_number	7289	9557 9770 9817 9949
flags	1997	7714/M 8226/M 8931 8932 8957 8958
flush_pages	9514	7798/M 7843/M 7938 7939 8448 8449 8701/M 8701
gfc\$fde_size	5566	7597/M 7606/M 7638/P 7640/P
gfc\$fde_table_base	5564	7597 7695/M 8595/M 8599/P 9020/P 9955/P 10031/P
		7714 7896 9278 9321
		8592/M
		9546/M 9549
		5549 5795 7514 7612 7712 7717 8243 8417
		8552 8627 8926 9006 9338 9535 9545 9557
		9770 9817 9949
		5549 5565 5795 7514 7612 7712 7717 8243
		8417 8552 8627 8926 9006 9338 9535 9545

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES
i	7572	7645/M
i	7662	7712/M 7717/M
i	7662	7914/M 7955/M
i	7669	7698 7699/S 7729 7730/S 7733/S 7733/S 7734/S 7738/S
		7739/S 7741/S 7743/S 7748/S 7756/S 7761/S 7766/S 7769/S
		7772/S 7783/S 7785/S 7787/S 7792/S 7798/S 7800/S 7803/S
		7805/S 7811/S 7816/S 7819/S 7824/S 7825/S 7832/S 7833/S
		7834/S 7835/S 7843/S 7845/S 7847/S 7851/S 7856/S 7857/S
		7859/S 7869/S 7873/S
i	7992	8022/M
i	8074	8094/M
i	8121	8243/M
i	8121	8245/M
i	8320	8379 8380/S 8380/S
i	8390	8417/M
i	8424	8520/M
i	8432	8438 8439/S 8441/S 8442/S 8444/S 8446/S 8448/S 8449/S
		8451/S 8451/S 8454/S 8456/S 8459/S 8459/S 8462/S 8462/S
		8465/S 8465/S 8471/S 8471/S 8474/S 8474/S 8480/S 8480/S
		8483/S 8483/S 8486/S 8486/S 8492/S 8492/S 8497/S 8499/S
		8501/S 8503/S 8505/S 8508/S 8510/S 8512/S 8513/S 8514/S
i	8526	8552/M
i	8526	8555/M
i	8573	8599/M
i	8649	8711/M
i	8733	8764 8765/P 8767/S 8773 8774/S
i	8849	8896/M 8907/M 8929/M 8952/M
i	8849	8926/M
i	8858	8875 8876/S 8914 8915/S
i	9120	9130 9132/P
i	9295	9338/M
i	9295	9340/M
i	9507	9535/M
i	9507	9538/M
i	9607	9619 9638/S
i	9781	9817/M
i	9781	9819/M
i	9791	9810 9811/S 9814/S 9815/S 9818/S 9824/S 9829/S 9834/S
		9842/S 9847/S 9852/S 9855/S 9858/S 9868/S 9872/S 9878/S
		9883/S 9886/S 9891/S 9892/S
i	9985	10009/M
i#build_adaptable_heap_pointer	5804	7948 10019/M 10028/M
i#build_adaptable_seq_pointer	5810	7946
i#call_monitor	5528	5522 5577 5625 5710 5746 5775 5797 7177
		7263 7390 7514 7516 7612 7645 7712 7717
		7914 7955 8022 8094 8243 8245 8361 8417
		8446 8520 8552 8555 8599 8628 8711 8793
		8896 8907 8926 8929 8952 9016 9255 9338
		9340 9535 9538 9550 9775 9817 9819 10009
		10019 10028
i#move	5818	5834 9143
i#program_error	5444	5418 8553
i#real_memory_address	5836	8190 9131

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES
ijl_ordinal	6188	7361 7363
ijl_ordinal	6792	7361 7363 7367
ijle_p	6187	7486
in_memory	9121	9127/M 9134/M 9139
in_use	6793	7360
index	2075	5577 5621 5797 7514 7612 7712 7717 8243
		8417 8552 8926 9338 9535 9817
inheritance	3864	7845 8503/M
inheritance	4492	7845/M 7980 7981/M 7982 7983/M 8160 8160 8202
		8220 8237 8238/M 8503 8593/M 8895 9049 9946
init_new_io	3720	8791/M 8816/M
ioc\$max_unit_number	1864	1858 1867
iot\$io_error	6860	6309 6835
iot\$transfer_count	4662	4650
jmc\$detached_job_wait_time_max	6216	6213
jmc\$highest_det_job_wait_time	6226	6216 6227
jmc\$highest_prio_age_interval	4349	4340 4350
jmc\$highest_service_accumulator	4381	4382
jmc\$highest_service_factor_valu	4423	4416
jmc\$highest_working_set_size	6923	6914 6924 6926 6928 6930
jmc\$ies_job_swapped	6429	6438
jmc\$ies_swapin_in_progress	6428	6437
jmc\$iss_idle_tasks_initiated	6444	6471
jmc\$iss_swapin_io_complete	6469	6472
jmc\$iss_swapin_requested	6465	6472
jmc\$iss_swapout_complete	6464	6471
jmc\$iss_swapped_io_cannot_init	6455	6482
jmc\$iss_swapped_no_io	6446	6481
jmc\$keyword_offset_maximum	4366	4341 6915
jmc\$klj_maximum_entries	4314	4307 4308 6552
jmc\$kol_maximum_entries	4324	4309
jmc\$max_active_jobs	4305	4292 4300 4301
jmc\$max_ajl_ord	4306	4305 6373
jmc\$max_dispatching_control	4250	4254
jmc\$max_dispatching_priority	4151	4111 4114 4115
jmc\$maximum_job_classes	6541	6544
jmc\$maximum_job_count	4321	4314
jmc\$maximum_output_count	4331	4324
jmc\$maximum_service_classes	4399	4402
jmc\$min_dispatching_control	4249	4253
jmc\$null_service_class	4392	4393
jmc\$priority_aging_interval_max	4340	4337
jmc\$priority_p1	4165	4112
jmc\$priority_p10	4174	4113
jmc\$priority_p14	4178	4113
jmc\$priority_p8	4172	4112
jmc\$required_offset	4364	6929
jmc\$reserved_ajls	4310	4305
jmc\$service_accumulator_maximum	4373	4370
jmc\$service_factor_value_max	4416	4413
jmc\$system_default_offset	4365	4366 6931
jmc\$system_supplied_name_size	6714	6711

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED	REFERENCES							
	ON LINE								
jmc\$unlimited_offset	4362	4351	4383	6217	6228	6925			
jmc\$unspecified_offset	4363	6927							
jmc\$working_set_size_maximum	6914	6911							
jmt\$ajl_ordinal	6373	6245							
jmt\$delayed_swapin_work	6390	6275	6394						
jmt\$detached_job_wait_time	6213	6198							
jmt\$dispatching_control	4220	4203							
jmt\$dispatching_control_index	4253	4220	6409						
jmt\$dispatching_controls	4223	4221							
jmt\$dispatching_priority	4111	4076	4078	4225	6257	6410	6411	6412	
jmt\$ijl_block_index	2205	2201							
jmt\$ijl_block_number	2204	2200							
jmt\$ijl_dispatching_control	6408	6258							
jmt\$ijl_entry_status	6424	6244							
jmt\$ijl_ordinal	2199	6188	6264	6292	6729	6730	6792	6827	6966
jmt\$ijl_page_fault_count	6388	6493	6494	6495					
jmt\$ijl_page_stats	6492	6498							
jmt\$ijl_service_class_stats	6486	6279							
jmt\$ijl_statistics	6516	6278							
jmt\$ijl_swap_count	6507	6503	6504						
jmt\$ijl_swap_counts	6502	6298	6489						
jmt\$ijl_swap_status	6442	6247	6248	6249					
jmt\$initiated_job_list_entry	6241	6187	6756						
jmt\$input_file_location	6572	6567							
jmt\$job_abort_disposition	6581	6565							
jmt\$job_class	6544	6303							
jmt\$job_control_block	6169	6155							
jmt\$job_mode	6547	6260							
jmt\$job_priority	4281	4212	4213	4214	4215	6300	6301		
jmt\$job_recovery_disposition	6584	6566							
jmt\$job_system_id	6903	6184							
jmt\$kl_index	6552	6246	6903						
jmt\$maximum_active_jobs	4292	4197							
jmt\$priority_aging_interval	4337	4205							
jmt\$queue_file_ijkl_information	6564	6285							
jmt\$scheduling_data	6291	6269							
jmt\$scheduling_priority	4211	4204							
jmt\$service_accumulator	4370	4195	4196	6293	6294	6295			
jmt\$service_class_index	4402	4188	4198	6304					
jmt\$service_class_name	4405	4190	4191						
jmt\$service_factor_value	4413	4199							
jmt\$service_factors	4409	4199							
jmt\$swap_data	6307	6271							
jmt\$swapout_reasons	6589	6299							
jmt\$swapped_job_entry	6604	6207	6316	6757					
jmt\$system_supplied_name	6711	6182	6242						
jmt\$task_time_slice	4263	4243	4244						
jmt\$time_slice_values	4242	4089	4227						
jmt\$user_supplied_name	6907	6183							
jmt\$working_set_size	6911	6194	6195						
jmv\$executing_within_system_job	6145	7618							
jmv\$jcb	6155	7361	7363	7486					
jmv\$system_ijkl_ordinal	6966	7367							

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED	REFERENCES							
	ON LINE								
jmv\$task_private_temp_p	6971	8744	8745						
job_lock	1995	5577/P	5710/P	5797/P	7514/P	7516/P	7612/P	7645/P	7712/P
		7717/P	7914/P	7955/P	8022/P	8094/P	8243/P	8245/P	8417/P
		8520/P	8552/P	8555/P	8599/P	8711/P	8896/P	8907/P	8926/P
		8929/P	8952/P	9338/P	9340/P	9535/P	9538/P	9817/P	9819/P
		10009/P	10019/P	10028/P					
jsc\$isqi_swapped_io_completed	6734	6736							
jsc\$isqi_swapped_io_not_init	6733	6736							
jst\$changed_asid_entry	6779	6770							
jst\$ijl_swap_queue_id	6733	6728							
jst\$ijl_swap_queue_link	6727	6253							
jst\$io_control_information	6741	6272							
jst\$swap_file_descriptor	6755	6273							
jst\$swapped_page_descriptor	6764	6762							
jst\$swapped_page_descriptors	6761	6758							
key_lock	4474	7800/M	8454						
keyword	449	7617/M							
keyword	3836	7730	8439	9811					
last_segment_number	2015	7921/M	8228/M						
leftover	8734	8755/M	8758/M	8772					
length	5459	8561/M							
length	5820	5827	5830/M	5830					
length	5940	5952	5952	5957/S					
length	8308	8338	8338	8338/S					
length	9111	9144	9144/M	9144					
length	9380	9404	9411/P						
length_to_allocate	7583	7632/M	7633	7635	7636/M	7638/P			
list_overflow	8532	8559/M	8567/P						
local_fde_p	5414	5420/M	5421	5422					
local_fde_p	8526	8553/M	8553	8553					
local_sdt	7458	7469/M	7478	7478	7479	7488/M	7493/M	7507/P	7510/P
		7522	7525/S	7526	7543/P	7544/P			
local_sdt_p	9926	9961/M	9963	9964/P	9964/P	9964/P	9968		
local_sdt_p	8131	8167	8176/P	8177/P	8182/P	8182/P	8190/P	8194	
local_sdtx	7459	7470/M	7506/M	7507/P	7509	7510/P	7513	7514/P	7535/M
		7535	7542	7544/P					
local_sdtx_p	9927	9962/M	9967						
local_sdtxp	8132	8188	8189/P	8189/P	8195				
lock	5572	5577	5577	5577/M	5577	5577/M	5577	5577/M	5577/M
		5577	5577						
lock	5583	5606	5606	5607/M	5607	5608/M	5612	5614/M	5615/M
		5621	5621	5624					
lock	5706	5710	5710	5710/M	5710	5710	5710	5710/M	5710/P
lock	5716	5728	5732	5733/M	5733	5735/M	5736	5737/M	5746/P
lock	5789	5797	5797	5797/M	5797	5797/M	5797	5797/M	5797/M
		5797	5797						
lock	7447	7514	7514	7514/M	7514	7514/M	7514	7514/M	7514/M
		7514	7514						
lock	7447	7516	7516	7516/M	7516	7516/M	7516	7516/M	7516/P
lock	7572	7612	7612	7612/M	7612	7612/M	7612	7612/M	7612/M
		7612	7612						

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

IDENTIFIER	ON LINE	REFERENCES
mmc\$kw_ring_numbers	3821	3837 7732 8440 9813
mmc\$kw_segment_access_control	3825	3859 7804 8506 9867
mmc\$kw_segment_number	3822	3840 7740 8443
mmc\$kw_shadow_segment	3827	3865 7846
mmc\$kw_software_attributes	3824	3857 7781 8496
mmc\$kw_wired_segment	3827	3868 7840
mmc\$lss_none	4595	7110 8904 10016
mmc\$lus_free	4548	8905/P 10017/P
mmc\$pmo_user_stack	7026	7523/S
mmc\$ppq_avail	6621	6667
mmc\$ppq_free	6620	6679
mmc\$ppq_job_fixed	6661	6668 6680
mmc\$ppq_job_working_set	6663	6680 6681 7363
mmc\$ppq_shared_first	6669	7366
mmc\$ppq_shared_first_site	6671	6675
mmc\$ppq_shared_last	6676	7366
mmc\$ppq_shared_num_sites	6672	6675
mmc\$ppq_shared_other	6630	6670
mmc\$ppq_shared_site_01	6632	6671
mmc\$ppq_shared_site_25	6656	6676
mmc\$ppq_shared_task_service	6625	6669
mmc\$ppq_swapped_io_error	6659	6679
mmc\$ppq_wired	6623	6666
mmc\$ring_crossing_offset	7041	7523
mmc\$sa_fixed	3886	7601 7787
mmc\$sa_free_behind	3887	9349 9454 9457 9479 9704 9707 9711
mmc\$sa_no_append	3888	7507 7543 9354 9729
mmc\$sa_read_transfer_unit	3887	7924 9349 9704 9707 9710 9714
mmc\$sa_stack	3887	7521 7536 7784 7787 8229 8247 8675
mmc\$sa_wired	3886	7600 7787 7842
mmc\$sar_modify	4559	7507 7543 9354 9730
mmc\$sar_read	4559	7507 7543 9344 9354 9728
mmc\$sar_write_extend	4560	7110 7507 7543 8691 9354 9732
mmc\$sas_allow_access	4565	7109
mmc\$segment_fault_processor_id	5005	5059
mmc\$sequence_pointer	3899	3906 7945
mmc\$sf_get_segment_length_fde_p	3797	5520 8446 8628
mmc\$sf_set_segment_length_fde_p	3798	9772
mmc\$shadow_allocation_size	3918	7847 7851 8685
mmc\$si_new_segment	3917	8220 9946
mmc\$si_none	3917	7109 7982 7983 8160 8160 8202 8238 8895
mmc\$si_share_segment	3917	7982 7983 8593
mmc\$si_transfer_segment	3918	7980 7981 8237
mmc\$srl_change_swap_file_queue	3732	3739
mmc\$srl_delete_job_seg_by_sf_id	3734	3740
mmc\$srl_delete_seg_segnum	3721	3743 9014
mmc\$srl_delete_seg_sf_id	3722	3738 7175
mmc\$srl_detach_file	3725	3738 8797
mmc\$srl_end_job_recovery	3729	3749
mmc\$srl_flush_avail_modified	3736	3741 9526
mmc\$srl_flush_delete_seg_sf_id	3726	3738 8797
mmc\$srl_flush_seg_segnum	3727	3739 8797
mmc\$srl_get_highest_offset	3733	3758

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

IDENTIFIER	ON LINE	REFERENCES
mmc\$srl_make_mfw_cache	3730	3752
mmc\$srl_remove_detached_pages	3735	3740
mmc\$srl_remove_job_shared_pages	3731	3754 7386
mmc\$srl_replace_sf_id	3728	3745
mmc\$srs_not_reserved	4605	7110 7892 9278/P 9320 9632
mmc\$srs_reserved	4605	9636
mmc\$srs_reserved_shared_stack	4606	7894 8160 8162 8204 8255 8258 9634
mmc\$ssk_none	4615	4587 7110 7513 7864 8209 8211 8925 8943
mmc\$ssk_read_only_file	4615	10038 8689
mmc\$ssk_read_write_file	4615	10007
mmc\$ssk_segment_number	4616	4585 7868 8210 8942
mmc\$ssrl_flush_delete_seg_sf_id	3780	3770
mmc\$ssrl_free_delete_seg_sf_id	3780	3770
mmc\$ssrl_move_modified_df_page	3781	3772 9251
mmc\$volume_unavailable_flag	5237	9181/P
mme\$address_not_0_mod_16384	3152	7852/P
mme\$asid_specified	3028	7828/P
mme\$binding_attribute_invalid	2979	7752/P 7821/P 9838/P 9886/P
mme\$caller_not_in_read_bracket	2985	8623/P
mme\$caller_not_in_write_bracket	2988	9763/P
mme\$execute_global_invalid	2982	7774/P 7813/P 9860/P 9880/P
mme\$execute_local_invalid	3001	9825/P 9869/P
mme\$illegal_segment_origin_chg	3188	7985/P
mme\$init_shadow_improper_seg	3158	8676/P
mme\$invalid_asid_specified	3031	7837/P
mme\$invalid_close_segment_req	2991	8897/P
mme\$invalid_ring_brackets	2976	7735/P
mme\$invalid_sf_id	2932	7719/P
mme\$invalid_shadow_segment	3155	7865/P 10008/P
mme\$invalid_shared_taskid	3191	7701/P 8878/P
mme\$invalid_task_id	3052	8152/P 9937/P
mme\$io_write_error	3062	8795
mme\$length_not_0_mod_16384	3149	7848/P
mme\$no_write_access	3086	9766/P
mme\$ring_violation	2972	7976/P 8011/P 8547/P 9445/P 9699/P 9804/P
mme\$segment_not_pageable	3065	7602/P
mme\$segment_number_is_in_use	3010	7908/P
mme\$segment_number_not_in_use	3013	7899 8061/P 8407/P 8415/P 10087/P 10095/P
mme\$segment_number_too_big	3016	8055/P 8405/P 10085/P
mme\$segment_table_is_full	3007	7234/P
mme\$set_unmodifiable_attribute	3004	9894/P
mme\$software_attribute_invalid	2985	7789/P
mme\$unable_to_assign_fde	3280	7709/P
mme\$unsupported_keyword	3019	7795/P 8234/P 8662/P
mmk\$job_base	3470	3271 3275 3278 3283 3287 3291 3295 3299
		3303 3306 3310 3314 3317 3321 3324 3327
		3331 3335 3339 3343 3346 3349 3352 3355
		3358 3361 3364 3368 3372 3376 3380 3384
		3388 3392 3396 3400 3404 3408 3412 3416
		3420 3424 3428 3432 3436 3440
mmp\$advise_in	5839	9140
mmp\$asid	7148	7159 7355

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES
mmp\$assign_contiguous_memory	5853	7242 8177
mmp\$assign_mass_storage	7572	7658 9411 9537 9547
mmp\$ast	7128	7145
mmp\$build_segment	7662	7957
mmp\$change_seg_inheritance_r1	7960	7988
mmp\$change_segment_number_r1	7992	8024
mmp\$change_stack_attribute_r1	8027	8039
mmp\$close_asid_based_segment	8042	8071
mmp\$close_device_file	8074	8096
mmp\$convert_ps_transfer_size	5863	5888 7626 7877 7929 7931 9352 9460
mmp\$create_inherited_sdt	8121	8264
mmp\$delete_non_inherited_segs	8282	8305
mmp\$fetch_offset_mod_pages_r1	8308	8386
mmp\$fetch_sdt_sdtx_locked_fde	8390	8005 8086 8420 8437 8587 8664 8885 9999
mmp\$fetch_segment_attributes_r1	8424	8522
mmp\$free_pages	5891	7241 7275 8176 9964
mmp\$get_allocated_addresses_r1	8526	8569
mmp\$get_max_sdt_sdtx_pointer	5899	5909 7306 8159 8297 8938 9046 9528 9941
mmp\$get_sdt_entry_p	5913	7504 7540 8059 8249 8412 8765 8917 9001
mmp\$get_sdt_entry_p	5915	9284 9329 9629 10092
mmp\$get_sdt_for_job_template	8573	5917/M 7504/M 7540/M 8059/M 8249/M 8412/M 8765/M 8917/M
mmp\$get_sdtx_entry_p	5925	9001/M 9284/M 9329/M 9629/M 10092/M
mmp\$get_sdtx_entry_p	5927	8601 7505 7541 7596 8250 8259 8413 9003 9286
mmp\$get_segment_length_r1	8604	9330 9630 10093
mmp\$init_system_privilege_map	8728	5929/M 7505/M 7541/M 7596/M 8250/M 8259/M 8413/M 9003/M
mmp\$initiate_shadowing_r1	8648	9286/M 9330/M 9630/M 10093/M
mmp\$invalidate_segment	8849	8630
mmp\$issue_ring1_segment_request	8781	8778
mmp\$job_delete_inherited_sdt	8982	8713
mmp\$job_multiprocessing_control	9033	8713 8301 8965
mmp\$mfh_for_segment_manager	9072	8819
mmp\$mfh_shadow_file_reference	9111	9030
mmp\$mfh_volume_unavailable	9149	9060
mmp\$mm_move_mod_server_page	9238	9108
mmp\$open_asid_based_segment	9263	9146
mmp\$open_file_by_sfid	9295	9187
mmp\$os_preallocate_file_space	9378	9260
mmp\$preset_page_streaming_r1	9420	9291
mmp\$process_file_alloc	9507	9360
mmp\$preserve_segment_number_r1	9600	9417
mmp\$set_access_mode	9657	9486
mmp\$set_access_selections_r1	9679	9089 9096 9575
mmp\$set_segment_access_rights	9723	9641 9676
mmp\$set_segment_length_r1	9738	9679 9719
mmp\$store_segment_attributes_r1	9781	9707 7543 9354 9735
mmp\$task_delete_inherited_sdt	9920	9777
mmp\$terminate_shadowing_r1	9985	9902
mmp\$touch_all_pages	5938	9970
mmp\$unlock_segment	5962	10043 5959 8338 8905 10017

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES
mmt\$validate_segment_number	10071	7859 7898 7970 8346 8542 8615 9394 9437
mmt\$access_selections	3550	9591 9755 9798 10098
mmt\$active_segment_table	6804	9289 9682
mmt\$active_segment_table_entry	6785	6989
mmt\$ast_index	2110	6767 6805 6834 7343
mmt\$attribute_descriptor	3835	2006 3748 4442 6315 6782 7130 7148 7152
mmt\$attribute_keyword	3821	7345
mmt\$seoi_state	2119	3807 8427 9784
mmt\$global_page_queue_index	6679	3836
mmt\$global_page_queue_list_ent	6888	2008
mmt\$hardware_attribute_set	3890	6679 6898
mmt\$hardware_attributes	3878	6898
mmt\$job_page_queue_index	6680	3856 8456 8460 8463 8466 8472 8475 8481
mmt\$job_page_queue_list	6899	8484 8487 8493
mmt\$link	6812	3890
mmt\$lock_segment_status	4595	3890
mmt\$locked_page	6846	6606 6899
mmt\$lus_page_disposition	4547	6270
mmt\$max_sdt	4456	6790 6824 6825 6885
mmt\$max_sdt_p	4456	4496
mmt\$max_sdtx	4520	6830
mmt\$max_sdtx_p	4524	4547 4456
mmt\$memory_reserve_request	8666	5901 7296 8136 8287 8864 9038 9517 9929
mmt\$page_age	6853	4524 5902 7297 8137 8288 8865 9039 9518 9930
mmt\$page_frame_index	6750	6263
mmt\$page_frame_queue_id	6681	6833 6857 6857
mmt\$page_frame_table_entry	6823	6742 6744 6745 6746 6814 6814 6868 6869
mmt\$page_map_offsets	7029	6743 6798 6828
mmt\$page_map_offsets_ord	7026	6765 6839
mmt\$page_queue_list_entry	6884	7018
mmt\$rb_change_segment_table	3557	7029
mmt\$rb_fetch_offset_mod_pages	3588	6889 6899
mmt\$rb_ring1_segment_request	3715	7210
mmt\$rb_ring1_server_seg_request	3766	8324
mmt\$rb_set_get_segment_length	3790	3715 7168 7346 8782 8861 8986 9515 9928 9993
mmt\$stream_data	4503	4245
mmt\$segment_access_condition	5032	5516 9751
mmt\$segment_access_rights	4559	4499
mmt\$segment_access_state	4565	5060
mmt\$segment_attribute_descriptor	3802	4495 9300
mmt\$segment_descriptor	4439	4490
mmt\$segment_descriptor_extended	4488	7663
		4449 4453 5915 7105 7105 7325 7327 7347
		7448 7458 7461 7464 7586 7675 7677 7684
		7967 8000 8002 8048 8081 8127 8141 8325
		8392 8433 8539 8575 8582 8612 8660 8737
		8862 8871 8988 9264 9270 9307 9308 9312
		9390 9433 9608 9686 9724 9752 9793 9994
		10073
		4517 4521 5927 5930 7109 7109 7276/P 7449
		7459 7462 7465 7505 7541 7587 7596 7676
		7878 7885 7968 8001 8003 8082 8128 8142

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

Table with columns for IDENTIFIER, DEFINED (ON LINE), and REFERENCES. Contains entries for various system parameters like osc\$asid_ei, osc\$base_exception, etc.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

Table with columns for IDENTIFIER, DEFINED (ON LINE), and REFERENCES. Contains entries for various system parameters like osc\$task_time_slice_maximum, osc\$svl_cache_bypass, etc.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table with columns for identifier names, defined line numbers, and reference line numbers. Includes entries like ost\$family_name, ost\$flags, ost\$frame_descriptor, etc.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table with columns for identifier names, defined line numbers, and reference line numbers. Includes entries like ost\$signature_lock, ost\$stack_frame_save_area, ost\$status, etc.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

IDENTIFIER	DEFINED ON LINE	REFERENCES
p	5765	5772
p	5789	5797
p	7447	7514
p	7447	7516 7516
p	7447	7516
p	7572	7612
p	7572	7645 7645
p	7572	7645
p	7662	7712 7717
p	7662	7914 7914 7955 7955
p	7662	7914 7955
p	7892	8022 8022
p	7892	8022
p	8074	8094 8094
p	8074	8094
p	8121	8243
p	8121	8245 8245
p	8121	8245
p	8390	8417
p	8424	8520 8520
p	8424	8520
p	8526	8552
p	8526	8555 8555
p	8526	8555
p	8573	8599 8599
p	8573	8599
p	8649	8711 8711
p	8649	8711
p	8849	8896 8896 8907 8907 8929 8929 8952 8952
p	8849	8896 8907 8929 8952
p	8849	8926
p	9295	9338
p	9295	9340 9340
p	9295	9340
p	9507	9535
p	9507	9538 9538
p	9507	9538
p	9781	9817
p	9781	9819 9819
p	9781	9819
p	9885	10009 10009 10019 10019 10028 10028
p	9885	10009 10019 10028
p1	5693	5577/M 5623/M 5710/M 5746/M 5772/M 5797/M 7514/M 7516/M 7612/M 7645/M 7712/M 7717/M 7914/M 7955/M 8022/M 8094/M 8243/M 8245/M 8417/M 8520/M 8552/M 8555/M 8599/M 8711/M 8896/M 8907/M 8926/M 8929/M 8952/M 9338/M 9340/M 9535/M 9538/M 9817/M 9819/M 10009/M 10019/M 10028/M
p2	5694	5577/M 5624/M 5710/M 5746/M 5773/M 5797/M 7514/M 7516/M 7612/M 7645/M 7712/M 7914/M 7955/M 8022/M 8094/M 8243/M 8245/M 8417/M 8520/M 8552/M 8555/M 8711/M 8896/M 8907/M 8926/M 8929/M 8952/M 9338/M 9340/M 9535/M
p_register	2068	9538/M 9817/M 9819/M 10009/M 10019/M 10028/M 5577/M 5615/M 5797/M 7514/M 7612/M 7712/M 7717/M 8243/M

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

IDENTIFIER	DEFINED ON LINE	REFERENCES
p_register_2	2069	8417/M 8552/M 8926/M 9338/M 9535/M 9817/M 5577/M 5608/M 5797/M 7514/M 7612/M 7712/M 7717/M 8243/M 8417/M 8552/M 8926/M 9338/M 9535/M 9817/M
page_count	4622	9130 9141/P
page_size	5944	5948/M 5954
page_size	8308	8338/M 8338
page_streaming_transfer_size	7674	7873/M 7874 7875/M 7877/P
page_streaming_ts_shift	7585	7626/P 7627
page_streaming_ts_shift	7673	7877/P 7878 7929/P 7931/P 7933
page_streaming_ts_shift	9306	9352/P 9353
page_streaming_ts_shift	9432	9460/P 9462 9463
pages_to_touch	5939	5949
pages_to_touch	8308	8338
parent_sdt_p	8136	8159/P 8160 8160 8160 8162 8202 8203 8208
parent_sdtX_p	8137	8159/P 8160 8160 8160 8162 8202 8203 8208
parent_xcb_p	8138	8237 8254 8257
passive_for_shadow_by_segnum	4588	8156/P 8158 8159/P
pfcaappend	1024	7871/M 8212/M 8936 8944/M 10039/M
pfcaexecute	1025	9672
pfcamodify	1024	1027 1030 9664
pfcaread	1024	9673
pfcashorten	1024	1027 1030 9668
pft\$share_options	1030	9672
pft\$usage_options	1027	1031
pft\$usage_selections	1028	1028
pmc\$internal_base_exception	6075	612 9659 9661 9664 9668 9672 9672 9673
pmc\$kill_task_flag	5227	9788
pmc\$max_signal_contents	5210	6079 6086 6095
pmc\$max_task_id	4965	5243
pmc\$min_ecc	6067	5204
pmc\$pc_base_exception	6063	4962
pmp\$delay	6002	6063
pmp\$find_executing_task_xcb	6010	6075 6095 9173 9413 9095 6025 7472 7594 7888 8036 8052 8156 8293 8401 8749 8912 8937 8996 9044 9083 9126 9276 9316 9527 9613 10081 7538 7699 8150 8876 8915 9935
pmp\$find_task_xcb	6028	6051 9099 9180
pmp\$get_executing_task_gtid	6033	9100 9181
pmp\$set_system_flag	6054	7247 7254 8182 8189
pmp\$zero_out_table	6101	6189
pmt\$binary_mainframe_id	6953	6189
pmt\$condition_identifier	5039	5033
pmt\$cpu_model_number	1489	1478 1485
pmt\$cpu_serial_number	1492	1479 1484
pmt\$initialization_value	2184	2013 3851 7055
pmt\$program_name	919	620 624 640 689 907
pmt\$sense_switches	6962	6200
pmt\$signal	5166	5160
pmt\$signal_contents	5204	5168
pmt\$signal_id	5171	5167
pmt\$task_id	4962	4082 4957 6028 7451 7664 8122 8852 9921
pmt\$task_private_descriptor	6980	6977

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES
pmt\$task_template	6975	6971
pointer_kind	3805	7944
power	5868	5877/M 5879/M 5879 5882 5885
power	7572	7626/M 7626/M 7626 7626 7626
power	7662	7877/M 7877/M 7877 7877 7877 7929/M 7929/M 7929
power	9295	7929 7929 7931/M 7931/M 7931
power	9420	9352/M 9352/M 9352 9352 9352
power	9420	9460/M 9460/M 9460 9460 9460
preset_and_save_fb_and_ts	9423	9449
preset_streaming	4510	9466/M 9476/M
preset_value	2013	7803/M 8501
preset_value	3851	7803 8501/M
process_virtual_address	9379	8393
ps_transfer_size	3872	7873
ps_transfer_size	5863	5876
ps_transfer_size	7572	7626
ps_transfer_size	7662	7877 7929 7931
ps_transfer_size	9295	9352
ps_transfer_size	9420	9460
ps_transfer_size_power	5864	5882 5883/M 5883 5885/M
ps_transfer_size_power	7572	7626/M 7626 7626/M
ps_transfer_size_power	7662	7877 7877/M 7877 7877/M 7929 7929/M 7929 7929/M
ps_transfer_size_power	9295	7931 7931/M 7931 7931/M
ps_transfer_size_power	9420	9352 9352/M 9352 9352/M
ps_transfer_size_power	9420	9460 9460/M 9460 9460/M
psa	9155	9161/M 9164
pva	4807	7525/M 8997
pva	5415	5421/M 5422/M 5423
pva	5435	5422/M 8553/M
pva	8047	8067/M 8068
pva	8080	8090/M 8091
pva	8139	8239/M 8240
pva	8526	8553/M 8553/M 8553
pva	8574	8586
pva	8860	8890/M 8905/P 8962
pva	9792	9899/M 9900
pva	9992	9998/M 10017/P 10041
pva_p	7460	7524/M 7525
queue_id	6798	7363 7366 7366
queue_status	2012	7364 7365 7368 7369 7384 7485 7491 7713/M
r1	3838	7733 7738 8441/M 9814
r1	4471	7522 7525/S 7526 7692/M 7738/M 7946/P 7949/P 7952
r1	9297	8230 8441 8574 9336/M 9762 9814/M
r2	3839	9336
r2	4472	7733 7734 7739 8442/M 9815
r2	9298	7693/M 7739/M 7975 8442 8546 8622 9337/M 9444
rb	7168	9444 9698 9815/M
rb	7346	9337
rb	8782	7174/M 7175/M 7176/M 7177/P 7177/P
rb	8782	7385/M 7386/M 7387/M 7388/M 7389/M 7390/P 7390/P
rb	8782	8791/M 8793/P 8793/P 8794 8795 8796 8799 8816/M

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES
rb	8986	9013/M 9014/M 9015/M 9016/P 9016/P
rb	9515	9525/M 9526/M 9544/M 9545/P 9550/P 9550/P 9557/P
rb_ring1_server_seg_request	9245	9249/M 9250/M 9251/M 9252/M 9253/M 9255/P 9255/P 9257
read_privilege	3925	9258/P
read_write_access_selection	9300	7819 7824 8513/M 8886 8891
referenced_byte	5945	9344
referenced_byte	8308	5953/M 5957/M
reqcode	3589	8338/M 8338/M
reqcode	3716	8355/M
reqcode	3767	7174/M 7385/M 9013/M 9525/M
reqcode	5691	9249/M
request	3721	5577/M 5619/M 5710/M 5746/M 5769/M 5797/M 7514/M 7516/M
request	3769	7612/M 7645/M 7712/M 7717/M 7914/M 7955/M 8022/M 8094/M
request_block	5516	8243/M 8245/M 8417/M 8520/M 8552/M 8555/M 8599/M 8711/M
request_block	7210	8896/M 8907/M 8926/M 8929/M 8952/M 9338/M 9340/M 9535/M
request_block	8324	9538/M 9817/M 9819/M 10009/M 10019/M 10028/M
request_block	8424	7175/M 7386/M 8796 9014/M 9526/M
request_block	8604	9251/M
request_block	9751	5519/M 5520/M 5521/M 5522/P 5522/P
request_code	3558	7259/M 7260/M 7261/M 7262/M 7263/P 7264/P
request_code	3791	8355/M 8356/M 8357/M 8358/M 8359/M 8361/P 8361/P 8362/P
request_code	3791	8370 8371 8372 8382
request_code	3791	8446/M 8446/M 8446/M 8446/P 8446/P
request_code	3791	8628/M 8628/M 8628/M 8628/P 8628/P
request_code	3791	9770/P 9771/M 9772/M 9773/M 9775/P 9775/P
request_code	3791	7259/M
request_code	3791	5519/M 8446/M 8628/M 9771/M
residence	1948	5542 5543 5795 5795 7361 7362 7514 7514
residence	1948	7612 7612 7712 7712 7717 7717 8243 8243
residence	1948	8417 8417 8552 8552 8627 8627 8926 8926
residence	1948	9006 9006 9338 9338 9535 9535 9545 9545
residence	1948	9557 9557 9770 9770 8817 8949 9949
return_unallocated_offsets	3594	8359/M
return_unallocated_offsets	8311	8359
ring	4043	8890
ring_1_stack_segnum	8887	8997/M 9005
rma	8140	8190/P 8191 8192
rma	9122	9132/P 9133
rmc\$external_vsn_size	1592	1598
rmc\$msc_system_critical_files	5345	7619
rmc\$msc_user_temporary_files	5344	7621
rmc\$recorded_vsn_size	1595	1605
rmc\$unspecified_file_class	1380	1373
rmt\$external_vsn	1805	1612 1598
rmt\$recorded_vsn	1810	495 1546 1558 1611 1846
rmt\$volume_descriptor	4469	1810 1817
rmt\$volume_descriptor	4469	7746/M 7746/M 7750/M 7824/M 8457 8513 8546 9667
rmt\$volume_descriptor	4469	9830/M 9832/M 9836/M 9891/M
saved_free_behind	9426	9454/M 9455 9477
saved_transfer_size	9425	9459/M 9481 9482
scan_sdt_for_inherited_segs	8989	8989 9026
scan_sdt_for_inherited_segs	8943	8943 9957

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES
sd	7447	7507 7543
sd	9295	9354
sd	9724	9727
sd_p	9390	9394/P
sd_p	9886	9891/P 9688
sdt_entry	7347	7349/M 7351 7352 7354/M 7356/M 7357 7358/S
sdt_entry	7448	7469
sdt_entry	7675	7690/M 7692/M 7693/M 7738/M 7739/M 7744/M 7746/M 7750/M
		7757/M 7759/M 7762/M 7764/M 7767/M 7770/M 7771/M 7800/M
		7806/M 7808/M 7816/M 7824/M 7825/M 7835/M 7836/P 7946/P
		7949/P 7952
sdt_entry	8141	8205/M 8206/M 8207/M 8230 8251
sdt_entry	8575	8597/M 8598/M
sdt_entry	9264	9285
sdt_entry	9307	9335/M 9336/M 9337/M 9342/M 9345/M 9354/P 9356/P
sdt_entry_p	7461	7504/M 7510/P
sdt_entry_p	7677	7898/P
sdt_entry_p	7967	7970/P 7975
sdt_entry_p	8048	8059/M 8060 8069/M
sdt_entry_p	8081	8086/P 8092/M
sdt_entry_p	8325	8346/P
sdt_entry_p	8392	8412/M 8414
sdt_entry_p	8433	8437/P 8441 8442 8454 8457 8469 8478 8490
		8507 8512 8513 8514
sdt_entry_p	8539	8542/P 8546 8546
sdt_entry_p	8582	8587/P 8597
sdt_entry_p	8612	8615/P 8622
sdt_entry_p	8660	8664/P 8674 8690/M 8706/M
sdt_entry_p	8737	8765/M 8766
sdt_entry_p	8862	8885/P 8953/M
sdt_entry_p	8988	9001/M 9002 9021/M
sdt_entry_p	9270	9284/M 9285/M
sdt_entry_p	9308	9329/M 9356/P
sdt_entry_p	9433	9437/P 9444 9444
sdt_entry_p	9608	9629/M 9631
sdt_entry_p	9752	9755/P 9762 9765
sdt_entry_p	9793	9798/P 9814/M 9815/M 9830/M 9832/M 9836/M 9843/M 9845/M
		9848/M 9850/M 9853/M 9856/M 9863/M 9873/M 9875/M 9883/M
		9891/M 9892/M
sdt_entry_p	9994	9999/P 10036/M
sdt_entry_p	10073	10092/M 10094
sdt_offset	4084	5906 5918 7217 7306 7504 7540 8059 8159
		8194/M 8249 8297 8412 8765 8917 8938 9001
		9046 9284 9329 9528 9629 9941 9961 10092
sdt_p	5901	5906/M
sdt_p	7289	7306/M
sdt_p	7296	7306/P 7317
sdt_p	8121	8159/M
sdt_p	8282	8297/M
sdt_p	8287	8297/P 8299
sdt_p	8849	8938/M
sdt_p	8864	8938/P 8940
sdt_p	9033	9046/M

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES
sdt_p	9038	9046/P 9048 9052/M 9054/M
sdt_p	9507	9528/M
sdt_p	9517	9528/P 9542
sdt_p	9920	9941/M
sdt_p	9929	9941/P 9945
sdt_x	7447	7507/M 7507 7507/M 7507/M 7543/M 7543 7543/M 7543/M
sdt_x	9295	9354/M 9354 9354/M 9354/M
sdt_x	9725	9728/M 9729 9730/M 9732/M
sdt_x_entry	7449	7470
sdt_x_entry	7676	7691/M 7694/M 7695/M 7724/M 7783/M 7792/M 7841/M 7841
		7845/M 7855/M 7857/M 7868/M 7869/M 7870/M 7878/M 7879/M
		7924 7925/M 7928 7933/M 7936/P
sdt_x_entry	8142	8208/M 8209 8210 8211/M 8212/M 8213/M 8213 8216/M
		8220 8229 8232/M 8242 8243/P 8246/M 8246
sdt_x_entry	8576	8596/M
sdt_x_entry	9285	9287
sdt_x_entry_p	7462	7505/M 7506 7509/M
sdt_x_entry_p	7587	7596/M 7597 7598 7599 7600 7601 7625 7627/M
		7652/M
sdt_x_entry_p	7678	7898/P
sdt_x_entry_p	7968	7970/P 7980 7981/M 7982 7983/M
sdt_x_entry_p	8082	8086/P
sdt_x_entry_p	8325	8346/P 8350
sdt_x_entry_p	8393	8413/M 8417/P
sdt_x_entry_p	8434	8437/P 8497 8503
sdt_x_entry_p	8540	8542/P 8552/P 8566/P
sdt_x_entry_p	8583	8587/P 8593/M 8594/M 8595/M 8596
sdt_x_entry_p	8613	8615/P 8627/P
sdt_x_entry_p	8661	8664/P 8674 8675 8691/M 8696/M 8697/M 8697 8698/M
sdt_x_entry_p	8863	8699/M 8702/M 8885/P 8895 8904 8925 8926/P 8933/P 8936 8959/P
		8959/P
sdt_x_entry_p	8989	9003/M 9004 9006/P 9020/P 9020/P
sdt_x_entry_p	9271	9286/M 9287/M
sdt_x_entry_p	9309	9330/M 9333/M 9334/M 9348/M 9350/M 9353/M 9354/P
sdt_x_entry_p	9434	9437/P 9454 9456/M 9456 9462 9463/M 9465
		9466/M 9467 9476/M 9478/M 9478 9481 9482/M
sdt_x_entry_p	9609	9630/M 9632 9634/M 9636/M
sdt_x_entry_p	9753	9755/P 9770/P
sdt_x_entry_p	9794	9798/P 9803 9817/P
sdt_x_entry_p	9995	9999/P 10007 10016 10031/P 10031/P 10037/M 10037 10038/M
		10039/M
sdt_x_entry_p	10074	10083/M
sdt_x_offset	4085	5907 5930 7218 7306 7541 7596 8159
		8195/M 8250 8259 8413 8938 9003 9046
		9286 9330 9528 9630 9941 9962 10093
sdt_x_p	5902	5907/M
sdt_x_p	7289	7306/M
sdt_x_p	7297	7306/P 7318
sdt_x_p	8121	8159/M
sdt_x_p	8282	8297/M
sdt_x_p	8288	8297/P 8300

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

Table with columns: IDENTIFIER, DEFINED ON LINE, REFERENCES. Rows include identifiers like sdtx_p, seg, segment_attributes, and various reference codes.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

Table with columns: IDENTIFIER, DEFINED ON LINE, REFERENCES. Rows include identifiers like segment_number, segment_pointer, segment_res_state, and various reference codes.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

System Constant Management

```

3 MODULE sym$system_constant_manager;
4
5 { PURPOSE:
6 {   This module contains procedures that allow the operator to fetch and
7 {   modify the values of system constants during deadstart or during system
8 {   operation. The privilege of modifying the constants is restricted to the
9 {   system job.
10

```

System Constant Management

Global Declarations Referenced by this module

```

O 1618
O 1619     PROCEDURE [XREF] dmp$fetch_debug_option_value (name: string(*);
O 1620     VAR value: integer;
O 1621     VAR status: ost$status);
O 1622
O 1673     PROCEDURE [XREF] dmp$store_debug_option_value (name: string (*);
O 1674     value: integer;
O 1675     VAR status: ost$status);
O 1676
O 1680
O 1681     PROCEDURE [XREF] iop$fetch_debug_option_value (name: string ( * );
O 1682     VAR value: integer;
O 1683     VAR status: ost$status);
O 1684
O 1687     PROCEDURE [XREF] iop$store_debug_option_value (name: string ( * );
O 1688     value: integer;
O 1689     VAR status: ost$status);
O 1690
O 1694
O 1695     PROCEDURE [XREF] osp$append_status_parameter ALIAS 'ospasp'
O 1696     (
O 1697     delimiter: char;
O 1698     text: string ( * (<= osc$max_string_size);
O 1699     VAR status (input, output) : ost$status);
O 1702
O 1703     PROCEDURE [XREF] osp$set_status_abnormal ALIAS 'ospssa'
O 1704     (
O 1705     identifier: ost$status_identifier;
O 1706     condition: ost$status_condition_code;
O 1707     text: string ( * (<= osc$max_string_size);
O 1708     VAR status: ost$status);
O 1715
O 1716     PROCEDURE [XREF] osp$system_error (error_message: string ( * );
O 1717     status: ^ost$status);
O 1718
O 1721     VAR
O 1722     bav$force_direct_tape_io: [XREF] boolean;
O 1723     VAR
O 1724     bav$max_allowed_tape_block_size: [XREF] integer;
O 1725     VAR
O 1726     bav$max_bytes_per_tape_io: [XREF] integer;
O 1727     VAR
O 1728     bav$max_indirect_tape_block: [XREF] integer;
O 1729     VAR
O 1730     bav$use_assign_pages_for_tape: [XREF] boolean;
O 1731
O 1732     VAR
O 1733     dfv$file_server_debug_enabled: [XREF] boolean;
O 1734
O 1735     VAR
O 1736     dfv$file_server_info_enabled: [XREF] boolean;
O 1737
O 1738     VAR

```

System Constant Management

Global Declarations Referenced by this module

```

1739     dfv$job_recovery_enabled: [XREF] boolean;
1740
1741     VAR
1742     { This is an integer to allow use with sym$system_constant_manager.
1743     { The true value range is 1 .. dfc$max_job_list_p_array_size
1744     dfv$maximum_client_job_lists: [XREF] integer;
o
o 3204
o 3205     VAR
o 3206     { This is an integer to allow use with sym$system_constant_manager
o 3207     { The true value range is dft$family_pointer_index
o 3208     dfv$number_served_family_lists: [XREF] integer;
o 3211
o 3212     VAR
o 3213     dmv$permanent_file_overflow: [XREF] boolean;
o 3214
o 3215     VAR
o 3216     dmv$recycle_device_log: [XREF] boolean;
o 3217
o 3218     VAR
o 3219     dmv$temporary_file_overflow: [XREF] boolean;
o 3220
o 3221     VAR
o 3222     dmv$test_recovery: [XREF] boolean;
o 3223
o 3224     VAR
o 3225     dpv$enable_console_bell: [XREF] boolean;
o 3226
o 3227     VAR
o 3228     dsv$unload_deadstart_tape: [XREF] boolean;
o 3229
o 3230
o 3231     {Job Control Block (JCB).}
o 3232
o 3233     VAR
o 3234     jmv$job: [XREF] jmt$job_control_block;
o 4138
o 4139     VAR
o 4140     jmv$maximum_job_classes: [XREF] 0..0ffffff(16);
o 4141
o 4142
o 4143     VAR
o 4144     jmv$maximum_known_jobs: [XREF, oss$mainframe_pageable] ost$halfword;
o 4145
o 4151     VAR
o 4152     jmv$maximum_known_outputs: [XREF, oss$mainframe_pageable] ost$halfword;
o 4154
o 4157
o 4158     VAR
o 4159     jmv$maximum_service_classes: [XREF] 0..0ffffff(16);
o 4160
o 4161
o 4162     VAR
o 4163     jmv$scan_idle_dispatch_interval: [XREF] integer;
o 4164

```

System Constant Management

Global Declarations Referenced by this module

```

o 4165     VAR
o 4166     jmv$sched_memory_wait_factor: [XREF] integer;
o 4167
o 4168     VAR
o 4169     jmv$scheduler_wait_time: [XREF] integer;
o 4170
o 4171     {Define boolean that specifies whether jobs that go into long wait should be
o 4172     {swapped immediately.
o 4173
o 4174     VAR
o 4175     jmv$swap_jobs_in_long_wait: [XREF] boolean;
o 4176
o 4177     {Define value of AJL ORDINAL used by the system job
o 4178
o 4179     VAR
o 4180     jmv$system_ajl_ordinal: [XREF] jmt$ajl_ordinal;
o 4181
o 4184     VAR
o 4185     jmv$system_ijl_ordinal: [XREF] jmt$ijl_ordinal;
o 4186
o 4189
o 4190     VAR
o 4191     jsv$enable_debug_code: [XREF] boolean;
o 4192
o 4193     VAR
o 4194     jsv$enable_swap_file_statistics: [XREF] boolean;
o 4195
o 4196
o 4197
o 4198     VAR
o 4199     jsv$halt_on_swapin_failure: [XREF] boolean;
o 4200
o 4201     VAR
o 4202     jsv$free_working_set_on_swapout: [XREF] boolean;
o 4203
o 4204
o 4205     VAR
o 4206     jsv$maximum_pages_to_swap: [XREF] integer;
o 4207
o 4208     VAR
o 4209     jsv$max_pages_first_swap_task: [XREF] integer;
o 4210
o 4211     VAR
o 4212     jsv$max_time_swap_io_complete: [XREF] integer;
o 4213
o 4214     VAR
o 4215     jsv$max_time_swap_io_not_init: [XREF] integer;
o 4216
o 4217
o 4218     VAR
o 4219     jsv$think_expiration_time: [XREF] integer;
o 4220
o 4221     {Define minimum number of pages that must be kept in the free + available page
o 4222     {queues. If the actual number drops below this value, memory manager begins
o 4223     {an aggressive aging policy. If the number of page frames drops below mmv$aggressive_aging_level_2

```


System Constant Management
Global Declarations Referenced by this module

```

o 4224 {then only critical system tasks are assigned memory. User tasks are put into a memory wait queue.
o 4225
o 4226 VAR
o 4227 mmv$aggressive_aging_level: [XREF] integer;
o 4228 mmv$aggressive_aging_level_2: [XREF] integer;
o 4229
o 4230 {The following variable defines the aging algorithm that is used by memory manager.
o 4231 { 0 - no swapping active
o 4232 { 1 - swapping active
o 4233 { > 1 - to be defined
o 4234 VAR
o 4235 mmv$aging_algorithm: [XREF] integer;
o 4236
o 4237 VAR
o 4238 mmv$ai0_limit: [XREF] integer;
o 4239
o 4240 { Define variable used to activate/deactivate assign_multiple_pages. When activated
o 4241 { mmp$page_pull will assign more than one page to a task that has faulted for a new page
o 4242 { and it is determined that extra pages will highly likely be used. Multiple pages will
o 4243 { be assigned if mmv$reassignable_page_frames.now is greater or equal to this value.
o 4244
o 4245 VAR
o 4246 mmv$assign_multiple_pages: [XREF] integer;
o 4247 { ----- Declarations for forcing the use of cache and maps omitted at compile time -----
o 4248
o 4249
o 4250 VAR
o 4251 mmv$disable_write_for_perf_meas: [XREF] boolean;
o 4252
o 4253 VAR
o 4254 mmv$file_allocation_interval: [XREF] integer;
o 4255 { ----- Declarations for forcing the use of cache and maps omitted at compile time -----
o 4256 {Define number of page frames that memory manager should attempt to
o 4257 {keep in the available + free queues.
o 4258
o 4259 VAR
o 4260 mmv$free_queue_threshold: [XREF] integer;
o 4261
o 4262 { Global Page Queue List array.
o 4263
o 4264 VAR
o 4265 mmv$gpql: [XREF] mmt$global_page_queue_list;
o 4266
o 4269 {This variable defines the rate at which memory manager scans all jobs and
o 4270 {ages the working sets of any CP bound job that is found.
o 4271
o 4272 VAR
o 4273 mmv$jws_queue_age_interval: [XREF] integer;
o 4274
o 4275
o 4276 { The data consists of pointers to the various mmv$ variables managed by MMU and also the default value
o 4277 { for each of the variables and the default value of the Global Page Queue List. The default values
o 4278 { are saved by mmm$deadstart_initialization.
o 4279
o 4280 VAR

```

System Constant Management
Global Declarations Referenced by this module

```

o 4281 mmv$manage_memory_utility: [XREF] mmt$manage_memory_utility;
o 4316
o 4317 {Maximum number of pages that will be assigned to a segment that does not
o 4318 {have a backing file. A signal is sent to the task to assign a file when
o 4319 {the number of assigned pages exceeds this value.
o 4320
o 4321 VAR
o 4322 mmv$max_pages_no_file: [XREF] integer;
o 4323
o 4324 VAR
o 4325 mmv$min_avail_pages: [XREF] integer;
o 4326
o 4327 {Define variable used to activate/deactivate multi-page write. Multi-page write activated
o 4328 {means that memory manager attempts to write all modified pages in a transfer unit whenever
o 4329 {any page in the transfer unit is written.
o 4330
o 4331 VAR
o 4332 mmv$multi_page_write: [XREF] boolean;
o 4333
o 4334 {Define option used to disable buffering of aged out pages in memory, i.e. when a page
o 4335 {is aged out, it is written to disk (if necessary) and placed in the free queue.
o 4336
o 4337 VAR
o 4338 mmv$no_memory_buffering: [XREF] boolean;
o 4339
o 4340 {This variable the time interval between calls from CP Monitor to
o 4341 {memory manager MMP$PERIODIC_CALL procedure.
o 4342
o 4343 VAR
o 4344 mmv$periodic_call_interval: [XREF] integer;
o 4345 VAR
o 4346 mmv$read_to_read_write: [XREF] 0 .. 0ffffff(16);
o 4347 mmv$read_to_execute: [XREF] 0 .. 0ffffff(16);
o 4348
o 4349
o 4350 VAR
o 4351 mmv$shared_pages_in_jws: [XREF] boolean;
o 4352
o 4353 {This variable the rate at which Memory Manager ages out all pages of all
o 4354 {working sets that have not been referenced since the last time the
o 4355 {aging was done.
o 4356
o 4357 VAR
o 4358 mmv$shared_queue_age_interval: [XREF] integer;
o 4359 {Define the fundamental page aging constant.
o 4360
o 4361 VAR
o 4362 mmv$tick_time: [XREF] integer;
o 4363
o 4364
o 4365 {Define option used to force aged out pages to be written to disk
o 4366 {immediately when it is aged out of a job working set.
o 4367 {Page are written if S00N + NOW < mmv$write_aged_out_pages.
o 4368
o 4369 VAR

```

System Constant Management

Global Declarations Referenced by this module

```

0 4370     mmv$write_aged_out_pages: [XREF] integer;
0 4371
0 4372
0 4373     VAR
0 4374     mtv$halt_on_proc_malf: [XREF] boolean;
0 4375
0 4376     VAR
0 4377     mtv$aborted_task_threshold: [XREF] integer;
0 4378
0 4379
0 4380     VAR
0 4381     mtv$automatic_unstep_resume: [XREF] boolean;
0 4382
0 4383     [If a MCR fault occurs in a task executing in a ring <= the value
0 4384     [of this variable, Monitor will halt the CPU. The variable is located
0 4385     [in Monitor and is intended to be used for debug only.
0 4386
0 4387     VAR
0 4388     mtv$system_haltring: [XREF] 0 .. 255,
0 4389     mtv$halt_cpu_ring_number: [XREF] 0 .. 255;
0 4390
0 4391     VAR
0 4392     mtv$processor_due_threshold: [XREF] integer;
0 4393
0 4394     VAR
0 4395     mtv$sys_core_init_complete: [XREF] boolean;
0 4396     [At deadstart time, monitor copies the job Exchange Package to this location
0 4397     [before the first exchange to job mode.
0 4398
0 4399     VAR
0 4400     mtv$xp_initial_value: [XREF] ost$exchange_package;
0 4401
4594     [System page size.]
4595
4596     VAR
4597     osv$page_size: [XREF] ost$page_size;
4598
0 4601
0 4602     VAR
0 4603     osv$global_processor_model_info: [XREF] ost$processor_model_definition;
0 4604
0 4693
4694     VAR
4695     osv$special_aam_trap: [XREF] boolean;
4696
4697     VAR
4698     pmv$quantum: [XREF] integer;
4699
4700     VAR
4701     syv$enable_heap_trace: [XREF] boolean;
4702
4703     [ Variable which determines number of lines displayed on the console during
4704     [ a SYSDEBUG session display command.
4705
4706     VAR

```

System Constant Management

Global Declarations Referenced by this module

```

4707     syv$debugger_page_wait_lines: [XREF] integer;
4708
4709
4710     VAR
4711     syv$dfmt_debug_output_disposal: [XREF] syt$dfmt_debug_output_disposal;
4712
0 4733
0 4734     [ The following boolean will be set TRUE by SETSA to enable the fault injection utility.
0 4735
0 4736     VAR
0 4737     syv$enable_fault_injection: [XREF] boolean;
0 4738     VAR
0 4739     syv$job_recovery_option: [XREF] integer;
0 4740     CONST
0 4741     syc$jre_enabled = 0,
0 4742
0 4743     syc$jre_command_disabled = 3,
0 4744
0 4745     syc$jre_prior_ds_disabled = 5,
0 4746     syc$jre_no_image = 6,
0 4747     syc$jre_different_system = 7,
0 4748     syc$jre_page_size_mismatch = 8,
0 4749
0 4750     syc$jre_system_disabled = 9;
0 4751
0 4752
0 4753     VAR
0 4754     syv$mandatory_dualstate: [XREF] boolean;
0 4755
0 4756
0 4757     VAR
0 4758     syv$max_debug_output_lines: [XREF] integer;
0 4759
0 4760
0 4761     VAR
0 4762     syv$verify_heap_linkage: [XREF] boolean;
0 4763     [Define length of timeout that occurs when a user issues a 'CYCLE' request.
0 4764
0 4765     VAR
0 4766     tmv$cycle_delay_time: [XREF] integer;
0 4767
0 4768
0 4769     VAR
0 4770     tmv$dedicate_a_cpu_to_nos: [XREF] boolean;
0 4771     [Define variables that control action taken by monitor when system errors occur.
0 4772
0 4773     VAR
0 4774     tmv$halt_on_hung_task: [XREF] boolean,
0 4775     tmv$system_debug_ring: [XREF] integer,
0 4776     tmv$system_debug_segment: [XREF] integer,
0 4777     tmv$system_error_hang_count: [XREF] 0 .. 0ffffff(16);
0 4778
0 4779
0 4780     VAR
0 4781     tmv$max_idle_sit_value: [XREF] integer;

```

System Constant Management

Global Declarations Referenced by this module

```

O 4782
O 4783 VAR
O 4784 ,tmv$timed_wait_not_queued: [XREF] integer;
O 4785
O 4786

```

System Constant Management

Global Declarations Declared by this module

```

O 4788
O 4789 TYPE
O 4790 value_table_type = record
O 4791 deadstart_only: boolean,
O 4792 name: string (max_name_length),
O 4793 min,
O 4794 max: integer,
O 4795 case value_type: (v_integer, v_halfword, v_byte, v_boolean) of
O 4796 = v_integer :
O 4797 integer_p: ^integer,
O 4798 = v_halfword :
O 4799 halfword_p: ^0 .. 0ffffff(16),
O 4800 = v_byte :
O 4801 byte_p: ^0 .. 255,
O 4802 = v_boolean :
O 4803 boolean_p: ^boolean,
O 4804 casend,
O 4805 recend;
O 4806
O 4807 VAR
O 4808 clv$command_statistics_enabled: [XDCL, #GATE] boolean := FALSE,
O 4809 dmvs$space_messages_to_console: [XDCL, #GATE] boolean := TRUE,
O 4810 dmvs$trim_files: [XDCL, #GATE] boolean := TRUE,
O 4811 dmvs$volume_class_kludge: [XDCL] boolean := FALSE,
O 4812 dsv$display_delay: [XDCL, #GATE] integer := 1000,
O 4813 dsv$display_deadstart_messages: [XDCL] boolean := TRUE,
O 4814 dsv$load_files: [XDCL, #GATE] boolean := FALSE,
O 4815 dsv$ignore_image: [XDCL] boolean := FALSE,
O 4816 iiv$condition_handler_trace: [XDCL, #GATE] boolean := false,
O 4817 iiv$interactive_wait_time: [XDCL, #GATE] integer := 60000000,
O 4818 iiv$output_option: [XDCL, #GATE] integer := 0,
O 4819 iiv$suspended_job_timeout: [XDCL, #GATE] integer := 180 * 60 * 1000,
O 4820 iov$unused_boolean: [XDCL, #GATE] boolean := TRUE,
O 4821 jmv$enable_queue_file_access: [XDCL, #GATE] boolean := FALSE,
O 4822 jmv$swap_file_allocation_size: [XDCL, #GATE] 0 .. 0ffffff(16) := 262144,
O 4823 lov$enable_source_type_checking: [XDCL, #GATE] boolean := FALSE,
O 4824 lov$ignore_param_verification: [XDCL, #GATE] integer := 0,
O 4825
O 4826 { Debug, define maximum memory that 180 will use. This is a way of limiting the amount of memory NOS/VE will
O 4827 [ use. Can limit the memory less than defined upper bound. Default to a value larger than existing memory
O 4828 [ sizes.
O 4829
O 4830 mmv$maximum_180_memory: [XDCL] integer := 100000000000(16),
O 4831 mmv$shadow_by_segnum: [XDCL, #GATE] boolean := TRUE,
O 4832
O 4833 { Maximum segment length is no longer used by the OS. It must remain as a SETSA until release 1.6.1
O 4834 { because IM/DM uses it. As soon as they react to the change-this variable will be deleted.
O 4835
O 4836 mmv$max_segment_length: [XDCL, #GATE] integer := 2147483647,
O 4837 mtv$mx_a1_entries: [XDCL, #GATE] 0 .. 0ffffff(16) := 100,
O 4838 mtv$mx_segments: [XDCL] 0 .. 0ffffff(16) := 0,
O 4839 mtv$mx_tasks: 0 .. 0ffffff(16) := 255, {this is a no-op and will be removed next release}
O 4840 mtv$scd_vector_sim_attribute: [XREF] 0 .. 255,
O 4841 osv$catalog_name_security: [XDCL, #GATE] boolean := FALSE,
O 4842 osv$default_pit: [XDCL] integer := 7ffffff(16), {default value for PIT}

```

System Constant Management

Global Declarations Declared by this module

```

0 4843 osv$delete_unreconciled_files: [XDCL, #GATE] boolean := FALSE,
0 4844 osv$dump_when_debug: [XDCL] boolean := FALSE,
0 4845 osv$emergency_intervention: [XDCL, #GATE] boolean := FALSE,
0 4846 osv$enable_hyperchannel: [XDCL, #GATE] boolean := FALSE,
0 4847 osv$reconcile_permanent_files: [XDCL, #GATE] boolean := FALSE,
0 4848 osv$recover_at_all_costs: [XDCL] boolean := FALSE,
0 4849 osv$reorganize_permanent_files: [XDCL, #GATE] boolean := FALSE,
0 4850 osv$validate_active_sets: [XDCL, #GATE] boolean := FALSE,
0 4851 osv$validate_permanent_files: [XDCL, #GATE] boolean := FALSE,
0 4852 pfv$restrict_catalog_flushing: [XDCL, #GATE] boolean := TRUE,
0 4853 osv$verify_missing_volumes: [XDCL, #GATE] boolean := TRUE,
0 4854 pfv$binary_catalog_search: [XDCL, #GATE] boolean := TRUE,
0 4855 pmv$debug_logging_enabled: [XDCL, #GATE] boolean := FALSE,
0 4856 pmv$constrain_meape_segments: [XDCL, #GATE] boolean := FALSE,
0 4857 rav$deadstart_intervention: [XDCL, #GATE] boolean := FALSE,
0 4858 rav$development_deadstart: [XDCL, #GATE] boolean := FALSE,
0 4859 rav$network_activation: [XDCL, #GATE] boolean := TRUE,
0 4860 rav$system_activation: [XDCL, #GATE] boolean := TRUE,
0 4861 syv$halt_on_exit_with_io: [XDCL, #GATE] boolean := FALSE,
0 4862 syv$nosve_internal_operations: [XDCL, #GATE] boolean := FALSE,
0 4863 nav$disable_network_relays: [XDCL, #GATE] boolean := FALSE,
0 4864 syv$user_templates: [XREF] boolean,
0 4865 tmv$display_actual_priority: [XDCL, #GATE] boolean := FALSE,
0 4866 dmvpf_sparse: [XREF] boolean,
0 4867 syv$clone_enabled: [XREF] boolean,
0 4868 endvar: char;
0 4869
0 4870
0 4871 { NOTE:
0 4872 { The following variable cannot be set via the set_system_attribute command.
0 4873 { It is set via the change_secure_logging command during System operation.
0 4874
0 4875
0 4876 CONST
0 4877   clc$change_secure_logging_name = '* CHANGE_SECURE_LOGGING *';
0 4878
0 4879
0 4880 VAR
0 4881   clv$secure_logging_activated: [XDCL, #GATE] boolean := FALSE;
0 4882
0 4883
0 4884 { NOTE:
0 4885 { The following variable cannot be set via the set_system_attribute command.
0 4886 { It is set via the set_validation_level command during system operation.
0 4887
0 4888
0 4889 CONST
0 4890   avc$validation_level_const_name = '* VALIDATION_LEVEL *';
0 4891
0 4892
0 4893
0 4894
0 4895
0 4896
0 4897

```

System Constant Management

Global Declarations Declared by this module

```

0 4898 VAR
0 4899   avv$validation_level: [XDCL, #GATE] 0 .. 255 := 0;
0 4900
0 4901
0 4902
0 4903 { NOTE:
0 4904 { The following variable cannot be set via the set_system_attribute command.
0 4905 { It is set via the activate_system_logging and deactivate_system_logging
0 4906 { commands during system operation.
0 4907
0 4908
0 4909 CONST
0 4910   clc$system_logging_active_name = '* SYSTEM_LOGGING_ACTIVE *';
0 4911
0 4912
0 4913 VAR
0 4914   clv$system_logging_activated: [XDCL, #GATE] boolean := FALSE;
0 4915
0 4916
0 4917 { Define values for the segment offset array. This array defines page offsets
0 4918 { for the beginning of some special segments. These segments do NOT start at
0 4919 { offset zero. This is an attempt to reduce thrashing in the page map. This
0 4920 { array is defined here because it may need to be model dependent.
0 4921
0 4922 VAR
0 4923   mmv$page_map_offsets: [XDCL, #GATE] mmt$page_map_offsets := [0, 0, 0, 0, 0];
0 4924
0 4925
0 4926 VAR
0 4927   dmvs$maximum_allocation_size: [XREF] integer,
0 4928   dmvs$quick_deadstart: [XREF] boolean,
0 4929   jmv$max_think_time: [XREF] integer,
0 4930   jmv$min_think_time: [XREF] integer,
0 4931   jsv$enable_swap_resident: [XREF] boolean,
0 4932   jsv$enable_swap_resident_no_io: [XREF] boolean,
0 4933   jsv$write_stale_pages: [XREF] boolean,
0 4934   mlv$wire_mli_tables: [XREF] boolean,
0 4935   mmv$advise_in_aio_limit: [XREF] integer,
0 4936   mmv$sage_interval_ceiling: [XREF] 0 .. 255,
0 4937   mmv$sage_interval_floor: [XREF] 0 .. 255,
0 4938   mmv$check_queues: [XREF] integer,
0 4939   mmv$maxws_aio_threshold: [XREF] integer,
0 4940   mmv$swapping_aic: [XREF] integer,
0 4941   osv$debug: [XREF] array [0..15] of integer,
0 4942   osv$default_sit_value: [XREF] integer,
0 4943   osv$disk_fault_simulation: [XREF] boolean,
0 4944   osv$error_idle_halt: [XREF] boolean,
0 4945   osv$keypoint_enable: [XREF] integer,
0 4946   osv$trap_task_errors: [XREF] boolean,
0 4947   sfv$dynamic_file_space_limits: [XREF] boolean,
0 4948   syv$allow_jf_test: [XREF] boolean,
0 4949   syv$debug_job_recovery: [XREF] boolean,
0 4950   svv$system_job_multiprocessing: [XREF] boolean,
0 4951   tmv$long_wait_force_swap_time: [XREF] integer,
0 4952   tmv$long_wait_swap_time: [XREF] integer;

```

System Constant Management
Global Declarations Declared by this module

O 4953

System Constant Management
Global Declarations Declared by this module
System Constant Definition Table

```

O 4955
O 4956 CONST
O 4957   max_name_length = 31,
O 4958   value_table_length = 179;
O 4959
O 4960 { Note: The multiple shared queues feature introduces multiple shared queues and it also introduces the
O 4961 {   Manage Memory Utility. The memory variables that it manages are also controlled by the
O 4962 {   SET_SYSTEM_ATTRIBUTE command. So that users do not have to immediately change to use of the
O 4963 {   MMU, the old SETSA commands are being left in place. For the attribute MINIMUM_SHARED_WORKING_SET
O 4964 {   the field controlled is to be mmv$gpq||mmc$pq_shared_task_service.minimum. To make this
O 4965 {   change we need to change the appropriate entry in the value_table. However, this can not be done
O 4966 {   because the object library generator will generate incorrect code. A work-around has been coded.
O 4967 {   To remove the work-around, delete these comments, all lines referencing the
O 4968 {   VAR temp_workaround_min_sws and change the appropriate value_table entry.
O 4969 {
O 4970 VAR temp_workaround_min_sws : [STATIC] 0..0ffffff(16) := 0;
O 4971
O 4972
O 4973 VAR
O 4974   value_table: [READ, oss$mainframe_paged_literal] array [1..value_table_length] of value_table_type := [
O 4975 { } [FALSE, 'ASSIGN_MULTIPLE_PAGES', 0, 100000, v_integer, Ammv$assign_multiple_pages],
O 4976 { } [FALSE, 'avc$validation_level_const_name', 0, 2, v_byte, Aavv$validation_level],
O 4977 { } [FALSE, 'clc$change_secure_logging_name', 0, 1, v_boolean, Aclv$secure_logging_activated],
O 4978 { } [FALSE, 'clc$system_logging_active_name', 0, 1, v_boolean, Aclv$system_logging_activated],
O 4979 { } [FALSE, 'ABORTED_TASK_THRESHOLD', 0, 7FFFFFFFFF(16), v_integer, Amtv$aborted_task_threshold],
O 4980 { } [FALSE, 'ADVISE_IN_LIMIT', 1, 1000, v_integer, Ammv$advise_in_ain_limit],
O 4981 { } [FALSE, 'AGE_INTERVAL_CEILING', 1, 255, v_byte, Ammv$age_interval_ceiling],
O 4982 { } [FALSE, 'AGE_INTERVAL_FLOOR', 1, 255, v_byte, Ammv$age_interval_floor],
O 4983 { } [FALSE, 'AGGRESSIVE_AGING_LEVEL', 0, 100000, v_integer, Ammv$aggressive_aging_level],
O 4984 { } [FALSE, 'AGGRESSIVE_AGING_LEVEL_2', 0, 100000, v_integer, Ammv$aggressive_aging_level_2],
O 4985 { } [FALSE, 'AGING_ALGORITHM', 0, 10000, v_integer, Ammv$aging_algorithm],
O 4986 { } [FALSE, 'AIO_LIMIT', 0, 5000000, v_integer, Ammv$aio_limit],
O 4987 { } [FALSE, 'ALLOW_JR_TEST', 0, 1, v_boolean, Asyv$allow_jr_test],
O 4988 { } [FALSE, 'AUTOMATIC_UNSTEP_RESUME', 0, 1, v_boolean, Amtv$automatic_unstep_resume],
O 4989 { } [TRUE, 'CATALOG_NAME_SECURITY', 0, 1, v_boolean, Aovv$catalog_name_security],
O 4990 { } [FALSE, 'CHECK_IDLE_DISPATCHING_INTERVAL', 1000, 100000000, v_integer,
O 4991 {   Ajmv$scan_idle_dispatch_interval],
O 4992 { ----- Declarations for forcing the use of cache and maps omitted at compile time -----
O 4993 { } [FALSE, 'CLONE_ENABLED', 0, 1, v_boolean, Asyv$clone_enabled],
O 4994 { } [FALSE, 'COMMAND_STATISTICS_ENABLED', 0, 1, v_boolean, Aclv$command_statistics_enabled],
O 4995 { } [FALSE, 'CYCLE_WAIT_TIME', 0, 10000000, v_integer, Amtv$cycle_delay_time],
O 4996 { } [TRUE, 'DEADSTART_INTERVENTION', 0, 1, v_boolean, Arav$deadstart_intervention],
O 4997 { } [FALSE, 'DEBUG0', 0, 0xffffffff(16), v_integer, Aovv$debug[0]],
O 4998 { } [FALSE, 'DEBUG1', 0, 0xffffffff(16), v_integer, Aovv$debug[1]],
O 4999 { } [FALSE, 'DEBUG2', 0, 0xffffffff(16), v_integer, Aovv$debug[2]],
O 5000 { } [FALSE, 'DEBUG3', 0, 0xffffffff(16), v_integer, Aovv$debug[3]],
O 5001 { } [FALSE, 'DEBUG4', 0, 0xffffffff(16), v_integer, Aovv$debug[4]],
O 5002 { } [FALSE, 'DEBUG5', 0, 0xffffffff(16), v_integer, Aovv$debug[5]],
O 5003 { } [FALSE, 'DEBUG6', 0, 0xffffffff(16), v_integer, Aovv$debug[6]],
O 5004 { } [FALSE, 'DEBUG7', 0, 0xffffffff(16), v_integer, Aovv$debug[7]],
O 5005 { } [FALSE, 'DEBUG8', 0, 0xffffffff(16), v_integer, Aovv$debug[8]],
O 5006 { } [FALSE, 'DEBUG9', 0, 0xffffffff(16), v_integer, Aovv$debug[9]],
O 5007 { } [FALSE, 'DEBUG10', 0, 0xffffffff(16), v_integer, Aovv$debug[10]],
O 5008 { } [FALSE, 'DEBUG11', 0, 0xffffffff(16), v_integer, Aovv$debug[11]],

```

System Constant Management

Global Declarations Declared by this module

System Constant Definition Table

```

0 5009 [ ] [FALSE, 'DEBUG12', 0, 0xffffffff(16), v_integer, A osv$debug[12]],
0 5010 [ ] [FALSE, 'DEBUG13', 0, 0xffffffff(16), v_integer, A osv$debug[13]],
0 5011 [ ] [FALSE, 'DEBUG14', 0, 0xffffffff(16), v_integer, A osv$debug[14]],
0 5012 [ ] [FALSE, 'DEBUG15', 0, 0xffffffff(16), v_integer, A osv$debug[15]],
0 5013 [ ] [FALSE, 'DEBUG_JOB_RECOVERY', 0, 1, v_boolean, A sv$debug_job_recovery],
0 5014 [ ] [FALSE, 'DEDICATE_A_CPU_TO_NOS', 0, 1, v_boolean, A tmv$dedicate_a_cpu_to_nos],
0 5015 [ ] [FALSE, 'DEFAULT_PIT', 1, 0xffffffff(16), v_integer, A osv$default_pit],
0 5016 [ ] [FALSE, 'DEFAULT_SIT', 500, 100000000, v_integer, A osv$default_sit_value],
0 5017 [ ] [TRUE, 'DELETE_UNRECONCILED_FILES', 0, 1, v_boolean, A osv$delete_unreconciled_files],
0 5018 [ ] [TRUE, 'DEVELOPMENT_DEADSTART', 0, 1, v_boolean, A rav$development_deadstart],
0 5019 [ ] [FALSE, 'DISABLE_WRITE_FOR_PERF_MEAS', 0, 1, v_boolean, A mmv$disable_write_for_perf_meas],
0 5020 [ ] [FALSE, 'DISDELAY', 1, 100000, v_integer, A dpv$display_delay],
0 5021 [ ] [TRUE, 'DISPLAY_DEADSTART_MESSAGES', 0, 1, v_boolean, A osv$display_deadstart_messages],
0 5022 [ ] [FALSE, 'DUMP_WHEN_DEBUG', 0, 1, v_boolean, A osv$dump_when_debug],
0 5023 [ ] [FALSE, 'DYNAMIC_FILE_SPACE_LIMITS', 0, 1, v_boolean, A svf$dynamic_file_space_limits],
0 5024 [ ] [FALSE, 'ENABLE_DEBUG_CODE', 0, 1, v_boolean, A jsv$enable_debug_code],
0 5025 [ ] [FALSE, 'ENABLE_FAULT_INJECTION', 0, 1, v_boolean, A sv$enable_fault_injection],
0 5026 [ ] [TRUE, 'ENABLE_HEAP_TRACE', 0, 1, v_boolean, A osv$enable_heap_trace],
0 5027 [ ] [TRUE, 'ENABLE_HYPERCHANNEL', 0, 1, v_boolean, A osv$enable_hyperchannel],
0 5028 [ ] [FALSE, 'ENABLE_QUEUE_FILE_ACCESS', 0, 1, v_boolean, A jmv$enable_queue_file_access],
0 5029 [ ] [FALSE, 'ENABLE_SOURCE_CYBIL_CHECKING', 0, 1, v_boolean, A jsv$enable_source_type_checking],
0 5030 [ ] [FALSE, 'ENABLE_SWAP_FILE_STATISTICS', 0, 1, v_boolean, A jsv$enable_swap_statistics],
0 5031 [ ] [FALSE, 'ENABLE_SWAP_RESIDENT', 0, 1, v_boolean, A jsv$enable_swap_resident],
0 5032 [ ] [FALSE, 'ENABLE_SWAP_RESIDENT_NO_ID', 0, 1, v_boolean, A jsv$enable_swap_resident_no_id],
0 5033 [ ] [FALSE, 'FILE_ALLOCATION_INTERVAL', 0, 7fffffff(16), v_integer, A mmv$file_allocation_interval],
0 5034 [ ] [FALSE, 'FILE_SERVER_DEBUG_ENABLED', 0, 1, v_boolean, A dfv$file_server_debug_enabled],
0 5035 [ ] [FALSE, 'FILE_SERVER_RECOVERY_ENABLED', 0, 1, v_boolean, A dfv$job_recovery_enabled],
0 5036 [ ] [FALSE, 'FILE_SERVER_INFO_ENABLED', 0, 1, v_boolean, A dfv$file_server_info_enabled],
0 5037 [ ] [FALSE, 'FORCE_DIRECT_TAPE_IO', 0, 1, v_boolean, A bav$force_direct_tape_io],
0 5038 [ ] [FALSE, 'FREE_WORKING_SET_ON_SWAPOUT', 0, 1, v_boolean, A jsv$free_working_set_on_swapout],
0 5039 [ ] [FALSE, 'HALTRING', 0, 16, v_byte, A mtv$halt_cpu_ring_number],
0 5040 [ ] [FALSE, 'HALT_ON_HUNG_TASK', 0, 1, v_boolean, A mtv$halt_on_hung_task],
0 5041 [ ] [FALSE, 'HALT_ON_PROCESSOR_MALFUNCTION', 0, 1, v_boolean, A mtv$halt_on_proc_malf],
0 5042 [ ] [FALSE, 'HALT_ON_SWAPIN_FAILURE', 0, 1, v_boolean, A jsv$halt_on_swapin_failure],
0 5043 [ ] [FALSE, 'IF_CONDITION_HANDLING_TRACE', 0, 1, v_boolean, A iiv$condition_handler_trace],
0 5044 [ ] [FALSE, 'IGNORE_IMAGE', 0, 1, v_boolean, A osv$ignore_image],
0 5045 [ ] [FALSE, 'IGNORE_PARAMETER_VERIFICATION', 0, 2, v_integer, A lov$ignore_param_verification],
0 5046 [ ] [FALSE, 'TRIM_FILES', 0, 1, v_boolean, A mv$trim_files],
0 5047 [ ] [TRUE, 'UNLOAD_DEADSTART_TAPE', 0, 1, v_boolean, A osv$unload_deadstart_tape],
0 5048 [ ] [TRUE, 'JOB_RECOVERY_OPTION', 0, 100, v_integer, A osv$job_recovery_option],
0 5049 [ ] [FALSE, 'JOB_WORKING_SET_AGE_INTERVAL', 0, 1000, 7fffffff(16), v_integer, A mmv$jws_queue_age_interval],
0 5050 [ ] [FALSE, 'KCU_ENABLED', 0, 100, v_integer, A osv$keypoint_enable],
0 5051 [ ] [FALSE, 'dummy', 0, 1, v_boolean, NIL],
0 5052 [ ] [FALSE, 'LONG_WAIT_SWAP_TIME', 0, 100000000, v_integer, A tmv$long_wait_swap_time],
0 5053 [ ] [FALSE, 'LONG_WAIT_FORCE_SWAP_TIME', 0, 100000000, v_integer, A tmv$long_wait_force_swap_time],
0 5054 [ ] [FALSE, 'MANDATORY_DUALSTATE', 0, 1, v_boolean, A osv$mandatory_dualstate],
0 5055 [ ] [TRUE, 'MAXIMUM_180_MEMORY', 1000000, 400000000, v_integer, A mmv$maximum_180_memory],
0 5056 [ ] [TRUE, 'MAXIMUM_ACTIVE_SEGMENTS', 100, 65535, v_halfword, A mtv$mx_segments],
0 5057 [ ] [FALSE, 'MAXIMUM_BYTES_PER_TAPE_IO', 1, 7fffffff(16), v_integer, A dav$max_bytes_per_tape_io],
0 5058 [ ] [FALSE, 'MAXIMUM_INDIRECT_TAPE_BLOCK', 1, 7fffffff(16), v_integer, A bav$max_indirect_tape_block],
0 5059 [ ] [FALSE, 'MAXIMUM_KNOWN_JOBS', 1, jmc$maximum_job_count, v_halfword, A jmv$maximum_known_jobs],
0 5060 [ ] [FALSE, 'MAXIMUM_OUTPUT_FILES', 1, jmc$maximum_output_count, v_halfword, A jmv$maximum_known_outputs],
0 5061 [ ] [FALSE, 'MAXIMUM_PAGES_NO_FILE', 0, 7fffffff(16), v_integer, A mmv$max_pages_no_file],
0 5062 [ ] [FALSE, 'MAXIMUM_PAGES_TO_SWAP', 0, 0xffff(16), v_integer, A jsv$maximum_pages_to_swap],

```

System Constant Management

Global Declarations Declared by this module

System Constant Definition Table

```

0 5063 [ ] [FALSE, 'MAX_PAGES_FIRST_SWAP_TASK', 0, 0xffff(16), v_integer, A jsv$max_pages_first_swap_task],
0 5064 [ ] [FALSE, 'MAXIMUM_SEGMENT_LENGTH', 150000000, 7fffffff(16), v_integer, A mmv$max_segment_length],
0 5065 [ ] [FALSE, 'MAXIMUM_SWAP_RESIDENT_TIME', 0, 864000000, v_integer, A jsv$max_time_swap_io_complete],
0 5066 [ ] [FALSE, 'MAXIMUM_TAPE_BLOCK_SIZE', 1, 7fffffff(16), v_integer, A bav$max_allowed_tape_block_size],
0 5067 [ ] [TRUE, 'MAXIMUM_TASKS', 20, osc$max_tasks, v_halfword, A mtv$mx_tasks],
0 5068 [ ] [FALSE, 'MAXIMUM_THINK_TIME', 0, 100000000, v_integer, A jmv$max_think_time],
0 5069 [ ] [FALSE, 'MAXWS_AIO_THRESHOLD', 0, 100000, v_integer, A mmv$maxws_aio_threshold],
0 5070 [ ] [FALSE, 'MAX_TIME_SWAP_IO_NOT_INIT', 0, 864000000, v_integer, A osv$max_time_swap_io_not_init],
0 5071 [ ] [FALSE, 'MINIMUM_AVAILABLE_PAGES', 0, 100000, v_integer, A mmv$min_avail_pages],
0 5072 [ ] [FALSE, 'MINIMUM_SHARED_WORKING_SET', 0, 100000, v_halfword, A mmv$min_shared_working_set],
0 5073 [ ] [FALSE, 'Atemp_workaround_min_sws],
0 5074 [ ] [FALSE, 'Note: The following line should be made active when the temp_workaround is no longer needed
0 5075 [ ] [FALSE, 'Ammv$ppq[mmc$ppq_shared_task_service].minimum],
0 5076 [ ] [FALSE, 'MINIMUM_THINK_TIME', 0, 100000000, v_integer, A jmv$min_think_time],
0 5077 [ ] [FALSE, 'MM_CHECK_QUEUES', 0, 2, v_integer, A mmv$check_queues],
0 5078 [ ] [FALSE, 'MM_PERIODIC_CALL', 1000, 1000000, v_integer, A mmv$periodic_call_interval],
0 5079 [ ] [FALSE, 'MULTI_PAGE_WRITE', 0, 1, v_boolean, A mmv$multi_page_write],
0 5080 [ ] [FALSE, 'NETWORK_ACTIVATION', 0, 1, v_boolean, A rav$network_activation],
0 5081 [ ] [FALSE, 'NOSVE_INTERNAL_OPERATIONS', 0, 1, v_boolean, A osv$nosve_internal_operations],
0 5082 [ ] [FALSE, 'NO_MEMORY_BUFFERING', 0, 1, v_boolean, A mmv$no_memory_buffering],
0 5083 [ ] [FALSE, 'dummy', 0, 1, v_boolean, NIL],
0 5084 [ ] [FALSE, 'OUTPUT_OPTION', 0, 100000000, v_integer, A iiv$output_option],
0 5085 [ ] [FALSE, 'dummy', 0, 1, v_boolean, NIL],
0 5086 [ ] [FALSE, 'PERMANENT_FILE_OVERFLOW', 0, 1, v_boolean, A dmv$permanent_file_overflow],
0 5087 [ ] [FALSE, 'PROCESSOR_DUE_THRESHOLD', 0, 7fffffff(16), v_integer, A mtv$processor_due_threshold],
0 5088 [ ] [FALSE, 'QUICKDS', 0, 1, v_boolean, A dmv$quick_deadstart],
0 5089 [ ] [FALSE, 'READ_TO_EXECUTE', 1, 32, v_halfword, A mmv$read_to_executel],
0 5090 [ ] [FALSE, 'READ_TO_READ_WRITE', 1, 32, v_halfword, A mmv$read_to_read_write],
0 5091 [ ] [TRUE, 'RECONCILE_PERMANENT_FILES', 0, 1, v_boolean, A osv$reconcile_permanent_files],
0 5092 [ ] [TRUE, 'RECOVER_AT_ALL_COSTS', 0, 1, v_boolean, A osv$recover_at_all_costs],
0 5093 [ ] [FALSE, 'RECYCLE_DEVICE_LOG', 0, 1, v_boolean, A dmv$recycle_device_log],
0 5094 [ ] [TRUE, 'REORGANIZE_PERMANENT_FILES', 0, 1, v_boolean, A osv$reorganize_permanent_files],
0 5095 [ ] [FALSE, 'SCHEDULER_WAIT_TIME', 0, 5000000, v_integer, A jmv$scheduler_wait_time],
0 5096 [ ] [FALSE, 'SCHED_MEMORY_WAIT_FACTOR', 0, 100, v_integer, A jmv$sched_memory_wait_factor],
0 5097 [ ] [FALSE, 'SHARED_PAGES_IN_JWS', 0, 1, v_boolean, A mmv$shared_pages_in_jws],
0 5098 [ ] [FALSE, 'SHARED_WORKING_SET_AGE_INTERVAL', 0, 7fffffff(16), v_integer, A mmv$shared_queue_age_interval],
0 5099 [ ] [FALSE, 'SWAPPING_AIC', 0, 100, v_integer, A mmv$swapping_aic],
0 5100 [ ] [FALSE, 'SWAP_FILE_ALLOCATION_SIZE', 4096, 1000000, v_halfword, A jmv$swap_file_allocation_size],
0 5101 [ ] [FALSE, 'SWAP_JOBS_IN_LONG_WAIT', 0, 1, v_boolean, A jmv$swap_jobs_in_long_wait],
0 5102 [ ] [FALSE, 'SYSTEM_DEBUG_RING', 0, 15, v_integer, A mtv$system_debug_ring],
0 5103 [ ] [FALSE, 'SYSTEM_DEBUG_SEGMENT', 0, 4095, v_integer, A mtv$system_debug_segment],
0 5104 [ ] [FALSE, 'SYSTEM_ERROR_HANG_COUNT', 0, 1000, v_halfword, A mtv$system_error_hang_count],
0 5105 [ ] [FALSE, 'SYSTEM_HALTRING', 0, 16, v_byte, A mtv$system_haltring],
0 5106 [ ] [TRUE, 'SYSTEM_JOB_MULTIPROCESSING', 0, 1, v_boolean, A osv$system_job_multiprocessing],
0 5107 [ ] [FALSE, 'TEMPORARY_FILE_OVERFLOW', 0, 1, v_boolean, A dmv$temporary_file_overflow],
0 5108 [ ] [FALSE, 'TEST_RECOVERY', 0, 1, v_boolean, A dmv$test_recovery],
0 5109 [ ] [FALSE, 'THINK_EXPIRATION_TIME', 0, 100000000, v_integer, A osv$think_expiration_time],
0 5110 [ ] [FALSE, 'USE_ASSIGN_PAGES_FOR_TAPE', 0, 1, v_boolean, A bav$use_assign_pages_for_tape],
0 5111 [ ] [TRUE, 'VALIDATE_PERMANENT_FILES', 0, 1, v_boolean, A osv$validate_permanent_files],

```

System Constant Management

Global Declarations Declared by this module

System Constant Definition Table

```

0 5117 [ ] [TRUE, 'VALIDATE_SYSTEM_SET', 0, 1, v_boolean, ^osv$validate_active_sets],
0 5118 [ ] [FALSE, 'VECTOR_STIMULATION', 0, 2, v_byte, ^mtv$scb_vector_sim_attribute],
0 5119 [ ] [FALSE, 'VERIFY_HEAP_LINKAGE', 0, 1, v_boolean, ^syv$verify_heap_linkage],
0 5120 [ ] [TRUE, 'VOLUME_CLASS', 0, 1, v_boolean, ^adm$volume_class_kludge],
0 5121 [ ] [TRUE, 'WIRE_MLI_TABLES', 0, 1, v_boolean, ^mlv$wire_mli_tables],
0 5122 [ ] [FALSE, 'WRITE_AGED_OUT_PAGES', 0, 10000, v_integer, ^amm$write_aged_out_pages],
0 5123 [ ] [TRUE, 'WRITE_STALE_PAGES', 0, 1, v_boolean, ^ajsv$write_stale_pages],
0 5124 [ ] [TRUE, 'MAXIMUM_ACTIVE_JOBS', 1, jmc$max_active_jobs, v_halfword, ^mtv$mx_ajl_entries],
0 5125 [ ] [FALSE, 'UNUSED_BOOLEAN_FOR_IO', 0, 1, v_boolean, ^iov$unused_boolean],
0 5126 [ ] [FALSE, 'MAXIMUM_JOB_CLASSES', jmc$minimum_job_classes, jmc$maximum_job_classes, v_halfword,
0 5127 [ ] ^jmv$maximum_job_classes],
0 5128 [ ] [FALSE, 'MAXIMUM_SERVICE_CLASSES', jmc$minimum_service_classes, jmc$maximum_service_classes, v_halfword,
0 5129 [ ] ^jmv$maximum_service_classes],
0 5130 [ ] [FALSE, 'MAXIMUM_DEBUG_OUTPUT_LINES', 100, 7FFFFFFF(16), v_integer, ^syv$max_debug_output_lines],
0 5131 [ ] [FALSE, 'DISK_FAULT_SIMULATION', 0, 1, v_boolean, ^osv$disk_fault_simulation],
0 5132 [ ] [FALSE, 'EMERGENCY_INTERVENTION', 0, 1, v_boolean, ^osv$emergency_intervention],
0 5133 [ ] [FALSE, 'DISABLE_NETWORK_RELAYS', 0, 1, v_boolean, ^nav$disable_network_relays],
0 5134 [ ] [FALSE, 'dummy', 0, 1, v_boolean, NIL],
0 5135 [ ] [FALSE, 'SHADOW_BY_SEGNUM', 0, 1, v_boolean, ^amm$shadow_by_segnum],
0 5136 [ ] [FALSE, 'ENABLE_PM_DEBUG_LOGGING', 0, 1, v_boolean, ^pmv$debug_logging_enabled],
0 5137 [ ] [FALSE, 'CONSTRAIN_MEAPE_SEGMENTS', 0, 1, v_boolean, ^pmv$constrain_meape_segments],
0 5138 [ ] [TRUE, 'SYSTEM_ACTIVATION', 0, 1, v_boolean, ^rav$system_activation],
0 5139 [ ] [FALSE, 'USER_TEMPLATES', 0, 1, v_boolean, ^syv$user_templates],
0 5140 [ ] [FALSE, 'CLIENT_JOB_LISTS', 1, dfc$max_job_list_p_array_size, v_integer,
0 5141 [ ] ^dfv$maximum_client_job_lists],
0 5142 [ ] [FALSE, 'SERVED_FAMILY_LISTS', 1, dfc$maximum_family_lists, v_integer,
0 5143 [ ] ^dfv$number_served_family_lists],
0 5144 [ ] [FALSE, 'SPECIAL_TRAP', 0, 1, v_boolean, ^osv$special_aam_trap],
0 5145 [ ] [FALSE, 'TRAP_TASK_ERRORS', 0, 1, v_boolean, ^osv$trap_task_errors],
0 5146 [ ] [FALSE, 'DISPLAY_ACTUAL_PRIORITY', 0, 1, v_boolean, ^mtv$display_actual_priority],
0 5147 [ ] [FALSE, 'TIMED_WAIT_NOT_QUEUED', 6000000, 7fffffff(16), v_integer, ^mtv$timed_wait_not_queued],
0 5148 [ ] [TRUE, 'VALIDATE_ACTIVE_SETS', 0, 1, v_boolean, ^osv$validate_active_sets],
0 5149 [ ] [FALSE, 'PF_SPARSE', 0, 1, v_boolean, ^adm$pf_sparse],
0 5150 [ ] [FALSE, 'MAXIMUM_ALLOCATION_SIZE', 16384, 0xffffffff(16), v_integer, ^adm$maximum_allocation_size],
0 5151 [ ] [FALSE, 'DEBUGGER_PAGE_WAIT_LINES', 0, 07fffffff(16), v_integer, ^syv$debugger_page_wait_lines],
0 5152 [ ] [FALSE, 'SPACE_MESSAGES_TO_CONSOLE', 0, 1, v_boolean, ^adm$space_messages_to_console],
0 5153 [ ] [FALSE, 'DEFAULT_DEBUG_OUTPUT_DISPOSAL', 0, 3, v_byte, ^syv$dfit_debug_output_disposal.byte],
0 5154 [ ] [TRUE, 'ENABLE_CONSOLE_BELL', 0, 1, v_boolean, ^dpv$enable_console_bell],
0 5155 [ ] [FALSE, 'BINARY_CATALOG_SEARCH', 0, 1, v_boolean, ^pfv$binary_catalog_search],
0 5156 [ ] [FALSE, 'HALT_ON_EXIT_WITH_IO', 0, 1, v_boolean, ^syv$halt_on_exit_with_io],
0 5157 [ ] [FALSE, 'dummy', 0, 1, v_boolean, NIL],
0 5158 [ ] [FALSE, 'dummy', 0, 1, v_boolean, NIL],
0 5159 [ ] [FALSE, 'dummy', 0, 1, v_boolean, NIL],
0 5160 [ ] [FALSE, 'dummy', 0, 1, v_boolean, NIL],
0 5161 [ ] [FALSE, 'dummy', 0, 1, v_boolean, NIL],
0 5162 [ ] [FALSE, 'dummy', 0, 1, v_boolean, NIL],
0 5163 [ ] [FALSE, 'dummy', 0, 1, v_boolean, NIL],
0 5164 [ ] [FALSE, 'dummy', 0, 1, v_boolean, NIL],
0 5165 [ ] [FALSE, 'dummy', 0, 1, v_boolean, NIL],
0 5166 [ ] [FALSE, 'dummy', 0, 1, v_boolean, NIL],
0 5167 [ ] [FALSE, 'dummy', 0, 1, v_boolean, NIL]];
0 5168
0 5169

```

System Constant Management

[XDCL] syp\$fetch_system_const_from_ssr

```

0 5172
0 5173 PROCEDURE [XDCL] syp$fetch_system_const_from_ssr
4 5174 (VAR status: ost$status);
4 5175
4 5176 status.normal := TRUE;
4 5177 PROCEND syp$fetch_system_const_from_ssr;
0 5178

```

System Constant Management

```

[XDCL, #GATE] syp$fetch_system_constant

0 5180
0 5181 PROCEDURE [XDCL, #GATE] syp$fetch_system_constant
0 5182 (VAR name: string ( * );
0 5183 VAR index: integer;
0 5184 VAR value: integer;
0 5185 VAR status: ost$status);
0 5186
0 5187 VAR
0 5188 tab_p: ^value_table_type,
0 5189 temp_status: ost$status;
0 5190
0 5191 temp_workaround_min_sws := mmv$gpq1[mmc$spq_shared_task_service].minimum;
4 5192 IF name = 'EVERYTHING' THEN
68 5193 IF (index >= 1) AND (index <= value_table_length) THEN
7E 5194 name := value_table [index].name;
D6 5195 IF name <> 'dummy' THEN
120 5196 CASE value_table [index].value_type OF
144 5197 = v_integer :
144 5198 value := value_table [index].integer_p^;
162 5199 = v_halfword :
162 5200 value := value_table [index].halfword_p^;
17A 5201 = v_byte :
17A 5202 value := value_table [index].byte_p^;
192 5203 = v_boolean :
192 5204 value := ORD (value_table [index].boolean_p^);
1A4 5205 CASEEND;
1A4 5206 IFEND;
1A4 5207 IF index = value_table_length THEN
180 5208 index := 0;
18A 5209 ELSE
18A 5210 index := index + 1;
1C0 5211 IFEND;
1C0 5212 IFEND;
1C2 5213 ELSE
*WARN* 5214 search_value_table (name, tab_p, status);
232 5215 IF status.normal THEN
23C 5216 CASE tab_p^.value_type OF
25A 5217 = v_integer :
25A 5218 value := tab_p^.integer_p^;
270 5219 = v_halfword :
270 5220 value := tab_p^.halfword_p^;
282 5221 = v_byte :
282 5222 value := tab_p^.byte_p^;
294 5223 = v_boolean :
294 5224 value := ORD (tab_p^.boolean_p^);
2A4 5225 CASEEND;
2A4 5226 RETURN;
2A6 5227 IFEND;
2A6 5228
2A6 5229 { The following code is executed only if the status returned from search_value_table was abnormal.
2A6 5230 { Use a temporary status in the following procedure calls to preserve the bad status from search_value_table.
2A6 5231
2A6 5232 dmp$fetch_debug_option_value (name, value, temp_status);
312 5233 IF temp_status.normal THEN
31A 5234 RETURN;

```

System Constant Management

```

[XDCL, #GATE] syp$fetch_system_constant

31C 5235 IFEND;
31C 5236
31C 5237 iop$fetch_debug_option_value (name, value, temp_status);
382 5238 IFEND;
382 5239 PROCEND syp$fetch_system_constant;
0 5240

```


System Constant Management
 [XDCL] syp\$save_system_const_in_ssr

```

0 5242
0 5243 PROCEDURE [XDCL] syp$save_system_const_in_ssr
4 5244   (VAR status: ost$status);
4 5245
4 5246   status.normal := TRUE;
4 5247 PROCEND syp$save_system_const_in_ssr;
0 5248

```

System Constant Management
 [XDCL] syp\$set_processor_attributes

```

0 5250
0 5251 PROCEDURE [XDCL] syp$set_processor_attributes
0 5252   ( model_number: integer;
0 5253     VAR status: ost$status);
0 5254
0 5255   VAR
0 5256     p: ARECORD
0 5257       fill1: string (12),
0 5258       a0_offset: 0 .. 0ffffff(16),
0 5259       fill2: string (4),
0 5260       a1_offset: 0 .. 0ffffff(16),
0 5261     RECCEND;
0 5262
0 5263     pmv$quantum := osv$global_processor_model_info.quantum;
0 5264     mmv$tick_time := osv$global_processor_model_info.tick_time;
4 5265
4 5266 { The default values of the variables managed by the Manage Memory Utility have already been saved in the
4 5267 { procedure mmp$initialize in mmm$deadstart_initialization. But now mmv$tick_time has been reset.
4 5268 { Therefore reset the default in mmv$manage_memory_utility
4 5269
4 5270     mmv$manage_memory_utility.ma[ma_tt].default := mmv$tick_time;
4 5271
4 5272     mtv$xp_initial_value.tos_registers[1].pva.offset :=
4 5273     . mmv$page_map_offsets [mmc$pmo_r1_stack] * osv$page_size +
4 5274     mmc$ring_crossing_offset;
4 5275     mtv$xp_initial_value.tos_registers[2].pva.offset :=
4 5276     mmv$page_map_offsets [mmc$pmo_r2_stack] * osv$page_size +
4 5277     mmc$ring_crossing_offset;
4 5278     mtv$xp_initial_value.tos_registers[3].pva.offset :=
4 5279     mmv$page_map_offsets [mmc$pmo_r3_stack] * osv$page_size +
4 5280     mmc$ring_crossing_offset;
4 5281     p := #LDC (mtv$xp_initial_value);
62 5282     p^.a0_offset := mmv$page_map_offsets [mmc$pmo_r1_stack] * osv$page_size +
62 5283     mmc$ring_crossing_offset;
62 5284     p^.a1_offset := mmv$page_map_offsets [mmc$pmo_r1_stack] * osv$page_size +
62 5285     mmc$ring_crossing_offset;
0 5286 PROCEND syp$set_processor_attributes;
0 5287

```

System Constant Management

[XDCL, #GATE] syp\$store_system_constant

```

0 5289
0 5290 PROCEDURE [XDCL, #GATE] syp$store_system_constant
0 5291 ( name: string ( * );
0 5292 index: integer;
0 5293 value: integer;
0 5294 VAR status: ost$status);
0 5295
0 5296 VAR
0 5297 tab_p: ^value_table_type,
0 5298 temp_status: ost$status;
0 5299
0 5300 search_value_table (name, tab_p, status);
22 5301 IF status.normal THEN
2A 5302
2A 5303 IF (mtv$sys_core_init_complete) AND (tab_p^.deadstart_only) THEN
42 5304 osp$set_status_abnormal ('SY', sye$not_changeable_after_ds,
76 5305 'Variable not changeable after ds complete', status);
76 5306 RETURN;
78 5307 IFEND;
78 5308
78 5309 IF (value > tab_p^.max) OR (value < tab_p^.min) THEN
90 5310 osp$set_status_abnormal ('SY', sye$range_error, 'Value out of range', status);
C6 5311 ELSE
C6 5312 temp_workaround_min_sws := mmv$gpql[mmc$mq_shared_task_service].minimum;
C6 5313 CASE tab_p^.value_type OF
F0 5314 = v_integer :
F0 5315 tab_p^.integer_p^ := value;
104 5316 = v_halfword :
104 5317 tab_p^.halfword_p^ := value;
110 5318 = v_byte :
110 5319 tab_p^.byte_p^ := value;
11C 5320 = v_boolean :
11C 5321 IF value = ORD (FALSE) THEN
120 5322 tab_p^.boolean_p^ := FALSE;
12E 5323 ELSE
12E 5324 tab_p^.boolean_p^ := TRUE;
136 5325 IFEND;
136 5326 CASEEND;
136 5327 mmv$gpql[mmc$mq_shared_task_service].minimum := temp_workaround_min_sws;
13E 5328 IFEND;
13E 5329 RETURN;
140 5330 IFEND;
140 5331
140 5332 osp$set_status_abnormal ('DS', cle$name_not_a_keyword_value, name, status);
16C 5333 osp$append_status_parameter (osc$status_parameter_delimiter, ' for parameter NAME', status);
194 5334
194 5335 { Use a temporary status in the following procedure calls to preserve the status returned for
194 5336 { search_value_table.
194 5337
194 5338 dmp$store_debug_option_value (name, value, temp_status);
184 5339 IF temp_status.normal THEN
18C 5340 RETURN;
18E 5341 IFEND;
18E 5342
18E 5343 iop$store_debug_option_value (name, value, temp_status);

```

System Constant Management

[XDCL, #GATE] syp\$store_system_constant

```

1E2 5344
1E2 5345
1E2 5346 PROCEND syp$store_system_constant;
0 5347

```

System Constant Management
search_value_table

```

0 5349
0 5350 PROCEDURE search_value_table
0 5351 ( name: string ( * );
0 5352   VAR tab_p: Avalue_table_type;
0 5353   VAR status: ost$status);
0 5354
0 5355   VAR
0 5356     error_string: string (55),
0 5357     i: integer;
0 5358     lname: string (max_name_length),
0 5359     table_i: integer;
0 5360
0 5361     table_i := 1;
0 5362     lname := name;
0 5363     FOR i := 1 TO STRLENGTH (lname) DO
0 5364       IF (lname (i) >= 'a') AND (lname (i) <= 'z') THEN
0 5365         lname (i) := CHR (ORD (lname (i)) - ORD ('a') + ORD ('A'));
0 5366       IFEND;
0 5367     FOREND;
0 5368
0 5369   /1p/
0 5370   WHILE lname <> value_table [table_i].name DO
0 5371     table_i := table_i + 1;
0 5372     IF table_i <= value_table_length THEN
0 5373       CYCLE /1p/
0 5374     IFEND;
0 5375     error_string := 'Unknown parameter name: ';
0 5376     error_string(25, *) := lname;
0 5377     osp$set_status_abnormal ('SY', sye$unknown_parameter_name, lname, status);
0 5378     RETURN;
0 5379   WHILEEND /1p/;
0 5380
0 5381   tab_p := Avalue_table [table_i];
0 5382   PROCEND search_value_table;
0 5383

```

```

0 5385 MODEND sym$system_constant_manager;

```

```

**** I=$05578173AS0102D19890821T183254 L=ZXXLIST B=LGD DA=NONE LO=R RC=NONE OPT=SCHED EL=F LF=CS612 PAD=0

```

System Constant Management
search_value_table

```

ERROR LINE TEXT
WARNING CY 821 5214 Code scheduling abandoned for this block due to register jamming.

```

LEVEL SUMMARY

```

**** 1 warning diagnostic

```


System Constant Management
search_value_table

IDENTIFIER	DEFINED ON LINE	REFERENCES
dft\$server_lifetime	3126	1771
dft\$server_state	3131	1770
dfv\$file_server_debug_enabled	1733	5034 3134
dfv\$file_server_info_enabled	1735	5036
dfv\$job_recovery_enabled	1739	5035
dfv\$maximum_client_job_lists	1744	5141
dfv\$number_served_family_lists	3208	5143
dmp\$fetch_debug_option_value	1619	5232
dmp\$store_debug_option_value	1674	5338
dmt\$global_file_name	2947	2600
dmt\$stored_fmd	2956	2848
dmt\$system_file_id	2016	1994
dmv\$maximum_allocation_size	4927	5150
dmv\$permanent_file_overflow	3213	5091
dmv\$pf_sparse	4866	5149
dmv\$quick_deadstart	4928	5093
dmv\$recycle_device_log	3216	5098
dmv\$space_messages_to_console	4809	5152
dmv\$temporary_file_overflow	3219	5112
dmv\$test_recovery	3222	5113
dmv\$trim_files	4810	5046
dmv\$volume_class_kludge	4811	5120
dpv\$display_delay	4812	5020
dpv\$enable_console_bell	3225	5154
dsv\$display_deadstart_messages	4813	5021
dsv\$ignore_image	4815	5044
dsv\$unload_deadstart_tape	3229	5047
error_string	5356	5375/M 5376/M
fmt\$file_label	2961	2876
fsc\$reserved_damage_symptom_9	2056	2083
fst\$cycle_damage_symptom	2052	2037
fst\$cycle_damage_symptoms	2037	1985 2333 2718
gft\$file_descriptor_index	2031	2021
gft\$system_file_identifier	2020	2016
gft\$table_residence	2034	2022
halfword_p	4799	5200 5220 5317/M
i	5357	5363
iiv\$condition_handler_trace	4816	5043
iiv\$output_option	4818	5089
index	5183	5193 5194/S 5196/S 5198/S 5200/S 5202/S 5204/S
integer_p	4797	5207 5208/M 5210/M 5210
iop\$fetch_debug_option_value	1681	5198 5218 5315/M
iop\$store_debug_option_value	1688	5237
io\$io_error	4082	5343
iovs\$unused_boolean	4820	4048 3425 5125

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I:I/O ref, R=read, W=write, P=parameter

System Constant Management
search_value_table

IDENTIFIER	DEFINED ON LINE	REFERENCES
jmc\$detached_job_wait_time_max	3295	3292
jmc\$highest_det_job_wait_time	3305	3295 3306
jmc\$highest_service_accumulator	3896	3897
jmc\$highest_working_set_size	4114	4105 4115 4117 4119 4121
jmc\$ies_job_swapped	3681	3680
jmc\$ies_swapin_in_progress	3680	3689
jmc\$iss_idle_tasks_initiated	3696	3723
jmc\$iss_swapin_io_complete	3721	3724
jmc\$iss_swapin_requested	3717	3724
jmc\$iss_swapout_complete	3716	3723
jmc\$iss_swapped_io_cannot_init	3707	3734
jmc\$iss_swapped_no_io	3688	3733
jmc\$keyword_offset_maximum	3322	4106
jmc\$kl_maximum_entries	1274	1267 1268 3848
jmc\$kol_maximum_entries	1284	1269
jmc\$max_active_jobs	1265	5124
jmc\$max_ajl_ord	1266	1265 3489
jmc\$max_dispatching_control	3640	3644
jmc\$max_dispatching_priority	3562	3522 3525 3526
jmc\$maximum_job_classes	3831	3834 5126
jmc\$maximum_job_count	1281	1274 5059
jmc\$maximum_output_count	1291	1284 5060
jmc\$maximum_service_classes	3914	3917 5128
jmc\$min_dispatching_control	3639	3643
jmc\$minimum_job_classes	3830	5128
jmc\$minimum_service_classes	3913	5128
jmc>null_service_class	3907	3908
jmc\$priority_p1	3575	3523
jmc\$priority_p10	3585	3524
jmc\$priority_p14	3589	3524
jmc\$priority_p8	3583	3523
jmc\$required_offset	3320	4120
jmc\$reserved_ajls	1270	1265
jmc\$service_accumulator_maximum	3888	3885
jmc\$system_default_offset	3321	3322 4122
jmc\$system_supplied_name_size	1883	1880
jmc\$unlimited_offset	3318	3296 3307 3898 4116
jmc\$unspecified_offset	3319	4118
jmc\$working_set_size_maximum	4105	4102
jmt\$ajl_ordinal	3489	3361 4180
jmt\$delayed_swapin_work	3506	3391 3510
jmt\$detached_job_wait_time	3292	3277
jmt\$dispatching_control_index	3643	3600 3610
jmt\$dispatching_controls	3613	3611
jmt\$dispatching_priority	3522	3373 3601 3602 3603 3615
jmt\$ijl_block_index	3341	3337
jmt\$ijl_block_number	3340	3336
jmt\$ijl_dispatching_control	3599	3374
jmt\$ijl_entry_status	3676	3360
jmt\$ijl_ordinal	3335	3267 3380 3408 3950 3951 4011 4040 4185

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System Constant Management
search_value_table

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
jmt\$ijl_page_fault_count	3750	3745 3746 3747
jmt\$ijl_page_stats	3744	3740
jmt\$ijl_service_class_stats	3738	3395
jmt\$ijl_statistTcs	3783	3394
jmt\$ijl_swap_count	3759	3755 3756
jmt\$ijl_swap_counts	3754	3414 3741
jmt\$ijl_swap_status	3694	3363 3364 3365
jmt\$initiated_job_list_entry	3357	3266 3975
jmt\$input_file_location	3868	3863
jmt\$job_abort_disposition	3877	3861
jmt\$job_class	3834	3419
jmt\$job_control_block	3248	3234
jmt\$job_mode	1873	1802 3376
jmt\$job_priority	3837	3416 3417
jmt\$job_recovery_disposition	3880	3862
jmt\$job_system_id	4098	3263
jmt\$skjl_index	3848	3362 4098
jmt\$queue_file_ijl_information	3860	3401
jmt\$scheduling_data	3407	3385
jmt\$service_accumulator	3885	3409 3410 3411
jmt\$service_class_index	3917	3420
jmt\$swap_data	3423	3387
jmt\$swapout_reasons	3920	3415
jmt\$swapped_job_entry	3935	3286 3432 3976
jmt\$system_supplied_name	1880	1800 3261 3358
jmt\$task_time_slice	3653	3633 3634
jmt\$time_slice_values	3632	3617
jmt\$user_supplied_name	1892	1801
jmt\$working_set_size	4102	3273 3274
jmv\$enable_queue_file_access	4821	5028
jmv\$max_think_time	4928	5072
jmv\$maximum_job_classes	4140	5127
jmv\$maximum_known_jobs	4144	5059
jmv\$maximum_known_outputs	4153	5060
jmv\$maximum_service_classes	4159	5129
jmv\$min_think_time	4930	5081
jmv\$scan_idle_dispatch_interval	4163	4991
jmv\$sched_memory_wait_factor	4166	5101
jmv\$scheduler_wait_time	4170	5100
jmv\$swap_file_allocation_size	4822	5105
jmv\$swap_jobs_in_long_wait	4175	5106
jsc\$isqi_swapped_io_completed	3955	3957
jsc\$isqi_swapped_io_not_init	3954	3957
jst\$changed_asid_entry	3998	3989
jst\$ijl_swap_queue_id	3954	3949
jst\$ijl_swap_queue_link	3948	3369
jst\$io_control_information	3962	3388
jst\$swap_file_descriptor	3974	3389
jst\$swapped_page_descriptor	3983	3981
jst\$swapped_page_descriptors	3980	3977
jsv\$enable_debug_code	4191	5024

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System Constant Management
search_value_table

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
jsv\$enable_swap_file_statistTcs	4195	5030
jsv\$enable_swap_resident	4931	5031
jsv\$enable_swap_resident_no_io	4932	5032
jsv\$free_working_set_on_swapout	4202	5038
jsv\$halt_on_swapin_failure	4199	5042
jsv\$max_pages_first_swap_task	4209	5063
jsv\$max_time_swap_io_complete	4212	5069
jsv\$max_time_swap_io_not_init	4215	5074
jsv\$maximum_pages_to_swap	4206	5062
jsv\$think_expiration_time	4219	5114
jsv\$write_stale_pages	4933	5123
lname	5358	5362/M 5363 5364 5364 5365/M 5365 5370 5370
lov\$enable_source_type_checking	4823	5376 5377/P
lov\$ignore_param_verification	4824	5045
lp	5369	5369 5373 5379
ma	4311	5270/M
ma_tt	4289	5270/S
max	4794	5309
max_name_length	4957	4782 5358
min	4793	5309
minimum	1331	5191 5312 5327/M
mlv\$wire_mli_tables	4934	5121
mmc\$pmo_r1_stack	1299	5273/S 5282/S 5284/S
mmc\$pmo_r2_stack	1299	5276/S
mmc\$pmo_r3_stack	1299	5279/S
mmc\$pp_avail	1489	1535
mmc\$pp_free	1488	1547
mmc\$pp_job_fixed	1529	1536 1548
mmc\$pp_job_working_set	1531	1548 1549
mmc\$pp_shared_first_site	1539	1543
mmc\$pp_shared_num_sites	1540	1543
mmc\$pp_shared_other	1488	1538
mmc\$pp_shared_site_01	1500	1539
mmc\$pp_shared_site_25	1524	1544
mmc\$pp_shared_task_service	1493	1537 5191/S 5312/S 5327/S
mmc\$pp_swapped_io_error	1527	1547
mmc\$pp_wired	1491	1534
mmc\$ring_crossing_offset	1313	5274 5277 5280 5283 5285
mmt\$active_segment_table_entry	4008	3986 4024 4047
mmt\$ast_index	4030	3431 4001
mmt\$global_page_queue_index	1547	1338
mmt\$global_page_queue_list	1338	4265 4293
mmt\$global_page_queue_list_ent	1328	1338
mmt\$job_page_queue_index	1548	1339 3937
mmt\$job_page_queue_list	1339	3386
mmt\$link	1448	1325 4009 4037 4038
mmt\$locked_page	4059	4043
mmt\$manage_memory_utility	4308	4281

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System Constant Management
search_value_table

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
ost\$exchange_package	4409	4400
ost\$family_name	1948	1820 1943 2352 2356 2361 2367 2372 2378
ost\$flags	4466	4416
ost\$frame_descriptor	4545	4560
ost\$free_running_clock	1432	3269 3278 3381 3382 3383 3384 3418 3428
		3429 3430 3604 3616 4014
ost\$global_task_id	1556	3265 3375 3404
ost\$halfword	1438	4144 4153
ost\$heap	1919	1760 1836 2531
ost\$key_lock_value	1405	1402 4483 4485
ost\$keypoint_class	4498	4429 4500
ost\$keypoint_mask	4500	4432
ost\$minimum_save_area	4555	4421 4530
ost\$monitor_condition	4504	4511
ost\$monitor_conditions	4511	4422 4426 4535
ost\$name	1261	1215 1231 1767 1854 1858 1892 1946 1948
		2289 2422 2431 2985 2990
ost\$p_register	4481	4410 4556
ost\$page_id	1464	1474
ost\$page_size	2261	2242 4597
ost\$page_table_entry	1469	1478 3985
ost\$page_table_index	1462	1478 4044
ost\$paging_statistics	3807	3785
ost\$processor_model_definition	4610	4603 4625
ost\$processor_model_index	4622	4624
ost\$processor_model_number	2191	2175 3102 4611
ost\$processor_serial_number	2269	2174 3103
ost\$pv	1421	4454 4472 4486
ost\$register_number	4477	4451 4541 4550 4551
ost\$ring	1410	1422 4471
ost\$segment	1412	1423 1773 4449 4571
ost\$segment_offset	1413	1353 1424 4572 4574
ost\$signature_lock	2090	1774 1830
ost\$stack_frame_save_area	4529	4563
ost\$status	1626	1621 1676 1683 1690 1698 1707 1717 5174
		5185 5189 5244 5253 5294 5298 5353
ost\$status_condition_code	1654	1629 1650 1705 3009
ost\$status_identifier	1712	1704
ost\$string	1667	1630
ost\$string_size	1661	1668
ost\$system_virtual_address	1351	4049
ost\$task_index	1562	1557 4077 4078
ost\$task_time_slice	3667	3653
ost\$top_of_stack_pointer	4469	4461
ost\$trap_enable	4524	4418
ost\$user_condition	4514	4521
ost\$user_conditions	4521	4424 4533 4562
ost\$user_identification	1941	1941 3264
ost\$user_name	1946	1821 1942 2368 2374 2381
ost\$valid_ring	1411	4461
ost\$virtual_machine_identifier	4491	4412 4414 4557

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System Constant Management
search_value_table

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
ost\$x_register	4478	4451 4541
osv\$catalog_name_security	4841	4989
osv\$debug	4941	4998 4998 4999 5000 5001 5002 5003 5004
		5005 5006 5007 5008 5009 5010 5011 5012
osv\$default_pit	4842	5015
osv\$default_sit_value	4942	5016
osv\$delete_unreconciled_files	4843	5017
osv\$disk_fault_simulation	4943	5131
osv\$dump_when_debug	4844	5022
osv\$emergency_intervention	4845	5132
osv\$enable_hyperchannel	4846	5027
osv\$global_processor_model_info	4603	5263 5264
osv\$keypoint_enable	4945	5050
osv\$page_size	4597	5273 5276 5279 5282 5284
osv\$reconcile_permanent_files	4847	5096
osv\$recover_at_all_costs	4848	5097
osv\$reorganize_permanent_files	4849	5099
osv\$special_aam_trap	4695	5144
osv\$strap_task_errors	4946	5145
osv\$validate_active_sets	4850	5117 5148
osv\$validate_permanent_files	4851	5116
P	5256	5281/M 5282/M 5284/M
pf\$account	2344	2354
pf\$attached_pf_awaiting_client	2415	1993
pf\$attached_pf_entry_unused	1972	1967
pf\$attached_pf_entry_valid	1973	1965
pf\$attached_pf_in_job_recovery	2415	1989
pf\$attached_pf_normal	2414	1983
pf\$catalog_object	2584	2576
pf\$charge_change	2316	2330
pf\$connected_file_device	3087	2747
pf\$cycle_expansion_count	2689	2690
pf\$cycle_number_change	2315	2324
pf\$delete_damage_change	2316	2332
pf\$execute	2394	2396 2399
pf\$external_catalog	3039	2598
pf\$family	2344	2350
pf\$family_name_index	2282	2283
pf\$family_path_index	2136	2137
pf\$file_object	2583	2570
pf\$free_cycle_entry	2732	2711
pf\$free_log_entry	2682	2672
pf\$free_mainframe_entry	2905	2896
pf\$free_object	2583	2568
pf\$free_permit_entry	2642	2482 2632
pf\$highest_cycle	2295	2301
pf\$internal_catalog	3039	2596
pf\$interstate_link_device	3088	2749
pf\$local_queue_device	3089	2751
pf\$log_change	2315	2328

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System Constant Management
search_value_table

IDENTIFIER	DEFINED ON LINE	REFERENCES
pfcs\$log_device	3090	2753
pfcs\$log_expansion_count	2649	2650
pfcs\$lowest_cycle	2295	2299
pfcs\$magnetic_tape_device	3091	2755
pfcs\$mass_storage_device	3092	2757
pfcs\$master_catalog_name_index	2283	2284
pfcs\$master_catalog_path_index	2127	2138
pfcs\$max_amd_size	2821	2824
pfcs\$maximum_archive_count	2784	2788 2789
pfcs\$maximum_catalog_depth	2140	2143 2144
pfcs\$maximum_cycle_count	3051	2694 3054
pfcs\$maximum_cycle_number	2277	2293
pfcs\$maximum_file_label_size	2867	2870
pfcs\$maximum_fmd_size	2843	2846
pfcs\$maximum_log_count	2651	2655 2656
pfcs\$maximum_object_count	2539	2544 2545
pfcs\$maximum_permit_count	2609	2615 2616
pfcs\$maximum_retention	2280	2310
pfcs\$member	2345	2376
pfcs\$memory_resident_device	3093	2761
pfcs\$minimum_cycle_number	2276	2293
pfcs\$minimum_retention	2279	2310
pfcs\$network_device	3094	2763
pfcs\$normal_cycle_entry	2732	2713
pfcs\$normal_log_entry	2682	2674
pfcs\$normal_mainframe_entry	2906	2898
pfcs\$normal_permit_entry	2642	2484 2634
pfcs>null_device	3095	2765
pfcs\$password_change	2314	2322
pfcs\$permit_expansion_count	2610	2611
pfcs\$pf_name_change	2314	2320
pfcs\$pipeline_device	3098	2767
pfcs\$project	2345	2359
pfcs\$public	2344	2348
pfcs\$purged_catalog_object	2585	2576
pfcs\$purged_cycle_entry	2733	2713
pfcs\$purged_file_object	2584	2570
pfcs\$read	2393	2396 2399
pfcs\$retention_change	2315	2326
pfcs\$rhfam_device	3097	2769
pfcs\$set_path_index	2135	2136
pfcs\$specific_cycle	2296	2303
pfcs\$terminal_device	3098	2771
pfcs\$user	2345	2365
pfcs\$user_account	2345	2370
pfts\$access_count	3034	2677 3032
pfts\$amd	3013	2830 3015
pfts\$amd_locator	2833	2812
pfts\$amd_size	2824	2834
pfts\$application_info	2391	2488 2638
pfts\$archive_count	2788	2799

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System Constant Management
search_value_table

IDENTIFIER	DEFINED ON LINE	REFERENCES
pfts\$archive_entry	2803	2795
pfts\$archive_entry_version	2978	2804
pfts\$archive_identification	2984	2806
pfts\$archive_list	2787	2790
pfts\$archive_list_locator	2798	2722
pfts\$archive_media_identifier	2989	2986
pfts\$attach_status	2735	2717
pfts\$attached_pf_entry	1977	1975
pfts\$attached_pf_entry_type	1972	1964
pfts\$attached_pf_header	1983	1961
pfts\$attached_pf_recovery_state	2414	1992
pfts\$attached_pf_table	1961	1960
pfts\$catalog_file	2510	2515 2923
pfts\$catalog_header	2524	2519
pfts\$catalog_heap	2531	2512 2532
pfts\$catalog_object_locator	2594	2577
pfts\$catalog_types	3039	2595
pfts\$change_descriptor	2318	2317
pfts\$change_type	2314	2319
pfts\$change_id	3019	2565
pfts\$checksum	3044	2518 2550 2621 2661 2699 2794 2829 2853 2875 2890 3047
pfts\$complete_path	2146	2147
pfts\$cycle_count	3054	2704
pfts\$cycle_device_information	2741	2727
pfts\$cycle_entry	2708	2700
pfts\$cycle_entry_types	2732	2710
pfts\$cycle_list	2693	2695
pfts\$cycle_list_locator	2703	2574
pfts\$cycle_number	2293	1987 2304 2325 2678 2714
pfts\$cycle_options	2295	2298
pfts\$cycle_statistics	3028	2715
pfts\$data_residence	3077	2723
pfts\$device_class	3083	2746
pfts\$file_label_locator	2878	2721
pfts\$file_label_size	2870	2850
pfts\$fmd	2848	2849 2854
pfts\$fmd_locator	2857	2599 2719
pfts\$fmd_size	2846	2858
pfts\$group	2346	2635
pfts\$group_types	2344	2347 2485
pfts\$internal_catalog_name	2164	2165 2463 2464 3111 3112
pfts\$internal_cycle_path	2152	1988 2157
pfts\$internal_name	2163	2149 2153 2164 2563 2709
pfts\$internal_path	2149	2150 2154
pfts\$log	2312	2329 2572
pfts\$log_count	2656	2666
pfts\$log_entry	2670	2662
pfts\$log_entry_types	2682	2671
pfts\$log_list	2654	2657
pfts\$log_list_locator	2665	2573

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System Constant Management
search_value_table

IDENTIFIER	DEFINED ON LINE	REFERENCES						
pft\$mainframe_count	2913	2909						
pft\$mainframe_entry_type	2905	2895						
pft\$mainframe_list_locator	2908	2725						
pft\$mainframe_usage_entry	2894	2724	2891					
pft\$name	2289	2146	2291	2308	2321	2462	2526	2562 3110
pft\$object_count	2545	2555	2557	2558				
pft\$object_entry	2551	2551						
pft\$object_list	2543	2546						
pft\$object_list_locator	2554	2527	2597					
pft\$object_types	2583	2567	2587					
pft\$sp_attached_pf_entry	1975	1966						
pft\$sp_attached_pf_table	1960	1833						
pft\$sp_complete_path	2147	1986						
pft\$sp_queued_catalog	2492	1832	2457	2458				
pft\$sp_queued_catalog_table	2494	1831						
pft\$sp_queued_internal_catalog	3107	2468	3116					
pft\$password	2308	2323	2571					
pft\$path	2291	2159						
pft\$permit_count	2616	2626						
pft\$permit_entry	2630	2622	3115					
pft\$permit_entry_types	2642	2481	2631					
pft\$permit_list	2614	2617						
pft\$permit_list_locator	2625	2564						
pft\$permit_options	2393	2340						
pft\$permit_selections	2340	2486	2636					
pft\$physical_and	2828	2826						
pft\$physical_archive	2793	2787	2791					
pft\$physical_catalog_header	2517	2511	2522					
pft\$physical_cycle	2698	2693	2696					
pft\$physical_file_label	2874	2872						
pft\$physical_fmd	2852	2850						
pft\$physical_log	2860	2854	2858					
pft\$physical_mainframe_usage	2889	2916						
pft\$physical_object	2549	2543	2547					
pft\$physical_permit	2620	2614	2618					
pft\$queued_catalog	2456	2492	2494					
pft\$queued_internal_catalog	3109	3107						
pft\$queued_permit_entry	2480	2470						
pft\$relative_cell_pointer	2923	2556	2627	2667	2705	2800	2835	2859 2881
		2910						
pft\$release_candidate	2994	2811						
pft\$retention	2310	2327						
pft\$retrieval_status	3003	2809						
pft\$sequence	2925	2927	2930					
pft\$sequence_record	2929	2933						
pft\$sfid_status	1991	1978						
pft\$share_options	2399	2341	2400	2738				
pft\$share_requirements	2341	2487	2637					
pft\$share_selections	2400	1982						
pft\$usage_count	2777	2736	2737	2738	2900	2901		
pft\$usage_options	2396	2397	2737					

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

System Constant Management
search_value_table

IDENTIFIER	DEFINED ON LINE	REFERENCES						
pft\$usage_selections	2397	1981						
pfv\$binary_catalog_search	4854	5155						
pmc\$mainframe_id_size	3143	3140						
pmc\$processor_model_number_size	3151	3143	3148					
pmc\$processor_model_type_size	4632	4629						
pmc\$processor_serial_num_size	3201	3144	3198					
pmt\$binary_mainframe_id	3101	1768	2899	3268				
pmt\$cpu_model_number	2251	2240	2247					
pmt\$cpu_serial_number	2254	2241	2246					
pmt\$mainframe_id	3140	1769						
pmt\$processor_model_number	3148	4612						
pmt\$processor_model_type	4629	4613	4614					
pmt\$sense_switches	4135	3279						
pmt\$vector_capability	4686	4619						
pmv\$constraint_neape_segments	4856	5137						
pmv\$debug_logging_enabled	4855	5136						
pmv\$quantum	4688	5263/M						
pva	4472	5272/M	5275/M	5276/M				
quantum	4615	5263						
rav\$deadstart_intervention	4857	4996						
rav\$development_deadstart	4858	5018						
rav\$network_activation	4859	5085						
rav\$system_activation	4860	5138						
search_value_table	5350	5214	5300	5382				
sft\$counter	3817	3280	3282	3283	3285	3786	3787	
svf\$dynamic_file_space_limits	4947	5023						
status	5174	5176/M						
status	5185	5214/P	5215					
status	5244	5246/M						
status	5294	5300/P	5301	5305/P	5310/P	5332/P	5333/P	
status	5353	5377/P						
stt\$set_name	2422	2461						
syc\$min_ecc	1570	1571	1572	1573	1574	1575		
syc\$min_ecc_commands	1572	1580	1583	1586	1589	1592	1595	1598 1601
		1604	1607	1610				
sy\$not_changeable_after_ds	1607	5304/P						
sy\$range_error	1592	5310/P						
sy\$unknown_parameter_name	1601	5377/P						
syp\$fetch_system_const_from_ssr	5173	5177						
syp\$fetch_system_constant	5181	5239						
syp\$save_system_const_in_ssr	5243	5247						
syp\$set_processor_attributes	5251	5286						
syp\$store_system_constant	5290	5346						
syt\$debug_output_disposition	4728	4721						
syt\$dfit_debug_output_disposal	4716	4711						
svy\$allow_jr_test	4948	4987						
svy\$clone_enabled	4867	4993						
svy\$debug_job_recovery	4949	5013						

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

System Constant Management
search_value_table

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

svv\$debugger_page_wait_lines	4707		5151							
svv\$dfit_debug_output_disposal	4711		5153							
svv\$enable_fault_injection	4737		5025							
svv\$enable_heap_trace	4701		5026							
svv\$halt_on_exit_with_io	4861		5156							
svv\$job_recovery_option	4739		5048							
svv\$mandatory_dualstate	4754		5054							
svv\$max_debug_output_lines	4758		5130							
svv\$nosve_internal_operations	4862		5086							
svv\$system_job_multiprocessing	4950		5111							
svv\$user_templates	4864		5139							
svv\$verify_heap_linkage	4762		5119							
tab_p	5188	5214/P	5216	5218	5220	5222	5224			
tab_p	5297	5300/P	5303	5309	5309	5313	5315/M	5317/M	5319/M	
		5322/M	5324/M							
tab_p	5352	5381/M								
table_i	5359	5361/M	5370/S	5370/S	5371/M	5371	5372	5381/S		
temp_status	5189	5232/P	5233	5237/P						
temp_status	5298	5338/P	5339	5343/P						
temp_workaround_min_sws	4970	5077	5191/M	5312/M	5327					
tick_time	4616	5264								
tmt\$task_queue_link	4076	4045								
tmv\$cycle_delay_time	4766	4995								
tmv\$dedicate_a_cpu_to_nos	4770	5014								
tmv\$display_actual_priority	4865	5146								
tmv\$halt_on_hung_task	4774	5040								
tmv\$long_wait_force_swap_time	4851	5053								
tmv\$long_wait_swap_time	4852	5052								
tmv\$system_debug_ring	4775	5107								
tmv\$system_debug_segment	4776	5108								
tmv\$system_error_hang_count	4777	5109								
tmv\$timed_wait_not_queued	4784	5147								
tos_registers	4461	5272/M	5275/M	5278/M						
v_boolean	4795	4802	4977	4978	4987	4988	4989	4993	4994	
		4996	5013	5014	5017	5018	5019	5021	5022	
		5023	5024	5025	5026	5027	5028	5029	5030	
		5031	5032	5034	5035	5036	5037	5038	5040	
		5041	5042	5043	5044	5046	5047	5051	5054	
		5084	5085	5086	5087	5088	5090	5091	5093	
		5096	5097	5098	5099	5102	5106	5111	5112	
		5113	5115	5116	5117	5119	5120	5121	5123	
		5125	5131	5132	5133	5134	5135	5136	5137	
		5138	5139	5144	5145	5146	5148	5149	5152	
		5154	5155	5156	5157	5158	5159	5160	5161	
		5162	5163	5164	5165	5166	5167	5203	5223	
		5320								
v_byte	4795	4800	4976	4981	4982	5039	5110	5118	5153	
		5201	5221	5318						
v_halfword	4795	4798	5056	5059	5060	5071	5076	5094	5095	

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

System Constant Management
search_value_table

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

v_integer	4795	5105	5108	5124	5126	5128	5199	5219	5316	
		4796	4975	4979	4980	4983	4984	4985	4986	
		4990	4995	4997	4998	4999	5000	5001	5002	
		5003	5004	5005	5006	5007	5008	5009	5010	
		5011	5012	5015	5016	5020	5033	5045	5048	
		5049	5050	5052	5053	5055	5057	5058	5061	
		5062	5063	5068	5069	5070	5072	5073	5074	
		5075	5081	5082	5083	5089	5092	5100	5101	
		5103	5104	5107	5108	5114	5122	5130	5140	
		5142	5147	5150	5151	5197	5217	5314		
value	5184	5198/M	5200/M	5202/M	5204/M	5218/M	5220/M	5222/M	5224/M	
		5232/P	5237/P							
value	5293	5309	5309	5315	5317	5319	5321	5338/P	5343/P	
value_table	4974	5194	5196	5198	5200	5202	5204	5370	5370	
		5381								
value_table_length	4958	4974	5193	5207	5372					
value_table_type	4790	4974	5188	5297	5352					
value_type	4795	5196	5216	5313						

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

NOS/VE Task Management : dispatcher

```

3 MODULE tmm$dispatcher;
4
5
6 {
7 { PURPOSE:
8 {   This module is used for managing the creation and deletion of tasks,
9 {   the creation and deletion of jobs, the status of tasks, and the
10 {   selection of tasks to execute.
11 {
12 {
13 ?VAR
14   debug: boolean := FALSE?;

O 5233
O 5234 PROCEDURE [INLINE] gfp$mr_get_fde_p (sfid: gft$system_file_identifier;
O 5235   ijle_p: ^jmt$initiated_job_list_entry;
O 5236   VAR fde_p: gft$file_desc_entry_p);
O 5237
O 5238
O 5285
O 5286 PROCEDURE [INLINE] gfp$mr_get_locked_fde_p (sfid: gft$system_file_identifier;
O 5287   ijle_p: ^jmt$initiated_job_list_entry;
O 5288   VAR fde_p: gft$locked_file_desc_entry_p);
O 5289
O 5290
O 5351
O 5352 { PURPOSE:
O 5353 {   This is the monitor mode procedure to change the entry status of a job. The caller
O 5354 {   of procedure must set the PTL lock if the entry status change is a SWAPPED/NOT SWAPPED
O 5355 {   transition because the swapped job counts will be changed.
O 5356
O 5357 PROCEDURE [INLINE] jmp$change_ijl_entry_status
O 5358 (
O 5359   ijle_p: ^jmt$initiated_job_list_entry;
O 5360   new_entry_status: jmt$ijl_entry_status);
O 5361
O 5362   VAR
O 5363     old_entry_status: jmt$ijl_entry_status;
O 5364     old_entry_status := ijle_p^.entry_status;
O 5365
O 5366   jmv$ijl_entry_status_statistics [old_entry_status] [new_entry_status] :=
O 5367     jmv$ijl_entry_status_statistics [old_entry_status] [new_entry_status] + 1;
O 5368
O 5369   ijle_p^.entry_status := new_entry_status;
O 5370
O 5371   IF (old_entry_status <= jmc$ies_swapin_in_progress) AND
O 5372     (new_entry_status > jmc$ies_swapin_in_progress) THEN
O 5373     jmp$increment_swapped_job_count (ijle_p);
O 5374
O 5375   ELSEIF (old_entry_status > jmc$ies_swapin_in_progress) AND
O 5376     (new_entry_status <= jmc$ies_swapin_in_progress) THEN
O 5377     jmp$decrement_swapped_job_count (ijle_p);
O 5378   IFEND;
O 5379

```

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NOS/VE Task Management : dispatcher

```

O 5380 PROCEND jmp$change_ijl_entry_status;
O 5381 PROCEDURE [XREF] jmp$decrement_swapped_job_count (ijle_p: ^jmt$initiated_job_list_entry);
O 5382
O 5385 PROCEDURE [inline] jmp$get_ijle_p (ijl_ordinal: jmt$ijl_ordinal;
O 5386   VAR ijle_p: ^jmt$initiated_job_list_entry);
O 5387
O 5395
O 5396 PROCEDURE [XREF] jmp$increment_swapped_job_count (ijle_p: ^jmt$initiated_job_list_entry);
O 5397
O 5400 PROCEDURE [XREF] jmp$ready_task_in_swapped_job (ijl_ord: jmt$ijl_ordinal;
O 5401   ijle_p: ^jmt$initiated_job_list_entry);
O 5402
O 5405
O 5406 PROCEDURE [XREF] jmp$set_job_terminated
O 5407 (
O 5408   ijle_p: ^jmt$initiated_job_list_entry);
O 5409
O 5412
O 5413 PROCEDURE [XREF] jmp$set_scheduler_event (event: jmt$job_scheduler_events);
O 5414
O 5441
O 5442 PROCEDURE [XREF] jmp$set_swapout_candidate
O 5443 (
O 5444   ajl_ordinal: jmt$ajl_ordinal;
O 5445   swapout_reason: jmt$swapout_reason);
O 5448
O 5449 PROCEDURE [XREF] jmp$ subsystem_priority_change
O 5450 (
O 5451   ijle_p: ^jmt$initiated_job_list_entry);
O 5454
O 5455 PROCEDURE [XREF] jmp$swap_non_dispatchable_job
O 5456 (
O 5457   ajl_ordinal: jmt$ajl_ordinal);
O 5460 PROCEDURE [XREF] jmp$assign_ajl_entry (asid: ost$asid,
O 5461   ijle_o: jmt$ijl_ordinal;
O 5462   caller: 0 .. 10(16);
O 5463   must_assign: boolean;
O 5464   VAR ajl_o: jmt$ajl_ordinal;
O 5465   VAR status: svt$monitor_status);
O 5466
O 5473 PROCEDURE [XREF] jmp$free_ajl_entry
O 5474 (
O 5475   ijle_p: ^jmt$initiated_job_list_entry;
O 5476   caller: 0 .. 10(16));
O 5479
O 5480 PROCEDURE [XREF] jmp$free_ajl_with_lock
O 5481 (
O 5482   ijle_p: ^jmt$initiated_job_list_entry;
O 5483   caller: 0 .. 10(16));
O 5486
O 5487 PROCEDURE [INLINE] jmp$unlock_ajl_with_lock
O 5488 (
O 5489   ijle_p: ^jmt$initiated_job_list_entry);
O 5489
O 5490   VAR
O 5491     ajlo: jmt$ajl_ordinal;
O 5492

```

NOS/VE Task Management : dispatcher

```

0 5493      ajlo := ijle_p^ajl_ordinal;
0 5494      IF {jmv$ajl_p^ [ajlo].in_use = jmc$lock_ajl} THEN
0 5495      jmp$free_ajl_with_lock (ijle_p, jmc$lock_ajl);
0 5496      ELSE
0 5497      jmv$ajl_p^ [ajlo].in_use := jmv$ajl_p^ [ajlo].in_use - jmc$lock_ajl;
0 5498      IFEND;
0 5499
0 5500      PROCEND jmp$unlock_ajl_with_lock;
0 5501
0 5505
0 5506      PROCEDURE [XREF] jmp$update_service_class_stats
0 5507      (
0 5508      ijle_p: ^ajm$initiated_job_list_entry);
0 5511      PROCEDURE [XREF] jsp$idle_tasks_complete (ijl_ordinal: jmt$ijl_ordinal);
0 5512
0 5515
0 5516      PROCEDURE [XREF] jsp$long_wait_aging (ijle_p: ^ajm$initiated_job_list_entry);
0 5517
0 5520
0 5521      PROCEDURE [XREF] jsp$relink_swap_queue (ijl_ordinal: jmt$ijl_ordinal;
0 5522      ijle_p: ^ajm$initiated_job_list_entry;
0 5523      new_queue: jst$ijl_swap_queue_id);
0 5524
0 5527      PROCEDURE [XREF] mtp$interrupt_processor (port_mask: ost$cpu_memory_port_mask);
0 5528
0 5529
0 5532 {REFERENCED BY ASSEMBLY CODE ALSO
0 5533
0 5534      VAR
0 5535      mtv$mli_status: [XREF] record
0 5536      ready: boolean,
0 5537      wait_inhibit: boolean,
0 5538      recend;
0 5539
0 5540      PROCEDURE [XREF] mmp$create_task (parent_xcb_p: ^ost$execution_control_block;
0 5541      xcb_p: ^ost$execution_control_block;
0 5542      ijle_p: ^ajm$initiated_job_list_entry);
0 5543
0 5546
0 5547      PROCEDURE [XREF] mmp$exit_task (xcb_p: ^ost$execution_control_block);
0 5548
0 5551
0 5552      PROCEDURE [XREF] mmp$create_job (new_job_ajl_ordinal: jmt$ajl_ordinal;
0 5553      xcb_segnum_relative_jobs_as: ost$segment;
0 5554      parent_xcb_p: ^ost$execution_control_block;
0 5555      xcb_p: ^ost$execution_control_block);
0 5556
0 5560
0 5561      PROCEDURE [XREF] mmp$exit_job (xcb_p: ^ost$execution_control_block);
0 5562
0 5565
0 5566      FUNCTION [INLINE] mmp$get_sdtx_entry_p
0 5567      (
0 5568      xcb_p: ^ost$execution_control_block;
0 5569      segnum: ost$segment): ^amnt$segment_descriptor_extended;
0 5570
0 5570      mmp$get_sdtx_entry_p := #address (1, #segment (xcb_p),

```

SOURCE LIST OF tmm\$dispatcher

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NOS/VE Task Management : dispatcher

```

0 5571      #SIZE (mmt$segment_descriptor_extended) * segnum + xcb_p^.sdtx_offset);
0 5572
0 5573      FUNCEND;
0 5574
0 5577
0 5578
0 5579      PROCEDURE [INLINE] i#disable_traps (VAR old_te: 0 .. 3);
0 5598
0 5599      PROCEDURE [INLINE] i#move (source: Acell;
0 5600      dest: Acell;
0 5601      length: 0 .. 7fffffff(16));
0 5602
0 5603      VAR
0 5604      str1: Astring (65535),
0 5605      str2: Astring (65535);
0 5606
0 5608      IF length <> 0 THEN
0 5609      str1 := source;
0 5610      str2 := dest;
0 5611      str2^ (1, length) := str1^ (1, length);
0 5612      #SPOIL (str2^);
0 5613      IFEND;
0 5615      PROCEND i#move;
0 5616
0 5617      PROCEDURE [INLINE] i#restore_traps (old_te: 0 .. 3);
0 5633      PROCEDURE [INLINE] mtp$ost_p (VAR ost_p: ^ost$cpu_state_table);
0 5644
0 5645      PROCEDURE [INLINE] mtp$set_status_abnormal (identifier: string (2);
0 5646      condition: osc$max_status_condition_number + 1 .. 0xffffffff(16);
0 5647      VAR status: syst$monitor_status);
0 5648
0 5658
0 5659      PROCEDURE [XREF] osp$update_job_keypoint_mask
0 5660      (
0 5661      ijle_p: ^ajm$initiated_job_list_entry;
0 5662      ijl_ordinal: jmt$ijl_ordinal);
0 5663 {
0 5664 {
0 5665 {
0 5666 {
0 5667
0 5668      PROCEDURE [INLINE] tmp$check_pt1_lock
0 5669      (VAR pt1_lock_set: boolean);
0 5670
0 5671      pt1_lock_set := tmv$pt1_lock.locked;
0 5672
0 5673      PROCEND tmp$check_pt1_lock;
0 5674
0 5675
0 5676      PROCEDURE [INLINE] tmp$get_xcb_access_status
0 5677      (
0 5678      ijle_p: ^ajm$initiated_job_list_entry;
0 5679      ijl_ordinal: jmt$ijl_ordinal;
0 5679      VAR inhibit_access: boolean);
0 5680
0 5742
0 5743

```

NDS/VE Task Management : dispatcher

```

o 5744 PROCEDURE [XREF] tmp$set_system_flag (task_id [input] : ost$global_task_id;
o 5745     flag_id [input] : ost$system_flag;
o 5746     VAR status [output] : syt$monitor_status);
o 5747
o 5750
o 5751 PROCEDURE [XREF] tmp$set_monitor_flag (task_id: ost$global_task_id;
o 5752     flag_id: syt$monitor_flag;
o 5753     VAR status: syt$monitor_status);
o 5756
o 5757 PROCEDURE [XREF] tmp$send_signal (taskid: ost$global_task_id;
o 5758     signal: pmt$signal;
o 5759     VAR status: syt$monitor_status);
o 5760
o 5763
o 5764 PROCEDURE [XREF] tmp$update_system_task_list (xcb_p: ^ost$execution_control_block);
o 5765

5767 [Define global and external variables.
5768 [Define SMU Communications Block (SCB).
5769
5770 VAR
5771     mtv$scb: [XREF] mtt$smu_communications_block;
o 5916
o 5917 VAR
o 5918     tmv$null_global_task_id: [READ] ost$global_task_id := [0, 0];
o 5919
o 5920 [Monitor segment table.]
o 5921
o 5922 VAR
o 5923     mtv$monitor_segment_table: [XREF] record
o 5924     st: ALIGNED [0 MOD 8] array [0 .. 4095] of mmt$segment_descriptor,
o 5925     recnd;
o 5926
o 5929
o 5930 VAR
o 5931     osv$cpus_logically_on: [XREF] 0 .. osc$max_number_of_processors;
o 5932
o 5935
o 5936 VAR
o 5937     osv$cpus_physically_configured: [XREF] 1 .. osc$max_number_of_processors;
o 5938
o 5941 VAR
o 5942     osv$keypoint_control: [XREF] ost$keypoint_control;
o 5943
o 6171
o 6172 VAR
o 6173     jmv$idle_dispatching_controls: [XREF] jmt$idle_dispatching_controls;
o 6174
o 6194
o 6195 VAR
o 6196     jmv$ijl_entry_status_statistics: [XREF] jmt$ijl_entry_status_statistics;
o 6197
o 6207
o 6208 VAR

```

SOURCE LIST OF tmm\$dispatcher

NDS/VE Task Management : dispatcher

```

o 6209     jmv$job_counts: [XREF] jmt$job_counts;
o 6210
o 6256 VAR
o 6257     jmv$service_classes: [XREF, oss$mainframe_wired]
o 6258     array [jmt$service_class_index] of ^jmt$service_class_entry;
o 6259
o 6305
o 6306
o 6307 VAR
o 6308     jmv$null_ijl_ordinal: [XREF] jmt$ijl_ordinal;
o 6309
o 6312
o 6313 VAR
o 6314     jmv$subsystem_priority_changes: [XREF] PACKED ARRAY [jmt$service_class_index] of boolean;
o 6315
o 6318 [Pointer to the Active Segment Table - (AST).]
o 6319
o 6320 VAR
o 6321     mmv$sast_p: [XREF] ^mmt$active_segment_table;
o 6322
o 6325 VAR
o 6326     null_pva: 0 .. 0fffffffffff[16],
o 6327     osv$system_family_name: [XDCL, #GATE] ost$name := '$SYSTEM
o 6328     jmv$system_core_id: [XDCL, #GATE] ost$name,
o 6329     tmv$cpu_execution_statistics: [XDCL, #GATE] tmt$cpu_execution_statistics,
o 6330     tmv$dct_priority_integer: integer,
o 6331     tmv$dispatch_priority_integer: [XDCL, #GATE] ARRAY [jmt$dispatching_priority] of integer,
o 6332     tmv$dispatching_controls: [XDCL] tmt$dispatching_controls,
o 6333     tmv$dispatching_control_sets: [XDCL, #GATE] tmt$dispatching_control_sets,
o 6334     tmv$dispatching_control_time: [XDCL] tmt$dispatching_prio_controls,
o 6335     tmv$dual_state_dispatch_prio: [XDCL, #GATE] tmt$dual_state_dispatch_prio :=
o 6336     [[1, 8], [1, 8], [2, 8], [2, 8], [3, 8], [3, 8], [4, 8], [4, 8], [5, 8], [5, 8],
o 6337     [6, 8], [6, 8], [7, 8], [7, 8]],
o 6338     tmv$io_wait_task_count: 0 .. tmc$maximum_ptl := 0,
o 6339     tmv$multiple_cpus_active: [XDCL, #GATE] boolean := FALSE,
o 6340     tmv$swap_in_progress: [XDCL] integer := 0,
o 6341     tmv$time_to_call_dispatcher: [XDCL] integer := 0,
o 6342     tmv$timed_wait_not_queued: [XDCL, #GATE] integer := 60000000,
o 6343     tmv$timed_wait_queue: tmt$task_queue_link := [0, 0],
o 6344     tmv$total_task_count: [XDCL, #GATE] 0 .. tmc$maximum_ptl := 0,
o 6345     tmv$long_wait_swap_time: [XDCL, #GATE] integer := 600000,
o 6346     tmv$long_wait_force_swap_time: [XDCL, #GATE] integer := 600000,
o 6347     tmv$cycle_delay_time: [XDCL, #GATE] integer := 2000,
o 6348     mlv$ci70_rqst_blk: [XREF] mlt$ci170_rqst_blk,
o 6349     tmv$stables_initialized: [XDCL, #GATE] boolean := FALSE,
o 6350     syv$debug_control: [XDCL, #GATE] syt$debug_control,
o 6351     syv$all_jobs_selected_for_debug: [XDCL, #GATE] boolean := FALSE,
o 6352     tmv$ptl_lock: [XDCL] tmt$ptl_lock := [FALSE, 0],
o 6353     tmv$system_job_monitor_gtid: [XDCL] ost$global_task_id := [1, 1],
o 6354     job_time_zero: [XDCL] integer := 0,
o 6355     tmv$ptl_p: [XDCL, #GATE] ^tmt$primary_task_list,
o 6356     jmv$ajl_p: [XDCL, #GATE] ^jmt$active_job_list := NIL,
o 6357     jmv$ijl_p: [XDCL, #GATE] jmt$ijl_p := [NIL, 0, 0],
o 6358     jmv$ijl_size: [XDCL, #GATE] jmt$ijl_size := 0,
o 6359     jmv$swap_jobs_in_long_wait: [XDCL, #GATE] boolean := TRUE,
o 6360     jmv$system_ajl_ordinal: [XDCL, #GATE] jmt$ajl_ordinal := 0,

```

NDS/VE Task Management : dispatcher

```

o 6361 jmv$system_ijl_ordinal: [XDCL, #GATE] jmt$ijl_ordinal := [0, 0],
o 6362 osv$special_aam_trap: [XDCL, #GATE] boolean := FALSE,
o 6363 pmv$quantum: [XDCL, #GATE] integer := 5000,
o 6364 tmv$dedicate_a_cpu_to_nos: [XDCL, #GATE] boolean := FALSE,
o 6365 tmv$subsystem_prior_threshold: [XDCL] 0 .. Off(16) := 5,
o 6366 tmv$act: [XDCL, #GATE] tmt$dispatch_control_table;
o 6367
o 6368
o 6369 {Define types and variables for tracing CYCLE requests. Note that the code to trace CYCLE requests
o 6370 {is normally disabled and a special version of the system must be built to record information.
o 6371 ?VAR
o 6372 tmc$debug_cycle_requests: boolean := FALSE?;
o 6373
o 6374 TYPE
o 6375 cyctrace = record
o 6376 code: tmc$cycle_reason,
o 6377 status: tmt$task_status,
o 6378 p1: Acell,
o 6379 p2: Acell,
o 6380 gtid: ost$global_task_id,
o 6381 xtask: ost$task_index,
o 6382 time: 0 .. Offfffffffffff(16),
o 6383 p: ost$pva,
o 6384 utp: Acell,
o 6385 recend;
o 6386
o 6387 ?IF tmc$debug_cycle_requests THEN
o 6388
o 6389 VAR
o 6390 osv$debug: [XREF] integer,
o 6391 tmv$cycle_trace: [XDCL, #GATE] array [0 .. 10001] of cyctrace,
o 6392 ti: [STATIC] integer;
o 6393
o 6394 ?IFEND
o 6395
o 6396 TYPE
o 6397
o 6398 { Priority_mask and dp_trick_conversion are types for the variable that is
o 6399 { used to determine the priority to be considered for select_next_task.
o 6400 { #unchecked_conversion is used to "pull off" the leftmost bit set.
o 6401
o 6402 priority_mask = packed record
o 6403 fill: 0 .. Off(16),
o 6404 set_number: 0 .. Of(16),
o 6405 dp: jmt$dispatching_priority,
o 6406 fill2: 0 .. Offfffffffffff(16),
o 6407 recend,
o 6408
o 6409 dp_trick_conversion = record
o 6410 case 0..1 of
o 6411 = 0 =
o 6412 p: real,
o 6413 = 1 =
o 6414 dp_mask: priority_mask,
o 6415 caseEnd,
o 6416 recend;

```

NDS/VE Task Management : dispatcher

```

6417
6418

```

NDS/VE Task Management : dispatcher

```

6420
6421 PROCEDURE [INLINE] tmp$set_lock (VAR lock: tmt$pt1_lock);
6422
6423 VAR
6424   b: boolean;
6425   bc: integer;
6426
6427 IF osv$cpus_logically_on > 1 THEN
6428   bc := #read_register (osc$pr_base_constant);
6429   IF lock.id <> bc THEN
6430     REPEAT
6431       #TEST_SET (lock.locked, b);
6432     UNTIL NOT b;
6433     lock.id := bc;
6434   ELSE
6435     lock.count := lock.count + 1;
6436   IFEND;
6437 IFEND;
6438
6439 PROCEND tmp$set_lock;
6440
O 6443
O 6444 PROCEDURE [INLINE] tmp$clear_lock (VAR lock: tmt$pt1_lock);
O 6445
O 6446   IF osv$cpus_logically_on > 1 THEN
O 6447     IF lock.id <> #READ_REGISTER (osc$pr_base_constant) THEN
O 6448       i#program_error; [interlock failure - no message passed for performance reasons]
O 6449     IFEND;
O 6450     IF lock.count > 0 THEN
O 6451       lock.count := lock.count - 1;
O 6452     ELSE
O 6453       lock.clear := 0;
O 6454     IFEND;
O 6455   IFEND;
O 6456
O 6457 PROCEND tmp$clear_lock;
O 6458
O 6461

```

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[XDCL, INLINE] tmp\$check_taskid, tmp\$check_taskid_with_lock_set

```

O 6463
O 6464 PROCEDURE [XDCL, INLINE] tmp$check_taskid
4 6465   ( taskid: ost$global_task_id;
4 6466   option: tmt$option;
4 6467   VAR status: syt$monitor_status);
4 6468
4 6469
4 6470 {
4 6471 { The purpose of this procedure is to check for a valid taskid.
4 6472 { Depending on the option selected, this module either halts the
4 6473 { system or returns the status in case an error condition is
4 6474 { encountered.
4 6475 { The callers of this procedure DO NOT set the pt1 lock. This
4 6476 { procedure should be used by callers outside of the 'TM' decks.
4 6477 { 'TM' callers should use TMP$CHECK_TASKID_WITH_LOCK_SET.
4 6478 {
4 6479 {   TMP$CHECK_TASKID (TASKID, OPTION, STATUS)
4 6480 {
4 6481 { TASKID: (INPUT) This parameter specifies the taskid.
4 6482 {
4 6483 { OPTION: (INPUT) This parameter specifies the option to
4 6484 { halt the system or to return an error status.
4 6485 {
4 6486 { STATUS: (OUTPUT) This parameter specifies the error status.
4 6487 {
4 6488   status.normal := TRUE;
4 6489   tmp$set_lock (tmv$pt1_lock);
4A 6491   IF (taskid = tmv$null_global_task_id) OR (taskid.index > UPPERBOUND (tmv$pt1_p^)) OR
8C 6492     (tmv$pt1_p^ [taskid.index].sequence_number <> taskid.seqno) OR
8C 6493     (tmv$pt1_p^ [taskid.index].status = tmc$ts_null) THEN
8C 6494     tmp$clear_lock (tmv$pt1_lock);
C2 6495     IF option = tmc$opt_return THEN
CA 6496       mtp$set_status_abnormal ('TM', tme$invalid_global_taskid, status);
CA 6497     RETURN;
E2 6498     ELSE
E2 6499       mtp$error_stop ('TM01 - taskid error');
104 6500     IFEND;
104 6501   IFEND;
104 6502   tmp$clear_lock (tmv$pt1_lock);
138 6503 PROCEND tmp$check_taskid;

O 6505 PROCEDURE [XDCL, INLINE] tmp$check_taskid_with_lock_set
4 6506   ( taskid: ost$global_task_id;
4 6507   option: tmt$option;
4 6508   VAR status: syt$monitor_status);
4 6509
4 6510 {
4 6511 { The purpose of this procedure is to check for a valid taskid.
4 6512 { Depending on the option selected, this module either halts the
4 6513 { system or returns the status in case an error condition is
4 6514 { encountered.
4 6515 { The callers of this procedure MUST set the pt1 lock. Callers
4 6516 { outside of the 'TM' decks should use TMP$CHECK_TASKID, which

```


[XDCL, INLINE] tmp\$check_taskid, tmp\$check_taskid_with_lock_set

```

4 6517 { does the ptl lock management.
4 6518 {
4 6519 {   TMP$CHECK_TASKID_WITH_LOCK_SET (TASKID, OPTION, STATUS)
4 6520 {
4 6521 {   TASKID: (INPUT) This parameter specifies the taskid.
4 6522 {
4 6523 {   OPTION: (INPUT) This parameter specifies the option to
4 6524 {             halt the system or to return an error status.
4 6525 {
4 6526 {   STATUS: (OUTPUT) This parameter specifies the error status.
4 6527 {
4 6528 {
4 6529     status.normal := TRUE;
4 6530     IF (taskid = tmv$null_global_task_id) OR (taskid.index > UPPERBOUND (tmv$ptl_p^)) OR
50 6531         (tmv$ptl_p^ [taskid.index].sequence_number <> taskid.seqno) OR
50 6532         (tmv$ptl_p^ [taskid.index].status = tmc$ts_null) THEN
50 6533         IF option = tmc$opt_return THEN
58 6534             mtp$set_status_abnormal ('TM', tme$invalid_global_taskid, status);
58 6535         RETURN;
6C 6536     ELSE
6C 6537         mtp$error_stop ('TM01 - taskid error');
8C 6538     IFEND;
8C 6539     IFEND;
8C 6540 PROCEND tmp$check_taskid_with_lock_set;
O 6541

```

[INLINE] tmp\$stop_if_bad_taskid

```

O 6543
O 6544 PROCEDURE [INLINE] tmp$stop_if_bad_taskid
O 6545     ( taskid: ost$global_task_id);
O 6546
O 6547 { NOTE: The caller of this procedure MUST set the PTL lock.
O 6548
O 6549     IF (taskid = tmv$null_global_task_id) OR (taskid.index > UPPERBOUND (tmv$ptl_p^)) OR
O 6550         (tmv$ptl_p^ [taskid.index].sequence_number <> taskid.seqno) OR
O 6551         (tmv$ptl_p^ [taskid.index].status = tmc$ts_null) THEN
O 6552         mtp$error_stop ('TM01 - taskid error');
O 6553     IFEND;
O 6554
O 6555 PROCEND tmp$stop_if_bad_taskid;

```

TMP\$OBTAIN_IJL_ORDINAL_FOR_PTL

```

0 6557
0 6558 PROCEDURE [XDCL, INLINE] tmp$obtain_ijl_ordinal_from_ptl
4 6559 ( global_task_id: ost$global_task_id;
4 6560   VAR ijl_ordinal: jmt$ijl_ordinal);
4 6561
4 6562   ijl_ordinal := tmv$ptl_p^ [global_task_id.index].ijl_ordinal;
4 6563
4 6564 PROCEND tmp$obtain_ijl_ordinal_from_ptl;

```

ASSIGN_PTL, FREE_PTL

```

0 6566
0 6567 PROCEDURE [INLINE] tmp$assign_ptl
0 6568 (   xcb_p: ^ost$execution_control_block;
0 6569   ijl_ordinal: jmt$ijl_ordinal;
0 6570   VAR taskid: ost$global_task_id;
0 6571   VAR status: syt$monitor_status);
0 6572
0 6573 {
0 6574 { The purpose of this procedure is to assign PTL entries from
0 6575 { the free queue. An error status is returned if the PTL is full.
0 6576 {
0 6577 {   ASSIGN_PTL (XCB_P, JOB_ID, TASKID, STATUS)
0 6578 {
0 6579 {   XCB_P : (INPUT) This parameter specifies the pointer to XCB.
0 6580 {
0 6581 {   JOB_ID : (INPUT) This parameter specifies the ordinal of the
0 6582 { active job list entry for the job.
0 6583 {
0 6584 {   TASKID : (OUTPUT) This parameter specifies the taskid assigned
0 6585 { to the task.
0 6586 {
0 6587 {   STATUS : (OUTPUT) This parameter specifies the error status.
0 6588 {
0 6589 { NOTE : Anyone calling this procedure must have tmv$ptl_lock set.
0 6590 {
0 6591 {
0 6592 {   VAR
0 6593 {     ijle_p: ^jmt$initiated_job_list_entry,
0 6594 {     next_avail_ptlo: ost$task_index,
0 6595 {     ptl_p: ^tmt$primary_task_list_entry;
0 6596 {
0 6597 {
0 6598 {
0 6599 { Check if PTL space is available.
0 6600 {
0 6601 {   status.normal := TRUE;
0 6602 {   next_avail_ptlo := tmv$dct [jmc$null_dispatching_priority].queue_head;
0 6603 {   IF next_avail_ptlo = 0 THEN
0 6604 {     tmp$set_status_abnormal ('TM', tme$ptl_full, status);
0 6605 {     RETURN;
0 6606 {   IFEND;
0 6607 {   ptl_p := Atmv$ptl_p^ [next_avail_ptlo];
0 6608 {   tmv$dct [jmc$null_dispatching_priority].queue_head := ptl_p^.ptl_thread;
0 6609 {
0 6610 { If the job is selected for system breakpoints, set the flag for the task to set up debug registers.
0 6611 {
0 6612 {   jmp$get_ijle_p (ijl_ordinal, ijle_p);
0 6613 {   IF ijle_p^.system_breakpoint_selected THEN
0 6614 {     ptl_p^.monitor_flags := $syt$monitor_flags [syc$mf_system_debugger];
0 6615 {   ELSE
0 6616 {     ptl_p^.monitor_flags := $syt$monitor_flags [];
0 6617 {   IFEND;
0 6618 {
0 6619 { Initialize the PTL entry.
0 6620 {
0 6621 {   xcb_p^.keypoint_enable := FALSE;

```

ASSIGN_PTL, FREE_PTL

```

0 6622   xcb_pA.keypoint_register_enable := FALSE;
0 6623   pt1_pA.status := tmc$ts_ready;
0 6624   pt1_pA.i1_ordinal := i1_ordinal;
0 6625   pt1_pA.xcb_offset := #OFFSET(xcb_p);
0 6626   pt1_pA.dispatching_priority := xcb_pA.dispatching_priority;
0 6627   pt1_pA.sequence_number := pt1_pA.sequence_number MOD 255 + 1;
0 6628   pt1_pA.system_flags := $tmt$system_flags [1];
0 6629   pt1_pA.idle_status := tmc$is_not_idled;
0 6630   pt1_pA.queue_link_head := 0;
0 6631   pt1_pA.queue_link_tail := 0;
0 6632   pt1_pA.end_of_wait_time := 0;
0 6633   pt1_pA.readying_task_priority := 0;
0 6634   taskid.index := next_avail_ptlo;
0 6635   taskid.seqno := pt1_pA.sequence_number;
0 6636
0 6637   PROCEND tmp$assign_pt1;

0
0 6639
0 6640 { NOTE : Anyone calling this routine must have tmv$pt1_lock set.
0 6641 { The caller must also call the procedure remove_i1l to remove the task from the i1l_thread,
0 6642 { which links all tasks of a job, if the task being freed is NOT the job monitor task.
0 6643 { If all tasks are being freed, the caller can zero out the i1l_thread.
0 6644
0 6645   PROCEDURE [INLINE] free_pt1
0 6646   (
0 6647     ptlo: ost$task_index);
0 6648
0 6648   tmv$pt1_pA [ptlo].status := tmc$ts_null;
0 6649   tmv$pt1_pA [ptlo].dispatching_priority := jmc$null_dispatching_priority;
0 6650   tmv$pt1_pA [ptlo].pt1_thread := 0;
0 6651   IF tmv$dct [jmc$null_dispatching_priority].queue_head = 0 THEN
0 6652     tmv$dct [jmc$null_dispatching_priority].queue_head := ptlo;
0 6653   ELSE
0 6654     tmv$pt1_pA [tmv$dct [jmc$null_dispatching_priority].queue_tail].pt1_thread := ptlo;
0 6655   IFEND;
0 6656   tmv$dct [jmc$null_dispatching_priority].queue_tail := ptlo;
0 6657   PROCEND free_pt1;

```

REMOVE_TASK_FROM_DCT

```

0 6659
0 6660   PROCEDURE [INLINE] tmp$remove_task_from_dct
0 6661   (
0 6662     ptlo: ost$task_index);
0 6663
0 6664 { The purpose of this procedure is to remove the current task's
0 6665 { PTL entry from the DCT thread.
0 6666 {
0 6667 { REMOVE_TASK_FROM_DCT (PTLO)
0 6668 {
0 6669 { PTLO : (INPUT) This parameter specifies the PTL index of the
0 6670 { specified task.
0 6671 {
0 6672 { NOTE : Anyone calling this procedure must have tmv$pt1_lock set.
0 6673 {
0 6674
0 6675   VAR
0 6676     dcte: tmt$dct_entry,
0 6677     scan_ptlo: ost$task_index,
0 6678     save_ptlo: ost$task_index,
0 6679     ptle_p: Atmt$primary_task_list_entry;
0 6680
0 6681   ptle_p := Atmv$pt1_pA [ptlo];
0 6682   dcte := tmv$dct [ptle_pA.dispatching_priority];
0 6683
0 6684   IF dcte.queue_tail = dcte.queue_head THEN
0 6685     IF dcte.queue_head <> ptlo THEN
0 6686       mtp$error_stop ('TM--remove task from empty DCT');
0 6687     IFEND;
0 6688     dcte.queue_tail := 0;
0 6689     dcte.queue_head := 0;
0 6690     dcte.minor_priority := 0;
0 6691     dcte.major_priority := 0;
0 6692     tmv$dispatching_control_sets.ready_tasks := tmv$dispatching_control_sets.ready_tasks -
0 6693     $jmt$dispatching_priority_set [jmc$dp_conversion + ptle_pA.dispatching_priority];
0 6694     tmp$calculate_dct_priority_int;
0 6695   ELSEIF ptlo = dcte.queue_head THEN
0 6696     dcte.queue_head := ptle_pA.pt1_thread;
0 6697     IF ptlo = dcte.major_priority THEN
0 6698       dcte.major_priority := ptle_pA.pt1_thread;
0 6699     IFEND;
0 6700     IF ptlo = dcte.minor_priority THEN
0 6701       dcte.minor_priority := ptle_pA.pt1_thread;
0 6702     IFEND;
0 6703   ELSE
0 6704     scan_ptlo := dcte.queue_head;
0 6705     WHILE scan_ptlo <> ptlo DO
0 6706       save_ptlo := scan_ptlo;
0 6707       scan_ptlo := tmv$pt1_pA [scan_ptlo].pt1_thread;
0 6708       IF scan_ptlo = 0 THEN
0 6709         mtp$error_stop ('TM--cant find ptlo to remove from DCT');
0 6710       IFEND;
0 6711     WHILEND;
0 6712     tmv$pt1_pA [save_ptlo].pt1_thread := tmv$pt1_pA [ptlo].pt1_thread;
0 6713     IF ptlo = dcte.queue_tail THEN
0 6714       dcte.queue_tail := save_ptlo;

```

REMOVE_TASK_FROM_DCT

```

0 6715     IFEND;
0 6716     IF ptlo = dcte.major_priority THEN
0 6717         dcte.major_priority := save_ptlo;
0 6718     IFEND;
0 6719     IF ptlo = dcte.minor_priority THEN
0 6720         dcte.minor_priority := save_ptlo;
0 6721     IFEND;
0 6722     IFEND;
0 6723     ptle_p^.ptl_thread := 0;
0 6724     tmv$dct [ptle_p^.dispatching_priority] := dcte;
0 6725
0 6726     PROCEND tmp$remove_task_from_dct;
0 6727

```

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REMOVE_TASK_FROM_FREE_QUEUE

```

0 6729
0 6730     PROCEDURE [INLINE] remove_task_from_free_queue
0 6731     (
0 6732         ptlo: ost$task_index);
0 6733 {
0 6734 { The purpose of this procedure is to remove a task's PTL entry
0 6735 { from the free DCT thread.
0 6736 {
0 6737 {     REMOVE_TASK_FROM_FREE_QUEUE (PTLO)
0 6738 {
0 6739 {     PTLO : (INPUT) This parameter specifies the PTL index of the
0 6740 {             specified task.
0 6741 {
0 6742 {     NOTE : Anyone calling this procedure must have tmv$ptl_lock set.
0 6743 {
0 6744 {
0 6745     VAR
0 6746         dcte: tmt$dct_entry,
0 6747         save_ptlo: ost$task_index,
0 6748         scan_ptlo: ost$task_index,
0 6749         ptle_p: ^tmt$primary_task_list_entry;
0 6750
0 6751     ptle_p := ^tmv$ptl_p^ [ptlo];
0 6752     dcte := tmv$dct [jmc$null_dispatching_priority];
0 6753
0 6754     IF dcte.queue_tail = dcte.queue_head THEN
0 6755         IF dcte.queue_head <> ptlo THEN
0 6756             mtp$error_stop ('TM--task not in free queue');
0 6757         IFEND;
0 6758         dcte.queue_head := 0;
0 6759         dcte.queue_tail := 0;
0 6760     ELSEIF ptlo = dcte.queue_head THEN
0 6761         dcte.queue_head := ptle_p^.ptl_thread;
0 6762     ELSE
0 6763         scan_ptlo := dcte.queue_head;
0 6764         WHILE (scan_ptlo <> ptlo) AND (scan_ptlo <> 0) DO
0 6765             save_ptlo := scan_ptlo;
0 6766             scan_ptlo := tmv$ptl_p^ [scan_ptlo].ptl_thread;
0 6767         WHILEND;
0 6768         IF scan_ptlo = 0 THEN
0 6769             mtp$error_stop ('TM--task not in free queue');
0 6770         IFEND;
0 6771         tmv$ptl_p^ [save_ptlo].ptl_thread := tmv$ptl_p^ [ptlo].ptl_thread;
0 6772         IF ptlo = dcte.queue_tail THEN
0 6773             dcte.queue_tail := save_ptlo;
0 6774         IFEND;
0 6775     IFEND;
0 6776     tmv$dct [jmc$null_dispatching_priority] := dcte;
0 6777
0 6778     PROCEND remove_task_from_free_queue;
0 6779

```

[INLINE] tmp\$dct_ready_task

```

o 8781
o 8782 { PURPOSE:
o 8783 {   The purpose of this procedure is to pre-select a CPU for a ready task
o 8784 {   to execute on, or place the ready task into the dispatch_control_table (DCT).
o 8785 {
o 8786 { DESIGN:
o 8787 {   Integer representation of dispatching priorities:
o 8788 {   To accommodate dispatching allocation (guaranteeing minimum and maximum
o 8789 {   percentages of the total CPU time to specified user dispatching priorities),
o 8790 {   each dispatching priority falls into one of three sets. The sets are defined
o 8791 {   in one word of memory in the following order:
o 8792 {
o 8793 {   BYTE: 63.....0
o 8794 {   PRIORITY: 15 .. 0 15 .. 0 15 .. 0
o 8795 {
o 8796 {
o 8797 {
o 8798 {
o 8799 {
o 8800 {   SET 1: Tasks of priorities in this set have exceeded their maximum dispatching
o 8801 {   priority allocation.
o 8802 {   SET 2: Tasks of priorities in this set have achieved their minimum dispatching
o 8803 {   priority allocation, but have not exceeded their maximum allocation.
o 8804 {   SET 3: Tasks of priorities in this set have not yet achieved their minimum
o 8805 {   dispatching allocation.
o 8806 {
o 8807 {   When the word containing the dispatching priority sets is normalized and
o 8808 {   converted to an integer, any SET 3 task has a higher dispatching priority integer
o 8809 {   than any SET 2 task, which in turn has a higher dispatching priority integer
o 8810 {   than any SET 1 task. Thus, a SET 3-P4 task has a higher dispatching priority
o 8811 {   integer than a SET 2-P8 task.
o 8812 {
o 8813 {   NOTE: Sub-system and system priorities (P8..P14) are always placed in SET 3, so
o 8814 {   they will always have a higher dispatching priority integer value than
o 8815 {   any user dispatching priority.
o 8816 {
o 8817 {   Task pre-selection or placement in the DCT:
o 8818 {   Task pre-selection will be attempted only if the job with the ready task allows
o 8819 {   multiprocessing or has no other tasks currently executing. If the ready task
o 8820 {   is going through task_switch and is still ready (its time slice has expired),
o 8821 {   pre-selection will be attempted only if the job allows multiprocessing or has
o 8822 {   no other ready tasks. (This prevents a non-multiprocessing job with a CPU bound
o 8823 {   task from shutting out other tasks of the job.)
o 8824 {
o 8825 {   The ready task will be pre-selected to run on the first idle CPU that is found,
o 8826 {   or on the the lowest dispatching priority (integer) CPU currently executing, if
o 8827 {   the ready task has a higher dispatching priority integer. If the CPU that is
o 8828 {   pre-selected is currently executing another task, that CPU will be interrupted.
o 8829 {
o 8830 {   If the ready task cannot be pre-selected to execute on a CPU, the task is inserted
o 8831 {   into the DCT.
o 8832 {
o 8833 {   The dispatch_control_table (DCT):
o 8834 {   The DCT is an array of singularly linked lists. There is a list for each
o 8835 {   dispatching priority. There are four different pointers into each list:
o 8836 {   the head pointer, the tail pointer, the minor priority pointer, and the

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[INLINE] tmp\$dct_ready_task

```

o 8837 {   major priority pointer.
o 8838 {
o 8839 {   Each job class has a major and a minor timeslice. The major and minor
o 8840 {   timeslice of a job class can be modified by using the change_class_attribute
o 8841 {   command. When a task is initially dispatched, it is placed after the minor
o 8842 {   priority pointer in the DCT. "Ready tasks" are placed after the minor
o 8843 {   priority pointer as long as the minor timeslice in the tasks XCB is greater
o 8844 {   than one_eighth of the job class minor timeslice. Both the minor and the
o 8845 {   major timeslice in the XCB of a task are decremented by the amount of time that
o 8846 {   the task uses the CPU. Once the minor timeslice in the XCB is zero, the task
o 8847 {   is placed after the major priority pointer in the dispatch control table.
o 8848 {   The minor timeslice in the XCB is also reset. When a task finally uses all
o 8849 {   of its major timeslice, it is next dispatched at the tail of the DCT. Both
o 8850 {   the major and minor timeslice in the XCB are then reset.
o 8851 {
o 8852 {
o 8853 {   HEAD -----> TASK
o 8854 {   MINOR -----> TASK
o 8855 {   MAJOR -----> TASK
o 8856 {   TAIL -----> TASK
o 8857 {
o 8858 {
o 8859 {
o 8860 {
o 8861 {
o 8862 {
o 8863 { PROCEDURE [INLINE] tmp$dct_ready_task
o 8864 {   ( xcb_p: Aost$execution_control_block;
o 8865 {     jle_p: Ajmt$initiated_job_list_entry;
o 8866 {     ptlo: ost$task_index;
o 8867 {     attempt_preselection: boolean);
o 8868 {
o 8869 {   VAR
o 8870 {     cst_index: integer;
o 8871 {     cst_p: Aost$cpu_state_table;
o 8872 {     dcte: tmt$dct_entry;
o 8873 {     dct_placement: (minor_timeslice_insert, major_timeslice_insert, tail_timeslice_insert);
o 8874 {     insert_ptle_p: Atmt$primary_task_list_entry;
o 8875 {     insert_jle_p: Ajmt$initiated_job_list_entry;
o 8876 {     low_priority: integer;
o 8877 {     low_prio_csti: integer;
o 8878 {     ptle_p: Atmt$primary_task_list_entry;
o 8879 {     timeslice: jmt$time_slice_values;
o 8880 {
o 8881 {     ptle_p := Atmv$ptl_p^ [ptlo];
o 8882 {
o 8883 {   ?IF NOT debug THEN
o 8884 {     timeslice := jmv$service_classes [jle_p.job_scheduler_data.service_class]^ .attributes.
o 8885 {     dispatching_control [jle_p.dispatching_control.dispatching_control_index].dispatching_timeslice;
o 8886 {   ?ELSE
o 8887 {     timeslice.minor := 50;
o 8888 {     timeslice.major := 50;
o 8889 {   ?IFEND;
o 8890 {
o 8891 { Determine where the task is to be positioned in the DISPATCH_CONTROL_TABLE.
o 8892 {

```

[INLINE] tmp\$dct_ready_task

```

o 6893 IF xcb_pA.timeslice.minor > (timeslice.minor DIV 8) THEN
o 6894   dct_placement := minor_timeslice_insert;
o 6895 ELSEIF xcb_pA.timeslice.major > (timeslice.minor DIV 8) THEN
o 6896   dct_placement := major_timeslice_insert;
o 6897   xcb_pA.timeslice.minor := timeslice.minor;
o 6898 ELSE
o 6899   dct_placement := tail_timeslice_insert;
o 6900   xcb_pA.timeslice.major := timeslice.major;
o 6901   xcb_pA.timeslice.minor := timeslice.minor;
o 6902 IFEND;
o 6903
o 6904 { If a task has subsystem table locks set for more than the subsystem lock threshold, then
o 6905 { lower the tasks priority. If the task received a READY request while it had subsystem
o 6906 { locks set, the priority is lowered to the greater of the task's or the READING task's
o 6907 { dispatching priority.
o 6908
o 6909 IF (xcb_pA.system_table_lock_count > 0) THEN
o 6910 IF (xcb_pA.system_table_lock_count < osc$system_table_lock_set) THEN
o 6911 IF xcb_pA.subsystem_lock_priority_count >= tmv$subsystem_prior_threshold THEN
o 6912 IF (xcb_pA.dispatching_priority < ptle_pA.reading_task_priority) THEN
o 6913   ptle_pA.dispatching_priority := ptle_pA.reading_task_priority;
o 6914 ELSE
o 6915   ptle_pA.dispatching_priority := xcb_pA.dispatching_priority;
o 6916 IFEND;
o 6917   xcb_pA.subsystem_give_up_cpu := TRUE;
o 6918 ELSE
o 6919 IF ptle_pA.dispatching_priority < jmc$prior_subsystem_tbls_locked THEN
o 6920   ptle_pA.dispatching_priority := jmc$prior_subsystem_tbls_locked;
o 6921 IFEND;
o 6922 IF dct_placement <> minor_timeslice_insert THEN
o 6923   xcb_pA.subsystem_lock_priority_count := xcb_pA.subsystem_lock_priority_count + 1;
o 6924 IFEND;
o 6925   xcb_pA.subsystem_give_up_cpu := TRUE;
o 6926 IFEND;
o 6927 ELSE
o 6928   xcb_pA.system_give_up_cpu := TRUE;
o 6929 IF ptle_pA.dispatching_priority < jmc$prior_system_tbls_locked THEN
o 6930   ptle_pA.dispatching_priority := jmc$prior_system_tbls_locked;
o 6931 IFEND;
o 6932 IFEND;
o 6933 IFEND;
o 6934
o 6935 { Determine if the priority is high enough to preempt a task that is currently executing.
o 6936 { Send an interrupt to the processor executing the lowest priority task (or an idle processor).
o 6937
o 6938 IF attempt_preselection THEN
o 6939
o 6940   low_priority := tmv$dispatch_priority_integer [ptle_pA.dispatching_priority];
o 6941   mtp$cst_p [cst_p];
o 6942   cst_index := osv$cpus_physically_configured - 1;
o 6943   low_prio_csti := -1;
o 6944   /preselect_loop/
o 6945 BEGIN
o 6946 REPEAT
o 6947   IF (mtv$cst0 [cst_index].dispatching_priority_integer < low_priority)
o 6948   AND (cst_index IN xcb_pA.processor_selections) THEN

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[INLINE] tmp\$dct_ready_task

```

o 6949 IF tmv$dedicate_a_cpu_to_nos AND (cst_index = 0) THEN
o 6950   EXIT /preselect_loop/;
o 6951 IFEND;
o 6952   low_prio_csti := cst_index;
o 6953   low_priority := mtv$cst0 [cst_index].dispatching_priority_integer;
o 6954 IFEND;
o 6955   cst_index := cst_index - 1;
o 6956 UNTIL (cst_index < 0) OR (low_priority = 0);
o 6957 END /preselect_loop/;
o 6958
o 6959 IF low_prio_csti >= 0 THEN
o 6960 IF (mtv$cst0 [low_prio_csti].next_ptlo_to_dispatch <> 0) THEN
o 6961
o 6962 { A task has already been pre-selected to run on the pre-selected processor. The task must be inserted
o 6963 { at the head of its priority DCT queue.
o 6964
o 6965   insert_ptle_p := Atmv$ptl_pA [mtv$cst0 [low_prio_csti].next_ptlo_to_dispatch];
o 6966   jmp$get_ijle_p (insert_ptle_pA.ijl_ordinal, insert_ijle_p);
o 6967   insert_ijle_pA.executing_task_count := insert_ijle_pA.executing_task_count - 1;
o 6968   insert_ptle_pA.status := tmc$ts_ready;
o 6969   dcte := tmv$dct [insert_ptle_pA.dispatching_priority];
o 6970 IF dcte.queue_head = 0 THEN
o 6971   insert_ptle_pA.ptl_thread := 0;
o 6972   dcte.queue_head := mtv$cst0 [low_prio_csti].next_ptlo_to_dispatch;
o 6973   dcte.queue_tail := mtv$cst0 [low_prio_csti].next_ptlo_to_dispatch;
o 6974   dcte.minor_priority := mtv$cst0 [low_prio_csti].next_ptlo_to_dispatch;
o 6975   dcte.major_priority := mtv$cst0 [low_prio_csti].next_ptlo_to_dispatch;
o 6976   tmv$dispatching_control_sets.ready_tasks := tmv$dispatching_control_sets.ready_tasks +
o 6977   $jmt$dispatching_priority_set [jmc$dp_conversion -
o 6978   insert_ptle_pA.dispatching_priority];
o 6979   tmp$calculate_dct_priority_int;
o 6980 ELSE
o 6981   insert_ptle_pA.ptl_thread := dcte.queue_head;
o 6982   dcte.queue_head := mtv$cst0 [low_prio_csti].next_ptlo_to_dispatch;
o 6983 IFEND;
o 6984   tmv$dct [insert_ptle_pA.dispatching_priority] := dcte;
o 6985 IFEND;
o 6986
o 6987   mtv$cst0 [low_prio_csti].next_ptlo_to_dispatch := ptlo;
o 6988   mtv$cst0 [low_prio_csti].dispatching_priority_integer := tmv$dispatch_priority_integer
o 6989   [ptle_pA.dispatching_priority];
o 6990   mtv$cst0 [low_prio_csti].dispatch_control.call_dispatcher := TRUE;
o 6991   ptle_pA.status := tmc$ts_ready_and_selected;
o 6992   ijle_pA.executing_task_count := ijle_pA.executing_task_count + 1;
o 6993 IF (low_prio_csti <> cst_pA.cst_index) AND ((low_priority < 0) OR
o 6994 (mtv$cst0 [low_prio_csti].dual_state_ips <> 0)) THEN
o 6995   mtp$interrupt_processor (mtv$cst0 [low_prio_csti].memory_port_mask);
o 6996 IFEND;
o 6997 RETURN;
o 6998 IFEND; { low_prio_csti >= 0 }
o 6999 IFEND; { attempt_preselection }
o 7000
o 7001 { If no processor is executing a task with a lower priority then the task being readied must be
o 7002 { placed in the DCT queue.
o 7003
o 7004   dcte := tmv$dct [ptle_pA.dispatching_priority];

```

[INLINE] tmp\$dct_ready_task

```

0 7005
0 7006 IF dcte.queue_head = 0 THEN
0 7007   ptle_p^ptl_thread := 0;
0 7008   dcte.queue_head := ptlo;
0 7009   dcte.queue_tail := ptlo;
0 7010   dcte.minor_priority := ptlo;
0 7011   dcte.major_priority := ptlo;
0 7012   tmv$dispatching_control_sets.ready_tasks := tmv$dispatching_control_sets.ready_tasks +
0 7013     $jmt$dispatching_priority_set [jmc$dp_conversion - ptle_p^dispatching_priority];
0 7014   tmp$calculate_dct_priority_int;
0 7015   tmv$dct [ptle_p^dispatching_priority] := dcte;
0 7016   RETURN;
0 7017 IFEND; { dcte.queue_head = 0 }
0 7018
0 7019 IF dct_placement = minor_timeslice_insert THEN
0 7020   ptle_p^ptl_thread := tmv$ptl_p^ [dcte.minor_priority].ptl_thread;
0 7021   tmv$ptl_p^ [dcte.minor_priority].ptl_thread := ptlo;
0 7022
0 7023 { The following is required to insure that the major pointer
0 7024 { is never higher than the minor pointer in the DCT.
0 7025
0 7026   IF dcte.minor_priority = dcte.major_priority THEN
0 7027     dcte.major_priority := ptlo;
0 7028   IFEND;
0 7029   dcte.minor_priority := ptlo;
0 7030   IF ptle_p^ptl_thread = 0 THEN
0 7031     dcte.queue_tail := ptlo;
0 7032   IFEND;
0 7033   ELSEIF dct_placement = major_timeslice_insert THEN
0 7034     ptle_p^ptl_thread := tmv$ptl_p^ [dcte.major_priority].ptl_thread;
0 7035     tmv$ptl_p^ [dcte.major_priority].ptl_thread := ptlo;
0 7036     dcte.major_priority := ptlo;
0 7037   IF ptle_p^ptl_thread = 0 THEN
0 7038     dcte.queue_tail := ptlo;
0 7039   IFEND;
0 7040   ELSEIF dct_placement = tail_timeslice_insert THEN
0 7041     tmv$ptl_p^ [dcte.queue_tail].ptl_thread := ptlo;
0 7042     ptle_p^ptl_thread := 0;
0 7043     dcte.queue_tail := ptlo;
0 7044   ELSE
0 7045     mtp$error_stop (' TM99--Illegal insert ');
0 7046   IFEND;
0 7047
0 7048 { If any task in a job has specified a relative task priority, all of
0 7049 { the tasks in the job are subject to the relative priority algorithms.
0 7050
0 7051   IF (ijle_p^.relative_priority_enabled) AND (ijle_p^.statistics.
0 7052     ready_task_count > 1) THEN
0 7053     IF dct_placement = minor_timeslice_insert THEN
0 7054       relative_insert_minor_dct [xcb_p^.relative_task_priority, ptlo, ijle_p, dcte];
0 7055     ELSEIF dct_placement = major_timeslice_insert THEN
0 7056       relative_insert_major_dct [xcb_p^.relative_task_priority, ptlo, ijle_p, dcte];
0 7057     ELSEIF dct_placement = tail_timeslice_insert THEN
0 7058       relative_insert_tail_dct [xcb_p^.relative_task_priority, ptlo, ijle_p, dcte];
0 7059     IFEND;
0 7060   IFEND;

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[INLINE] tmp\$dct_ready_task

```

0 7061
0 7062   tmv$dct [ptle_p^dispatching_priority] := dcte;
0 7063
0 7064   PROCEND tmp$dct_ready_task;
0 7065

```

RELATIVE_INSERT_MINOR_DCT

```

0 7067
0 7068 PROCEDURE [INLINE] relative_insert_minor_dct
0 7069 {
0 7070 {   relative_priority: 0 .. 255;
0 7071 {   ptlo: ost$task_index;
0 7072 {   ijl_p: Ajmt$initiated_job_list_entry;
0 7073 {   VAR dcte: tmt$dct_entry);
0 7074
0 7075 { The following procedure is responsible for making sure a task which was
0 7076 { inserted in the MINOR timeslice position is corrected prioritized with the
0 7077 { other tasks of the same job. This job is currently running with relative
0 7078 { prioritization enabled. The position of tasks of other jobs can only be
0 7079 { improved, they will never regress.
0 7080
0 7081 VAR
0 7082   insert_ptlo: ost$task_index,
0 7083   next_search_ptlo: ost$task_index,
0 7084   save_ptlo: ost$task_index,
0 7085   search_ptlo: ost$task_index,
0 7086   search_xcb: Aost$execution_control_block;
0 7087
0 7088 { Insert_ptlo is the most recently moved ptlo.
0 7089 { Search_ptlo is the task which we are currently testing for
0 7090 { possible movement.
0 7091 { Save_ptlo is the last task which was NOT moved.
0 7092
0 7093   insert_ptlo := ptlo;
0 7094   search_ptlo := dcte.queue_head;
0 7095   save_ptlo := 0;
0 7096
0 7097 { The task has been positioned in the DCT. Now, all the tasks of that
0 7098 { job (currently in DCT) must be prioritized. The task has been placed
0 7099 { in the minor priority position. Tasks above that point are checked to
0 7100 { determine if any of them have to be moved below the new task. If the
0 7101 { task is in the same job, and has a lower relative priority, the task is
0 7102 { moved below the new task.
0 7103
0 7104 WHILE [search_ptlo <> ptlo] DO
0 7105   next_search_ptlo := tmv$ptl_p^ [search_ptlo].ptl_thread;
0 7106   IF [tmv$ptl_p^ [search_ptlo].ijl_ordinal = tmv$ptl_p^ [ptlo].ijl_ordinal] THEN
0 7107     search_xcb := #ADDRESS (1, ijl_p^ .ajl_ordinal + mtc$job_fixed_segment,
0 7108     tmv$ptl_p^ [search_ptlo].xcb_offset);
0 7109     IF [search_xcb.relative_task_priority < relative_priority] THEN
0 7110       IF search_ptlo = dcte.queue_head THEN
0 7111         dcte.queue_head := tmv$ptl_p^ [search_ptlo].ptl_thread;
0 7112       ELSE
0 7113         tmv$ptl_p^ [save_ptlo].ptl_thread := tmv$ptl_p^ [search_ptlo].ptl_thread;
0 7114       IFEND;
0 7115       tmv$ptl_p^ [search_ptlo].ptl_thread := tmv$ptl_p^ [insert_ptlo].ptl_thread;
0 7116       tmv$ptl_p^ [insert_ptlo].ptl_thread := search_ptlo;
0 7117       insert_ptlo := search_ptlo;
0 7118     ELSE
0 7119       save_ptlo := search_ptlo;
0 7120     IFEND;
0 7121   ELSE
0 7122     save_ptlo := search_ptlo;
0 7123   IFEND;

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SOURCE LIST OF tmm\$dispatcher

RELATIVE_INSERT_MINOR_DCT

```

0 7123   search_ptlo := next_search_ptlo;
0 7124   WHILEND;
0 7125
0 7126   IF insert_ptlo <> ptlo THEN
0 7127
0 7128 { One or more tasks have been moved below the new task.
0 7129
0 7130   IF dcte.minor_priority = dcte.major_priority THEN
0 7131     dcte.major_priority := insert_ptlo;
0 7132   IFEND;
0 7133   IF tmv$ptl_p^ [insert_ptlo].ptl_thread = 0 THEN
0 7134     dcte.queue_tail := insert_ptlo;
0 7135   IFEND;
0 7136   dcte.minor_priority := insert_ptlo;
0 7137   ELSE
0 7138
0 7139 { There were not any tasks moved below the new task. We must now check to
0 7140 { see if there are any tasks below the new task, which have a relative priority
0 7141 { higher than the new task. A search is made of all of the tasks below the new
0 7142 { task, the new task is moved below the last task (of the same job) which
0 7143 { has a higher relative priority than the new task.
0 7144
0 7145   insert_ptlo := 0;
0 7146   search_ptlo := tmv$ptl_p^ [ptlo].ptl_thread;
0 7147   WHILE search_ptlo <> 0 DO
0 7148     IF [tmv$ptl_p^ [search_ptlo].ijl_ordinal = tmv$ptl_p^ [ptlo].ijl_ordinal] THEN
0 7149       search_xcb := #ADDRESS (1, ijl_p^ .ajl_ordinal + mtc$job_fixed_segment,
0 7150       tmv$ptl_p^ [search_ptlo].xcb_offset);
0 7151       IF [search_xcb.relative_task_priority > relative_priority] THEN
0 7152         insert_ptlo := search_ptlo;
0 7153       IFEND;
0 7154     IFEND;
0 7155     search_ptlo := tmv$ptl_p^ [search_ptlo].ptl_thread;
0 7156   WHILEND;
0 7157
0 7158   IF insert_ptlo <> 0 THEN
0 7159     IF ptlo = dcte.queue_head THEN
0 7160       dcte.queue_head := tmv$ptl_p^ [ptlo].ptl_thread;
0 7161     IFEND;
0 7162     IF ptlo = dcte.minor_priority THEN
0 7163       dcte.minor_priority := tmv$ptl_p^ [ptlo].ptl_thread;
0 7164     IFEND;
0 7165     IF insert_ptlo = dcte.major_priority THEN
0 7166       dcte.major_priority := ptlo;
0 7167     IFEND;
0 7168     tmv$ptl_p^ [save_ptlo].ptl_thread := tmv$ptl_p^ [ptlo].ptl_thread;
0 7169     tmv$ptl_p^ [ptlo].ptl_thread := tmv$ptl_p^ [insert_ptlo].ptl_thread;
0 7170     tmv$ptl_p^ [insert_ptlo].ptl_thread := ptlo;
0 7171     IF tmv$ptl_p^ [ptlo].ptl_thread = 0 THEN
0 7172       dcte.queue_tail := ptlo;
0 7173     IFEND;
0 7174   IFEND;
0 7175   IFEND;
0 7176
0 7177 PROCEND relative_insert_minor_dct;

```


RELATIVE_INSERT_MAJOR_DCT

```

0 7179
0 7180 PROCEDURE [INLINE] relative_insert_major_dct
0 7181 {   relative_priority: 0 .. 255;
0 7182   ptlo: ost$task_index;
0 7183   ijl_p: ^jmt$initiated_job_list_entry;
0 7184   VAR dcte: tmt$dct_entry);
0 7185
0 7186 { The following procedure is responsible for making sure a task which was
0 7187 { inserted in the MAJOR timeslice position is corrected prioritized with the
0 7188 { other tasks of the same job. This job is currently running with relative
0 7189 { prioritization enabled. The position of tasks of other jobs can only be
0 7190 { improved, they will never regress.
0 7191
0 7192   VAR
0 7193     insert_ptlo: ost$task_index,
0 7194     next_search_ptlo: ost$task_index,
0 7195     save_ptlo: ost$task_index,
0 7196     search_ptlo: ost$task_index,
0 7197     search_xcb: ^ost$execution_control_block;
0 7198
0 7199 { Insert_ptlo is the most recently moved ptlo.
0 7200 { Search_ptlo is the task which we are currently testing for
0 7201 { possible movement.
0 7202 { Save_ptlo is the last task which was NOT moved.
0 7203
0 7204     insert_ptlo := ptlo;
0 7205     search_ptlo := dcte.queue_head;
0 7206     save_ptlo := 0;
0 7207
0 7208 { The task has been positioned in the DCT. Now, all the tasks of that
0 7209 { job (currently in DCT) must be prioritized. The task has been placed
0 7210 { in the major priority position. Tasks above that point are checked to
0 7211 { determine if any of them have to be moved below the new task. If the
0 7212 { task is in the same job, and has a lower relative priority, the task is
0 7213 { moved below the new task.
0 7214
0 7215   WHILE [search_ptlo <> ptlo] DO
0 7216     next_search_ptlo := tmv$pt1_p^ [search_ptlo].pt1_thread;
0 7217     IF [tmv$pt1_p^ [search_ptlo].ijl_ordinal = tmv$pt1_p^ [ptlo].ijl_ordinal] THEN
0 7218       search_xcb := #ADDRESS [1, ijl_p^.ajl_ordinal + mtc$job_fixed_segment,
0 7219         tmv$pt1_p^ [search_ptlo].xcb_offset];
0 7220       IF [search_xcb^.relative_task_priority < relative_priority] THEN
0 7221         IF search_ptlo = dcte.queue_head THEN
0 7222           dcte.queue_head := tmv$pt1_p^ [search_ptlo].pt1_thread;
0 7223         ELSE
0 7224           tmv$pt1_p^ [save_ptlo].pt1_thread := tmv$pt1_p^ [search_ptlo].pt1_thread;
0 7225         IFEND;
0 7226         IF search_ptlo = dcte.minor_priority THEN
0 7227           dcte.minor_priority := tmv$pt1_p^ [search_ptlo].pt1_thread;
0 7228         IFEND;
0 7229         tmv$pt1_p^ [search_ptlo].pt1_thread := tmv$pt1_p^ [insert_ptlo].pt1_thread;
0 7230         tmv$pt1_p^ [insert_ptlo].pt1_thread := search_ptlo;
0 7231         insert_ptlo := search_ptlo;
0 7232       ELSE
0 7233         save_ptlo := search_ptlo;
0 7234       IFEND;

```

RELATIVE_INSERT_MAJOR_DCT

```

0 7235     ELSE
0 7236       save_ptlo := search_ptlo;
0 7237     IFEND;
0 7238     search_ptlo := next_search_ptlo;
0 7239   WHILEND;
0 7240
0 7241   IF insert_ptlo <> ptlo THEN
0 7242     dcte.major_priority := insert_ptlo;
0 7243     IF tmv$pt1_p^ [insert_ptlo].pt1_thread = 0 THEN
0 7244       dcte.queue_tail := insert_ptlo;
0 7245     IFEND;
0 7246   ELSE
0 7247
0 7248 { There were not any tasks moved below the new task. We must now check to
0 7249 { see if there are any tasks below the new task, which have a relative priority
0 7250 { higher than the new task. A search is made of all of the tasks below the new
0 7251 { task, the new task is moved below the last task (of the same job) which
0 7252 { has a higher relative priority than the new task.
0 7253
0 7254     insert_ptlo := 0;
0 7255     search_ptlo := tmv$pt1_p^ [ptlo].pt1_thread;
0 7256     WHILE search_ptlo <> 0 DO
0 7257       IF [tmv$pt1_p^ [search_ptlo].ijl_ordinal = tmv$pt1_p^ [ptlo].ijl_ordinal] THEN
0 7258         search_xcb := #ADDRESS [1, ijl_p^.ajl_ordinal + mtc$job_fixed_segment,
0 7259           tmv$pt1_p^ [search_ptlo].xcb_offset];
0 7260         IF [search_xcb^.global_task_id <> tmv$null_global_task_id] AND
0 7261           [search_xcb^.relative_task_priority > relative_priority] THEN
0 7262           insert_ptlo := search_ptlo;
0 7263         IFEND;
0 7264       IFEND;
0 7265       search_ptlo := tmv$pt1_p^ [search_ptlo].pt1_thread;
0 7266     WHILEND;
0 7267     IF insert_ptlo <> 0 THEN
0 7268       IF ptlo = dcte.queue_head THEN
0 7269         dcte.queue_head := tmv$pt1_p^ [ptlo].pt1_thread;
0 7270       IFEND;
0 7271       IF ptlo = dcte.minor_priority THEN
0 7272         dcte.minor_priority := tmv$pt1_p^ [ptlo].pt1_thread;
0 7273       IFEND;
0 7274       IF ptlo = dcte.major_priority THEN
0 7275         dcte.major_priority := tmv$pt1_p^ [ptlo].pt1_thread;
0 7276       IFEND;
0 7277       tmv$pt1_p^ [save_ptlo].pt1_thread := tmv$pt1_p^ [ptlo].pt1_thread;
0 7278       tmv$pt1_p^ [ptlo].pt1_thread := tmv$pt1_p^ [insert_ptlo].pt1_thread;
0 7279       tmv$pt1_p^ [insert_ptlo].pt1_thread := ptlo;
0 7280       IF tmv$pt1_p^ [ptlo].pt1_thread = 0 THEN
0 7281         dcte.queue_tail := ptlo;
0 7282       IFEND;
0 7283     IFEND;
0 7284   IFEND;
0 7285
0 7286 PROCEND relative_insert_major_dct;

```

RELATIVE_INSERT_TAIL_DCT

```

o 7288
o 7289 PROCEDURE [INLINE] relative_insert_tail_dct
o 7290 (
o 7291   relative_priority: 0 .. 255;
o 7292   ptlo: ost$task_index;
o 7293   ijle_p: Ajmt$initiated_job_list_entry;
o 7294   VAR dcte: tmt$dcte_entry);
o 7295 { The new task has been placed at the tail of the DCT.
o 7296 { All of the tasks in the DCT are scanned. Any tasks (of the
o 7297 { same job) which have a lower relative priority than the new
o 7298 { task, are moved below the new task.
o 7299
o 7300 VAR
o 7301   insert_ptlo: ost$task_index;
o 7302   next_search_ptlo: ost$task_index;
o 7303   save_ptlo: ost$task_index;
o 7304   search_ptlo: ost$task_index;
o 7305   search_xcb: ^ost$execution_control_block;
o 7306
o 7307 { Insert_ptlo is the most recently moved ptlo.
o 7308 { Search_ptlo is the task which we are currently testing for
o 7309 { possible movement.
o 7310 { Save_ptlo is the last task which was NOT moved.
o 7311
o 7312   search_ptlo := dcte.queue_head;
o 7313   save_ptlo := 0;
o 7314   insert_ptlo := ptlo;
o 7315
o 7316 WHILE (search_ptlo <> ptlo) DO
o 7317   next_search_ptlo := tmv$ptl_p^ [search_ptlo].pt1_thread;
o 7318   IF (tmv$ptl_p^ [search_ptlo].ijl_ordinal = tmv$ptl_p^ [ptlo].ijl_ordinal) THEN
o 7319     search_xcb := #ADDRESS (1, ijle_p.ajl_ordinal + mtc$job_fixed_segment,
o 7320     tmv$ptl_p^ [search_ptlo].xcb_offset);
o 7321     tmv$ptl_p^ [search_ptlo].xcb_offset);
o 7322     IF (search_xcb^.relative_task_priority < relative_priority) THEN
o 7323       IF (search_ptlo = dcte.queue_head) THEN
o 7324         dcte.queue_head := tmv$ptl_p^ [search_ptlo].pt1_thread;
o 7325       IFEND;
o 7326       IF (search_ptlo = dcte.minor_priority) THEN
o 7327         dcte.minor_priority := tmv$ptl_p^ [search_ptlo].pt1_thread;
o 7328       IFEND;
o 7329       IF (search_ptlo = dcte.major_priority) THEN
o 7330         dcte.major_priority := tmv$ptl_p^ [search_ptlo].pt1_thread;
o 7331       IFEND;
o 7332       IF save_ptlo <> 0 THEN
o 7333         tmv$ptl_p^ [save_ptlo].pt1_thread := tmv$ptl_p^ [search_ptlo].pt1_thread;
o 7334       IFEND;
o 7335       tmv$ptl_p^ [search_ptlo].pt1_thread := 0;
o 7336       tmv$ptl_p^ [insert_ptlo].pt1_thread := search_ptlo;
o 7337       insert_ptlo := search_ptlo;
o 7338     ELSE
o 7339       save_ptlo := search_ptlo;
o 7340     IFEND;
o 7341   ELSE
o 7342     save_ptlo := search_ptlo;
o 7343   IFEND;

```

SOURCE LIST OF tmm\$dispatcher

RELATIVE_INSERT_TAIL_DCT

```

o 7344   search_ptlo := next_search_ptlo;
o 7345   WHILEND;
o 7346
o 7347   dcte.queue_tail := insert_ptlo;
o 7348
o 7349 PROCEND relative_insert_tail_dct;

```

INSERT_IJL

```

0 7351
0 7352 PROCEDURE [INLINE] insert_ijl
0 7353 ( taskid: ost$global_task_id;
0 7354   cst_p: ^ost$cpu_state_table);
0 7355
0 7356 {
0 7357 { The purpose of this procedure is to insert the selected
0 7358 { task's PTL entry in the IJL thread for it's job. The
0 7359 { first task in the IJL thread is the job monitor task.
0 7360 {
0 7361 {   INSERT_IJL (TASKID)
0 7362 {
0 7363 { TASKID : (INPUT) This parameter specifies the taskid of
0 7364 {           of the specified task.
0 7365 {
0 7366 { NOTE : Anyone calling this routine must have tmv$pt1_lock set.
0 7367 {
0 7368 {
0 7369 {   tmv$pt1_p^ [taskid.index].ijl_thread := tmv$pt1_p^ [cst_p^.ijle_p^.job_monitor_taskid.index].ijl_thread;
0 7370 {   tmv$pt1_p^ [cst_p^.ijle_p^.job_monitor_taskid.index].ijl_thread := taskid.index;
0 7371 {
0 7372 PROCEND insert_ijl;

```

REMOVE_IJL

```

0 7374
0 7375 PROCEDURE [INLINE] remove_ijl
0 7376 ( taskid: ost$global_task_id;
0 7377   cst_p: ^ost$cpu_state_table);
0 7378
0 7379
0 7380 {
0 7381 { The purpose of this procedure is to remove the PTL
0 7382 { entry for the specified task from the IJL thread.
0 7383 {
0 7384 {   REMOVE_IJL (TASKID)
0 7385 {
0 7386 { TASKID : (INPUT) This parameter specifies the taskid
0 7387 {           of the selected task.
0 7388 {
0 7389 { NOTE : Anyone calling this routine must have tmv$pt1_lock set.
0 7390 {
0 7391 {
0 7392 {   VAR
0 7393 {     last_ptlo,
0 7394 {     curr_ptlo: ost$task_index;
0 7395 {
0 7396 {
0 7397 {
0 7398 {     last_ptlo := 0;
0 7399 {     curr_ptlo := cst_p^.ijle_p^.job_monitor_taskid.index;
0 7400 {
0 7401 { /search_loop/
0 7402 {   WHILE curr_ptlo <> 0 DO
0 7403 {     IF curr_ptlo = taskid.index THEN
0 7404 {       EXIT /search_loop/;
0 7405 {     IFEND;
0 7406 {     last_ptlo := curr_ptlo;
0 7407 {     curr_ptlo := tmv$pt1_p^ [last_ptlo].ijl_thread;
0 7408 {   WHILEND /search_loop/;
0 7409 {
0 7410 {   tmv$pt1_p^ [last_ptlo].ijl_thread := tmv$pt1_p^ [curr_ptlo].ijl_thread;
0 7411 {   tmv$pt1_p^ [curr_ptlo].ijl_thread := 0;
0 7412 {
0 7413 PROCEND remove_ijl;

```

TMP\$FIND_XCB

```

O 7415
O 7416 PROCEDURE [XDCL] tmp$find_xcb
O 7417 {
O 7418 {   taskid: ost$global_task_id;
O 7419 {   VAR xcb_p: ^ost$execution_control_block;
O 7420 {   VAR ijle_p: ^jmt$initiated_job_list_entry;
O 7421 {   VAR status: syt$monitor_status};
O 7422 {
O 7423 {
O 7424 { The purpose of this request is to get the pointer to the execution control
O 7425 { block of a specified task. If the job is swapped but IO has not been initiated
O 7426 { an ajl entry is assigned to allow access to the xcb. Otherwise an
O 7427 { error is returned.
O 7428 {
O 7429 {   TMP$FIND_XCB (TASKID, XCB_P, IJLE_P, STATUS)
O 7430 {
O 7431 { TASKID : (INPUT) This parameter specifies the taskid of the
O 7432 { selected task.
O 7433 {
O 7434 { XCB_P : (OUTPUT) This parameter specifies the pointer to the
O 7435 { execution control block.
O 7436 {
O 7437 { IJLE_P : (OUTPUT) This parameter specifies the pointer to the
O 7438 { initiated job list entry.
O 7439 {
O 7440 { STATUS : (OUTPUT) This parameter specifies the request status.
O 7441 {
O 7442 { NOTE : This procedure will lock the ajl entry of the job if xcb access is
O 7443 { possible. It is the callers responsibility to unlock the ajl with
O 7444 { a call to jmp$free_ajl_entry.
O 7445 {
O 7446 {   VAR
O 7447 {     inhibit_access: boolean;
O 7448 {
O 7449 {     status.normal := TRUE;
O 7450 {     tmp$set_lock (tmv$pt1_lock);
4A 7451
4A 7452 tmp$check_taskid_with_lock_set (taskid, tmc$opt_return, status);
AC 7453 IF NOT status.normal THEN
B4 7454   tmp$clear_lock (tmv$pt1_lock);
E6 7455   RETURN;
E8 7456   IFEND;
E8 7457
E8 7458 jmp$get_ijle_p (tmv$pt1_p^ [taskid.index].ijl_ordinal, ijle_p);
E8 7459 tmp$get_xcb_access_status (ijle_p, tmv$pt1_p^ [taskid.index].ijl_ordinal, inhibit_access);
IA2 7460 IF inhibit_access THEN
IAA 7461   tmp$set_status_abnormal ('TM', tme$job_swapped_out, status);
IBE 7462 ELSE
IBE 7463   xcb_p := #ADDRESS (1, ijle_p^.ajl_ordinal + mtc$job_fixed_segment, tmv$pt1_p^ [taskid.index].
IFO 7464   xcb_offset);
IFO 7465   IFEND;
IFO 7466   tmp$clear_lock (tmv$pt1_lock);
222 7467
222 7468 PROCEND tmp$find_xcb;
O 7469

```

SOURCE LIST OF tmm\$dispatcher

TMP\$GET_XCB_P

```

O 7471
O 7472 PROCEDURE [XDCL] tmp$get_xcb_p
O 7473 {
O 7474 {   taskid: ost$global_task_id;
O 7475 {   VAR xcb_p: ^ost$execution_control_block;
O 7476 {   VAR ijle_p: ^jmt$initiated_job_list_entry;
O 7477 {
O 7478 { The purpose of this request is to get a pointer to the XCB of the task
O 7479 { specified by a global task id (GTID). If the GTID is invalid, the system is halted
O 7480 { with an 'taskid error'. If the taskid is valid but the task is swapped out,
O 7481 { a NIL pointer is returned. A pointer to the initiated job list entry is also
O 7482 { returned.
O 7483 {
O 7484 {   tmp$get_xcb_p (taskid, xcb_p, ijle_p);
O 7485 {
O 7486 { TASKID: (input) This parameter specifies the GTID of the task to be located.
O 7487 {
O 7488 { XCB_P: (output) This parameter contains a pointer to the XCB of the specified
O 7489 { task. If the task is swapped out, a NIL pointer is returned.
O 7490 {
O 7491 { IJLE_P: (output) This parameter contains a pointer to the initiated job list
O 7492 { entry of the job.
O 7493 {
O 7494 { NOTE: This procedure will increment the ajl in use count or assign an ajl if
O 7495 { xcb access is possible. It is the callers responsibility to decrement it
O 7496 { with a call to jmp$free_ajl_entry.
O 7497 {
O 7498 {   VAR
O 7499 {     inhibit_access: boolean;
O 7500 {
O 7501 {     tmp$set_lock (tmv$pt1_lock);
42 7502     tmp$stop_if_bad_taskid (taskid);
A8 7503     jmp$get_ijle_p (tmv$pt1_p^ [taskid.index].ijl_ordinal, ijle_p);
A8 7504
A8 7505     tmp$get_xcb_access_status (ijle_p, tmv$pt1_p^ [taskid.index].ijl_ordinal, inhibit_access);
164 7506 IF inhibit_access THEN
16C 7507   xcb_p := NIL;
17C 7508 ELSE
17C 7509   xcb_p := #ADDRESS (1, ijle_p^.ajl_ordinal + mtc$job_fixed_segment, tmv$pt1_p^ [taskid.index].
IAE 7510   xcb_offset);
IAE 7511   IFEND;
IAE 7512   tmp$clear_lock (tmv$pt1_lock);
IE2 7513
IE2 7514 PROCEND tmp$get_xcb_p;

```

TMP\$TEST_GET_XCB_P

```

O 7516
O 7517 PROCEDURE [XDCL] tmp$test_get_xcb_p
O 7518 [ taskid: ost$global_task_id;
O 7519 VAR xcb_p: ^ost$execution_control_block;
O 7520 VAR ijle_p: ^jmt$initiated_job_list_entry];
O 7521
O 7522 [ NOTE: This procedure will increment the ajl in use count or assign an ajl if
O 7523 [ xcb access is possible. It is the callers responsibility to decrement it
O 7524 [ with a call to jmp$free_ajl_entry.
O 7525 [
O 7526 VAR
O 7527 inhibit_access: boolean;
O 7528
O 7529 tmp$set_lock (tmv$ptl_lock);
42 7530
42 7531 IF (taskid = tmv$null_global_task_id) OR (taskid.index > UPPERBOUND (tmv$ptl_p^)) OR
84 7532 [ (tmv$ptl_p^ [taskid.index].sequence_number <> taskid.seqno) OR
84 7533 [ (tmv$ptl_p^ [taskid.index].status = tmc$ts_null) THEN
84 7534 xcb_p := NIL;
84 7535 ijle_p := NIL;
9C 7536 ELSE
9C 7537 jmp$get_ijle_p (tmv$ptl_p^ [taskid.index].ijl_ordinal, ijle_p);
9C 7538
9C 7539 tmp$get_xcb_access_status (ijle_p, tmv$ptl_p^ [taskid.index].ijl_ordinal, inhibit_access);
14C 7540 IF inhibit_access THEN
154 7541 xcb_p := NIL;
164 7542 ELSE
164 7543 xcb_p := #ADDRESS [1, ijle_p^.ajl_ordinal + mtc$job_fixed_segment, tmv$ptl_p^ [taskid.index].
196 7544 xcb_offset];
196 7545 IFEND;
196 7546 IFEND;
196 7547 tmp$clear_lock (tmv$ptl_lock);
1CA 7548 PROCEND tmp$test_get_xcb_p;

```

TMP\$GET_TOP_OF_STACK

```

O 7551 [
O 7552 [ PURPOSE:
O 7553 [ This procedure is used to fetch the value of the TOP-OF-STACK pointer for a specified
O 7554 [ ring of a specified task?
O 7555 [ DESIGN:
O 7556 [ The value of the TOS register is determined from the exchange package as follows:
O 7557 [ If the value of TOS cannot be determined because the task is executing on another
O 7558 [ processor of a dual CPU system, TOS cannot be determined; a value of 2**31-1 is
O 7559 [ returned.
O 7560 [
O 7561
O 7562 PROCEDURE [XDCL] tmp$get_top_of_stack
O 7563 [ taskid: ost$global_task_id;
O 7564 [ ring: ost$valid_ring;
O 7565 [ VAR tos: integer];
O 7566
O 7567 VAR
O 7568 cst_p: ^ost$cpu_state_table;
O 7569 ijle_p: ^jmt$initiated_job_list_entry;
O 7570 inhibit_access: boolean;
O 7571 xcb_p: ^ost$execution_control_block;
O 7572
O 7573 tmp$set_lock (tmv$ptl_lock);
46 7574 tmp$stop_if_bad_taskid (taskid);
AC 7575 jmp$get_ijle_p (tmv$ptl_p^ [taskid.index].ijl_ordinal, ijle_p);
AC 7576
AC 7577 tmp$get_xcb_access_status (ijle_p, tmv$ptl_p^ [taskid.index].ijl_ordinal, inhibit_access);
162 7578 IF inhibit_access THEN
16A 7579 mtp$error_stop ('TM - XCB not accessible');
18E 7580 ELSE
18E 7581 xcb_p := #ADDRESS [1, ijle_p^.ajl_ordinal + mtc$job_fixed_segment, tmv$ptl_p^ [taskid.index].
18A 7582 xcb_offset];
18A 7583 IFEND;
18A 7584
18A 7585 mtp$cst_p (cst_p);
1CA 7586
1CA 7587 IF ((tmv$ptl_p^ [taskid.index].status = tmc$ts_executing) AND (cst_p^.taskid <> taskid)) OR
20E 7588 [ (xcb_p^.stack_pages_saved [ring] = TRUE) THEN
20E 7589 tos := 7fffffff[16]; [ TOS cannot be determined if executing on another processor.]
21E 7590 ELSEIF xcb_p^.xp.p_register.pva.ring > ring THEN
22C 7591 tos := 0; [ TOS is zero if executing in a higher ring.]
23A 7592 ELSEIF xcb_p^.xp.p_register.pva.ring = ring THEN
23E 7593 tos := #OFFSET [xcb_p^.xp.a0_dynamic_space_pointer]; [TOS in XP.A0 is correct if in same ring.]
252 7594 ELSE
252 7595 tos := xcb_p^.xp.tos_registers [ring].pva.offset; [TOS in XP.TOS is correct if in lower ring.]
268 7596 IFEND;
268 7597
268 7598 jmp$unlock_ajl_with_lock (ijle_p);
2AA 7599 tmp$clear_lock (tmv$ptl_lock);
2DE 7600
2DE 7601 PROCEND tmp$get_top_of_stack;
O 7602

```

TMP\$GET_XCB_P_FROM_PTLO

```

0 7604
0 7605 { NOTE : Any procedure calling this routine must have tmv$ptl_lock set.
0 7606
0 7607 PROCEDURE [INLINE] tmp$get_xcb_p_from_ptlo
0 7608 {
0 7609     ptlo: ost$task_index;
0 7610     ajl_ordinal: jmt$ajl_ordinal;
0 7611     VAR xcb_p: ^ost$execution_control_block);
0 7612
0 7613     xcb_p := #ADDRESS (1, ajl_ordinal + mtc$job_fixed_segment, tmv$ptl_p^ [ptlo].xcb_offset);
0 7614
0 7615 PROCEND tmp$get_xcb_p_from_ptlo;

```

SOURCE LIST OF tmm\$dispatcher

TMP\$FIND_NEXT_XCB

```

0 7617
0 7618 PROCEDURE [XDCL] tmp$find_next_xcb
0 7619 {
0 7620     search: tmt$fnx_search_type;
0 7621     ijle_p: ^jmt$initiated_job_list_entry;
0 7622     ijl_ordinal: jmt$ijl_ordinal;
0 7623     VAR state: tmt$find_next_xcb_state;
0 7624     VAR xcb_p: ^ost$execution_control_block);
0 7625 {
0 7626 { The purpose of this request is to scan the PTL entry for each
0 7627 { task in a JOB or the SYSTEM and return its XCB_P.
0 7628 {
0 7629 { SEARCH: (INPUT) This parameter specifies the type of search to be
0 7630 { done, JOB, SYSTEM, SWAPPING_JOB, of CONTINUE
0 7631 { previous search.
0 7632 {
0 7633 { IJLE_P: (INPUT) This parameter specifies a pointer to the initiated
0 7634 { job list entry of the job to be searched.
0 7635 {
0 7636 { IJL_ORDINAL: (INPUT) This parameter specifies the initiated job list
0 7637 { ordinal of the job to be searched.
0 7638 {
0 7639 { STATE: (INPUT,OUTPUT) This parameter specifies the type of search
0 7640 { that had been started by a previous call.
0 7641 {
0 7642 { XCB_P: (OUTPUT) This parameter specifies the XCB_P of the next task
0 7643 { in the JOB or SYSTEM.
0 7644 {
0 7645 {
0 7646     VAR
0 7647     ij1_bi: jmt$ij1_block_index,
0 7648     ij1_bn: jmt$ij1_block_number,
0 7649     inhibit_access: boolean,
0 7650     next_ijle_p: ^jmt$initiated_job_list_entry,
0 7651     start_index: integer;
0 7652
0 7653     tmp$set_lock (tmv$ptl_lock);
42 7654
42 7655 {Initialize the search state variable if this is not a continuation search.
42 7656
42 7657     IF search <> tmc$fnx_continue THEN
4A 7658         state.search := search;
4A 7659         IF search = tmc$fnx_system THEN
58 7660             state.ajl_ordinal := jmv$system_ajl_ordinal;
58 7661             state.ijl_ordinal := jmv$system_ijl_ordinal;
58 7662             state.in_use_incremented := TRUE;
58 7663             jmv$ajl_p^ [jmv$system_ajl_ordinal].in_use := jmv$ajl_p^ [jmv$system_ajl_ordinal].in_use +
58 7664             jmc$lock_ajl;
58 7665             jmp$get_ijle_p (jmv$system_ijl_ordinal, state.ijle_p);
58 7666             state.next_ptlo := jmv$ajl_p^ [state.ajl_ordinal].ijle_p^.job_monitor_taskid.index;
D2 7667     ELSE
D2 7668         tmp$get_xcb_access_status (ijle_p, ijl_ordinal, inhibit_access);
164 7669         state.in_use_incremented := NOT inhibit_access;
164 7670         IF (search = tmc$fnx_job) AND inhibit_access THEN
17C 7671             state.next_ptlo := 0;
186 7672     ELSE

```

TMP\$FIND_NEXT_XCB

```

186 7673      state.ajl_ordinal := ijle_p^.ajl_ordinal;
186 7674      state.next_ptlo := ijle_p^.job_monitor_taskid.index;
186 7675      state.ijl_ordinal := ijl_ordinal;
186 7676      state.ijle_p := ijle_p;
1AC 7677      IFEND;
1AC 7678      IFEND;
1B0 7679
1B0 7680 { If a full system search is in process and we have reached the end of the current job.
1B0 7681 { Release the temporary ajl ordinal if necessary. Find the next job to be searched.
1B0 7682
1B0 7683      ELSEIF (state.next_ptlo = 0) AND (state.search = tmc$fnx_system) THEN
1C6 7684
1C6 7685          jmp$unlock_ajl_with_lock (state.ijle_p);
20C 7686          state.in_use_incremented := FALSE;
20C 7687
20C 7688          start_index := state.ijl_ordinal.block_index + 1;
20C 7689
20C 7690      /find_next_job/
20C 7691      FOR ijl_bn := state.ijl_ordinal.block_number TO jmv$ijl_p.max_block_in_use DO
232 7692          IF jmv$ijl_p.block_p^[ijl_bn].index_p <> NIL THEN
244 7693              FOR ijl_bi := start_index TO UPPERVALUE (jmt$ijl_block_index) DO
24E 7694                  next_ijle_p := ^jmv$ijl_p.block_p^[ijl_bn].index_p^[ijl_bi];
24E 7695                  IF next_ijle_p^.entry_status <> jmc$ies_entry_free THEN
268 7696                      state.ijl_ordinal.block_index := ijl_bi;
268 7697                      state.ijl_ordinal.block_number := ijl_bn;
268 7698                      tmp$get_xcb_access_status (next_ijle_p, state.ijl_ordinal, inhibit_access);
306 7699                      state.in_use_incremented := NOT inhibit_access;
306 7700                      IF NOT inhibit_access THEN
316 7701                          state.next_ptlo := next_ijle_p^.job_monitor_taskid.index;
316 7702                          state.ajl_ordinal := next_ijle_p^.ajl_ordinal;
316 7703                          state.ijle_p := next_ijle_p;
316 7704                          EXIT /find_next_job/;
32E 7705                      IFEND;
32E 7706                  IFEND;
32E 7707              FOREND;
332 7708              start_index := LOWERVALUE (jmt$ijl_block_index);
336 7709              IFEND;
336 7710          FOREND /find_next_job/;
33A 7711
33A 7712      IFEND;
33A 7713
33A 7714      IF state.next_ptlo = 0 THEN
342 7715          IF state.in_use_incremented THEN
34A 7716              jmp$unlock_ajl_with_lock (state.ijle_p);
390 7717              state.in_use_incremented := FALSE;
396 7718          IFEND;
396 7719          xcb_p := NIL;
3A6 7720      ELSE
3A6 7721          xcb_p := #ADDRESS (1, tmc$job_fixed_segment + state.ajl_ordinal, tmv$pt1_p^[state.next_ptlo].
3A6 7722              xcb_offset);
3A6 7723          state.next_ptlo := tmv$pt1_p^[state.next_ptlo].ijl_thread;
3E0 7724      IFEND;
3E0 7725      tmp$clear_lock (tmv$pt1_lock);
41C 7726
41C 7727      PROCEND tmp$find_next_xcb;

```

TMP\$SET_TASK_WAIT

```

0 7729
0 7730      PROCEDURE [XDCL] tmp$set_task_wait
0 7731      ( task_status: tmt$task_status);
0 7732
0 7733
0 7734 { The purpose of this procedure is to change the status
0 7735 { of the current task to a specified WAIT status.
0 7736 {
0 7737 {     TMP$SET_TASK_WAIT (TASK_STATUS)
0 7738 {
0 7739 { TASK_STATUS : (INPUT) This parameter specifies the new task
0 7740 {     status.
0 7741 {
0 7742 {
0 7743      VAR
0 7744          cst_p: ^ost$cpu_state_table,
0 7745          taskid: ost$global_task_id;
0 7746
0 7747          mtp$cst_p (cst_p);
14 7748          tmp$set_lock (tmv$pt1_lock);
60 7749          IF (task_status >= tmc$ts_first_external_queue) OR (tmv$pt1_p^[cst_p^.taskid.index].new_task_status >
86 7750              tmc$ts_null) THEN
86 7751              mtp$error_stop ('TM37 - bad call to set_task_wait');
A6 7752          IFEND;
A6 7753          IF (task_status = tmc$ts_io_wait_not_queued) THEN
B0 7754              tmv$io_wait_task_count := tmv$io_wait_task_count + 1;
BE 7755          IFEND;
BE 7756          tmv$pt1_p^[cst_p^.taskid.index].new_task_status := task_status;
BE 7757          tmp$clear_lock (tmv$pt1_lock);
110 7758          cst_p^.dispatch_control.call_dispatcher := TRUE;
110 7759          IF (cst_p^.next_ptlo_to_dispatch = 0) THEN
120 7760              cst_p^.dispatching_priority_integer := tmv$dct_priority_integer;
128 7761          IFEND;
128 7762
128 7763      PROCEND tmp$set_task_wait;

```

TMP\$SET_TASK_READY

```

0 7765
0 7766 PROCEDURE [XDCL] tmp$set_task_ready
0 7767 {
0 7768   taskid: oest$global_task_id;
0 7769   readying_task_priority: jmt$dispatching_priority;
0 7770   ready_condition: tmt$ready_condition);
0 7771
0 7772 {
0 7773 { The purpose of this procedure is to make a task ready either
0 7774 { conditionally or unconditionally. A task can be conditionally
0 7775 { made ready if it is currently in a wait status and can be
0 7776 { unconditionally made ready irrespective of it's current status.
0 7777 { The WAIT INHIBITED flag in the PTL is set if it is specified
0 7778 { on the request.
0 7779 {
0 7780 {   TMP$SET_TASK_READY (TASKID, READY_CONDITION)
0 7781 {
0 7782 {   TASKID : (INPUT) This parameter specifies the task to be made ready.
0 7783 {
0 7784 {   READYING_TASK_PRIORITY : (INPUT) This parameter specifies the
0 7785 { priority of the task which has issued the
0 7786 { request to ready this task. This value is only used
0 7787 { if the task being readied has subsystem locks set.
0 7788 {
0 7789 {   READY_CONDITION : (INPUT) This parameter specifies if the task
0 7790 { is to be made ready conditionally or
0 7791 { unconditionally.
0 7792 {
0 7793 {
0 7794 {   VAR
0 7795 { attempt_preselection: boolean;
0 7796 { cst_p: Aost$cpu_state_table;
0 7797 { old_dispatching_priority: jmt$dispatching_priority;
0 7798 { ijle_p: Ajmt$initiated_job_list_entry;
0 7799 { null_dispatching_info: jmt$dispatching_control_info;
0 7800 { psuedo_rb: tmt$rb_update_job_task_enviro;
0 7801 { ptl_p: Atmt$primary_task_list_entry;
0 7802 { xcb_p: Aost$execution_control_block;
0 7803 { task_priority_changed: boolean;
0 7804 {
0 7805 {
0 7806 { tmp$set_lock (tmv$ptl_lock);
42 7807 { tmp$stop_if_bad_taskid (taskid);
A8 7808 { ptl_p := Atmv$ptl_p [taskid.index];
A8 7809 { jmp$set_ijle_p (ptl_p^.ijl_ordinal, ijle_p);
A8 7810 { xcb_p := #ADDRESS (1, mtc$job_fixed_segment + ijle_p^.ajl_ordinal, ptl_p^.xcb_offset);
A8 7811 {
A8 7812 { IF (ijle_p^.interactive_task_gtid = taskid) THEN
FE 7813 { psuedo_rb.reqcode := Syc$rc_update_job_task_enviro;
FE 7814 { psuedo_rb.subcode := tmc$ujte_dispatching_priority;
FE 7815 { psuedo_rb.request_origin := tmc$cpo_interactive_command;
FE 7816 { psuedo_rb.i_jl_ordinal := ptl_p^.ijl_ordinal;
FE 7817 { psuedo_rb.system_supplied_name := ijle_p^.system_supplied_name;
11E 7818 { psuedo_rb.dispatching_control_info := null_dispatching_info;
11E 7819 { old_dispatching_priority := ijle_p^.dispatching_control.dispatching_priority;
11E 7820 { mtp$set_p (cst_p);

```

SOURCE LIST OF tmm\$dispatcher

TMP\$SET_TASK_READY

```

13A 7821   tmp$mtr_update_job_task_enviro (psuedo_rb, cst_p);
164 7822   IF (ijle_p^.dispatching_control.dispatching_priority <> old_dispatching_priority) AND
17A 7823     (ijle_p^.entry_status = jmc$ies_swapin_candidate) THEN
17A 7824     jmv$subsystem_priority_changes [ijle_p^.job_scheduler_data.service_class] := TRUE;
17A 7825     jmp$set_scheduler_event (jmc$subsystem_priority_change);
198 7826   IFEND;
198 7827   ijle_p^.interactive_task_gtid := tmv$null_global_task_id;
1A4 7828   IFEND;
1A4 7829
1A4 7830 { Tasks with subsystem locks set will execute with the higher
1A4 7831 { of the task's or the "readying" task's priority, after it
1A4 7832 { has run for the subsystem lock threshold with a priority of P9.
1A4 7833
1A4 7834   task_priority_changed := FALSE;
1A4 7835
1A4 7836   IF (readying_task_priority <> 0) AND ((ptl_p^.ptl_flags.subsystem_locks_set) OR
1CE 7837     ((ptl_p^.idle_status < tmc$ts_idled) AND (xcb_p^.system_table_lock_count > 0))) THEN
1CE 7838     IF readying_task_priority > ptl_p^.readying_task_priority THEN
1DA 7839       ptl_p^.readying_task_priority := readying_task_priority;
1DA 7840       task_priority_changed := TRUE;
1E2 7841     IFEND;
1E2 7842     IF ijle_p^.scheduling_dispatching_priority < readying_task_priority THEN
1F2 7843       ijle_p^.scheduling_dispatching_priority := readying_task_priority;
1F2 7844       jmp$subsystem_priority_change (ijle_p);
208 7845     IFEND;
20C 7846     IFEND;
20C 7847
20C 7848     IF (ptl_p^.status = tmc$ts_executing) OR (ptl_p^.status = tmc$ts_ready_and_selected) THEN
21C 7849       IF ptl_p^.new_task_status < tmc$ts_first_ready_uncond THEN
226 7850         ptl_p^.new_task_status := tmc$ts_null;
22A 7851       IFEND;
22E 7852
22E 7853     ELSEIF ptl_p^.status = tmc$ts_ready THEN
232 7854
232 7855 { If the task being readied has subsystem locks set, it must be
232 7856 { removed from the ready string and reinserted. It is possible
232 7857 { that the task will execute at a different priority depending
232 7858 { on how long the task has had subsystem locks set, and
232 7859 { the priority of the readying task.
232 7860
232 7861     IF task_priority_changed AND (xcb_p^.system_table_lock_count > 0) AND
254 7862       (xcb_p^.system_table_lock_count < osc$system_table_lock_set) AND
254 7863       (xcb_p^.subsystem_lock_priority_count >= tmv$subsystem_prior_threshold) THEN
254 7864       tmp$remove_task_from_dct (taskid.index);
3C8 7865       attempt_preselection := (ijle_p^.multiprocessing_allowed) OR (ijle_p^.executing_task_count = 0);
*WARN= 7866       tmp$dct_ready_task (xcb_p, ijle_p, taskid.index, attempt_preselection);
DOC 7867     IFEND;
D10 7868
D10 7869     ELSEIF ptl_p^.status < tmc$ts_ready_but_swapped THEN
D16 7870
D16 7871 { If the task is in the timed wait queue, take it out. Adjust the tasks_not_in_long_wait count based upon
D16 7872 { the task status. If the job the task belongs to is swapped, call scheduler to swap the job in. If the
D16 7873 { job is in memory, insert the task into the DCT.
D16 7874
D16 7875
D16 7876     IF ptl_p^.status <= tmc$ts_last_status_in_wait_q THEN

```


TMP\$SET_TASK_READY

```

D1C 7877      tmp$remove_task_from_q (ptl_p, tmv$timed_wait_queue);
D66 7878      IF (ptl_p^.status = tmc$ts_timeout_reqexp_longvlong) THEN
D78 7879      ijle_p^.statistics.tasks_not_in_long_wait := ijle_p^.statistics.tasks_not_in_long_wait + 1;
D86 7880      IFEND;
D8A 7881      ELSE
D8A 7882      IF (ptl_p^.status = tmc$ts_timeout_reqexp_infvlong) OR (ptl_p^.status =
D86 7883      tmc$ts_timed_wait_not_queued) THEN
D86 7884      ijle_p^.statistics.tasks_not_in_long_wait := ijle_p^.statistics.tasks_not_in_long_wait + 1;
DA4 7885      IFEND;
DA4 7886      IFEND;
DA4 7887
DA4 7888      IF ptl_p^.idle_status = tmc$sis_idled THEN
DAE 7889      ijle_p^.statistics.ready_task_count := ijle_p^.statistics.ready_task_count + 1;
DAE 7890      ptl_p^.status := tmc$ts_ready_but_swapped;
DAE 7891      ptl_p^.idle_status := tmc$sis_idled_sched_notified;
DAE 7892      jmp$ready_task_in_swapped_job (ptl_p^.ijl_ordinal, ijle_p);
DE4 7893      ELSEIF ptl_p^.idle_status < tmc$sis_idled THEN
DE8 7894      ijle_p^.statistics.ready_task_count := ijle_p^.statistics.ready_task_count + 1;
DE8 7895      ptl_p^.status := tmc$ts_ready;
DE8 7896      jmv$a_jl_p [ijle_p^.ajl_ordinal].job_is_good_swap_candidate := FALSE;
DE8 7897      attempt_preselection := (ijle_p^.multiprocessing_allowed) OR (ijle_p^.executing_task_count = 0);
*WARN* 7898      tmp$dct_ready_task (xcb_p, ijle_p, taskid.index, attempt_preselection);
170A 7899      IFEND;
170A 7900
170A 7901      ?IF NOT debug THEN
170A 7902      IF mlv$ci170_rqst_blk.req <> NIL THEN
171C 7903      IF taskid = mlv$ci170_rqst_blk.req^.task_id THEN
1728 7904      mvv$mli_status.ready := TRUE;
1730 7905      IFEND;
1730 7906      IFEND;
1734 7907      ?IFEND
1734 7908
1734 7909      ELSEIF ptl_p^.status = tmc$ts_ready_but_swapped THEN
1738 7910      jmp$ready_task_in_swapped_job (ptl_p^.ijl_ordinal, ijle_p);
1754 7911      IFEND;
1754 7912
1754 7913      IF ptl_p^.status = tmc$ts_segment_lock_wait THEN
175E 7914      remove_task_from_seg_lock_q (taskid, ijle_p, ptl_p);
1774 7915      IFEND;
1774 7916
1774 7917      {The 'wait_inhibited' flag in the PTL is set to tmc$wi_wait_selected only after
1774 7918      {the successful completion of a wait request. This prevents potential timing
1774 7919      {conflicts between ready_task and wait requests.
1774 7920
1774 7921      IF (ready_condition = tmc$rc_ready_conditional_wi) AND
1790 7922      (NOT (ptl_p^.ptl_flags.wait_inhibited = tmc$wi_wait_selected) AND
1790 7923      NOT (ptl_p^.ptl_flags.wait_inhibited = tmc$wi_wait_selected_r3)) THEN
1790 7924      ptl_p^.ptl_flags.wait_inhibited := tmc$wi_wait_inhibited;
1798 7925      IFEND;
1798 7926      ijle_p^.long_wait_aging_complete := FALSE;
1798 7927      tmp$clear_lock (tmv$ptl_lock);
17D6 7928
17D6 7929      PROCEND tmp$set_task_ready;

```

TMP\$SET_TASK_READY_UNCOND

```

O 7931
O 7932      PROCEDURE [XDCL] tmp$set_task_ready_uncond
O 7933      (
O 7934      taskid: ost$global_task_id;
O 7935      task_status: tmt$task_status);
O 7936
O 7937      { The purpose of this procedure is to unconditionally ready the
O 7938      { specified task. The task to be readied must be in the task
O 7939      { status specified on this request.
O 7940
O 7941      {
O 7942      {
O 7943      { TASKID : (OUTPUT) This parameter specifies the taskid of the
O 7944      { task to be readied.
O 7945      {
O 7946      { TASKSTATUS: (INPUT) This parameter specifies the current status
O 7947      { of the task.
O 7948
O 7949      VAR
O 7950      attempt_preselection: boolean,
O 7951      ijle_p: ^ajmt$initiated_job_list_entry,
O 7952      xcb_p: ^ost$execution_control_block,
O 7953      ptl_p: ^tmt$primary_task_list_entry;
O 7954
O 7955
O 7956      tmp$set_lock (tmv$ptl_lock);
42 7957      tmp$stop_if_bad_taskid (taskid);
A8 7958      ptl_p := ^tmv$ptl_p [taskid.index];
A8 7959      jmp$get_ijle_p (ptl_p^.ijl_ordinal, ijle_p);
A8 7960      IF (task_status = tmc$ts_io_wait_not_queued) THEN
E2 7961      tmv$io_wait_task_count := tmv$io_wait_task_count - 1;
EC 7962      IFEND;
EC 7963      IF ptl_p^.status <> task_status THEN
F8 7964      IF (ptl_p^.status <> tmc$ts_executing) OR (ptl_p^.new_task_status <> task_status) THEN
106 7965      mtp$error_stop ('TM96 - task not queued');
126 7966      IFEND;
126 7967      ptl_p^.new_task_status := tmc$ts_null;
130 7968      ELSE
130 7969      IF ptl_p^.idle_status = tmc$sis_idled THEN
13C 7970      ijle_p^.statistics.ready_task_count := ijle_p^.statistics.ready_task_count + 1;
13C 7971      ptl_p^.status := tmc$ts_ready_but_swapped;
13C 7972      jmp$ready_task_in_swapped_job (ptl_p^.ijl_ordinal, ijle_p);
168 7973      ptl_p^.idle_status := tmc$sis_idled_sched_notified;
172 7974      ELSEIF ptl_p^.idle_status < tmc$sis_idled THEN
176 7975      ijle_p^.statistics.ready_task_count := ijle_p^.statistics.ready_task_count + 1;
176 7976      ptl_p^.status := tmc$ts_ready;
176 7977      attempt_preselection := (ijle_p^.multiprocessing_allowed) OR (ijle_p^.executing_task_count = 0);
19C 7978      xcb_p := #ADDRESS [1, tmc$job_fixed_segment + ijle_p^.ajl_ordinal, ptl_p^.xcb_offset];
19C 7979      tmp$dct_ready_task (xcb_p, ijle_p, taskid.index, attempt_preselection);
A76 7980      IFEND;
A78 7981      IFEND;
A78 7982
A78 7983      tmp$clear_lock (tmv$ptl_lock);
AAA 7984
AAA 7985      PROCEND tmp$set_task_ready_uncond;

```

TMP\$REISSUE_MONITOR_REQUEST

```

0 7987
0 7988 PROCEDURE [XDCL] tmp$reissue_monitor_request;
0 7989
0 7990 {
0 7991 { The purpose of this request is to cause the P register of the current task to
0 7992 { be decremented by 2. This will cause the monitor request issued by the task
0 7993 { to be reissued when the task is next executed. This request does NOT change
0 7994 { the status of the task or cause a task-switch. This request is intended
0 7995 { to be used when the resources required to process the current request from the
0 7996 { task are unavailable.
0 7997 {
0 7998 { tmp$reissue_monitor_request;
0 7999 {
0 8000
0 8001 VAR
0 8002 cst_p: ^ost$cpu_state_table;
0 8003
0 8004 mtp$cst_p [cst_p];
14 8005
14 8006 cst_p^.xcb_p^.xp.p_register.pva.offset := cst_p^.xcb_p^.xp.p_register.pva.offset - 2;
14 8007
14 8008 PROCEND tmp$reissue_monitor_request;

0 8010
0 8011 PROCEDURE [XDCL] tmp$cause_task_switch;
0 8012
0 8013 VAR
0 8014 cst_p: ^ost$cpu_state_table;
0 8015
0 8016 mtp$cst_p [cst_p];
14 8017 tmp$set_lock [tmv$ptl_lock];
80 8018 IF [tmv$ptl_p^ [cst_p^.taskid.index].new_task_status > tmc$ts_null] AND
82 8019 [tmv$ptl_p^ [cst_p^.taskid.index].new_task_status < tmc$ts_timeout_reqexp_shortshrt] THEN
82 8020 mtp$error_stop ['TM38 - bad call to cause_task_switch'];
A2 8021 IFEND;
A2 8022 tmv$ptl_p^ [cst_p^.taskid.index].new_task_status := tmc$ts_timeout_reqexp_shortshrt;
A2 8023 tmv$ptl_p^ [cst_p^.taskid.index].end_of_wait_time := #FREE_RUNNING_CLOCK [0] + tmv$cycle_delay_time;
C0 8024 cst_p^.dispatch_control.call_dispatcher := TRUE;
C0 8025 IF [cst_p^.next_ptio_to_dispatch = 0] THEN
D6 8026 cst_p^.dispatching_priority_integer := tmv$dct_priority_integer;
DE 8027 IFEND;
DE 8028
DE 8029 ?IF tmc$debug_cycle_requests THEN
8030 IF [osv$ddebug > 0] THEN
8031 tmv$cycle_trace [ti].code := tmc$cyc_cause_task_switch;
8032 tmv$cycle_trace [ti].time := #FREE_RUNNING_CLOCK [0];
8033 ti := ti + 1;
8034 IF ti > 10000 THEN
8035 mtp$error_stop ['TM - trace buffer full'];
8036 IFEND;
8037 IFEND;
DE 8038 ?IFEND;
DE 8039 tmp$clear_lock [tmv$ptl_lock];
110 8040

```

TMP\$REISSUE_MONITOR_REQUEST

```

110 8041 PROCEND tmp$cause_task_switch;

```

TMP\$QUEUE_TASK

```

0  8043
0  8044 PROCEDURE [XDCL] tmp$queue_task
0  8045 (
0  8046   taskid: ^ost$global_task_id;
0  8047   task_status: tmt$task_status;
0  8048   VAR queue_link: tmt$task_queue_link);
0  8049
0  8050 {
0  8051 { The purpose of this request is to add a task to the
0  8052 { end of a task queue.
0  8053 {
0  8054 {   TMP$QUEUE_TASK (TASKID, QUEUE_LINK)
0  8055 {
0  8056 { TASKID : (INPUT) This parameter specifies the taskid of
0  8057 {   the task to be placed in the queue.
0  8058 {
0  8059 { QUEUE_LINK : (INPUT) This parameter specifies the head of
0  8060 {   the task queue.
0  8061 {
0  8062 {
0  8063 {   VAR
0  8064 {     cst_p: ^ost$cpu_state_table,
0  8065 {     ptl_p: ^tmt$primary_task_list_entry;
0  8066 {
0  8067 {
0  8068 {     ptl_p := ^tmv$ptl_p^ [taskid.index];
0  8069 {
0  8070 {     tmp$set_lock (tmv$ptl_lock);
50  8071 {
50  8072 {     tmp$stop_if_bad_taskid (taskid);
B2  8073 {
B2  8074 {     IF (ptl_p^.queue_link.head <> 0) OR (ptl_p^.queue_link.tail <> 0) OR
D2  8075 {       (queue_link.tail = taskid.index) THEN
D2  8076 {       mtp$error_stop ('TM02 - already queued');
F2  8077 {       IFEND;
F2  8078 {       IF (task_status < tmc$ts_first_external_queue) OR (ptl_p^.new_task_status > tmc$ts_null) THEN
104 8079 {       mtp$error_stop ('TM49 - bad call to queue_task');
124 8080 {       IFEND;
124 8081 {
124 8082 {       IF queue_link.tail = 0 THEN
130 8083 {         queue_link.head := taskid.index;
130 8084 {         queue_link.tail := taskid.index;
140 8085 {       ELSE
140 8086 {         tmv$ptl_p^ [queue_link.tail].queue_link.head := taskid.index;
140 8087 {         ptl_p^.queue_link.tail := queue_link.tail;
140 8088 {         queue_link.tail := taskid.index;
15C 8089 {       IFEND;
15C 8090 {
15C 8091 {       mtp$cst_p (cst_p);
16C 8092 {       cst_p^.dispatch_control.call_dispatcher := TRUE;
18C 8093 {       IF (cst_p^.next_ptlo_to_dispatch = 0) THEN
18A 8094 {         cst_p^.dispatching_priority_integer := tmv$dct_priority_integer;
192 8095 {       IFEND;
192 8096 {       IF taskid <> cst_p^.taskid THEN
1A6 8097 {         mtp$error_stop ('TM - attempt to queue task not xtask');
1C6 8098 {       IFEND;

```

TMP\$QUEUE_TASK

```

1C6 8099 IF (task_status = tmc$ts_page_wait) OR (task_status = tmc$ts_io_wait_queued) THEN
1D8 8100   tmv$io_wait_task_count := tmv$io_wait_task_count + 1;
1E2 8101 IFEND;
1E2 8102   ptl_p^.new_task_status := task_status;
1E2 8103
1E2 8104   tmp$clear_lock (tmv$ptl_lock);
21E 8105
21E 8106 PROCEND tmp$queue_task;
0  8107

```

TMP\$DEQUEUE_TASK

```

O 8109
O 8110 PROCEDURE [XDCL] tmp$dequeue_task
O 8111 (VAR queue_link: tmt$task_queue_link;
O 8112 VAR taskid: ost$global_task_id);
O 8113
O 8114
O 8115 { The purpose of this procedure is to delink the task at the
O 8116 { head of the task queue.
O 8117 {
O 8118 { TMP$DEQUEUE_TASK (QUEUE_LINK, TASKID)
O 8119 {
O 8120 { QUEUE_LINK : (INPUT,OUTPUT) This parameter specifies the head
O 8121 { of the task queue.
O 8122 {
O 8123 { TASKID : (OUTPUT) This parameter specifies the taskid of the
O 8124 { delinked task.
O 8125 {
O 8126 {
O 8127 { VAR
O 8128 { attempt_preselection: boolean;
O 8129 { ijle_p: ^jmt$initiated_job_list_entry;
O 8130 { xcb_p: ^ost$execution_control_block;
O 8131 { ptl_p: ^tmt$primary_task_list_entry;
O 8132 {
O 8133 {
O 8134 { tmp$set_lock (tmt$ptl_lock);
42 8135 { taskid.index := queue_link.head;
42 8136 { ptl_p := ^tmt$ptl_p [taskid.index];
42 8137 { taskid.seqno := ptl_p.sequence_number;
42 8138 { tmp$stop_if_bad_taskid (taskid);
C8 8139 {
C8 8140 { jmp$set_ijle_p (ptl_p.ijl_ordinal, ijle_p);
C8 8141 {
C8 8142 { IF queue_link.head = queue_link.tail THEN
F6 8143 { queue_link.head := 0;
F6 8144 { queue_link.tail := 0;
104 8145 { ELSE
104 8146 { queue_link.head := ptl_p.queue_link.head;
104 8147 { tmt$ptl_p [queue_link.head].queue_link.tail := 0;
104 8148 { ptl_p.queue_link.head := 0;
120 8149 { IFEND;
120 8150 {
120 8151 { IF (ptl_p.status = tmc$ts_page_wait) OR (ptl_p.status = tmc$ts_io_wait_queued) OR
13E 8152 { (ptl_p.new_task_status = tmc$ts_page_wait) OR (ptl_p.new_task_status = tmc$ts_io_wait_queued) THEN
13E 8153 { tmt$io_wait_task_count := tmt$io_wait_task_count - 1;
148 8154 { IFEND;
148 8155 {
148 8156 { IF ptl_p.status < tmc$ts_first_external_queue THEN
152 8157 { IF (ptl_p.status <> tmc$ts_executing) OR (ptl_p.new_task_status < tmc$ts_first_external_queue) THEN
160 8158 { mtp$error_stop ('TMS2 - task not queued');
180 8159 { IFEND;
180 8160 { ptl_p.new_task_status := tmc$ts_null;
188 8161 { ELSE
188 8162 { IF ptl_p.idle_status = tmc$ts_idled THEN
194 8163 { ijle_p.statistics.ready_task_count := ijle_p.statistics.ready_task_count + 1;
194 8164 { ptl_p.status := tmc$ts_ready_but_swapped;

```

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TMP\$DEQUEUE_TASK

```

194 8165 { ptl_p.idle_status := tmc$ts_idled_sched_notified;
194 8166 { jmp$ready_task_in_swapped_job (ptl_p.ijl_ordinal, ijle_p);
1CA 8167 { ELSEIF ptl_p.idle_status < tmc$ts_idled THEN
1CE 8168 { ijle_p.statistics.ready_task_count := ijle_p.statistics.ready_task_count + 1;
1CE 8169 { ptl_p.status := tmc$ts_ready;
1CE 8170 { xcb_p := #ADDRESS [1, tmc$job_fixed_segment + ijle_p.ijl_ordinal, ptl_p.xcb_offset];
1CE 8171 { attempt_preselection := (ijle_p.multiprocessing_allowed) OR (ijle_p.executing_task_count = 0);
20C 8172 { tmp$dct_ready_task (xcb_p, ijle_p, taskid.index, attempt_preselection);
AB8 8173 { IFEND;
AB8 8174 { IFEND;
AB8 8175 {
AB8 8176 { tmp$clear_lock (tmt$ptl_lock);
AEA 8177 {
AEA 8178 { PROCEND tmp$dequeue_task;
O 8179 {

```

REMOVE_TASK_FROM_SEG_LOCK_Q

```

0 8181
0 8182 PROCEDURE remove_task_from_seg_lock_q
0 8183 {
0 8184 { taskid: ost$global_task_id;
0 8185 { ijle_p: ^jmt$initiated_job_list_entry;
0 8186 { ptl_p: ^tmt$primary_task_list_entry};
0 8187 {
0 8188 { The purpose of this procedure is to remove a task from the segment
0 8189 { lock queue. This task has been readied by another task. It will NOT
0 8190 { have the segment lock.
0 8191 {
0 8192 { REMOVE_TASK_FROM_SEG_LOCK_Q (taskid, ijle_p, ptl_p)
0 8193 {
0 8194 { TASKID: (INPUT) This parameter specifies the taskid of the
0 8195 { task which is being removed from the segment lock queue.
0 8196 {
0 8197 { IJLE_P: (INPUT) This parameter specifies the ij1 pointer of the job
0 8198 { owning the task being removed.
0 8199 {
0 8200 { PTL_P: (INPUT) This parameter specifies the pointer to the PTL entry
0 8201 { of the task being removed.
0 8202 {
0 8203 { NOTE : Anyone calling this procedure must have tmv$ptl_lock set.
0 8204 {
0 8205 {
0 8206 { VAR
0 8207 { asti: mmt$ast_index,
0 8208 { fde_p: gft$locked_file_desc_entry_p,
0 8209 { found: boolean,
0 8210 { queue_link: tmt$task_queue_link,
0 8211 { queue_link_save: ^tmt$task_queue_link,
0 8212 { sdtx_p: ^ammt$segment_descriptor_extended,
0 8213 { segnum: ost$segment,
0 8214 { task_rb: ^ammt$rb_lock_unlock_segment,
0 8215 { xcb_p: ^ost$execution_control_block;
0 8216 {
0 8217 { found := FALSE;
0 8218 { IF ptl_p.idle_status < tmc$is_idled THEN
12 8219 { xcb_p := #ADDRESS (1, tmc$job_fixed_segment + ijle_p^.aj1_ordinal, ptl_p^.xcb_offset);
12 8220 { task_rb := #LOC (xcb_p^.xp_registers [0]);
32 8221 { segnum := #SEGMENT (task_rb^.pva);
32 8222 { sdtx_p := mmp$get_sdtx_entry_p (xcb_p, segnum);
32 8223 { gfp$ptr_get_locked_fde_p (sdtx_p^.sfid, ijle_p, fde_p);
E2 8224 { tmp$remove_task_from_q (ptl_p, fde_p^.segment_lock.task_queue);
13C 8225 { ijle_p^.statistics.ready_task_count := ijle_p^.statistics.ready_task_count + 1;
13C 8226 { ptl_p^.status := tmc$ts_ready;
*WARN* 8227 { jmv$aj1_p [ijle_p^.aj1_ordinal].job_is_good_swap_candidate := FALSE;
83E 8228 { tmp$dst_ready_task (xcb_p, ijle_p, taskid.index, TRUE {attempt_preselection});
83E 8229 { ELSE
83E 8230 { /search_loop/
83E 8231 { FOR asti := LOWERBOUND (mmv$ast_p) TO UPPERBOUND (mmv$ast_p) DO
83E 8232 { IF (mmv$ast_p [asti].in_use) THEN
83E 8233 { gfp$ptr_get_locked_fde_p (mmv$ast_p [asti].sfid, ijle_p, fde_p);
83E 8234 { queue_link := fde_p^.segment_lock.task_queue;
83E 8235 { WHILE NOT found AND (queue_link.head <> 0) DO
83E 8236 { IF queue_link.head = taskid.index THEN
83E 8237 { found := TRUE;

```

REMOVE_TASK_FROM_SEG_LOCK_Q

```

COA 8237 EXIT /search_loop/;
C18 8238 ELSE
C18 8239 queue_link := tmv$ptl_p [queue_link.head].queue_link;
C32 8240 IFEND;
C32 8241 WHILEND;
C5C 8242 IFEND;
C5C 8243 FOREND /search_loop/;
C60 8244
C60 8245 IF NOT found THEN
C64 8246 mtp$error_stop ('TM112-task not found in seg lock Q');
C84 8247 IFEND;
C84 8248
C84 8249 tmp$remove_task_from_q (ptl_p, fde_p^.segment_lock.task_queue);
CDE 8250 ijle_p^.statistics.ready_task_count := ijle_p^.statistics.ready_task_count + 1;
CDE 8251 ptl_p^.status := tmc$ts_ready_but_swapped;
CDE 8252 ptl_p^.idle_status := tmc$is_idled_sched_notified;
CDE 8253 jmp$ready_task_in_swapped_job (ptl_p^.ij1_ordinal, ijle_p);
D14 8254 IFEND;
D14 8255
D14 8256 PROCEND remove_task_from_seg_lock_q;

```

TMP\$FIND_NEXT_QUEUED_TASK

```

O 8258
O 8259 PROCEDURE [XDCL] tmp$find_next_queued_task
O 8260   (VAR taskid: ost$global_task_id);
O 8261
O 8262   VAR
O 8263     pt1_p: ^tmt$primary_task_list_entry;
O 8264
O 8265     tmp$set_lock (tmv$pt1_lock);
42 8266     tmp$stop_if_bad_taskid (taskid);
AC 8267     pt1_p := ^tmv$pt1_p^ [taskid.index];
AC 8268     taskid.index := pt1_p.queue_link.head;
AC 8269     taskid.seqno := tmv$pt1_p^ [pt1_p.queue_link.head].sequence_number;
AC 8270     tmp$clear_lock (tmv$pt1_lock);
108 8271
108 8272 PROCEND tmp$find_next_queued_task;
O 8273

```

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[XDCL] tmp\$check_timed_wait_not_queued

```

O 8275
O 8276 PROCEDURE [XDCL] tmp$check_timed_wait_not_queued
O 8277   ( time_next_scan_wait_not_queued: integer);
O 8278
O 8279   VAR
O 8280     pt1o: ost$task_index;
O 8281
O 8282     tmp$set_lock (tmv$pt1_lock);
42 8283
42 8284     FOR pt1o := 1 TO UPPERBOUND (tmv$pt1_p^ ) DO
56 8285       IF (tmv$pt1_p^ [pt1o].status = tmc$ts_timed_wait_not_queued) AND (tmv$pt1_p^ [pt1o].
74 8286         end_of_wait_time <= time_next_scan_wait_not_queued) THEN
74 8287         tmv$pt1_p^ [pt1o].status := tmc$ts_timeout_reqexp_longvlong;
74 8288         tmp$insert_timed_wait_queue (pt1o);
144 8289       IFEND;
144 8290     FOREND;
14A 8291
14A 8292     tmp$clear_lock (tmv$pt1_lock);
182 8293
182 8294 PROCEND tmp$check_timed_wait_not_queued;
O 8295

```

TMP\$INSERT_TIMED_WAIT_QUEUE

```

0 8287
0 8288 PROCEDURE [INLINE] tmp$insert_timed_wait_queue
0 8289 ( ptlo: ost$task_index);
0 8300
0 8301 {
0 8302 { The purpose of this request is to add a task to
0 8303 { tmv$timed_wait_queue. Tasks are arranged in the queue
0 8304 { in ascending order according to their 'end_of_wait_time'.
0 8305 {
0 8306 { TMP$INSERT_TIMED_WAIT_QUEUE (PTLO)
0 8307 {
0 8308 { PTLO : (INPUT) This parameter specifies the ptl ordinal of
0 8309 { the task to be placed in the queue.
0 8310 {
0 8311 { NOTE : Anyone calling this routine must have tmv$ptl_lock set.
0 8312 {
0 8313 {
0 8314 VAR
0 8315 next_index: ost$task_index,
0 8316 previous_index: ost$task_index,
0 8317 end_of_wait_time: integer;
0 8318
0 8319 next_index := tmv$timed_wait_queue.head;
0 8320 previous_index := 0;
0 8321 end_of_wait_time := tmv$ptl_p^ [ptlo].end_of_wait_time;
0 8322
0 8323 IF tmv$timed_wait_queue.head = 0 THEN
0 8324 tmv$timed_wait_queue.head := ptlo;
0 8325 tmv$timed_wait_queue.tail := ptlo;
0 8326 ELSE
0 8327 WHILE (tmv$ptl_p^ [next_index].end_of_wait_time <= end_of_wait_time) AND
0 8328 (next_index <> 0) AND (next_index <> ptlo) DO
0 8329 previous_index := next_index;
0 8330 next_index := tmv$ptl_p^ [next_index].queue_link.head;
0 8331 WHILEND;
0 8332
0 8333 IF ptlo = next_index THEN {Dont put this in the while loop - it kills optimization.}
0 8334 mtp$error_stop ('TMO2.5 - already queued');
0 8335 IFEND;
0 8336
0 8337 tmv$ptl_p^ [ptlo].queue_link.head := next_index;
0 8338 IF next_index = tmv$timed_wait_queue.head THEN
0 8339 tmv$timed_wait_queue.head := ptlo;
0 8340 ELSE
0 8341 tmv$ptl_p^ [previous_index].queue_link.head := ptlo;
0 8342 IFEND;
0 8343 tmv$ptl_p^ [ptlo].queue_link.tail := previous_index;
0 8344 IF previous_index = tmv$timed_wait_queue.tail THEN
0 8345 tmv$timed_wait_queue.tail := ptlo;
0 8346 ELSE
0 8347 tmv$ptl_p^ [next_index].queue_link.tail := ptlo;
0 8348 IFEND;
0 8349 IFEND;
0 8350
0 8351 PROCEND tmp$insert_timed_wait_queue;
0 8352

```

[INLINE] tmp\$remove_task_from_q

```

0 8354 { Purpose:
0 8355 { This procedure removes a task from either the timed wait queue, or
0 8356 { a segment lock queue.
0 8357
0 8358 PROCEDURE [INLINE] tmp$remove_task_from_q
0 8359 ( ptle_p: ^tmt$primary_task_list_entry;
0 8360 VAR queue: tmt$task_queue_link);
0 8361
0 8362 IF ptle_p^.queue_link.head = 0 THEN
0 8363 queue.tail := ptle_p^.queue_link.tail;
0 8364 ELSE
0 8365 tmv$ptl_p^ [ptle_p^.queue_link.head].queue_link.tail := ptle_p^.queue_link.tail;
0 8366 IFEND;
0 8367
0 8368 IF ptle_p^.queue_link.tail = 0 THEN
0 8369 queue.head := ptle_p^.queue_link.head;
0 8370 ELSE
0 8371 tmv$ptl_p^ [ptle_p^.queue_link.tail].queue_link.head := ptle_p^.queue_link.head;
0 8372 IFEND;
0 8373
0 8374 ptle_p^.queue_link.head := 0;
0 8375 ptle_p^.queue_link.tail := 0;
0 8376
0 8377 PROCEND tmp$remove_task_from_q;
0 8378

```

TMP\$CREATE_JOB

```

0 8380
0 8381 PROCEDURE [XDCL] tmp$create_job
0 8382 (VAR rb: tmt$rb_initiate_job;
0 8383 cst_p: Aost$cpu_state_table);
0 8384
0 8385
0 8386 [
0 8387 [ This procedure is used to schedule a newly created job monitor
0 8388 [ task. A PTL entry is assigned to the job monitor task and it
0 8389 [ is inserted in the DCT chain. The segment table entry for the
0 8390 [ new job's JFS is deleted from the address space of the current
0 8391 [ task that initiated this job. Execution of the new job may
0 8392 [ actually begin before the requesting task continues execution.
0 8393 [
0 8394 [ TMP$CREATE_JOB (RB)
0 8395 [
0 8396 [ RB : (INPUT,OUTPUT) This parameter specifies the request block.
0 8397 [
0 8398 [
0 8399 [ VAR
0 8400 [ attempt_preselection: boolean,
0 8401 [ sdt_p: mmt$max_sdt_p,
0 8402 [ ktt: packed record
0 8403 [ case boolean of
0 8404 [ = TRUE :
0 8405 [ s: string (5),
0 8406 [ = FALSE :
0 8407 [ f1: 0 .. Offfff(16),
0 8408 [ f2: 0 .. Offf(16),
0 8409 [ casend,
0 8410 [ recend,
0 8411 [ job_segnum: ost$segment,
0 8412 [ segnum: ost$segment,
0 8413 [ ijl_ordinal: jmt$ijl_ordinal,
0 8414 [ ijle_p: Ajmt$initiated_job_list_entry,
0 8415 [ xcb_p: Aost$execution_control_block,
0 8416 [ ajl_ordinal: jmt$ajl_ordinal,
0 8417 [ jm: ost$keypoint_mask,
0 8418 [ jcb_p: Ajmt$job_control_block;
0 8419 [
0 8420 [
0 8421 [
0 8422 [
0 8423 [ Make an entry in the monitor segment table for the JOB FIXED SEGMENT of the new job.
0 8424 [
0 8425 [ sdt_p := #ADDRESS (1, cst_p^.ajlo + mtc$job_fixed_segment, cst_p^.xcb_p^.sdt_offset);
0 8426 [ job_segnum := #SEGMENT (rb.xcb_p);
0 8427 [ jmp$assign_ajl_entry (sdt_p.st [job_segnum].ste.asid, rb.ijlo, jmc$swapping_ajl,
70 8428 [ FALSE (must assign), ajl_ordinal, rb.status);
70 8429 [ IF NOT rb.status.normal THEN
78 8430 [ RETURN;
7A 8431 [ IFEND;
7A 8432 [
7A 8433 [ rb.ajo := ajl_ordinal;
7A 8434 [
7A 8435 [ Make an entry in the PTL for the new task.

```

SOURCE LIST OF tmm\$dispatcher

TMP\$CREATE_JOB

```

7A 8436
7A 8437 segnum := rb.ajo + mtc$job_fixed_segment;
7A 8438 ijl_ordinal := jmv$ajl_p [rb.ajo].ijl_ordinal;
7A 8439 jmp$get_ijle_p (ijl_ordinal, ijle_p);
7A 8440 IF syv$all_jobs_selected_for_debug THEN
C4 8441 [ ijle_p^.system_breakpoint_selected := TRUE;
C8 8442 [ IFEND;
C8 8443 [ xcb_p := #ADDRESS (1, segnum, #OFFSET (rb.xcb_p));
C8 8444 [ tmp$set_lock (tmv$ptl_lock);
114 8445 [ tmp$assign_ptl (xcb_p, ijl_ordinal, rb.jmtr_taskid, rb.status);
1E8 8446 [ IF NOT rb.status.normal THEN
1F0 8447 [ mtv$monitor_segment_table.st [segnum].ste.v1 := osc$vl_invalid_entry;
1F0 8448 [ #PURGE_BUFFER (osc$purge_all_page_seg_map, null_pval);
21C 8449 [ jmp$free_ajl_with_lock (ijle_p, jmc$swapping_ajl);
234 8450 [ rb.ajo := jmc$null_ajl_ordinal;
234 8451 [ tmp$clear_lock (tmv$ptl_lock);
2E8 8452 [ RETURN;
270 8453 [ IFEND;
270 8454 [ xcb_p^.global_task_id := rb.jmtr_taskid;
270 8455 [ tmv$total_task_count := tmv$total_task_count + 1;
270 8456 [
270 8457 [ Initialize the IJL entry and the JCB for the job.
270 8458 [
270 8459 [ jcb_p := #ADDRESS (1, rb.ajo + mtc$job_fixed_segment, 0);
270 8460 [ jcb_p^.jcb_identifier := Off00(16);
270 8461 [ jcb_p^.job_monitor_id := rb.jmtr_taskid;
270 8462 [ jcb_p^.last_execution_time := #FREE_RUNNING_CLOCK (0);
2B0 8463 [ jcb_p^.ijle_p := ijle_p;
2B0 8464 [ jcb_p^.ijl_ordinal := ijl_ordinal;
2B0 8465 [ ijle_p^.swap_status := jmc$iss_executing;
2B0 8466 [ ijle_p^.sfd_p := NIL;
2B0 8467 [ ijle_p^.job_fixed_contiguous_pages := 0;
2B0 8468 [ ijle_p^.statistics.ready_task_count := 1;
2B0 8469 [ ijle_p^.statistics.tasks_not_in_long_wait := 1;
2B0 8470 [ ijle_p^.job_monitor_taskid := rb.jmtr_taskid;
2B0 8471 [ jmp$change_ajl_entry_status (ijle_p, jmc$ies_job_in_memory_non_swap);
33C 8472 [ IF syv$perf_keypoints_enabled.swapping_keypoints THEN
348 8473 [ ktt.s := ijle_p^.system_supplied_name (16, 4);
35E 8474 [ #KEYPOINT (osk$performance, osk$m + [ktt.f1, ptk$new_job_name_1];
36E 8475 [ #KEYPOINT (osk$performance, osk$m + [ktt.f2 * 256] + rb.ajo, ptk$new_job_name_2);
380 8476 [ IFEND;
380 8477 [ jm := $ost$keypoint_mask [];
380 8478 [ IF (osv$keypoint_control.environment = osc$system_keypoints) OR
394 8479 [ (osv$keypoint_control.environment = osc$system_sample_keypoints) THEN
394 8480 [ jm := osv$keypoint_control.jm;
394 8481 [ xcb_p^.keypoint_register_enable := TRUE;
39C 8482 [ IFEND;
39C 8483 [ IF jm <> $ost$keypoint_mask [] THEN
3A4 8484 [ xcb_p^.xp.flags := xcb_p^.xp.flags + $ost$flags [osc$keypoint_enable];
3BA 8485 [ ELSE
3BA 8486 [ xcb_p^.xp.flags := xcb_p^.xp.flags - $ost$flags [osc$keypoint_enable];
3CC 8487 [ IFEND;
3CC 8488 [ xcb_p^.xp.keypoint_mask := jm;
3CC 8489 [
3CC 8490 [ Set up the new job's segment table from calling task's segment table. The xcb segment in calling
3CC 8491 [ task will be put in the proper place in the new job's segment table.

```


TMPSCREATE_JOB

```

3CC 8492      mmp$create_job (rb.ajo, job_segnum, cst_p^.xcb_p, xcb_p);
3CC 8493
3F4 8494
3F4 8495 { Insert the PTL entry for the new job into the Dispatch Tables.
3F4 8496      xcb_p^.timeslice := jmv$service_classes [ijle_p^.job_scheduler_data.service_class]^attributes.
3F4 8497      dispatching_control [ijle_p^.dispatching_control.dispatching_control_index].dispatching_timeslice;
3F4 8498      attempt_preselection := [ijle_p^.multiprocessing_allowed] OR [ijle_p^.executing_task_count = 0];
430 8499      tmp$dot_ready_task (xcb_p, ijle_p, rb.jmtr_taskid.index, attempt_preselection);
CD6 8500
CD6 8501 { Increment the count of initiated jobs that scheduler checks. This count must be changed in
CD6 8502 { monitor with the ptl lock set.
CD6 8503
CD6 8504      jmv$job_counts.service_class_counts [ijle_p^.job_scheduler_data.service_class].scheduler_initiated_jobs :=
CD6 8505      jmv$job_counts.service_class_counts [ijle_p^.job_scheduler_data.service_class].
CD6 8506      scheduler_initiated_jobs + 1;
CD6 8507
CD6 8508      tmp$clear_lock (tmv$ptl_lock);
D22 8509
D22 8510      PROCEND tmp$create_job;

```

TMPSEXIT_JOB

```

0 8512
0 8513      PROCEDURE [XDCL] tmp$exit_job
0 8514      (VAR rb: tmt$rb_exit_job;
0 8515      cst_p: ^ost$cpu_state_table);
0 8516
0 8517
0 8518 {
0 8519 { The purpose of this procedure is to exit a job monitor task.
0 8520 { The PTL entry for the job monitor task is deleted and the job
0 8521 { status in the ajl entry is set to terminated.
0 8522 {
0 8523 {      TMPSEXIT_JOB (RB)
0 8524 {
0 8525 {      RB : (INPUT,OUTPUT) This parameter specifies the request block.
0 8526 {
0 8527
0 8528      VAR
0 8529      xcb_p: ^ost$execution_control_block;
0 8530
0 8531
0 8532      tmp$set_lock (tmv$ptl_lock);
42 8533      IF tmv$ptl_p^ [cst_p^.taskid.index].idle_status = tmc$is_idled THEN
60 8534      mtp$error_stop ('TM04 - swapped job exiting');
80 8535      IFEND;
80 8536      IF tmv$ptl_p^ [cst_p^.taskid.index].ijl_thread < 0 THEN
94 8537      mtp$error_stop ('TM - exit job with active task(s).');
B4 8538      IFEND;
B4 8539
B4 8540 { Free the PTL entry for the task.
B4 8541
B4 8542      tmv$total_task_count := tmv$total_task_count - 1;
B4 8543      cst_p^.ijle_p^.executing_task_count := cst_p^.ijle_p^.executing_task_count - 1;
B4 8544      free_ptl (cst_p^.taskid.index);
102 8545
102 8546 { Decrement the job count that scheduler uses. This count must be changed in monitor
102 8547 { with the ptl lock set.
102 8548
102 8549      jmv$job_counts.service_class_counts [cst_p^.ijle_p^.job_scheduler_data.service_class].
102 8550      scheduler_initiated_jobs := jmv$job_counts.service_class_counts [cst_p^.ijle_p^.
102 8551      job_scheduler_data.service_class].scheduler_initiated_jobs - 1;
102 8552
102 8553      tmp$clear_lock (tmv$ptl_lock);
154 8554
154 8555 { Delete the current task from the ready string and cause a task switch.
154 8556
154 8557      xcb_p := cst_p^.xcb_p;
154 8558      cst_p^.xcb_p := NIL;
154 8559      cst_p^.dispatch_control.call_dispatcher := TRUE;
154 8560      IF [cst_p^.next_ptlo_to_dispatch = 0] THEN
16C 8561      cst_p^.dispatching_priority_integer := tmv$dot_priority_integer;
174 8562      IFEND;
174 8563
174 8564 { Free memory resources of any segments still in jobs address space that are unique to the job.
174 8565 { Deletes the job fixed segment from monitor's address space too, can no longer reference the job's xcb.
174 8566
174 8567      mmp$exit_job (xcb_p);

```

TMP\$EXIT_JOB

```

184 8568
184 8569 [ Update the service class statistics for the service class this job belongs to.
184 8570
184 8571     jmp$update_service_class_stats (cst_p^,ijle_p);
188 8572
188 8573 [ Free the AJL and notify scheduler that the job has terminated.
188 8574
188 8575     jmp$free_ajl_entry (cst_p^,ijle_p, jmc$swapping_ajl);
180 8576     jmp$set_job_terminated (cst_p^,ijl_ordinal, cst_p^,ijle_p);
1CC 8577
1CC 8578 PROCEND tmp$exit_job;

```

SOURCE LIST OF tmm\$dispatcher

TMP\$CREATE_TASK

```

0 8580
0 8581 PROCEDURE [XDCL] tmp$create_task
0 8582   (VAR rb: tmt$rb_initiate_task;
0 8583    cst_p: ^ost$cpu_state_table);
0 8584
0 8585
0 8586 {
0 8587 [ This procedure is used to schedule a newly created task.
0 8588 [ A PTL entry is assigned to the task and it is inserted
0 8589 [ in the DCT chain. The PTL entry is also linked to the
0 8590 [ AJL thread for this job.
0 8591 {
0 8592   TMP$CREATE_TASK (RB)
0 8593 {
0 8594 [ RB : (INPUT,OUTPUT) This parameter specifies the request block.
0 8595 {
0 8596
0 8597   VAR
0 8598     attempt_preselection: boolean,
0 8599     jm: ost$keypoint_mask,
0 8600     wrb: tmt$rb_delay,
0 8601     offset: integer,
0 8602     xcb_p: ^ost$execution_control_block;
0 8603
0 8604
0 8605     offset := #OFFSET (rb.xcb_p);
0 8606     xcb_p := #ADDRESS (1, cst_p^.ajlo + mtc$job_fixed_segment, offset);
0 8607     tmp$set_lock (tmv$ptl_lock);
66 8608     tmp$assign_ptl (xcb_p, cst_p^.ijl_ordinal, rb.taskid, rb.status);
148 8609     IF NOT rb.status.normal THEN
150 8610       tmp$clear_lock (tmv$ptl_lock);
182 8611     RETURN;
184 8612   IFEND;
184 8613
184 8614     IF tmv$ptl_p^ [cst_p^.taskid.index].idle_status = tmc$sis_idle_initiated THEN
188 8615       tmv$ptl_p^ [rb.taskid.index].idle_status := tmc$sis_idle_sched_notified;
188 8616       tmv$ptl_p^ [rb.taskid.index].status := tmc$sts_ready_but_swapped;
188 8617       jmp$ready_task_in_swapped_job (cst_p^.ijl_ordinal, cst_p^.ijle_p);
1CC 8618     ELSE
1CC 8619       IF cst_p^.ijl_ordinal = jmv$system_ajl_ordinal THEN
1D8 8620         cst_p^.ijle_p^.job_scheduler_data.service_class := 1;
1E0 8621       IFEND;
1E0 8622       xcb_p^.subsystem_lock_priority_count := 0;
1E0 8623       xcb_p^.dispatching_priority := cst_p^.xcb_p^.dispatching_priority;
1E0 8624       xcb_p^.timeslice := jmv$service_classes [cst_p^.ijle_p^.job_scheduler_data.service_class]^.attributes.
1E0 8625         dispatching_control [cst_p^.ijle_p^.dispatching_control.dispatching_control_index].
1E0 8626         dispatching_timeslice;
1E0 8627       attempt_preselection := (cst_p^.ijle_p^.multiprocessing_allowed) OR
22E 8628         (cst_p^.ijle_p^.executing_task_count = 0);
22E 8629       IF tmv$tables_initialized THEN
236 8630         tmp$dct_ready_task (xcb_p, cst_p^.ijle_p, rb.taskid.index, attempt_preselection);
AEA 8631       IFEND;
AEA 8632     IFEND;
AEA 8633
AEA 8634     IF tmv$tables_initialized THEN
AF2 8635       insert_ajl (rb.taskid, cst_p);

```

TMP\$CREATE_TASK

```

B2A 8636 ELSE
B2A 8637 tmv$tables_initialized := TRUE;
B2E 8638 IFEND;
B2E 8639
B2E 8640 tmv$total_task_count := tmv$total_task_count + 1;
B2E 8641
B2E 8642 cst_p^.ijle_p^.statistics.ready_task_count := cst_p^.ijle_p^.statistics.ready_task_count + 1;
B2E 8643 cst_p^.ijle_p^.statistics.tasks_not_in_long_wait := cst_p^.ijle_p^.statistics.tasks_not_in_long_wait + 1;
B2E 8644 cst_p^.ijle_p^.task_created_after_last_swap := TRUE;
B2E 8645
B2E 8646 xcb_p^.global_task_id := rb.taskid;
B2E 8647 xcb_p^.parent_global_task_id := cst_p^.taskid;
B2E 8648
B2E 8649 { Each task inherits its parents keypoint_enable flag.
B2E 8650 xcb_p^.keypoint_enable := cst_p^.xcb_p^.keypoint_enable;
B2E 8651
B2E 8652 IF (osv$keypoint_control.environment = osc$system_keypoints) OR
B84 8653 (osv$keypoint_control.environment = osc$system_sample_keypoints) THEN
B84 8654 jm := osv$keypoint_control.jm;
B84 8655 xcb_p^.keypoint_register_enable := TRUE;
B90 8656 ELSE
B90 8657 { check if correct job
B90 8658 IF xcb_p^.keypoint_enable = TRUE THEN
B94 8659 { correct - Update masks
B94 8660 jm := osv$keypoint_control.jm;
B94 8661 xcb_p^.keypoint_register_enable := TRUE;
BA0 8662 ELSE
BA0 8663 { different - clear masks
BA0 8664 jm := $ost$keypoint_mask [];
BA0 8665 xcb_p^.keypoint_register_enable := FALSE;
BA6 8666 IFEND;
BA6 8667 IFEND;
BA6 8668 IF jm (<) $ost$keypoint_mask [] THEN
BAE 8669 xcb_p^.xp.flags := xcb_p^.xp.flags + $ost$flags [osc$keypoint_enable];
BC6 8670 ELSE
BC6 8671 xcb_p^.xp.flags := xcb_p^.xp.flags - $ost$flags [osc$keypoint_enable];
BDA 8672 IFEND;
BDA 8673 xcb_p^.xp.keypoint_mask := jm;
BDA 8674
BDA 8675 mmp$create_task (cst_p^.xcb_p, xcb_p, cst_p^.ijle_p);
BFE 8676
BFE 8677 tmp$clear_lock (tmv$pt1_lock);
C30 8678
C30 8679 PROCEND tmp$create_task;

```

TMP\$TASK_EXIT

```

O 8681
O 8682 PROCEDURE [XDCL] tmp$task_exit
O 8683 (VAR rb: tmt$rb_task_exit;
O 8684 cst_p: ^ost$cpu_state_table);
O 8685
O 8686
O 8687 {
O 8688 { The purpose of this procedure is to exit the current task.
O 8689 { The PTL entry for the task is deleted and the parent task
O 8690 { is notified of callee termination.
O 8691 {
O 8692 { TMP$TASK_EXIT (RB)
O 8693 {
O 8694 { RB: [INPUT,OUTPUT] This parameter specifies the request block.
O 8695 {
O 8696
O 8697 VAR
O 8698 osv$trap_task_errors: [XDCL, #GATE] boolean := FALSE,
O 8699 pit_value: integer;
O 8700
O 8701
O 8702 rb.status.normal := TRUE;
4 8703 IF rb.parent_global_task_id (<) cst_p^.xcb_p^.parent_global_task_id THEN
26 8704 mtp$set_status_abnormal ('TM', tme$invalid_global_taskid, rb.status);
26 8705 RETURN;
3E 8706 IFEND;
3E 8707
3E 8708 IF osv$trap_task_errors AND ((cst_p^.xcb_p^.system_table_lock_count DIV 256) (<)
5C 8709 0) THEN
5C 8710 mtp$error_stop (' Task exit with system tables locked ');
7C 8711 IFEND;
7C 8712
7C 8713 tmp$update_system_task_list (cst_p^.xcb_p);
90 8714
90 8715 { Send signal to the parent task.
90 8716
90 8717 tmp$send_signal (rb.parent_global_task_id, rb.signal, rb.status);
BE 8718 IF NOT rb.status.normal THEN
C6 8719 RETURN;
C8 8720 IFEND;
C8 8721
C8 8722
C8 8723 mmp$exit_task (cst_p^.xcb_p);
DC 8724 IF osv$special_aam_trap AND (cst_p^.xcb_p^.special_trap_count (<) 0) THEN
FO 8725 mtp$error_stop (' Trap AAM Lock problem ');
110 8726 IFEND;
110 8727 tmp$set_lock (tmv$pt1_lock);
148 8728 cst_p^.ijle_p^.maxws_aio_slowdown_display := 0;
148 8729 cst_p^.ijle_p^.statistics.ready_task_count := cst_p^.ijle_p^.statistics.ready_task_count - 1;
148 8730 cst_p^.ijle_p^.statistics.tasks_not_in_long_wait := cst_p^.ijle_p^.statistics.tasks_not_in_long_wait - 1;
148 8731 cst_p^.ijle_p^.task_created_after_last_swap := FALSE;
148 8732 cst_p^.accumulated_monitor_cptime := 0ffffff[16] - #READ_REGISTER (osc$pr_process_interval_timer);
170 8733 pit_value := cst_p^.xcb_p^.xp.process_interval_timer_1 * 10000[16] + cst_p^.xcb_p^.xp.
170 8734 process_interval_timer_2;
170 8735 IF pit_value > 7ffffff[16] THEN
194 8736 pit_value := pit_value - 10000000[16];

```

TMP\$TASK_EXIT

```

19A 8737 IFEND;
19A 8738 cst_p^accumulated_job_cptime := cst_p^accumulated_job_cptime - pit_value;
*WARN= 8739 update_cp_statistics (cst_p);
840 8740 cst_p^ijle_p^executing_task_count := cst_p^ijle_p^executing_task_count - 1;
840 8741 free_ptl (cst_p^taskid.index);
882 8742 remove_ijl (cst_p^taskid, cst_p);
88C 8743 cst_p^xcb_p^task_has_terminated := TRUE;
88C 8744 cst_p^jcb_p^last_lpid_for_job := cst_p^cst_index;
88C 8745 cst_p^xcb_p := NIL;
88C 8746 cst_p^dispatch_control.call_dispatcher := TRUE;
88C 8747 IF (cst_p^next_ptlo_to_dispatch = 0) THEN
8FC 8748   cst_p^dispatching_priority_integer := tmv$dct_priority_integer;
704 8749 IFEND;
704 8750
704 8751 tmv$total_task_count := tmv$total_task_count - 1;
704 8752
704 8753 IF tmv$ptl_p [cst_p^taskid.index].idle_status = tmc$sis_idle_initiated THEN
722 8754   initiate_swap_if_possible (cst_p);
7A6 8755 IFEND;
7A6 8756 tmp$clear_lock (tmv$ptl_lock);
7D8 8757
7D8 8758 PROCEND tmp$task_exit;

```

SOURCE LIST OF tmm\$dispatcher

TMP\$MTR_BEGIN_SYSTEM_ACTIVITY

```

O 8760
O 8761 PROCEDURE [XDCL] tmp$mtr_begin_lock_activity
4 8762 (   xcb_p: ^ost$execution_control_block;
4 8763   activity: 1 .. 256);
4 8764
4 8765   xcb_p^system_table_lock_count := xcb_p^system_table_lock_count + activity;
4 8766
4 8767 PROCEND tmp$mtr_begin_lock_activity;

O 8769
O 8770 PROCEDURE [XDCL] tmp$mtr_end_lock_activity
O 8771 (   cst_p: ^ost$cpu_state_table;
O 8772   activity: 256;
O 8773   VAR xcb_p: ^ost$execution_control_block);
O 8774
O 8775   VAR
O 8776   ijle_p: ^jmt$initiated_job_list_entry,
O 8777   new_scheduling_priority: jmt$dispatching_priority,
O 8778   ptlo: ost$task_index,
O 8779   state: tmt$find_next_xcb_state,
O 8780   status: syt$monitor_status,
O 8781   temp_xcb_p: ^ost$execution_control_block;
O 8782
O 8783   tmp$set_lock (tmv$ptl_lock);
42 8784
42 8785 { Debug code--verify the lock count.
42 8786
42 8787   IF activity = 256 THEN
4E 8788     IF xcb_p^system_table_lock_count < 256 THEN
5E 8789       mtp$error_stop ('TM--system_table_lock_count 1');
7E 8790     IFEND;
82 8791   ELSEIF activity = 1 THEN
86 8792     IF ((xcb_p^system_table_lock_count MOD 256) = 0) THEN
AO 8793       mtp$error_stop ('TM--system_table_lock_count 2');
CO 8794     IFEND;
C4 8795   ELSE
C4 8796     mtp$error_stop ('TM--system_table_lock_count 3');
E4 8797   IFEND;
E4 8798
E4 8799 { End debug code.
E4 8800
E4 8801
E4 8802   xcb_p^system_table_lock_count := xcb_p^system_table_lock_count - activity;
E4 8803   ptlo := cst_p^taskid.index;
E4 8804
E4 8805   IF (xcb_p^system_give_up_cpu) AND
116 8806     (xcb_p^system_table_lock_count < 256) AND
116 8807     (xcb_p^system_table_lock_count > 0) THEN
116 8808
116 8809 { Reset the task to it's original priority. If the task has subsystem locks set
116 8810 { its correct PTL dispatching priority will be determined when the task is readied.
116 8811 { The TMP$DCT_READY_TASK procedure will sort out the priorities.
116 8812
116 8813   tmv$ptl_p [ptlo].dispatching_priority := xcb_p^dispatching_priority;

```

TMP\$MTR_BEGIN_SYSTEM_ACTIVITY

```

116 8814      xcb_p^system_give_up_cpu := FALSE;
116 8815      cst_p^dispatch_control.call_dispatcher := TRUE;
116 8816      IF (cst_p^next_ptlo_to_dispatch = 0) THEN
13C 8817          cst_p^dispatching_priority_integer := tmv$dispatch_priority_integer
14C 8818          [xcb_p^dispatching_priority];
14C 8819      IFEND;
150 8820      ELSEIF (xcb_p^system_table_lock_count <= 0) AND
16C 8821          (xcb_p^system_give_up_cpu OR xcb_p^subsystem_give_up_cpu) THEN
16C 8822
16C 8823          xcb_p^subsystem_give_up_cpu := FALSE;
16C 8824          xcb_p^system_give_up_cpu := FALSE;
16C 8825          cst_p^dispatch_control.call_dispatcher := TRUE;
16C 8826          IF (cst_p^next_ptlo_to_dispatch = 0) THEN
186 8827              cst_p^dispatching_priority_integer := tmv$dispatch_priority_integer
19A 8828              [xcb_p^dispatching_priority];
19A 8829          IFEND;
19A 8830
19A 8831 [ Reset the task to it's original priority. Also, reset the subsystem lock priority
19A 8832 [ which is maintained in the PTL.
19A 8833
19A 8834      tmv$ptl_p^ [ptlo].dispatching_priority := xcb_p^dispatching_priority;
19A 8835      tmv$ptl_p^ [ptlo].readying_task_priority := 0;
19A 8836
19A 8837      jmp$get_ijle_p (tmv$ptl_p^ [xcb_p^global_task_id.index].ijl_ordinal, ijle_p);
19A 8838      new_scheduling_priority := ijle_p^dispatching_control.dispatching_priority;
19A 8839      tmp$find_next_xcb (tmc$fnx_job, ijle_p, tmv$ptl_p^ [xcb_p^global_task_id.index].ijl_ordinal,
20E 8840          state, temp_xcb_p);
20E 8841      WHILE temp_xcb_p <> NIL DO
21C 8842          IF tmv$ptl_p^ [temp_xcb_p^global_task_id.index].readying_task_priority >
23A 8843              new_scheduling_priority THEN
23A 8844              new_scheduling_priority := tmv$ptl_p^ [temp_xcb_p^global_task_id.index].readying_task_priority;
238 8845          IFEND;
238 8846          tmp$find_next_xcb (tmc$fnx_continue, NIL, jmv$null_ijl_ordinal, state, temp_xcb_p);
268 8847      WHILEND;
27E 8848      ijle_p^scheduling_dispatching_priority := new_scheduling_priority;
27E 8849      xcb_p^subsystem_lock_priority_count := 0;
27E 8850      tmv$ptl_p^ [ptlo].ptl_flags.subsystem_locks_set := FALSE;
2A4 8851      IFEND;
2A4 8852
2A4 8853      IF xcb_p^stlc_allocation THEN
2B4 8854          xcb_p^stlc_allocation := FALSE;
2B4 8855          tmp$set_monitor_flag (xcb_p^global_task_id, mmc$mf_segment_mgr_flag, status);
2DA 8856          IFEND;
2DA 8857
2DA 8858      ?IF tmc$debug_cycle_requests THEN
2DA 8859          IF (osv$debug > 0) THEN
2860          tmp$set_lock (tmv$ptl_lock);
2861          tmv$cycle_trace [ti].code := tmc$sync_mtr_end_sys_activity;
2862          tmv$cycle_trace [ti].time := #FREE_RUNNING_CLOCK (0);
2863          ti := ti + 1;
2864          IF ti > 10000 THEN
2865              tmp$error_stop ('TM - trace buffer full');
2866          IFEND;
2867          tmp$clear_lock (tmv$ptl_lock);
2868          IFEND;
2DA 8869      ?IFEND;

```

TMP\$MTR_BEGIN_SYSTEM_ACTIVITY

```

2DA 8870      tmp$clear_lock (tmv$ptl_lock);
314 8871
314 8872      PROCEND tmp$mtr_end_lock_activity;

```

TMP\$CYCLE

```

0 8874
0 8875 PROCEDURE [XDCL] tmp$cycle
0 8876 (VAR rb: tmt$rb_cycle;
0 8877 cst_p: ^ost$cpu_state_table);
0 8878
0 8879
0 8880 {
0 8881 { The purpose of this procedure is to cause a task switch to
0 8882 { occur. The CYCLE request is issued by the currently executing
0 8883 { task.
0 8884 {
0 8885 { TMP$CYCLE
0 8886 {
0 8887 {
0 8888 VAR
0 8889 xcb_p: ^ost$execution_control_block;
0 8890 ijle_p: ^jmt$initiated_job_list_entry;
0 8891 state: tmt$find_next_xcb_state;
0 8892 new_scheduling_priority: jmt$dispatching_priority;
0 8893 lock_ptlo: ost$task_index;
0 8894 ptlo: ost$task_index;
0 8895
0 8896 ptlo := cst_p^.taskid.index;
4 8897 tmp$set_lock (tmv$ptl_lock);
4A 8898
4A 8899 IF NOT tmv$tables_initialized THEN
5A 8900 ELSEIF (cst_p^.xcb_p^.system_table_lock_count > 0) AND
7E 8901 (cst_p^.xcb_p^.system_table_lock_count < 256) AND
7E 8902 (cst_p^.xcb_p^.system_give_up_cpu) THEN
7E 8903 cst_p^.xcb_p^.system_give_up_cpu := FALSE;
80 8904 ELSEIF (cst_p^.xcb_p^.system_table_lock_count < 0) AND
9C 8905 (cst_p^.xcb_p^.system_give_up_cpu OR cst_p^.xcb_p^.subsystem_give_up_cpu) THEN
9C 8906
9C 8907 cst_p^.xcb_p^.subsystem_give_up_cpu := FALSE;
9C 8908 cst_p^.xcb_p^.system_give_up_cpu := FALSE;
9C 8909
9C 8910 { Reset the task to it's original priority. Also, reset the subsystem lock priority
9C 8911 { which is maintained in the PTL.
9C 8912
9C 8913 tmv$ptl_p^ [ptlo].dispatching_priority := cst_p^.xcb_p^.dispatching_priority;
9C 8914 tmv$ptl_p^ [ptlo].readying_task_priority := 0;
9C 8915
9C 8916 new_scheduling_priority := cst_p^.ijle_p^.dispatching_control.dispatching_priority;
9C 8917 tmp$find_next_xcb (tmc$fnx_job, cst_p^.ijle_p, cst_p^.ijl_ordinal, state, xcb_p);
F2 8918 WHILE xcb_p <> NIL DO
100 8919 IF tmv$ptl_p^ [xcb_p^.global_task_id.index].readying_task_priority >
118 8920 new_scheduling_priority THEN
118 8921 new_scheduling_priority := tmv$ptl_p^ [xcb_p^.global_task_id.index].readying_task_priority;
11C 8922 IFEND;
11C 8923 tmp$find_next_xcb (tmc$fnx_continue, NIL, jmv$null_ijl_ordinal, state, xcb_p);
14C 8924 WHILEND;
162 8925 cst_p^.ijle_p^.scheduling_dispatching_priority := new_scheduling_priority;
162 8926 cst_p^.xcb_p^.subsystem_lock_priority_count := 0;
162 8927 tmv$ptl_p^ [ptlo].ptl_flags.subsystem_locks_set := FALSE;
18A 8928 ELSEIF tmv$cycle_delay_time > 0 THEN
192 8929 tmv$ptl_p^ [ptlo].new_task_status := tmc$ts_timeout_reqexp_shortshrt;

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SOURCE LIST OF tmm\$dispatcher

TMP\$CYCLE

```

192 8930 tmv$ptl_p^ [ptlo].end_of_wait_time := #FREE_RUNNING_CLOCK (0) + tmv$cycle_delay_time;
1AE 8931 IFEND;
1AE 8932
1AE 8933 cst_p^.dispatch_control.call_dispatcher := TRUE;
1AE 8934 IF (cst_p^.next_ptlo_to_dispatch = 0) THEN
18A 8935 cst_p^.dispatching_priority_integer := tmv$dct_priority_integer;
1C2 8936 IFEND;
1C2 8937
1C2 8938 ?IF tmc$debug_cycle_requests THEN
8939 IF (osv$debug > 0) THEN
8940 tmv$cycle_trace [ti].code := rb.code;
8941 tmv$cycle_trace [ti].p1 := rb.p1;
8942 tmv$cycle_trace [ti].p2 := rb.p2;
8943 tmv$cycle_trace [ti].time := #FREE_RUNNING_CLOCK (0);
8944 tmv$cycle_trace [ti].xtask := ptlo;
8945 lock_ptlo := rb.lock_value DIV 256;
8946 tmv$cycle_trace [ti].gtid := lock_ptlo;
8947 tmv$cycle_trace [ti].status := tmv$ptl_p^ [lock_ptlo].status;
8948 IF tmv$cycle_trace [ti].status = tmc$ts_page_wait THEN
8949 jmp$get_ijle_p (tmv$ptl_p^ [lock_ptlo].ijl_ordinal, ijle_p);
8950 xcb_p := #ADDRESS (1, ijle_p^.ajl_ordinal + mtc$job_fixed_segment, tmv$ptl_p^ [lock_ptlo].
8951 xcb_offset);
8952 tmv$cycle_trace [ti].utp := xcb_p^.page_wait_info.pva;
8953 tmv$cycle_trace [ti].p := xcb_p^.xp_p_register.pva;
8954 IFEND;
8955 ti := ti + 1;
8956 IF ti > 10000 THEN
8957 mtp$error_stop ('TM - trace buffer full');
8958 IFEND;
8959 IFEND;
1C2 8960 ?IFEND;
1C2 8961 tmp$clear_lock (tmv$ptl_lock);
1FA 8962
1FA 8963 PROCEND tmp$cycle;

```

TMP\$DELAY

```

0 8965
0 8966 PROCEDURE [XDCL] tmp$delay
0 8967 (VAR rb: tmt$rb_delay;
0 8968 cst_p: ^ost$cpu_state_table);
0 8969
0 8970
0 8971 {
0 8972 { The purpose of this procedure is to suspend execution of the
0 8973 { requesting task until the time specified by the task. The
0 8974 { DELAY request is issued by the currently executing task.
0 8975 {
0 8976 { TMP$DELAY (RB)
0 8977 {
0 8978 { RB : (INPUT,OUTPUT) This parameter specifies the request block.
0 8979 {
0 8980 { TYPE
0 8981 { TMT$RB_DELAY = RECORD
0 8982 { REOCODE,
0 8983 { STATUS,
0 8984 { REQUESTED_WAIT_TIME,
0 8985 { EXPECTED_WAIT_TIME,
0 8986 { RECCND;
0 8987 {
0 8988 { REOCODE: (input) The value of this parameter is SYC$SRC_DELAY.
0 8989 { STATUS: (output) This parameter specifies the system status.
0 8990 { REQUESTED_WAIT_TIME: (input) This parameter specifies the requested
0 8991 { wakeup time for this task.
0 8992 { EXPECTED_WAIT_TIME: (input) This parameter specifies the expected
0 8993 { wakeup time for this task.
0 8994 {
0 8995 {
0 8996 VAR
0 8997 ptle_p: ^tmt$primary_task_list_entry;
0 8998
0 8999 IF NOT tmv$stables_initialized THEN
10 9000 cst_p^.max_cp_time := 20000;
10 9001 RETURN;
1E 9002 IFEND;
1E 9003
1E 9004 tmp$set_lock (tmv$ptl_lock);
58 9005 IF mlv$c170_rqst_blk.req <> NIL THEN
6A 9006 IF (cst_p^.xcb_p^.global_task_id = mlv$c170_rqst_blk.req^.task_id) AND
6A 9007 (cst_p^.xcb_p^.xp_p.register.pva.ring > 1) THEN
6A 9008 IF mlv$mli_status.wait_inhibit THEN
96 9009 cst_p^.xcb_p^.wait_inhibited := TRUE;
96 9010 tmp$clear_lock (tmv$ptl_lock);
CE 9011 RETURN;
DA 9012 ELSE
DA 9013 mlv$mli_status.ready := FALSE;
DA 9014 IFEND;
DA 9015 IFEND;
DA 9016 IFEND;
DA 9017
DA 9018 ptle_p := ^tmv$ptl_p [cst_p^.taskid.index];
DA 9019
DA 9020 IF ptle_p^.monitor_flags <> $syt$monitor_flags [] THEN

```

TMP\$DELAY

```

F4 9021 cst_p^.xcb_p^.xp.user_condition_register := cst_p^.xcb_p^.xp.
F4 9022 user_condition_register + $ost$user_conditions [osc$free_flag];
F4 9023 cst_p^.xcb_p^.monitor_flags := cst_p^.xcb_p^.monitor_flags + ptle_p^.monitor_flags;
F4 9024 cst_p^.xcb_p^.system_flags := cst_p^.xcb_p^.system_flags + ptle_p^.system_flags;
F4 9025
F4 9026 ptle_p^.monitor_flags := $syt$monitor_flags [];
F4 9027 ptle_p^.system_flags := $tmt$system_flags [];
F4 9028 tmp$clear_lock (tmv$ptl_lock);
15A 9029 RETURN;
15C 9030 IFEND;
15C 9031
15C 9032 IF rb.requested_wait_time < UPPERVALUE (ost$free_running_clock) THEN
16C 9033 IF (rb.expected_wait_time < tmv$long_wait_swap_time) OR (cst_p^.xcb_p^.system_table_lock_count > 0) THEN
184 9034 ptle_p^.new_task_status := tmc$ts_timeout_reqexp_longlong;
18E 9035 ELSEIF (rb.expected_wait_time < tmv$timed_wait_not_queued) THEN
196 9036 ptle_p^.new_task_status := tmc$ts_timeout_reqexp_longlong;
1A0 9037 ELSE
1A0 9038 ptle_p^.new_task_status := tmc$ts_timed_wait_not_queued;
1A6 9039 IFEND;
1AA 9040 ELSE
1AA 9041 IF (rb.expected_wait_time < tmv$long_wait_swap_time) OR (cst_p^.xcb_p^.system_table_lock_count > 0) THEN
1C2 9042 ptle_p^.new_task_status := tmc$ts_timeout_reqexp_inflong;
1CC 9043 ELSE
1CC 9044 ptle_p^.new_task_status := tmc$ts_timeout_reqexp_infvlong;
1D2 9045 IFEND;
1D2 9046 IFEND;
1D2 9047 ptle_p^.end_of_wait_time := rb.requested_wait_time;
1D2 9048 tmp$clear_lock (tmv$ptl_lock);
210 9049 cst_p^.dispatch_control.call_dispatcher := TRUE;
210 9050 IF (cst_p^.next_ptlo_to_dispatch = 0) THEN
21C 9051 cst_p^.dispatching_priority_integer := tmv$dct_priority_integer;
224 9052 IFEND;
224 9053
224 9054
224 9055 PROCEND tmp$delay;

```

TMP\$MTR_WAIT

```

0 9057 PROCEDURE [XDCL] tmp$mtr_wait
0 9058 (VAR rb {input, output} : tmt$rb_wait_signal;
0 9059 cst_p: ^ost$cpu_state_table);
0 9060
0 9061 {
0 9062 {
0 9063 { The purpose of this procedure is to process the job mode
0 9064 { request to suspend execution of the current task until the
0 9065 { specified time has expired or an event has occurred. However
0 9066 { execution of the task is not suspended if the WAIT INHIBITED
0 9067 { flag is set in the XCB.
0 9068 {
0 9069 { TMP$MTR_WAIT (RB)
0 9070 {
0 9071 { RB : (INPUT,OUTPUT) This parameter specifies the request block.
0 9072 {
0 9073 { TYPE
0 9074 { TMT$RB_WAIT_SIGNAL = RECORD
0 9075 { REOCODE,
0 9076 { STATUS,
0 9077 { REQUESTED_WAIT_TIME,
0 9078 { EXPECTED_WAIT_TIME,
0 9079 { RECENT;
0 9080 {
0 9081 { REOCODE: {input} The value of this parameter is SYC$SRC_WAIT.
0 9082 { STATUS: {output} This parameter specifies the standard monitor status.
0 9083 { REQUESTED_WAIT_TIME: {input} This parameter specifies the maximum amount
0 9084 { of time to wait before resuming execution.
0 9085 { EXPECTED_WAIT_TIME: {input} This parameter specifies the expected amount
0 9086 { of time to wait before resuming execution.
0 9087 {
0 9088 {
0 9089 { VAR
0 9090 { new_task_status: tmt$task_status,
0 9091 { ptle_p: ^tmt$primary_task_list_entry,
0 9092 { readied_ijle_p: ^jmt$initiated_job_list_entry,
0 9093 { readying_task_priority: jmt$dispatching_priority,
0 9094 { service_class_p: ^jmt$service_class_attributes;
0 9095 {
0 9096 {
0 9097 { If the request was PMP$READY_TASK_AND_WAIT, the rb.global_taskid contains the taskid of the task
0 9098 { to be readied. That taskid must be a valid taskid.
0 9099 {
0 9100 { tmp$set_lock (tmv$ptl_lock);
42 9101 { IF rb.global_taskid <> tmv$null_global_task_id THEN
5A 9102 { tmp$check_taskid_with_lock_set (rb.global_taskid, tmc$opt_return, rb.status);
BC 9103 { IF NOT rb.status.normal THEN
C4 9104 { tmp$clear_lock (tmv$ptl_lock);
F6 9105 { RETURN;
F8 9106 { IFEND;
F8 9107 { IFEND;
F8 9108 {
F8 9109 {
F8 9110 { Determine the new task status based on the length of time the task wants to wait.
F8 9111 {
F8 9112 { IF rb.requested_wait_time < UPPERVALUE (ost$free_running_clock) THEN

```

SOURCE LIST OF tmm\$dispatcher

TMP\$MTR_WAIT

```

104 9113 IF (rb.expected_wait_time < tmv$long_wait_swap_time) OR (cst_p^.xcb_p^.system_table_lock_count > 0) THEN
120 9114 new_task_status := tmc$ts_timeout_reqexp_longlong;
126 9115 ELSEIF (rb.expected_wait_time < tmv$timed_wait_not_queued) THEN
12E 9116 new_task_status := tmc$ts_timeout_reqexp_longvlong;
136 9117 ELSE
136 9118 new_task_status := tmc$ts_timed_wait_not_queued;
13A 9119 IFEND;
13E 9120 ELSE
13E 9121 IF (rb.expected_wait_time < tmv$long_wait_swap_time) OR (cst_p^.xcb_p^.system_table_lock_count > 0) THEN
15A 9122 new_task_status := tmc$ts_timeout_reqexp_infvlong;
160 9123 ELSE
160 9124 new_task_status := tmc$ts_timeout_reqexp_infvlong;
162 9125 IFEND;
164 9126 IFEND;
164 9127
164 9128
164 9129 [ If this WAIT request will cause the job to go into long wait then do the
164 9130 [ cyclic aging that is done as part of swapout. Note: long wait aging may cause tasks of the job to go ready
164 9131 [ to assign backing files to transient segments. In this case the NEXT long wait should NOT
164 9132 [ do LONG WAIT aging.
164 9133
164 9134 ptle_p := ^tmv$ptl_p [cst_p^.taskid.index];
164 9135 ptle_p^.new_task_status := new_task_status;
164 9136 IF ((new_task_status = tmc$ts_timeout_reqexp_infvlong) OR
1A8 9137 (new_task_status = tmc$ts_timeout_reqexp_longvlong) OR
1A8 9138 (new_task_status = tmc$ts_timed_wait_not_queued)) AND
1A8 9139 (cst_p^.ijle_p^.statistics.tasks_not_in_long_wait = 1) AND NOT
1A8 9140 (cst_p^.ijle_p^.long_wait_aging_complete AND (cst_p^.ajlo > 0)) THEN
1A8 9141 IF (ptle_p^.monitor_flags = $syt$monitor_flags []) AND NOT
1C8 9142 (cst_p^.xcb_p^.wait_inhibited OR
1C8 9143 (ptle_p^.ptl_flags.wait_inhibited = tmc$wi_wait_inhibited)) THEN
1C8 9144 jsp$long_wait_aging (cst_p^.ijle_p);
1D8 9145 cst_p^.ijle_p^.long_wait_aging_complete := TRUE;
1E0 9146 IFEND;
1E0 9147 IFEND;
1E0 9148
1E0 9149
1E0 9150 [ If the task has pending monitor flags or has wait inhibited, let it keep running.
1E0 9151 [ If long wait aging has readied this task then PTL.NEW_TASK_STATUS will be null.
1E0 9152
1E0 9153 new_task_status := ptle_p^.new_task_status;
1E0 9154 IF (ptle_p^.monitor_flags <> $syt$monitor_flags []) OR (new_task_status = tmc$ts_null) THEN
1F0 9155 cst_p^.xcb_p^.xp.user_condition_register := cst_p^.xcb_p^.xp
1F0 9156 user_condition_register + $ost$user_conditions [osc$free_flag];
1F0 9157 cst_p^.xcb_p^.monitor_flags := cst_p^.xcb_p^.monitor_flags + ptle_p^.monitor_flags;
1F0 9158 cst_p^.xcb_p^.system_flags := cst_p^.xcb_p^.system_flags + ptle_p^.system_flags;
1F0 9159 ptle_p^.monitor_flags := $syt$monitor_flags [];
1F0 9160 ptle_p^.system_flags := $tmt$system_flags [];
1F0 9161 new_task_status := tmc$ts_null;
22E 9162 ELSEIF (cst_p^.xcb_p^.wait_inhibited OR
246 9163 (ptle_p^.ptl_flags.wait_inhibited = tmc$wi_wait_inhibited)) THEN
246 9164 new_task_status := tmc$ts_null;
24E 9165 IFEND;
24E 9166
24E 9167 IF new_task_status <> tmc$ts_null THEN
252 9168 IF cst_p^.xcb_p^.xp.p_register.pva.ring <= osc$tsrv_ring THEN

```


TMP\$MTR_WAIT

```

264 9169     ptle_p^ptl_flags.wait_inhibited := tmc$wi_wait_selected_r3;
274 9170     ELSE
274 9171     ptle_p^ptl_flags.wait_inhibited := tmc$wi_wait_selected;
282 9172     IFEND;
282 9173     ptle_p^.end_of_wait_time := rb.requested_wait_time;
282 9174     cst_p^.dispatch_control.call_dispatcher := TRUE;
282 9175     IF (cst_p^.next_ptlo_to_dispatch = 0) THEN
296 9176     cst_p^.dispatching_priority_integer := tmv$dct_priority_integer;
296 9177     IFEND;
2A2 9178     ELSE
2A2 9179     cst_p^.xcb_p^.wait_inhibited := FALSE;
2A2 9180     ptle_p^ptl_flags.wait_inhibited := tmc$wi_null;
2A2 9181     ptle_p^.new_task_status := tmc$ts_null;
2BA 9182     IFEND;
2BA 9183
2BA 9184 { If user request was PMP$READY_TASK_AND_WAIT, the rb.global_taskid is the taskid of the task to
2BA 9185 { be readied.
2BA 9186
2BA 9187 IF rb.global_taskid <> tmv$null_global_task_id THEN
2CA 9188 IF cst_p^.xcb_p^.dispatching_priority >=
2E6 9189     tmv$ptl_p^ [cst_p^.xcb_p^.global_task_id.index].readying_task_priority THEN
2E6 9190     readying_task_priority := cst_p^.xcb_p^.dispatching_priority;
2EA 9191     ELSE
2EA 9192     readying_task_priority := tmv$ptl_p^ [cst_p^.xcb_p^.global_task_id.index].readying_task_priority;
2EC 9193     IFEND;
2EC 9194     tmp$set_task_ready (rb.global_taskid, readying_task_priority,
308 9195     tmc$rc_ready_conditional_wi);
308 9196     jmp$set_ijle_p [tmv$ptl_p^ [rb.global_taskid.index].ijl_ordinal, readied_ijle_p];
308 9197     IF readied_ijle_p^.entry_status = jmc$ies_job_swapped THEN
340 9198     service_class_p := ^jmv$service_classes [cst_p^.ijle_p^.job_scheduler_data.service_class]^attributes;
340 9199     cst_p^.ijle_p^.job_scheduler_data.service_accumulator_since_swap :=
340 9200     service_class_p^.guaranteed_service_quantum;
340 9201     IF readied_ijle_p^.job_scheduler_data.priority > service_class_p^.scheduling_priority.minimum THEN
36C 9202     IF cst_p^.ijle_p^.job_scheduler_data.priority >= readied_ijle_p^.job_scheduler_data.priority THEN
374 9203     cst_p^.ijle_p^.job_scheduler_data.priority := readied_ijle_p^.job_scheduler_data.priority - 1;
37C 9204     IFEND;
380 9205     ELSEIF readied_ijle_p^.job_scheduler_data.priority = service_class_p^.scheduling_priority.minimum THEN
384 9206     cst_p^.ijle_p^.job_scheduler_data.priority := readied_ijle_p^.job_scheduler_data.priority;
388 9207     IFEND;
388 9208     IFEND;
388 9209     IFEND;
388 9210
388 9211     tmp$clear_lock (tmv$ptl_lock);
3BA 9212
3BA 9213
3BA 9214 PROCEND tmp$mtr_wait;

```

SOURCE LIST OF tmm\$dispatcher

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TMP\$CHECK_FOR_SWAPOUT_CANDIDATE

```

0 9216
0 9217 PROCEDURE [XDCL] tmp$check_for_swapout_candidate
0 9218 { ajl_ordinal: jmt$ajl_ordinal);
0 9219
0 9220 VAR
0 9221     ijle_p: ^jmt$initiated_job_list_entry,
0 9222     next_ready_time: integer,
0 9223     ptlo: ost$task_index,
0 9224     xcb_p: ^ost$execution_control_block;
0 9225
0 9226
0 9227 IF [ajl_ordinal = jmv$system_ajl_ordinal] OR NOT jmv$swap_jobs_in_long_wait THEN
1C 9228     RETURN;
1E 9229     IFEND;
1E 9230
1E 9231     ijle_p := jmv$ajl_p^ [ajl_ordinal].ijle_p;
1E 9232     ptlo := ijle_p^.job_monitor_taskid.index;
1E 9233     next_ready_time := 0fffffffffff(16);
1E 9234
1E 9235 { Do long wait aging if not already done.
1E 9236
1E 9237 IF NOT ijle_p^.long_wait_aging_complete THEN
44 9238     jsp$long_wait_aging (ijle_p);
54 9239     ijle_p^.long_wait_aging_complete := TRUE;
5A 9240     IFEND;
5A 9241
5A 9242 { If the job now has a ready task (that condition must be checked with the PTL lock set) or if a
5A 9243 { task of the job has a system lock set, do NOT swap out the job.
5A 9244 { Scan the PTL thread for the job and search for next ready time. If the next ready time of all
5A 9245 { tasks is greater than the force-long-wait-swap-time, then swap the job out.
5A 9246
5A 9247     tmp$set_lock (tmv$ptl_lock);
94 9248
94 9249 IF ijle_p^.statistics.ready_task_count > 0 THEN
9C 9250     tmp$clear_lock (tmv$ptl_lock);
D0 9251     RETURN;
D2 9252     IFEND;
D2 9253
D2 9254 WHILE ptlo <> 0 DO
D6 9255     tmp$set_xcb_p_from_ptlo (ptlo, ajl_ordinal, xcb_p);
D6 9256     IF xcb_p^.system_table_lock_count <> 0 THEN
106 9257     tmp$clear_lock (tmv$ptl_lock);
13A 9258     RETURN;
140 9259     ELSEIF (tmv$ptl_p^ [ptlo].status >= tmc$ts_first_status_in_wait_q) AND
15C 9260     (tmv$ptl_p^ [ptlo].status <= tmc$ts_last_status_in_wait_q) AND
15C 9261     (tmv$ptl_p^ [ptlo].end_of_wait_time < next_ready_time) THEN
15C 9262     next_ready_time := tmv$ptl_p^ [ptlo].end_of_wait_time;
160 9263     IFEND;
160 9264     ptlo := tmv$ptl_p^ [ptlo].ijl_thread;
160 9265     WHILEND;
170 9266
170 9267 IF (next_ready_time - #FREE_RUNNING_CLOCK (0)) > tmv$long_wait_force_swap_time THEN
182 9268     tmp$set_swapout_candidate (ajl_ordinal);
192 9269     IFEND;
192 9270
192 9271     tmp$clear_lock (tmv$ptl_lock);

```

TMP\$CHECK_FOR_SWAPOUT_CANDIDATE

```

1C4 9272
1C4 9273 PROCEND tmp$check_for_swapout_candidate;

```

SOURCE LIST OF tmm\$dispatcher

TMP\$SET_SWAPOUT_CANDIDATE

```

O 9275
O 9276 { NOTE : Anyone calling this routine must have tmv$pt1_lock set.
O 9277
O 9278 PROCEDURE tmp$set_swapout_candidate
O 9279 ( ajl_ordinal: jmt$ajl_ordinal);
O 9280
O 9281 VAR
O 9282 temp_next_cyclic_aging: integer,
O 9283 ptlo: ost$task_index,
O 9284 jcb_p: ^ajmt$job_control_block,
O 9285 ijle_p: ^ajmt$initiated_job_list_entry;
O 9286
O 9287 IF (ajl_ordinal = jmv$system_ajl_ordinal) OR NOT jmv$swap_jobs_in_long_wait THEN
18 9288 RETURN;
1A 9289 IFEND;
1A 9290
1A 9291 ijle_p := jmv$ajl_p^ [ajl_ordinal].ijle_p;
1A 9292
1A 9293 { Scan the IJL thread and mark the entries swapped.
1A 9294
1A 9295 ptlo := ijle_p^.job_monitor_taskid.index;
1A 9296
1A 9297 WHILE ptlo <> 0 DO
36 9298 tmv$pt1_p^ [ptlo].idle_status := tmc$sis_idled;
36 9299 ptlo := tmv$pt1_p^ [ptlo].ijl_thread;
36 9300 WHILEND;
4A 9301
4A 9302 jcb_p := #ADDRESS (1, mtc$job_fixed_segment + ajl_ordinal, 0);
4A 9303 temp_next_cyclic_aging := jcb_p^.next_cyclic_aging_time - #FREE_RUNNING_CLOCK (0);
64 9304 IF temp_next_cyclic_aging < 0 THEN
70 9305 jcb_p^.next_cyclic_aging_time := 0;
78 9306 ELSE
78 9307 jcb_p^.next_cyclic_aging_time := temp_next_cyclic_aging;
7C 9308 IFEND;
7C 9309
7C 9310 jmp$set_swapout_candidate (ajl_ordinal, jmc$sr_long_wait);
92 9311
92 9312 PROCEND tmp$set_swapout_candidate;
O 9313

```

[XDCL] tmp\$idle_non_dispatchable_job

```

0 9315
0 9316 { PURPOSE:
0 9317 { This procedure will idle a job whose dispatching priority is currently too low to be dispatched.
0 9318 { DESIGN:
0 9319 { This procedure is called from mmp$periodic when a job is discovered to be non-dispatchable. The job is
0 9320 { swapped out only if all tasks can be idled.
0 9321
0 9322 PROCEDURE [XDCL] tmp$idle_non_dispatchable_job
0 9323 (
0 9324   ajl_ordinal: jmt$ajl_ordinal);
0 9325
0 9326   VAR
0 9327     ijle_p: Ajmt$initiated_job_list_entry,
0 9328     status: syt$monitor_status;
0 9329
0 9330   tmp$set_lock (tmv$ptl_lock);
42 9330
42 9331   ijle_p := jmv$ajl_p^ [ajl_ordinal].ijle_p;
42 9332   IF ijle_p^.entry_status = jmc$ies_job_in_memory THEN
66 9333     tmp$idle_tasks_in_job (ajl_ordinal, jmc$sr_idle_dispatching, status);
84 9334     IF status.normal THEN
8C 9335       jmp$swap_non_dispatchable_job (ajl_ordinal);
9C 9336     IFEND;
9C 9337   IFEND;
9C 9338
9C 9339   tmp$clear_lock (tmv$ptl_lock);
DO 9340
DO 9341   PROCEND tmp$idle_non_dispatchable_job;
0 9342

```

TMP\$IDLE_TASKS_IN_JOB

```

0 9344
0 9345 { PURPOSE:
0 9346 { The purpose of this procedure is to idle all tasks in a job.
0 9347 { NOTE!!! The caller of this procedure MUST set the PTL lock.
0 9348
0 9349 PROCEDURE [XDCL] tmp$idle_tasks_in_job
0 9350 (
0 9351   ajl_ordinal: jmt$ajl_ordinal;
0 9352   swapout_reason: jmt$swapout_reason;
0 9353   VAR status: syt$monitor_status);
0 9354
0 9355   VAR
0 9356     attempt_preselection: boolean,
0 9357     end_ptlo: ost$task_index,
0 9358     ijle_p: Ajmt$initiated_job_list_entry,
0 9359     jcb_p: Ajmt$job_control_block,
0 9360     ptlo: ost$task_index,
0 9361     ready_task_count: integer,
0 9362     tasks_not_swappable_count: 0 .. osc$max_tasks,
0 9363     temp_next_cyclic_aging: integer,
0 9364     xcb_p: ^ost$execution_control_block;
0 9365
0 9366   status.normal := TRUE;
4 9366   ijle_p := jmv$ajl_p^ [ajl_ordinal].ijle_p;
4 9367
4 9368   jcb_p := #ADDRESS (1, mtc$job_fixed_segment + ajl_ordinal, 0);
4 9369
4 9370 { Reject the request if the job is non-swappable.
4 9371
4 9372   IF jmv$ajl_p^ [ajl_ordinal].ijle_p^.entry_status = jmc$ies_job_in_memory_non_swap THEN
44 9373     mtp$set_status_abnormal ('JS', jse$job_executing_non_swappable, status);
44 9374     RETURN;
56 9375   IFEND;
56 9376
56 9377 { Scan the IJL thread and mark the entries swapped. If the swapout reason is idle dispatching and ALL
56 9378 { tasks cannot be idled, then do not idle any tasks.
56 9379
56 9380   ptlo := ijle_p^.job_monitor_taskid.index;
56 9381   ready_task_count := 0;
56 9382   tasks_not_swappable_count := 0;
56 9383
56 9384   WHILE ptlo <> 0 DO
66 9385     xcb_p := #ADDRESS (1, mtc$job_fixed_segment + ajl_ordinal, tmv$ptl_p^ [ptlo].xcb_offset);
66 9386     IF [xcb_p^.system_table_lock_count >= osc$system_table_lock_set] OR
B2 9387     [(tmv$ptl_p^ [ptlo].status = tmc$ts_executing) OR
B2 9388     [(tmv$ptl_p^ [ptlo].status = tmc$ts_ready_and_selected) OR
B2 9389     [(xcb_p^.system_table_lock_count > 0) AND ((tmv$ptl_p^ [ptlo].status <
B2 9390     tmc$ts_timeout_reqexp_longlong) OR (tmv$ptl_p^ [ptlo].status >
B2 9391     tmc$ts_timeout_reqexp_infvlong)] THEN
B2 9392     IF swapout_reason <> jmc$sr_idle_dispatching THEN
BC 9393     tasks_not_swappable_count := tasks_not_swappable_count + 1;
BC 9394     tmv$ptl_p^ [ptlo].idle_status := tmc$is_idle_initiated;
CE 9395   ELSE
CE 9396     end_ptlo := ptlo;
CE 9397     ptlo := ijle_p^.job_monitor_taskid.index;
CE 9398     WHILE (ptlo <> end_ptlo) DO
D8 9399     IF (tmv$ptl_p^ [ptlo].idle_status >= tmc$is_idled) THEN

```

TMP\$IDLE_TASKS_IN_JOB

```

E8 9400      IF tmv$ptl_p^ [ptlo].status = tmc$sts_ready_but_swapped THEN
F2 9401      tmv$ptl_p^ [ptlo].status := tmc$sts_ready;
F6 9402      IFEND;
F6 9403      IF tmv$ptl_p^ [ptlo].status <= tmc$sts_last_status_in_dct THEN
106 9404      attempt_preselection := (ijle_p^.multiprocessing_allowed) OR
11A 9405      (ijle_p^.executing_task_count = 0);
*WARN* 9406      tmp$dct_ready_task (xcb_p, ijle_p, ptlo, attempt_preselection);
A48 9407      IFEND;
A48 9408      IFEND;
A48 9409      tmv$ptl_p^ [ptlo].idle_status := tmc$sis_not_idled;
A48 9410      ptlo := tmv$ptl_p^ [ptlo].ijl_thread;
A48 9411      WHILEND;
A5C 9412      mtp$set_status_abnormal ('JS', jse$unable_to_idle_all_tasks, status);
A5C 9413      RETURN;
A6E 9414      IFEND;
A72 9415      IFEND;
A72 9416      ELSEIF tmv$ptl_p^ [ptlo].status <= tmc$sts_last_status_in_dct THEN
A82 9417      tmp$remove_task_from_dct (ptlo);
BFO 9418      tmv$ptl_p^ [ptlo].status := tmc$sts_ready_but_swapped;
BFO 9419      ready_task_count := ready_task_count + 1;
BFO 9420      tmv$ptl_p^ [ptlo].idle_status := tmc$sis_idled_sched_notified;
C1A 9421      ELSE
C1A 9422      tmv$ptl_p^ [ptlo].idle_status := tmc$sis_idled;
C1E 9423      IFEND;
C1E 9424      IFEND;
C1E 9425      IF xcb_p^.system_table_lock_count > 0 THEN
C26 9426      tmv$ptl_p^ [ptlo].ptl_flags.subsystem_locks_set := TRUE;
C36 9427      IFEND;
C36 9428      IFEND;
C36 9429      ptlo := tmv$ptl_p^ [ptlo].ijl_thread;
C36 9430      WHILEND;
C4E 9431      IFEND;
C4E 9432      IF tasks_not_swappable_count <> 0 THEN
C52 9433      mtp$set_status_abnormal ('JS', jse$unable_to_idle_all_tasks, status);
C64 9434      ELSE
C64 9435      temp_next_cyclic_aging := jcb_p^.next_cyclic_aging_time - #FREE_RUNNING_CLOCK (0);
C6A 9436      IF temp_next_cyclic_aging < 0 THEN
C76 9437      jcb_p^.next_cyclic_aging_time := 0;
C7C 9438      ELSE
C7C 9439      jcb_p^.next_cyclic_aging_time := temp_next_cyclic_aging;
C80 9440      IFEND;
C80 9441      IFEND;
C80 9442      IFEND;
C80 9443      PROCEND tmp$idle_tasks_in_job;

```

SOURCE LIST OF tmm\$dispatcher

TMP\$RESTART_IDLE_TASKS

```

O 9445
O 9446      PROCEDURE [XDCL] tmp$restart_idled_tasks
O 9447      (
O 9448      [
O 9449      ajl_ordinal: jmt$ajl_ordinal]);
O 9450      VAR
O 9451      attempt_preselection: boolean,
O 9452      cst_p: ^ost$cpu_state_table,
O 9453      ijle_p: ^ajmt$initiated_job_list_entry,
O 9454      ready_task_count: integer,
O 9455      xcb_p: ^ost$execution_control_block,
O 9456      ptlo: ost$task_index;
O 9457
O 9458      jmp$get_ijle_p (jmv$ajl_p^ [ajl_ordinal].ijl_ordinal, ijle_p);
4 9459
4 9460      IF jmc$dsw_update_keypoint_masks IN ijle_p^.delayed_swapin_work THEN
4A 9461      osp$update_job_keypoint_mask (ijle_p, jmv$ajl_p^ [ajl_ordinal].ijl_ordinal);
5E 9462      IFEND;
5E 9463
5E 9464      ptlo := ijle_p^.job_monitor_taskid.index;
5E 9465
5E 9466      { Scan the IJL thread and mark the entries swapped in.
5E 9467
5E 9468      ready_task_count := 0;
5E 9469      tmp$set_lock (tmv$ptl_lock);
A4 9470      WHILE ptlo <> 0 DO
AA 9471      xcb_p := #ADDRESS (1, mtc$job_fixed_segment + ajl_ordinal, tmv$ptl_p^ [ptlo].xcb_offset);
AA 9472      IF [tmv$ptl_p^ [ptlo].idle_status >= tmc$sis_idled] THEN
D2 9473      IF tmv$ptl_p^ [ptlo].status = tmc$sts_ready_but_swapped THEN
DC 9474      tmv$ptl_p^ [ptlo].status := tmc$sts_ready;
DC 9475      ready_task_count := ready_task_count + 1;
E2 9476      IFEND;
E2 9477      IF tmv$ptl_p^ [ptlo].status <= tmc$sts_last_status_in_dct THEN
F2 9478      attempt_preselection := (ijle_p^.multiprocessing_allowed) OR (ijle_p^.executing_task_count = 0);
*WARN* 9479      tmp$dct_ready_task (xcb_p, ijle_p, ptlo, attempt_preselection);
A64 9480      IFEND;
A64 9481      IFEND;
A64 9482      tmv$ptl_p^ [ptlo].idle_status := tmc$sis_not_idled;
A64 9483      ptlo := tmv$ptl_p^ [ptlo].ijl_thread;
A64 9484      WHILEND;
A78 9485
A78 9486      tmp$clear_lock (tmv$ptl_lock);
AAE 9487
AAE 9488      PROCEND tmp$restart_idled_tasks;

```

TMP\$IDLE_TASKS_IN_SYSTEM_JOB

```

0 9490
0 9491 PROCEDURE tmp$idle_tasks_in_system_job
0 9492 ( idle_resume_sys_task_kind: tmt$idle_resume_sys_task_kind;
0 9493 VAR status: syt$monitor_status);
0 9494
0 9495 VAR
0 9496 ptlo: ost$task_index,
0 9497 xcb_p: ^ost$execution_control_block,
0 9498 ready_task_count: integer,
0 9499 ijle_p: ^jmt$initiated_job_list_entry,
0 9500 tasks_not_swappable_count: 0 .. osc$max_tasks;
0 9501
0 9502 status.normal := TRUE;
4 9503 ijle_p := jmv$ajl_p^ [jmv$system_ajl_ordinal].ijle_p;
4 9504 tmp$set_lock (tmv$ptl_lock);
5C 9505
5C 9506 [ Scan the IJL thread and mark the entries swapped.
5C 9507
5C 9508 ptlo := ijle_p.job_monitor_taskid.index;
5C 9509 ready_task_count := 0;
5C 9510 tasks_not_swappable_count := 0;
5C 9511
5C 9512 IF idle_resume_sys_task_kind = tmc$ir_dm_system_tasks THEN [ Idle ONLY the Device_Management tasks. ]
6A 9513
6A 9514 WHILE ptlo <> 0 DO
6E 9515 xcb_p := #ADDRESS (1, mtc$job_fixed_segment + jmv$system_ajl_ordinal, tmv$ptl_p^ [ptlo].xcb_offset);
6E 9516 IF (xcb_p^.system_task_id = tmc$stid_administer_log) OR
A6 9517 (xcb_p^.system_task_id = tmc$stid_dm_split_a1) OR
A6 9518 (xcb_p^.system_task_id = tmc$stid_volume_space_management) THEN
A6 9519 IF (xcb_p^.system_table_lock_count >= osc$system_table_lock_set) OR
DA 9520 (tmv$ptl_p^ [ptlo].status = tmc$sts_executing) OR
DA 9521 (tmv$ptl_p^ [ptlo].status = tmc$sts_ready_and_selected) OR
DA 9522 (xcb_p^.system_table_lock_count > 0) AND ((tmv$ptl_p^ [ptlo].status <
DA 9523 tmc$sts_timeout_reqexp_shortshrt) OR (tmv$ptl_p^ [ptlo].status > tmc$sts_last_status_in_wait_q))
DA 9524 THEN
DA 9525 tasks_not_swappable_count := tasks_not_swappable_count + 1;
DA 9526 tmv$ptl_p^ [ptlo].idle_status := tmc$sis_idle_initiated;
EC 9527 ELSEIF tmv$ptl_p^ [ptlo].status <= tmc$sts_last_status_in_dct THEN
FC 9528 tmp$remove_task_from_dct (ptlo);
26A 9529 tmv$ptl_p^ [ptlo].status := tmc$sts_ready_but_swapped;
28A 9530 ready_task_count := ready_task_count + 1;
28A 9531 tmv$ptl_p^ [ptlo].idle_status := tmc$sis_idled_sched_notified;
29A 9532 ELSE
29A 9533 tmv$ptl_p^ [ptlo].idle_status := tmc$sis_idled;
29A 9534 IFEND;
29A 9535 IFEND;
29A 9536 ptlo := tmv$ptl_p^ [ptlo].ijl_thread;
29A 9537 WHILEND;
2A8 9538
2A8 9539 tmp$clear_lock (tmv$ptl_lock);
2E0 9540
2E0 9541 IF tasks_not_swappable_count <> 0 THEN
2E4 9542 { Some Device_Management task is still executing. }
2E4 9543 mtp$set_status_abnormal ('JS', jse$unable_to_idle_all_tasks, status);
2F4 9544 IFEND;
2F6 9545

```

TMP\$IDLE_TASKS_IN_SYSTEM_JOB

```

2F6 9546 ELSE [ Idle everything EXCEPT the Device_Management tasks. ]
2F6 9547
2F6 9548 WHILE ptlo <> 0 DO
2FA 9549 xcb_p := #ADDRESS (1, mtc$job_fixed_segment + jmv$system_ajl_ordinal, tmv$ptl_p^ [ptlo].xcb_offset);
2FA 9550 IF (xcb_p^.system_task_id <> tmc$stid_administer_log) AND
32A 9551 (xcb_p^.system_task_id <> tmc$stid_dm_split_a1) AND
32A 9552 (xcb_p^.system_task_id <> tmc$stid_volume_space_management) THEN
32A 9553 IF (xcb_p^.system_table_lock_count >= osc$system_table_lock_set) OR
35A 9554 (tmv$ptl_p^ [ptlo].status = tmc$sts_executing) OR
35A 9555 (tmv$ptl_p^ [ptlo].status = tmc$sts_ready_and_selected) OR
35A 9556 (xcb_p^.system_table_lock_count > 0) AND ((tmv$ptl_p^ [ptlo].status <
35A 9557 tmc$sts_timeout_reqexp_shortshrt) OR (tmv$ptl_p^ [ptlo].status > tmc$sts_last_status_in_wait_q))
35A 9558 THEN
35A 9559 tasks_not_swappable_count := tasks_not_swappable_count + 1;
35A 9560 tmv$ptl_p^ [ptlo].idle_status := tmc$sis_idle_initiated;
36C 9561 ELSEIF tmv$ptl_p^ [ptlo].status <= tmc$sts_last_status_in_dct THEN
37C 9562 tmp$remove_task_from_dct (ptlo);
4EA 9563 tmv$ptl_p^ [ptlo].status := tmc$sts_ready_but_swapped;
4EA 9564 ready_task_count := ready_task_count + 1;
4EA 9565 tmv$ptl_p^ [ptlo].idle_status := tmc$sis_idled_sched_notified;
514 9566 ELSE
514 9567 tmv$ptl_p^ [ptlo].idle_status := tmc$sis_idled;
518 9568 IFEND;
518 9569 IFEND;
518 9570 ptlo := tmv$ptl_p^ [ptlo].ijl_thread;
518 9571 WHILEND;
528 9572
528 9573 tmp$clear_lock (tmv$ptl_lock);
560 9574
560 9575 IF tasks_not_swappable_count <> 1 THEN
564 9576 { A system task other than the system JOB_MONITOR is still executing. }
564 9577 mtp$set_status_abnormal ('JS', jse$unable_to_idle_all_tasks, status);
574 9578 IFEND;
574 9579 IFEND;
574 9580
574 9581 PROCEND tmp$idle_tasks_in_system_job;

```

TMP\$RESTART_TASKS_IN_SYSTEM_JOB

```

0 9583
0 9584 PROCEDURE tmp$restart_tasks_in_system_job
0 9585 (
0 9586   idle_resume_sys_task_kind: tmt$idle_resume_sys_task_kind;
0 9587   VAR status: syt$monitor_status);
0 9588
0 9589 VAR
0 9590   attempt_preselection: boolean,
0 9591   ptlo: ost$task_index,
0 9592   ready_task_count: integer,
0 9593   xcb_p: ^ost$execution_control_block,
0 9594   ijle_p: ^jmt$initiated_job_list_entry;
0 9595
0 9596 status_normal := TRUE;
4 9597 jmp$set_ijle_p (jmv$ajl_p^ [jmv$system_ajl_ordinal].ijl_ordinal, ijle_p);
4 9598
4 9599 IF jmc$dsw_update_keypoint_masks IN ijle_p^.delayed_swapin_work THEN
6E 9600   osp$update_job_keypoint_mask (ijle_p, jmv$system_ijl_ordinal);
6E 9601 IFEND;
6E 9602
6E 9603 ptlo := ijle_p^.job_monitor_taskid.index;
6E 9604 { Scan the IJL thread and mark the entries swapped in.
6E 9605
6E 9606   ready_task_count := 0;
6E 9607   tmp$set_lock (tmv$ptl_lock);
AA 9608
AA 9609   IF idle_resume_sys_task_kind = tmc$ir_dm_system_tasks THEN { Restart ONLY the Device_Management tasks. }
AE 9610
AE 9611   WHILE ptlo <> 0 DO
B2 9612     xcb_p := #ADDRESS [1, tmc$job_fixed_segment + jmv$system_ajl_ordinal, tmv$ptl_p^ [ptlo].xcb_offset);
B2 9613     IF [xcb_p^.system_task_id = tmc$stid_administer_log] OR
EA 9614        [xcb_p^.system_task_id = tmc$stid_dm_split_a1] OR
EA 9615        [xcb_p^.system_task_id = tmc$stid_volume_space_managemnt] THEN
EA 9616       IF [tmv$ptl_p^ [ptlo].idle_status >= tmc$sis_idled] THEN
FC 9617         IF tmv$ptl_p^ [ptlo].status = tmc$sts_ready_but_swapped THEN
10E 9618           tmv$ptl_p^ [ptlo].status := tmc$sts_ready;
10E 9619           ready_task_count := ready_task_count + 1;
10C 9620         IFEND;
10C 9621         IF tmv$ptl_p^ [ptlo].status <= tmc$sts_last_status_in_dct THEN
11C 9622           attempt_preselection := [ijle_p^.multiprocessing_allowed] OR [ijle_p^.executing_task_count = 0];
*WARN* 9623           tmp$dct_ready_task (xcb_p, ijle_p, ptlo, attempt_preselection);
AE2 9624           IFEND;
AE2 9625           IFEND;
AE2 9626           tmv$ptl_p^ [ptlo].idle_status := tmc$sis_not_idled;
AE5 9627           IFEND;
AE5 9628           ptlo := tmv$ptl_p^ [ptlo].ijl_thread;
AE5 9629         WHILEND;
AE6 9630
AE6 9631         tmp$clear_lock (tmv$ptl_lock);
AA8 9632
AA8 9633     ELSE { Restart everything EXCEPT the Device_Management tasks. }
AA8 9634
AA8 9635     WHILE ptlo <> 0 DO
AAC 9636       xcb_p := #ADDRESS [1, tmc$job_fixed_segment + jmv$system_ajl_ordinal, tmv$ptl_p^ [ptlo].xcb_offset);
AAC 9637       IF [xcb_p^.system_task_id <> tmc$stid_administer_log] AND
AE4 9638          [xcb_p^.system_task_id <> tmc$stid_dm_split_a1] AND

```

TMP\$RESTART_TASKS_IN_SYSTEM_JOB

```

AE4 9639   [xcb_p^.system_task_id <> tmc$stid_volume_space_managemnt] THEN
AE4 9640     IF [tmv$ptl_p^ [ptlo].idle_status >= tmc$sis_idled] THEN
AEE 9641       IF tmv$ptl_p^ [ptlo].status = tmc$sts_ready_but_swapped THEN
AF8 9642         tmv$ptl_p^ [ptlo].status := tmc$sts_ready;
AF8 9643         ready_task_count := ready_task_count + 1;
AFE 9644       IFEND;
AFE 9645       IF tmv$ptl_p^ [ptlo].status <= tmc$sts_last_status_in_dct THEN
BOE 9646         attempt_preselection := [ijle_p^.multiprocessing_allowed] OR [ijle_p^.executing_task_count = 0];
*WARN* 9647         tmp$dct_ready_task (xcb_p, ijle_p, ptlo, attempt_preselection);
146A 9648         IFEND;
146A 9649         IFEND;
146A 9650         tmv$ptl_p^ [ptlo].idle_status := tmc$sis_not_idled;
147E 9651         IFEND;
147E 9652         ptlo := tmv$ptl_p^ [ptlo].ijl_thread;
147E 9653       WHILEND;
148E 9654
148E 9655       tmp$clear_lock (tmv$ptl_lock);
148C 9656     IFEND;
148C 9657
148C 9658 PROCEND tmp$restart_tasks_in_system_job;

```

update_cp_statistics

```

0 9660
0 9661 PROCEDURE [INLINE] update_cp_statistics
0 9662 ( cst_p: ^ost$cpu_state_table);
0 9663
0 9664 {
0 9665 { The purpose of this procedure is to update the cp statistics in the
0 9666 { IJL, XCB, JCB and the system statistics record.
0 9667 {
0 9668 { UPDATE_CP_STATISTICS (CST_P)
0 9669 {
0 9670 { CST_P: (INPUT) This parameter specifies the pointer to the CPU
0 9671 { STATE TABLE.
0 9672 {
0 9673 {
0 9674 VAR
0 9675 excess_cp_time_used: integer,
0 9676 minor_time_slice_remaining: integer,
0 9677 major_time_slice_remaining: integer,
0 9678 status: syt$monitor_status,
0 9679 task_time_slice_used: integer,
0 9680 total_cptime: integer;
0 9681
0 9682
0 9683 { Update cp statistics in IJL.
0 9684
0 9685 cst_p^.ijle_p^.statistics.cp_time.time_spent_in_mtr_mode := cst_p^.ijle_p^.statistics.cp_time.
0 9686 time_spent_in_mtr_mode + cst_p^.accumulated_monitor_cptime;
0 9687 cst_p^.ijle_p^.statistics.cp_time.time_spent_in_job_mode := cst_p^.ijle_p^.statistics.cp_time.
0 9688 time_spent_in_job_mode + cst_p^.accumulated_job_cptime;
0 9689
0 9690 task_time_slice_used := cst_p^.accumulated_job_cptime + cst_p^.accumulated_monitor_cptime;
0 9691
0 9692 { Update the dispatching priority controls.
0 9693
0 9694 update_dispatching_controls (task_time_slice_used, cst_p^.dispatching_priority);
0 9695
0 9696 { Update system data statistics.
0 9697
0 9698 tmv$cpu_execution_statistics [cst_p^.dispatching_priority].time_spent_in_mtr_mode :=
0 9699 tmv$cpu_execution_statistics [cst_p^.dispatching_priority].time_spent_in_mtr_mode + cst_p^.
0 9700 accumulated_monitor_cptime;
0 9701 tmv$cpu_execution_statistics [cst_p^.dispatching_priority].time_spent_in_job_mode :=
0 9702 tmv$cpu_execution_statistics [cst_p^.dispatching_priority].time_spent_in_job_mode + cst_p^.
0 9703 accumulated_job_cptime;
0 9704
0 9705 { Update cp statistics in XCB.
0 9706
0 9707 cst_p^.xcb_p^.cp_time.time_spent_in_mtr_mode := cst_p^.xcb_p^.cp_time.time_spent_in_mtr_mode + cst_p^.
0 9708 accumulated_monitor_cptime;
0 9709 cst_p^.xcb_p^.cp_time.time_spent_in_job_mode := cst_p^.xcb_p^.cp_time.time_spent_in_job_mode + cst_p^.
0 9710 accumulated_job_cptime;
0 9711
0 9712 IF task_time_slice_used >= cst_p^.ijle_p^.dispatching_control.service_remaining THEN
0 9713
0 9714 { Reset the dispatching control for the task, based on the service class dispatching controls.
0 9715

```

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update_cp_statistics

```

0 9716 excess_cp_time_used := task_time_slice_used - cst_p^.ijle_p^.dispatching_control.service_remaining;
0 9717 tmp$reset_dispatching_control (cst_p^.ijle_p, cst_p^.ijl_ordinal, excess_cp_time_used, TRUE);
0 9718
0 9719 ELSE
0 9720
0 9721 { Calculate the time remaining on this tasks time slice.
0 9722
0 9723 cst_p^.ijle_p^.dispatching_control.service_remaining := cst_p^.ijle_p^.dispatching_control.
0 9724 service_remaining - task_time_slice_used;
0 9725 minor_time_slice_remaining := cst_p^.xcb_p^.timeslice.minor - task_time_slice_used;
0 9726 major_time_slice_remaining := cst_p^.xcb_p^.timeslice.major - task_time_slice_used;
0 9727 IF minor_time_slice_remaining < 0 THEN
0 9728 cst_p^.xcb_p^.timeslice.minor := 0;
0 9729 ELSE
0 9730 cst_p^.xcb_p^.timeslice.minor := minor_time_slice_remaining;
0 9731 IFEND;
0 9732 IF major_time_slice_remaining < 0 THEN
0 9733 cst_p^.xcb_p^.timeslice.major := 0;
0 9734 ELSE
0 9735 cst_p^.xcb_p^.timeslice.major := major_time_slice_remaining;
0 9736 IFEND;
0 9737 IFEND;
0 9738
0 9739 { Send a flag to the job monitor of the current job if the flag interval
0 9740 { has expired.
0 9741
0 9742 total_cptime := cst_p^.ijle_p^.statistics.cp_time.time_spent_in_job_mode + cst_p^.ijle_p^.statistics.
0 9743 cp_time.time_spent_in_mtr_mode;
0 9744 IF total_cptime - cst_p^.jcb_p^.cptime_signal_last_sent >= cst_p^.jcb_p^.signal_interval THEN
0 9745 tmp$set_system_flag (cst_p^.jcb_p^.job_monitor_id, avc$monitor_statistics_flag, status);
0 9746 cst_p^.jcb_p^.cptime_signal_last_sent := total_cptime;
0 9747 IFEND;
0 9748
0 9749 PROCEND update_cp_statistics;
0 9750

```

[INLINE] update_dispatching_controls

```

0 9752
0 9753 PROCEDURE [INLINE] update_dispatching_controls
0 9754 (
0 9755     time_used: integer;
0 9756     dispatching_priority: jmt$dispatching_priority);
0 9757
0 9758 VAR
0 9759     dp: jmt$dispatching_priority;
0 9760
0 9761 IF (tmv$dispatching_controls.controls_defined) AND (dispatching_priority < jmc$priority_p9) THEN
0 9762 IF time_used >= tmv$dispatching_control_time.time_left_in_interval THEN {RESET THE TABLE}
0 9763     tmv$dispatching_control_time := tmv$dispatching_controls.controls;
0 9764     tmv$dispatching_control_sets.minimums_to_satisfy := tmv$dispatching_controls.minimums_to_satisfy;
0 9765     tmv$dispatching_control_sets.maximums_exceeded := $jmt$dispatching_priority_set [];
0 9766     tmv$dispatching_control_sets.enforce_maximums := $jmt$dispatching_priority_set [];
0 9767     tmp$calculate_dct_priority_int;
0 9768     FOR dp := jmc$priority_p1 TO jmc$priority_p8 DO
0 9769         update_priority_integer (dp);
0 9770     FOREND;
0 9771 ELSE
0 9772     tmv$dispatching_control_time.time_left_in_interval := tmv$dispatching_control_time.
0 9773         time_left_in_interval - time_used;
0 9774     IF dispatching_priority <> jmc$null_dispatching_priority THEN
0 9775         IF (jmc$dp_conversion - dispatching_priority) IN tmv$dispatching_control_sets.
0 9776             minimums_to_satisfy THEN
0 9777             IF time_used >= tmv$dispatching_control_time.dispatching_priority_time
0 9778                 [dispatching_priority].minimum_time THEN
0 9779                 tmv$dispatching_control_sets.minimums_to_satisfy := tmv$dispatching_control_sets.
0 9780                     minimums_to_satisfy - $jmt$dispatching_priority_set
0 9781                     [jmc$dp_conversion - dispatching_priority];
0 9782                 tmp$calculate_dct_priority_int;
0 9783                 update_priority_integer (dispatching_priority);
0 9784             ELSE
0 9785                 tmv$dispatching_control_time.dispatching_priority_time [dispatching_priority].
0 9786                     := tmv$dispatching_control_time.dispatching_priority_time [dispatching_priority].
0 9787                     minimum_time - time_used;
0 9788             IFEND;
0 9789         IFEND;
0 9790     IF ((jmc$dp_conversion - dispatching_priority) IN tmv$dispatching_controls.maximums_defined)
0 9791         AND NOT ((jmc$dp_conversion - dispatching_priority) IN tmv$dispatching_control_sets.
0 9792             maximums_exceeded) THEN
0 9793         IF time_used >= tmv$dispatching_control_time.dispatching_priority_time
0 9794             [dispatching_priority].maximum_time THEN
0 9795             tmv$dispatching_control_sets.maximums_exceeded := tmv$dispatching_control_sets.
0 9796                 maximums_exceeded + $jmt$dispatching_priority_set
0 9797                 [jmc$dp_conversion - dispatching_priority];
0 9798             jmv$idle_dispatching_controls.maximums_exceeded := jmv$idle_dispatching_controls.
0 9799                 maximums_exceeded + $jmt$dispatching_priority_set [jmc$dp_conversion -
0 9800                 dispatching_priority];
0 9801             IF (jmc$dp_conversion - dispatching_priority) IN tmv$dispatching_controls.enforce_maximums THEN
0 9802                 tmv$dispatching_control_sets.enforce_maximums := tmv$dispatching_control_sets.
0 9803                     enforce_maximums + $jmt$dispatching_priority_set
0 9804                     [jmc$dp_conversion - dispatching_priority];
0 9805             IFEND;
0 9806             tmp$calculate_dct_priority_int;
0 9807             update_priority_integer (dispatching_priority);

```

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[INLINE] update_dispatching_controls

```

0 9808     ELSE
0 9809         tmv$dispatching_control_time.dispatching_priority_time [dispatching_priority].maximum_time
0 9810             := tmv$dispatching_control_time.dispatching_priority_time [dispatching_priority].
0 9811             maximum_time - time_used;
0 9812     IFEND;
0 9813     IFEND;
0 9814     IFEND;
0 9815     IFEND;
0 9816     IFEND;
0 9817
0 9818 PROCEND update_dispatching_controls;
0 9819

```


[INLINE] update_priority_integer

```

0 9821 { PURPOSE:
0 9822 {   The purpose of this procedure is to calculate and change the integer priority value for a specific
0 9823 {   dispatching priority. This is done only when the priority crosses a minimum allocated or maximum
0 9824 {   allocated dispatching allocation threshold.
0 9825
0 9826 PROCEDURE [INLINE] update_priority_integer
0 9827 (   dp: jmt$dispatching_priority);
0 9828
0 9829 VAR
0 9830     local_set: tmt$dispatching_control_sets;
0 9831
0 9832     local_set := tmv$dispatching_control_sets;
0 9833
0 9834     local_set.ready_tasks := $jmt$dispatching_priority_set [jmc$dp_conversion - dp] -
0 9835     (local_set.enforce_maximums * local_set.maximums_exceeded);
0 9836     local_set.enforce_maximums := $jmt$dispatching_priority_set [];
0 9837     local_set.minimums_to_satisfy := local_set.minimums_to_satisfy * local_set.ready_tasks;
0 9838     local_set.maximums_exceeded := local_set.maximums_exceeded * local_set.ready_tasks;
0 9839     local_set.ready_tasks := local_set.ready_tasks XOR (local_set.minimums_to_satisfy +
0 9840     local_set.maximums_exceeded);
0 9841     #unchecked_conversion (local_set, tmv$dispatch_priority_integer [dp]);
0 9842
0 9843 PROCEND update_priority_integer;
0 9844

```

[XDCL, INLINE] tmp\$calculate_dct_priority_int

```

0 9846 { PURPOSE:
0 9847 {   The purpose of this procedure is to calculate the integer priority value for the DCT queues
0 9848 {   with ready tasks. If there is nothing queued in the DCT, the integer priority will be 0.
0 9849 {   If there are tasks queued, the integer priority represents the highest allocated dispatching
0 9850 {   priority.
0 9851
0 9852 PROCEDURE [XDCL, INLINE] tmp$calculate_dct_priority_int;
4 9853
4 9854 VAR
4 9855     local_set: tmt$dispatching_control_sets;
4 9856
4 9857     local_set := tmv$dispatching_control_sets;
4 9858     local_set.ready_tasks := local_set.ready_tasks - (local_set.enforce_maximums *
4 9859     local_set.maximums_exceeded);
4 9860     local_set.enforce_maximums := $jmt$dispatching_priority_set [];
4 9861     local_set.minimums_to_satisfy := local_set.minimums_to_satisfy * local_set.ready_tasks;
4 9862     local_set.maximums_exceeded := local_set.maximums_exceeded * local_set.ready_tasks;
4 9863     local_set.ready_tasks := local_set.ready_tasks XOR (local_set.minimums_to_satisfy +
4 9864     local_set.maximums_exceeded);
4 9865
4 9866     #unchecked_conversion (local_set, tmv$dct_priority_integer);
4 9867
4 9868 PROCEND tmp$calculate_dct_priority_int;
0 9869

```

INITIATE_SWAP_IF_POSSIBLE

```

0 9871
0 9872 { NOTE : Anyone calling this routine must have tmv$ptl_lock set.
0 9873
0 9874 PROCEDURE [INLINE] initiate_swap_if_possible
0 9875 ( cst_p: ^ost$cpu_state_table);
0 9876
0 9877 VAR
0 9878   jcb_p: ^jmt$job_control_block,
0 9879   temp_next_cyclic_aging: integer,
0 9880   ptlo: ost$task_index;
0 9881
0 9882   ptlo := cst_p^.ijle_p^.job_monitor_taskid.index;
0 9883   WHILE (ptlo < 0) AND (tmv$ptl_p^ [ptlo].idle_status >= tmc$sis_idled) DO
0 9884     ptlo := tmv$ptl_p^ [ptlo].ijl_thread;
0 9885   WHILEND;
0 9886   IF ptlo = 0 THEN
0 9887     jcb_p := #ADDRESS (1, tmc$job_fixed_segment + cst_p^.ajlo, 0);
0 9888     temp_next_cyclic_aging := jcb_p^.next_cyclic_aging_time - #FREE_RUNNING_CLOCK (0);
0 9889     IF temp_next_cyclic_aging < 0 THEN
0 9890       jcb_p^.next_cyclic_aging_time := 0;
0 9891     ELSE
0 9892       jcb_p^.next_cyclic_aging_time := temp_next_cyclic_aging;
0 9893     IFEND;
0 9894     jsp$idle_tasks_complete (cst_p^.ijl_ordinal);
0 9895   IFEND;
0 9896
0 9897 PROCEND initiate_swap_if_possible;

```

SOURCE LIST OF tmm\$dispatcher

TMP\$FETCH_TASK_STATISTICS

```

0 9899
0 9900 PROCEDURE [XDCL] tmp$fetch_task_statistics
0 9901 (VAR rb: tmt$rb_fetch_task_statistics;
0 9902   cst_p: ^ost$cpu_state_table);
0 9903
0 9904
0 9905 {
0 9906 { The purpose of this request is to return task statistics.
0 9907 { Currently this request returns the MONITOR MODE time
0 9908 { and the JOB MODE time for the task.
0 9909 {
0 9910 {   TMP$FETCH_TASK_STATISTICS (RB, CST_P)
0 9911 {
0 9912 { RB: (INPUT,OUTPUT) This parameter specifies the request block.
0 9913 {
0 9914 { CST_P: (INPUT) This parameter specifies the pointer to the CST.
0 9915 {
0 9916
0 9917 VAR
0 9918   current_pit_value: integer;
0 9919
0 9920   rb.status.normal := TRUE;
0 9921   rb.monitor_cptime := cst_p^.xcb_p^.cp_time.time_spent_in_mtr_mode + 0ffffff(16) -
20 9922     #READ_REGISTER (osc$pr_process_interval_timer);
20 9923
20 9924   current_pit_value := cst_p^.xcb_p^.xp.process_interval_timer_1 + 10000(16) + cst_p^.xcb_p^.xp.
20 9925     process_interval_timer_2;
20 9926   IF current_pit_value > 7ffffff(16) THEN
42 9927     current_pit_value := current_pit_value - 10000000(16);
48 9928   IFEND;
48 9929   rb.job_cptime := cst_p^.accumulated_job_cptime - current_pit_value +
48 9930     cst_p^.xcb_p^.cp_time.time_spent_in_job_mode;
48 9931
48 9932 PROCEND tmp$fetch_task_statistics;

```

[XDCL] tmp\$mtr_update_job_task_enviro

```

0 9935
0 9936 PROCEDURE [XDCL] tmp$mtr_update_job_task_enviro
0 9937 (VAR rb: tmt$rb_update_job_task_enviro;
0 9938 cst_p: ^ost$cpu_state_table);
0 9939
0 9940
0 9941 VAR
0 9942 current_pit_value: integer;
0 9943 dispatching_controls: jmt$dispatching_controls;
0 9944 i: integer;
0 9945 ijle_p: ^jmt$initiated_job_list_entry;
0 9946 new_max_ptlo: ost$task_index;
0 9947 old_max_ptlo: ost$task_index;
0 9948 service_used: integer;
0 9949
0 9950 rb.status.normal := TRUE;
4 9951 tmp$set_lock (tmv$pt1_lock);
4A 9952 CASE rb.subcode OF
A6 9953 = tmc$ujte_xp_register =
A6 9954 CASE rb.register_id OF
B2 9955 = osc$pr_process_interval_timer =
B2 9956 current_pit_value := cst_p^xcb_p^xp.process_interval_timer_1 * 10000(16) + cst_p^xcb_p^xp.
B2 9957 process_interval_timer_2;
B2 9958 IF current_pit_value > 7fffff(16) THEN
D0 9959 current_pit_value := current_pit_value - 10000000(16);
D6 9960 IFEND;
D6 9961 cst_p^accumulated_job_cptime := cst_p^accumulated_job_cptime + rb.pit_value - current_pit_value;
D6 9962 cst_p^xcb_p^pit_count := cst_p^xcb_p^pit_count + rb.pit_value - current_pit_value;
D6 9963 cst_p^xcb_p^xp.process_interval_timer_1 := rb.pit_value DIV 10000(16);
D6 9964 cst_p^xcb_p^xp.process_interval_timer_2 := rb.pit_value MOD 10000(16);
10A 9965 ELSE
10A 9966 CASEEND;
10E 9967
10E 9968 = tmc$ujte_dispatching_priority =
10E 9969 jmp$get_ijle_p (rb.ijl_ordinal, ijle_p);
10E 9970 IF ijle_p <> NIL THEN
13E 9971
13E 9972 { Verify that the same job is still using this ijl ordinal.
13E 9973
13E 9974 IF ijle_p^system_supplied_name <> rb.system_supplied_name THEN
14C 9975 tmp$clear_lock (tmv$pt1_lock);
182 9976 tmp$set_status_abnormal ('JM', jme$non_existent_job, rb.status);
182 9977 RETURN;
19A 9978 IFEND;
19A 9979
19A 9980 CASE rb.request_origin OF
1D0 9981 = tmc$cpo_operator, tmc$cpo_recovery =
1D0 9982
1D0 9983 { A null dispatching priority indicates the operator specified DEFAULT; the system must determine
1D0 9984 { the correct dispatching control set based on total service the job has used.
1D0 9985 { If the operator has specified a dispatching priority, assign the job that priority.
1D0 9986
1D0 9987 { If the null priority is coming from job recovery, we have completed job recovery and must now
1D0 9988 { set the dispatching priority back to its original value. It had been set up to the system job
1D0 9989 { dispatching priority to guarantee that it would swap in and recover.
1D0 9990

```

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[XDCL] tmp\$mtr_update_job_task_enviro

```

1D0 9991 IF rb.dispatching_control_info.dispatching_priority = jmc$null_dispatching_priority THEN
1D8 9992
1E0 9993 IF rb.request_origin = tmc$cpo_operator THEN
1EA 9994 ijle_p^dispatching_control.operator_set_dispatching_prio := jmc$null_dispatching_priority;
1EA 9995 IFEND;
1EA 9996 calculate_service_used (ijle_p, service_used);
1FE 9997 tmp$reset_dispatching_control (ijle_p, rb.ijl_ordinal, service_used, FALSE);
22C 9998
22C 9999 ELSE
22C 10000 ijle_p^dispatching_control.operator_set_dispatching_prio := rb.dispatching_control_info.
22C 10001 dispatching_priority;
22C 10002 ijle_p^dispatching_control.dispatching_priority := rb.dispatching_control_info.
22C 10003 dispatching_priority;
22C 10004 ijle_p^dispatching_control.service_remaining := jmc$dc_maximum_service_limit;
22C 10005 tmp$update_job_task_environment (ijle_p, rb.ijl_ordinal, tmc$fnx_job);
262 10006 IFEND;
262 10007
262 10008 IF (ijle_p^dispatching_control.dispatching_priority <
272 10009 ijle_p^scheduling_dispatching_priority) THEN
272 10010 ijle_p^scheduling_dispatching_priority := ijle_p^dispatching_control.dispatching_priority;
276 10011 IFEND;
27A 10012
27A 10013 = tmc$cpo_user =
27A 10014
27A 10015 { Store the user requested dispatching priority, determine which priority is allowed, and update.
27A 10016
27A 10017 ijle_p^dispatching_control.user_requested_dispatching_prio := rb.dispatching_control_info.
27A 10018 dispatching_priority;
27A 10019 determine_dispatching_priority (ijle_p);
286 10020 tmp$update_job_task_environment (ijle_p, rb.ijl_ordinal, tmc$fnx_job);
28C 10021
28C 10022 = tmc$cpo_set_job_unswappable =
28C 10023
28C 10024 { A job is set unswappable during job termination. The job is given system dispatching priority
28C 10025 { with unlimited service.
28C 10026
28C 10027 ijle_p^dispatching_control.dispatching_control_index := jmc$min_dispatching_control;
28C 10028 ijle_p^scheduling_dispatching_priority := jmc$priority_system_job;
28C 10029 ijle_p^dispatching_control.dispatching_priority := jmc$priority_system_job;
28C 10030 ijle_p^dispatching_control.service_remaining := jmc$dc_maximum_service_limit;
28C 10031 ijle_p^dispatching_control.operator_set_dispatching_prio := jmc$null_dispatching_priority;
28C 10032 ijle_p^dispatching_control.user_requested_dispatching_prio := jmc$null_dispatching_priority;
28C 10033 tmp$update_job_task_environment (ijle_p, rb.ijl_ordinal, tmc$fnx_job);
302 10034
302 10035 = tmc$cpo_interactive_command, tmc$cpo_save_swap_file_sfid =
302 10036
302 10037 { A new command line has been scanned for an interactive job or the job is through critical initiation;
302 10038 { the first dispatching control set for the class is used.
302 10039
302 10040 IF rb.request_origin = tmc$cpo_interactive_command THEN
30C 10041 ijle_p^interactive_task_gtid := tmv$null_global_task_id;
318 10042 IFEND;
318 10043 ijle_p^dispatching_control.dispatching_control_index := jmc$min_dispatching_control;
318 10044 ijle_p^dispatching_control.service_remaining := jmv$service_classes
318 10045 [ijle_p.job_scheduler_data.service_class]^attributes.dispatching_control [1].service_limit;
318 10046 determine_dispatching_priority (ijle_p);

```

[XDCL] tmp\$mtr_update_job_task_enviro

```

34C 10047      tmp$update_job_task_environment (ijle_p, rb.ijl_ordinal, tmc$fnx_job);
372 10048
372 10049      = tmc$cpo_interrupt_restore =
372 10050
372 10051 { Restore the job's dispatching control information to the values saved from before a user interrupt.
372 10052 { If the dispatching_control_index is no longer a valid one for the job's service class (the service class
372 10053 { has been changed since the dispatching control info was saved), calculate the correct dispatching_
372 10054 { control_index based on the CP service the job has used.
372 10055
372 10056      dispatching_controls := jmv$service_classes [ijle_p^.job_scheduler_data.service_class]^^.attributes.
39E 10057      dispatching_control [rb.dispatching_control_info.dispatching_control_index];
39E 10058      IF (NOT dispatching_controls.set_defined) OR (dispatching_controls.service_limit <
38E 10059      rb.dispatching_control_info.service_remaining) OR
38E 10060      [dispatching_controls.dispatching_priority <> rb.dispatching_control_info.
38E 10061      dispatching_priority] THEN
38E 10062
38E 10062      calculate_service_used (ijle_p, service_used);
38E 10063      tmp$reset_dispatching_control (ijle_p, rb.ijl_ordinal, service_used, FALSE);
404 10064
404 10065      ELSE
404 10066      ijle_p^.dispatching_control.dispatching_control_index := rb.dispatching_control_info.
404 10067      dispatching_control_index;
404 10068      ijle_p^.dispatching_control.service_remaining := rb.dispatching_control_info.
404 10069      service_remaining;
404 10070      determine_dispatching_priority (ijle_p);
418 10071      tmp$update_job_task_environment (ijle_p, rb.ijl_ordinal, tmc$fnx_job);
43A 10072
440 10073      IFEND;
442 10074
442 10075      CASEEND;
442 10076
442 10077      IFEND;
446 10078
446 10079      = tmc$ujte_set_non_swappable =
446 10080      IF tmv$ptl_p^ [cst_p^.taskid.index].idle_status = tmc$is_not_idled THEN
462 10081      jmp$change_ijl_entry_status (cst_p^.ijle_p, jmc$ies_job_in_memory_non_swap);
488 10082
488 10083      ELSE
488 10083      tmp$reissue_monitor_request;
4C0 10084      cst_p^.dispatch_control.call_dispatcher := TRUE;
4C4 10085
4C4 10085      IFEND;
4C8 10086
4C8 10087      = tmc$ujte_idle_other_sys_tasks =
4C8 10088      tmp$idle_tasks_in_system_job (tmc$sir_other_system_tasks, rb.status);
4DE 10089
4DE 10090      = tmc$ujte_restart_other_systasks =
4DE 10091      tmp$restart_tasks_in_system_job (tmc$sir_other_system_tasks, rb.status);
4F4 10092
4F4 10093      = tmc$ujte_idle_dm_sys_tasks =
4F4 10094      tmp$idle_tasks_in_system_job (tmc$sir_dm_system_tasks, rb.status);
50C 10095
50C 10096      = tmc$ujte_restart_dm_systasks =
50C 10097      tmp$restart_tasks_in_system_job (tmc$sir_dm_system_tasks, rb.status);
524 10098
524 10099      = tmc$ujte_expand_ptl =
524 10100
524 10101 { Copy the entries from the old PTL to the new PTL. Then link the new PTL entries into the free queue.
524 10102 { Reset tmv$ptl_p to point to the new expanded ptl.

```

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[XDCL] tmp\$mtr_update_job_task_enviro

```

524 10103
524 10104      old_max_ptlo := UPPERBOUND (tmv$ptl_p^);
524 10105      new_max_ptlo := UPPERBOUND (rb.ptl_p^);
524 10106      I$MOVE [tmv$ptl_p, rb.ptl_p, #SIZE [tmv$ptl_p^]];
570 10107      FOR i := old_max_ptlo + 1 TO new_max_ptlo - 1 DO
57C 10108      rb.ptl_p^[i].ptl_thread := i + 1;
57C 10109      FOREND;
590 10110      rb.ptl_p^[new_max_ptlo].ptl_thread := 0;
590 10111      tmv$ptl_p := rb.ptl_p;
5A8 10112
5A8 10113      IF tmv$dct [jmc$null_dispatching_priority].queue_head = 0 THEN
5B0 10114      tmv$dct [jmc$null_dispatching_priority].queue_head := old_max_ptlo + 1;
5BC 10115
5BC 10116      ELSE
5BC 10116      tmv$ptl_p^ [tmv$dct [jmc$null_dispatching_priority].queue_tail].ptl_thread := old_max_ptlo + 1;
5CE 10117
5CE 10118      tmv$dct [jmc$null_dispatching_priority].queue_tail := new_max_ptlo;
5D6 10119
5D6 10120      = tmc$ujte_update_debug_masks =
5D6 10121      tmp$update_debug_registers;
5E2 10122
5E2 10123      = tmc$ujte_set_task_terminating =
5E2 10124
5E2 10125 { The free_flag is not set here because immediately after the return to job mode,
5E2 10126 { all of the outstanding signals and flags are processed.
5E2 10127
5E2 10128      cst_p^.xcb_p^.task_is_terminating := TRUE;
5E2 10129      cst_p^.xcb_p^.monitor_flags := cst_p^.xcb_p^.monitor_flags + tmv$ptl_p^
5E2 10130      [cst_p^.taskid.index].monitor_flags;
5E2 10131      cst_p^.xcb_p^.system_flags := cst_p^.xcb_p^.system_flags + tmv$ptl_p^
5E2 10132      [cst_p^.taskid.index].system_flags;
5E2 10133      tmv$ptl_p^ [cst_p^.taskid.index].system_flags := $tmt$system_flags [ ];
5E2 10134      tmv$ptl_p^ [cst_p^.taskid.index].monitor_flags := $syt$monitor_flags [ ];
628 10135
628 10136      ELSE
628 10137      tmp$clear_lock (tmv$ptl_lock);
662 10138      mtp$error_stop ('TM - unimplemented subcode');
682 10139
682 10140      CASEEND;
682 10141      tmp$clear_lock (tmv$ptl_lock);
68A 10142
68A 10143      PROCEND tmp$mtr_update_job_task_enviro;
68A 10143
68A 10144

```

TMP\$UPDATE_JOB_TASK_ENVIRONMENT

```

O 10146
O 10147 { PURPOSE:
O 10148 { This procedure changes the dispatching priority and timeslice in the XCB
O 10149 { for all tasks of a job.
O 10150 { DESIGN:
O 10151 { The caller has stored the new dispatching priority in the IJL.
O 10152 { If the XCB of the job can be referenced (ie, the job is NOT swapped out)
O 10153 { changes to the XCB are made. If the job is swapped out, the delayed
O 10154 { swapin work bit is set, which causes this procedure to be called again
O 10155 { when the job swaps back in.
O 10156
O 10157 PROCEDURE [XDCL] tmp$update_job_task_environment
O 10158 { ijle_p: Ajmt$initiated_job_list_entry;
O 10159 { ij1_ordinal: jmt$ij1_ordinal;
O 10160 { xcb_search: tmt$fnx_search_type);
O 10161
O 10162 VAR
O 10163 { attempt_preselection: boolean,
O 10164 { ptlo: ost$task_index,
O 10165 { ptle_p: Aamt$primary_task_list_entry,
O 10166 { xcb_p: Aost$execution_control_block,
O 10167 { dispatching_priority: integer, {must be integer}
O 10168 { state: tmt$find_next_xcb_state;
O 10169
O 10170 tmp$set_lock (tmv$pt1_lock);
O 10171 tmp$find_next_xcb (xcb_search, ijle_p, ij1_ordinal, state, xcb_p);
O 10172 IF xcb_p = NIL THEN
O 10173 { ijle_p^.delayed_swapin_work := ijle_p^.delayed_swapin_work +
O 10174 { $jmt$delayed_swapin_work [jmc$dsw_update_job_task_enviro];
O 10175 ELSE
O 10176 WHILE xcb_p <> NIL DO
O 10177 IF xcb_p^.dispatching_priority_bias_id <> jmc$dpb_absolute THEN
O 10178 IF xcb_p^.dispatching_priority_bias_id = jmc$dpb_positive THEN
O 10179 dispatching_priority := ijle_p^.dispatching_control.dispatching_priority
O 10180 + xcb_p^.dispatching_priority_bias;
O 10181 ELSE
O 10182 dispatching_priority := ijle_p^.dispatching_control.dispatching_priority
O 10183 - xcb_p^.dispatching_priority_bias;
O 10184 IFEND;
O 10185 IF dispatching_priority > jmc$max_dispatching_priority THEN
O 10186 dispatching_priority := jmc$max_dispatching_priority;
O 10187 ELSEIF dispatching_priority < jmc$min_dispatching_priority THEN
O 10188 dispatching_priority := jmc$min_dispatching_priority;
O 10189 IFEND;
O 10190 xcb_p^.dispatching_priority := dispatching_priority;
O 10191 ptlo := xcb_p^.global_task_id.index;
O 10192 ptle_p := Aamt$pt1_p[ptlo];
O 10193 xcb_p^.timeslice := jmv$service_classes [ijle_p^.job_scheduler_data.service_class]^.attributes
O 10194 dispatching_control [ijle_p^.dispatching_control.dispatching_control_index].
O 10195 dispatching_timeslice;
O 10196 IF xcb_p^.system_table_lock_count <= 0 THEN
O 10197 IF ptle_p^.status <= tmc$ts_last_status_in_dct THEN
O 10198 tmp$remove_task_from_dct (ptlo);
O 10199 ptle_p^.dispatching_priority := xcb_p^.dispatching_priority;
O 10200 attempt_preselection := (ijle_p^.multiprocessing_allowed) OR (ijle_p^.executing_task_count = 0);
*WARN= 10201 tmp$dct_ready_task (xcb_p, ijle_p, ptlo, attempt_preselection);

```

TMP\$UPDATE_JOB_TASK_ENVIRONMENT

```

C46 10202 ELSE
C46 10203 { ptle_p^.dispatching_priority := xcb_p^.dispatching_priority;
C46 10204 IFEND;
C52 10205 IFEND;
C52 10206 IFEND;
C52 10207 tmp$find_next_xcb (tmc$fnx_continue, NIL, jmv$nul1_ij1_ordinal, state, xcb_p);
C84 10208 WHILEND;
C98 10209 IFEND;
C98 10210 tmp$clear_lock (tmv$pt1_lock);
CD4 10211
CD4 10212 PROCEND tmp$update_job_task_environment;
O 10213

```

TMP\$UPDATE_JOB_TASK_CPU_SELECTS

```

0 10215
0 10216 { PURPOSE:
0 10217 { This procedure changes the processor selections the XCB for all tasks of a job.
0 10218 { DESIGN:
0 10219 { The caller has stored the value of the processors which are still logically on in the SCB. If the XCB of
0 10220 { the job can be referenced (ie, the job is NOT swapped out) changes to the XCB are made. If the job is
0 10221 { swapped out, the delayed swapin work bit is set, which causes the job mode version of this procedure to be
0 10222 { called when the job swaps back in.
0 10223
0 10224 PROCEDURE [XDCL] tmp$update_job_task_cpu_selects
0 10225 {   ijle_p: ^jmt$initiated_job_list_entry;
0 10226 {     ijl_ordinal: jmt$ijl_ordinal;
0 10227 {     xcb_search: tmt$fnx_search_type);
0 10228
0 10229 VAR
0 10230 {   xcb_p: ^ost$execution_control_block,
0 10231 {     state: tmt$find_next_xcb_state;
0 10232
0 10233 tmp$set_lock (tmv$ptl_lock);
42 10234 tmp$find_next_xcb (xcb_search, ijle_p, ijl_ordinal, state, xcb_p);
76 10235 IF xcb_p = NIL THEN
84 10236   ijle_p^.delayed_swapin_work := ijle_p^.delayed_swapin_work +
9A 10237   $jmt$delayed_swapin_work [jmc$dsw_adjust_cpu_selections];
9A 10238 ELSE
9A 10239   WHILE xcb_p <> NIL DO
A0 10240     IF xcb_p^.processor_selections * mtv$scb.processors_logically_on = $ost$processor_id_set [ ] THEN
B6 10241       xcb_p^.processor_selections := mtv$scb.processors_logically_on;
BA 10242     IFEND;
BA 10243     tmp$find_next_xcb (tmc$fnx_continue, NIL, jmv$null_ijkl_ordinal, state, xcb_p);
EA 10244   WHILEND;
100 10245 IFEND;
100 10246 tmp$clear_lock (tmv$ptl_lock);
13C 10247
13C 10248 PROCEND tmp$update_job_task_cpu_selects;
0 10249

```

SOURCE LIST OF tmm\$dispatcher

CALCULATE_SERVICE_USED

```

0 10251
0 10252 { PURPOSE:
0 10253 { This procedure calculates the amount of CP service a job has used. The procedure
0 10254 { tmp$reset_dispatching_control determines the dispatching control index to use, based
0 10255 { on the SERVICE_USED returned.
0 10256 { DESIGN:
0 10257 { For interactive jobs, return 0 for service_used; the dispatching control index should
0 10258 { be reset to the first index. For batch classes, base service_used on total cp time.
0 10259 { If the job's service class has circular dynamic dispatching priorities defined,
0 10260 { MOD the service_used by the sums of the service limits.
0 10261
0 10262 PROCEDURE calculate_service_used
0 10263 {   ijle_p: ^jmt$initiated_job_list_entry;
0 10264 {     VAR service_used: integer);
0 10265
0 10266 VAR
0 10267 {   circular_service: integer,
0 10268 {   dispatching_control_p: ^jmt$dispatching_control,
0 10269 {   dispatching_control_index: jmt$dispatching_control_index;
0 10270
0 10271 IF ijle_p^.job_mode <> jmc$batch THEN
C 10272   service_used := 0;
14 10273 ELSE
14 10274   service_used := ijle_p^.statistics.cp_time.time_spent_in_job_mode + ijle_p^.statistics.
14 10275   cp_time.time_spent_in_mtr_mode - ijle_p^.dispatching_control.cp_service_at_class_switch;
14 10276   circular_service := 0;
14 10277   dispatching_control_p := ^jmv$service_classes [ijle_p^.job_scheduler_data.service_class]^.attributes.
14 10278   dispatching_control;
14 10279 /calculate_circular_service/
14 10280   FOR dispatching_control_index := jmc$max_dispatching_control DOWNT0
4C 10281     jmc$min_dispatching_control DO
4C 10282     IF dispatching_control_p [dispatching_control_index].set_defined THEN
5C 10283     IF dispatching_control_p [dispatching_control_index].service_limit <>
6A 10284     jmc$dc_maximum_service_limit THEN
6A 10285     circular_service := circular_service + dispatching_control_p [dispatching_control_index].
70 10286     service_limit;
70 10287     ELSE
70 10288     EXIT /calculate_circular_service/;
74 10289     IFEND;
74 10290   FOREND /calculate_circular_service/;
7A 10292 IF circular_service <> 0 THEN
7E 10293   service_used := service_used MOD circular_service;
8E 10294 IFEND;
8E 10295 IFEND;
8E 10296
8E 10297 PROCEND calculate_service_used;
0 10298

```

DETERMINE_DISPATCHING_PRIORITY

```

0 10300
0 10301 { PURPOSE:
0 10302 { This procedure determines which dispatching priority a job should have
0 10303 { and stores it in the IJL.
0 10304 { DESIGN:
0 10305 { If the operator has changed the dispatching priority for a job, that dispatching
0 10306 { priority is used. Otherwise the job is assigned the lesser of the user requested
0 10307 { dispatching priority (if there is one) and the dispatching priority defined in the
0 10308 { service class table dispatching control sets.
0 10309
0 10310 PROCEDURE determine_dispatching_priority
0 10311 ( ijle_p: ^jmt$initiated_job_list_entry);
0 10312
0 10313 VAR
0 10314 update_scheduling_priority: boolean;
0 10315
0 10316 { The scheduling_dispatching_priority and the dispatching_controls.dispatching_priority will only
0 10317 { be different in the case of a task of the job having subsystem locks set and being readied by
0 10318 { a task which has a higher dispatching priority. In that instance, the scheduling_dispatching_priority
0 10319 { will be updated when the task releases the lock and issues a CYCLE request.
0 10320
0 10321 update_scheduling_priority := (ijle_p^.dispatching_control.dispatching_priority = ijle_p^.
4 10322 scheduling_dispatching_priority);
0 10323 IF ijle_p^.dispatching_control.operator_set_dispatching_prio <> jmc$null_dispatching_priority THEN
1C 10324 ijle_p^.dispatching_control.dispatching_priority := ijle_p^.dispatching_control.
24 10325 operator_set_dispatching_prio;
24 10326 ELSE
24 10327 IF (ijle_p^.dispatching_control.user_requested_dispatching_prio <> jmc$null_dispatching_priority) AND
50 10328 (ijle_p^.dispatching_control.user_requested_dispatching_prio <
50 10329 jmv$service_classes [ijle_p^.job_scheduler_data.service_class]^
50 10330 attributes.dispatching_control [ijle_p^.dispatching_control.dispatching_control_index].
50 10331 dispatching_priority) THEN
50 10332 ijle_p^.dispatching_control.dispatching_priority := ijle_p^.dispatching_control.
58 10333 user_requested_dispatching_prio;
58 10334 ELSE
58 10335 ijle_p^.dispatching_control.dispatching_priority :=
7C 10336 jmv$service_classes [ijle_p^.job_scheduler_data.service_class]^
7C 10337 attributes.dispatching_control [ijle_p^.dispatching_control.dispatching_control_index].
7C 10338 dispatching_priority;
7C 10339 IFEND;
7C 10340 IFEND;
7C 10341 IF update_scheduling_priority THEN
7C 10342 ijle_p^.scheduling_dispatching_priority := ijle_p^.dispatching_control.dispatching_priority;
80 10343 IFEND;
88 10344
88 10345
88 10346 PROCEND determine_dispatching_priority;
0 10347

```

SOURCE LIST OF tmm\$dispatcher

TMP\$RESET_DISPATCHING_CONTROLS

```

0 10349
0 10350 { PURPOSE:
0 10351 { This procedure is called to reset the dispatching control set for a job.
0 10352 { DESIGN:
0 10353 { If expired_dispatching_control is TRUE, the procedure has been called because a
0 10354 { task switch determined that the job has used all the service allowed for the
0 10355 { current dispatching control index. The next index needs to be assigned.
0 10356 { Otherwise the procedure needs to determine the correct index based on
0 10357 { the total amount of service the job has used.
0 10358 { The procedure finds the correct dispatching control index, determines the
0 10359 { dispatching priority, and makes a call to update the dispatching priority
0 10360 { in the XCB.
0 10361
0 10362 PROCEDURE [XDCL] tmp$reset_dispatching_control
0 10363 ( ijle_p: ^jmt$initiated_job_list_entry;
0 10364 ijl_ordinal: jmt$ijl_ordinal;
0 10365 excess_service_used: integer;
0 10366 expired_dispatching_control: boolean);
0 10367
0 10368 VAR
0 10369 dispatching_control_p: ^jmt$dispatching_control,
0 10370 next_index: integer,
0 10371 service_used: integer;
0 10372
0 10373 dispatching_control_p := ^jmv$service_classes [ijle_p^.job_scheduler_data.service_class]^attributes.
4 10374 dispatching_control;
4 10375
4 10376 service_used := excess_service_used;
4 10377
4 10378 IF expired_dispatching_control THEN
2C 10379 next_index := ijle_p^.dispatching_control.dispatching_control_index + 1;
36 10380 ELSE
36 10381 next_index := jmc$min_dispatching_control;
38 10382 IFEND;
38 10383
38 10384 /find_dispatching_control_set/
38 10385 WHILE service_used >= 0 DO
3C 10386 IF (next_index <= jmc$max_dispatching_control) AND (dispatching_control_p^ [next_index].set_defined)
52 10387 THEN
52 10388 IF dispatching_control_p^ [next_index].service_limit > service_used THEN
5C 10389 ijle_p^.dispatching_control.service_remaining := dispatching_control_p^ [next_index].service_limit -
5C 10390 service_used;
5C 10391 ijle_p^.dispatching_control.dispatching_control_index := next_index;
5C 10392 determine_dispatching_priority (ijle_p);
7A 10393 tmp$update_job_task_environment (ijle_p, ijl_ordinal, tmc$fnx_job);
9A 10394 EXIT /find_dispatching_control_set/;
A0 10395 ELSE
A0 10396 service_used := service_used - dispatching_control_p^ [next_index].service_limit;
A4 10397 next_index := next_index + 1;
A4 10398 IFEND;
A8 10399 ELSE
A8 10400 next_index := jmc$min_dispatching_control;
AC 10401 IFEND;
AC 10402 WHILEND /find_dispatching_control_set/;
B0 10403
B0 10404 PROCEND tmp$reset_dispatching_control;

```

TMP\$RESET_DISPATCHING_CONTROLS

O 10405

SOURCE LIST OF tmm\$dispatcher

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TMP\$SWITCH_TASK

```

O 10407
O 10408 PROCEDURE [XDCL] tmp$switch_task
O 10409 { dummy: ^cell;
O 10410   cst_p: ^ost$cpu_state_table};
O 10411
O 10412
O 10413 {
O 10414 { The purpose of this procedure is to change the current task
O 10415 { of execution. The XCB, JCB and AJL accounting fields are
O 10416 { updated.
O 10417 {
O 10418 {   TMP$SWITCH_TASK
O 10419 {
O 10420 {
O 10421 {
O 10422 VAR
O 10423   attempt_preselection: boolean,
O 10424   locked_dp_integer: integer,
O 10425   next_ptlo_to_dispatch: ost$task_index,
O 10426   next_task_ijle_p: ^ajmt$initiated_job_list_entry,
O 10427   next_task_xcb_p: ^ost$execution_control_block,
O 10428   priority: dp_trick_conversion,
O 10429   local_set: tmt$dispatching_control_sets,
O 10430   integer_dp_sets: integer,
O 10431   r: real,
O 10432   idle_time: integer,
O 10433   time: integer,
O 10434   xcb_p: ^ost$execution_control_block,
O 10435   ptle_p: ^tmt$primary_task_list_entry,
O 10436   ptlo: ost$task_index;
O 10437
O 10438
O 10439 { Update task accounting info.
O 10440
O 10441   #KEYPOINT (osk$mtr, osk$monitor_multiplier * 0, tmk$switch_task);
O 10442
O 10443   IF NOT tmv$tables_initialized THEN
1A 10444     cst_p^.max_cptime := 20000;
1A 10445     RETURN;
28 10446   IFEND;
28 10447
28 10448 { Wake up memory link helper if 170 has set a flag to activate it.
28 10449
28 10450   ?IF NOT DEBUG THEN
28 10451     IF mtv$mli_status.wait_inhibit AND NOT mtv$mli_status.ready THEN
3C 10452       tmp$set_task_ready (mlv$c170_rqst_blk.req^.task_id, 0 {readying_task_priority},
62 10453         tmc$rc_ready_conditional_wi);
62 10454     IFEND;
62 10455   ?IFEND;
62 10456
62 10457 { If the CPU is not idle (cst_p^.xcb_p is not NIL), update the executing tasks's status.
62 10458
62 10459   xcb_p := cst_p^.xcb_p;
62 10460
62 10461   tmp$set_lock (tmv$pt1_lock);
A4 10462

```


TMP\$SWITCH_TASK

```

A4 10463 IF xcb_p <> NIL THEN
AA 10464
AA 10465 IF cst_p^.accumulated_job_cptime <> 0 THEN
*WARN* 10466 update_cp_statistics (cst_p);
56A 10467 IFEND;
56A 10468
56A 10469 xcb_p^.last_lpid_for_task := cst_p^.cst_index;
56A 10470 cst_p^.jcb_p^.last_lpid_for_job := cst_p^.cst_index;
56A 10471 cst_p^.jcb_p^.last_execution_time := #FREE_RUNNING_CLOCK (0);
582 10472 ptlo := cst_p^.taskid.index;
582 10473 ptle_p := Atmv$ptl_p^ [ptlo];
582 10474
582 10475 { If the task has system tables locked and is still ready, let it keep running.
582 10476
582 10477 /system_locks_set/
582 10478 BEGIN
582 10479 IF xcb_p^.system_table_lock_count > 0 THEN
582 10480 IF (ptle_p^.new_task_status = tmc$ts_null) AND (cst_p^.cpu_state.next_state <> osc$cpu_stepped) AND
582 10481 (cst_p^.next_processor_state = cmc$on) THEN
582 10482 IF (xcb_p^.system_table_lock_count > osc$system_table_lock_set) THEN
582 10483 cst_p^.dispatching_priority := jmc$prior_system_tbls_locked;
582 10484 locked_dp_integer := tmv$dispatch_priority_integer [jmc$prior_system_tbls_locked];
582 10485 xcb_p^.system_give_up_cpu := TRUE;
582 10486 ELSEIF (xcb_p^.subsystem_lock_priority_count <= tmv$subsystem_prior_threshold) THEN
582 10487 xcb_p^.subsystem_lock_priority_count := xcb_p^.subsystem_lock_priority_count + 1;
582 10488 cst_p^.dispatching_priority := jmc$prior_subsystem_tbls_locked;
582 10489 locked_dp_integer := tmv$dispatch_priority_integer [jmc$prior_subsystem_tbls_locked];
582 10490 xcb_p^.subsystem_give_up_cpu := TRUE;
582 10491 ELSE
582 10492 EXIT /system_locks_set/;
582 10493 IFEND;
582 10494 cst_p^.dual_state_prior_subpriority := tmv$dual_state_dispatch_prior
582 10495 [cst_p^.dispatching_priority];
582 10496 cst_p^.max_cptime := jmv$service_classes [cst_p^.ijle_p^.job_scheduler_data.service_class]^
582 10497 attributes.dispatching_control [cst_p^.ijle_p^.dispatching_control
582 10498 .dispatching_control_index].dispatching_timeslice.minor;
582 10499
582 10500 { If this processor was being interrupted to switch to a higher priority task, the task must be inserted
582 10501 { in the DCT because this processor cannot be interrupted now. Do not change the dispatching priority
582 10502 { integer to the locks-set value until after checking/removing a pre-selected task. When the task was
582 10503 { pre-selected the dispatching priority integer for the CPU was changed to the pre-selected task's priority.
582 10504 { If it is changed, the task may be pre-selected for this processor again.
582 10505 { Example: A P8 task is executing, a P10 task has been pre-selected (so dispatching priority integer
582 10506 { is P10). The P8 task has sub-system locks set, so its priority is raised to P9. Changing the dispatching
582 10507 { priority integer to P9 before calling dct_ready_task again for the pre-selected P10 task could cause
582 10508 { this CPU to be pre-selected again.
582 10509
582 10510 IF cst_p^.next_ptlo_to_dispatch <> 0 THEN
582 10511 next_ptlo_to_dispatch := cst_p^.next_ptlo_to_dispatch;
582 10512 jmp$set_ijle_p [tmv$ptl_p^ [next_ptlo_to_dispatch].ijl_ordinal, next_task_ijle_p];
582 10513 next_task_ijle_p^.executing_task_count := next_task_ijle_p^.executing_task_count - 1;
582 10514 tmv$ptl_p^ [next_ptlo_to_dispatch].status := tmc$ts_ready;
582 10515 tmp$set_xcb_p_from_ptlo [next_ptlo_to_dispatch, next_task_ijle_p^.ajl_ordinal,
582 10516 next_task_xcb_p];
582 10517 cst_p^.next_ptlo_to_dispatch := 0;
582 10518 tmp$dct_ready_task [next_task_xcb_p, next_task_ijle_p, next_ptlo_to_dispatch,
*WARN* 10519

```

SOURCE LIST OF tmm\$dispatcher

TMP\$SWITCH_TASK

```

1042 10519 {attempt_preselection =} TRUE);
1042 10520 IFEND;
1042 10521 cst_p^.dispatching_priority_integer := locked_dp_integer;
1042 10522
1042 10523 tmp$clear_lock [tmv$ptl_lock];
107E 10524 #KEYPOINT [osk$mtr, osk$monitor_multiplier * ptlo, tmk$switch_task + osk$m];
1086 10525 RETURN; {<-- }
1088 10526 IFEND;
108C 10527 ELSEIF (ptle_p^.idle_status = tmc$ts_idle_initiated) THEN
1094 10528 ptle_p^.idle_status := tmc$ts_idle;
1094 10529 cst_p^.xcb_p := NIL;
1094 10530 initiate_swap_if_possible (cst_p);
1124 10531 IFEND;
1124 10532 END /system_locks_set/;
1124 10533
1124 10534 { The current task will be removed from the CPU. Decrement this task's executing_task_
1124 10535 { count. If a task has not been pre-selected for this CPU, set dispatching_priority_integer
1124 10536 { equal to the highest priority task in the DCT for the pre-select code in
1124 10537 { dct_ready_task (which will be called if tasks are readied from the timed wait queue).
1124 10538
1124 10539 cst_p^.ijle_p^.executing_task_count := cst_p^.ijle_p^.executing_task_count - 1;
1124 10540 IF [cst_p^.next_ptlo_to_dispatch = 0] THEN
113A 10541 cst_p^.dispatching_priority_integer := tmv$dct_priority_integer;
1142 10542 IFEND;
1148 10543 IFEND; [xcb_p <> NIL]
1148 10544
1148 10545 { Update the timed wait queue. This must be done before considering the status of the task
1148 10546 { that is currently executing (if there is one); tasks being readied from the timed wait queue
1148 10547 { should be considered for pre-selection (in tmp$dct_ready_task) before the task that is
1148 10548 { currently executing.
1148 10549
*WARN* 10550 update_timed_wait_queue;
1BFA 10551
1BFA 10552 { If there is a task executing, check its new status.
1BFA 10553
1BFA 10554 IF xcb_p <> NIL THEN
1BFE 10555
1BFE 10556 IF ptle_p^.new_task_status > tmc$ts_last_status_in_dct THEN
1C06 10557 ptle_p^.status := ptle_p^.new_task_status;
1C06 10558 ptle_p^.new_task_status := tmc$ts_null;
1C06 10559 cst_p^.ijle_p^.statistics.ready_task_count := cst_p^.ijle_p^.statistics.ready_task_count - 1;
1C06 10560 IF ptle_p^.status <= tmc$ts_last_status_in_wait_q THEN
1C2E 10561 tmp$insert_timed_wait_queue (ptlo);
1CF6 10562 IFEND;
1CF6 10563 IF (ptle_p^.status = tmc$ts_timeout_reqexp_longvlong) OR (ptle_p^.status =
1D0C 10564 tmc$ts_timed_wait_not_queued) OR (ptle_p^.status = tmc$ts_timeout_reqexp_infvlong) THEN
1D0C 10565 cst_p^.ijle_p^.statistics.tasks_not_in_long_wait := cst_p^.ijle_p^.statistics
1D0C 10566 .tasks_not_in_long_wait - 1;
1D0C 10567 IF (cst_p^.ijle_p^.statistics.tasks_not_in_long_wait = 0) AND
1D30 10568 (cst_p^.ijle_p^.entry_status = jmc$ies_job_in_memory) AND
1D30 10569 (ptle_p^.idle_status = tmc$ts_not_idled) THEN
1D30 10570 cst_p^.xcb_p := NIL;
1D30 10571 tmp$set_swapout_candidate (cst_p^.ajl);
1D44 10572 IFEND;
1D44 10573 IFEND;
1D48 10574

```

TMP\$SWITCH_TASK

```

1D48 10575     ELSEIF ptle_p^.idle_status <> tmc$sis_idled THEN
1D52 10576         ptle_p^.status := tmc$sts_ready;
1D52 10577         attempt_preselection := (cst_p^.next_ptlo_to_dispatch = 0) AND
1D82 10578             ((cst_p^.ijle_p^.multiprocessing_allowed) OR (cst_p^.ijle_p^.statistics.ready_task_count = 1));
1D82 10579         tmp$dct_ready_task (xcb_p, cst_p^.ijle_p, ptlo, attempt_preselection);
264C 10580     ELSE
264C 10581         ptle_p^.status := tmc$sts_ready_but_swapped;
264C 10582         ptle_p^.idle_status := tmc$sis_idled_sched_notified;
264C 10583         jmp$ready_task_in_swapped_job (cst_p^.ijl_ordinal, cst_p^.ijle_p);
2678 10584     IFEND;
2678 10585     IFEND;
2678 10586
2678 10587     IF (cst_p^.cpu_state.next_state = osc$cpu_stepped) THEN
2684 10588         ptlo := 0;
2684 10589         tmp$clear_lock (tmv$ptl_lock);
268E 10590         tmp$error_stop ('terminated through the dispatcher path');
26DE 10591         { Any processor which follows this path is NOT the one driving the system into STEP mode. THEREFORE,
26DE 10592           { the call above is valid because the driving processor has the correct reason for calling STEP.
26DE 10593         tmp$set_lock (tmv$ptl_lock);
2716 10594     ELSEIF (cst_p^.processor_state <> cst_p^.next_processor_state) OR ((tmv$d dedicate_a_cpu_to_nos AND
2742 10595             (cst_p^.dual_state_jps <> 0) AND (cst_p^.next_ptlo_to_dispatch = 0))
2742 10596             AND tmv$multiple_cpus_active) THEN
2742 10597         cst_p^.dual_state_prior_subpriority.subpriority := 0;
2742 10598         ptlo := 0;
274C 10599     ELSE
274C 10600
274C 10601 { Select next task for execution.
274C 10602 { If cst_p^.next_ptlo_to_dispatch is non-zero, that task has already been selected to
274C 10603 { execute on this CPU through the ready task path, so switch to it.
274C 10604 { If the next_ptlo_to_dispatch field is zero, a new task to execute needs to be selected now.
274C 10605 { Tmv$dispatching_control_sets contains the following dispatching priority sets:
274C 10606 { enforce_maximums--priorities which cannot use more than the specified maximum % of the CPU.
274C 10607 { minimums_to_satisfy--priorities which have not received the specified minimum % of the CPU.
274C 10608 { (System priorities can use 100% of the CPU, so they are always in minimums_to_satisfy.)
274C 10609 { ready_tasks--priorities with tasks in the DCT.
274C 10610 { maximums_exceeded--priorities that have exceeded the specified maximum % of the CPU.
274C 10611 { For select_next_task, priorities which have exceeded maximum and for which the maximum is
274C 10612 { enforced are removed from the sets; they cannot execute for the rest of the interval.
274C 10613 { The remaining priorities with ready tasks are manipulated so that each priority is set
274C 10614 { only once in any of the three sets (which are ordered, left to right) : minimums_to_satisfy,
274C 10615 { ready_tasks, maximums_exceeded. The conversion process pulls off the leftmost bit set in the sets.
274C 10616 { Thus, priorities with a minimum percentage to satisfy are selected first, followed by priorities
274C 10617 { with ready tasks that have not exceeded the maximum percentage. Priorities which have exceeded
274C 10618 { the maximum but are not prevented from executing by enforce_maximums are selected last.
274C 10619 { When the dispatching priority has been selected, tasks from that DCT queue are considered,
274C 10620 { until one is found that can execute.
274C 10621 {
274C 10622 { NOTE: --PURGE BUFFERS-- If the task or the job the task belongs to did not last execute
274C 10623 { on this CPU, then cache must be purged. If multiprocessing is allowed, the last processor
274C 10624 { for the task is checked.
274C 10625 { For non-multiprocessing jobs, the last processor for the job is checked. The task can
274C 10626 { execute only if the job has no other tasks executing. If a task has been pre-selected to execute,
274C 10627 { the executing_task_count was checked and incremented by the pre-selection code.
274C 10628 { If the task is being selected from the DCT, the task is selected and cache purged if the
274C 10629 { executing_task_count = 0.
274C 10630

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SOURCE LIST OF tmm\$dispatcher

TMP\$SWITCH_TASK

```

274C 10631     IF cst_p^.next_ptlo_to_dispatch <> 0 THEN
2754 10632         ptlo := cst_p^.next_ptlo_to_dispatch;
2754 10633         ptle_p := ^tmv$ptl_p [ptlo];
2754 10634         cst_p^.next_ptlo_to_dispatch := 0;
2754 10635         cst_p^.ijl_ordinal := ptle_p^.ijl_ordinal;
2754 10636         jmp$get_ijle_p (cst_p^.ijl_ordinal, cst_p^.ijle_p);
2754 10637         cst_p^.ajlo := cst_p^.ijle_p^.ajl_ordinal;
2754 10638         cst_p^.jcb_p := #ADDRESS (1, cst_p^.ajlo + mtc$job_fixed_segment, 0);
2754 10639         xcb_p := #ADDRESS (1, mtc$job_fixed_segment + cst_p^.ajlo, ptle_p^.xcb_offset);
2754 10640         IF osv$cpus_logically_on > 1 THEN
27D0 10641             IF cst_p^.ijle_p^.multiprocessing_allowed THEN
27D8 10642                 IF xcb_p^.last_lpid_for_task <> cst_p^.cst_index THEN
27E6 10643                     { NOTE: The second parameter on the #purge_buffer command is just a dummy pointer.
27E6 10644                     #PURGE_BUFFER (osc$purge_all_cache, xcb_p);
27E8 10645                     #PURGE_BUFFER (osc$purge_all_page_seg_map, xcb_p);
27EA 10646                 IFEND;
27EE 10647             ELSE
27FA 10648                 IF cst_p^.jcb_p^.last_lpid_for_job <> cst_p^.cst_index THEN
27FA 10649                     #PURGE_BUFFER (osc$purge_all_cache, xcb_p);
27FC 10650                     #PURGE_BUFFER (osc$purge_all_page_seg_map, xcb_p);
27FE 10651                 IFEND;
2800 10652             IFEND;
2800 10653         IFEND;
2804 10654
2804 10655     ELSE { Select a new task to execute. }
2804 10656
2804 10657         local_set := tmv$dispatching_control_sets;
2804 10658         local_set.ready_tasks := local_set.ready_tasks - (local_set.enforce_maximums *
2804 10659             local_set.maximums_exceeded);
2804 10660         local_set.enforce_maximums := $jmt$dispatching_priority_set [];
2804 10661         local_set.minimums_to_satisfy := local_set.minimums_to_satisfy * local_set.ready_tasks;
2804 10662         local_set.maximums_exceeded := local_set.maximums_exceeded * local_set.ready_tasks;
2804 10663         local_set.ready_tasks := local_set.ready_tasks XOR (local_set.minimums_to_satisfy +
2804 10664             local_set.maximums_exceeded);
2804 10665
2804 10666         #unchecked_conversion (local_set, integer_dp_sets);
2804 10667
2804 10668         r := $real(integer_dp_sets);
2804 10669         priority.r := r;
2804 10670
2804 10671         ptlo := 0;
2804 10672         /find_next_task/
2804 10673         WHILE priority.dp_mask.dp <> 0 DO
285E 10674             ptlo := tmv$dct [priority.dp_mask.dp].queue_head;
285E 10675
285E 10676         WHILE ptlo <> 0 DO
2872 10677             ptle_p := ^tmv$ptl_p [ptlo];
2872 10678             cst_p^.ijl_ordinal := ptle_p^.ijl_ordinal;
2872 10679             jmp$get_ijle_p (cst_p^.ijl_ordinal, cst_p^.ijle_p);
2872 10680             cst_p^.ajlo := cst_p^.ijle_p^.ajl_ordinal;
2872 10681             cst_p^.jcb_p := #ADDRESS (1, cst_p^.ajlo + mtc$job_fixed_segment, 0);
2872 10682             xcb_p := #ADDRESS (1, mtc$job_fixed_segment + cst_p^.ajlo, ptle_p^.xcb_offset);
2872 10683
2872 10684             IF osv$cpus_logically_on > 1 THEN
28E6 10685                 IF cst_p^.cst_index IN xcb_p^.processor_selections THEN
28FC 10686                     IF cst_p^.ijle_p^.multiprocessing_allowed THEN

```

TMP\$SWITCH_TASK

```

2904 10687 IF xcb_p^.last_lpid_for_task <> cst_p^.cst_index THEN
290C 10688 { NOTE: The second parameter on the #purge_buffer command is just a dummy pointer.
290C 10689 #PURGE_BUFFER (osc$purge_all_cache, xcb_p);
290E 10690 #PURGE_BUFFER (osc$purge_all_page_seg_map, xcb_p);
2910 10691 IFEND;
2910 10692 EXIT /find_next_task/;
291A 10693 ELSEIF cst_p^.ijle_p^.executing_task_count = 0 THEN
2922 10694 IF cst_p^.jcb_p^.last_lpid_for_job <> cst_p^.cst_index THEN
292A 10695 #PURGE_BUFFER (osc$purge_all_cache, xcb_p);
292C 10696 #PURGE_BUFFER (osc$purge_all_page_seg_map, xcb_p);
292E 10697 IFEND;
292E 10698 EXIT /find_next_task/;
2936 10699 IFEND;
2938 10700 ELSE
293C 10701 ELSE
293C 10702 EXIT /find_next_task/;
2944 10703 IFEND;
2944 10704 ptlo := tmv$pt1_p^ [ptlo].pt1_thread;
2944 10705 WHILEND;
295A 10706
295A 10707 { No task of the priority selected could execute. Remove the priority from the sets and select
295A 10708 { another priority.
295A 10709
295A 10710 local_set.minimums_to_satisfy := local_set.minimums_to_satisfy - $jmt$dispatching_priority_set
295A 10711 [jmc$dp_conversion - priority.dp_mask.dp];
295A 10712 local_set.ready_tasks := local_set.ready_tasks - $jmt$dispatching_priority_set
295A 10713 [jmc$dp_conversion - priority.dp_mask.dp];
295A 10714 local_set.maximms_exceeded := local_set.maximms_exceeded - $jmt$dispatching_priority_set
295A 10715 [jmc$dp_conversion - priority.dp_mask.dp];
295A 10716
295A 10717 #unchecked_conversion (local_set, integer_dp_sets);
295A 10718
295A 10719 r := $real(integer_dp_sets);
295A 10720 priority.r := r;
295A 10721
295A 10722 WHILEND /find_next_task/;
29B2 10723 IF ptlo <> 0 THEN
29B6 10724 cst_p^.dispatching_priority_integer := integer_dp_sets;
29B6 10725 tmp$remove_task_from_dct (ptlo);
29C2 10726 cst_p^.ijle_p^.executing_task_count := cst_p^.ijle_p^.executing_task_count + 1;
29C4 10727 IFEND;
29C8 10728 IFEND;
29C8 10729 IFEND;
29C8 10730
29C8 10731 { Set up the CPU STATE TABLE.
29C8 10732
29C8 10733 IF ptlo = 0 THEN
29C8 10734 IF cst_p^.cpu_idle_statistics.idle_type = osc$not_idle THEN
29C8 10735 cst_p^.cpu_idle_statistics.idle_start_time := #FREE_RUNNING_CLOCK (0);
29C8 10736 IF [tmv$swapi_in_progress <> 0] OR [tmv$io_wait_task_count <> 0] THEN
29C8 10737 cst_p^.cpu_idle_statistics.idle_type := osc$idle_with_io_active;
29C8 10738 ELSE
29C8 10739 cst_p^.cpu_idle_statistics.idle_type := osc$idle_no_io_active;
29C8 10740 IFEND;
29C8 10741 IFEND;
29C8 10742 cst_p^.dispatching_priority := 0;

```

SOURCE LIST OF tmm\$dispatcher

TMP\$SWITCH_TASK

```

2B90 10743 cst_p^.dispatching_priority_integer := 0;
2B90 10744 cst_p^.dual_state_prior_subpriority.dual_state_priority := 0;
2B90 10745 cst_p^.xcb_p := NIL;
2B90 10746 cst_p^.taskid.index := 0;
2B90 10747 IF [tmv$timed_wait_queue.head = 0] OR [tmv$dedicate_a_cpu_to_nos] THEN
2B9A 10748 time := OffFFFFFfff(16);
2B9C 10749 ELSE
2B9C 10750 time := tmv$pt1_p^ [tmv$timed_wait_queue.head].end_of_wait_time;
2B9C 10751 IFEND;
2BCA 10752 tmv$time_to_call_dispatcher := time;
2BCA 10753 time := time - #free_running_clock (0);
2BD4 10754 IF time < 10 THEN
2BDC 10755 time := 10;
2BE2 10756 ELSEIF time > 50000 THEN
2BEE 10757 time := 50000;
2BF4 10758 IFEND;
2BF4 10759 cst_p^.max_cptime := time;
2BFC 10760
2BFC 10761 ELSE
2BFC 10762
2BFC 10763 { A new task has been selected. Update the cpu_idle_statistics.
2BFC 10764
2BFC 10765 IF cst_p^.cpu_idle_statistics.idle_type <> osc$not_idle THEN
2C04 10766 idle_time := #FREE_RUNNING_CLOCK (0) - cst_p^.cpu_idle_statistics.idle_start_time;
2C0A 10767 IF cst_p^.cpu_idle_statistics.idle_type = osc$idle_with_io_active THEN
2C14 10768 cst_p^.cpu_idle_statistics.idle_io_active := cst_p^.cpu_idle_statistics.idle_io_active +
2C22 10769 idle_time;
2C22 10770 ELSE
2C22 10771 cst_p^.cpu_idle_statistics.idle_no_io_active := cst_p^.cpu_idle_statistics.idle_no_io_active +
2C2C 10772 idle_time;
2C2C 10773 IFEND;
2C2C 10774 update_dispatching_controls (idle_time, jmc$null_dispatching_priority);
2D18 10775 cst_p^.cpu_idle_statistics.idle_type := osc$not_idle;
2D18 10776 cst_p^.cpu_idle_statistics.idle_count := cst_p^.cpu_idle_statistics.idle_count + 1;
2D26 10777 IFEND;
2D26 10778
2D26 10779 { Set task status to executing and set up the rest of the CST fields.
2D26 10780
2D26 10781 ptle_p^.status := tmc$ts_executing;
2D26 10782
2D26 10783 cst_p^.taskid.index := ptlo;
2D26 10784 cst_p^.taskid.seqno := ptle_p^.sequence_number;
2D26 10785 cst_p^.xcb_p := xcb_p;
2D26 10786 cst_p^.max_cptime := xcb_p^.timeslice.minor;
2D26 10787 cst_p^.dispatching_priority := ptle_p^.dispatching_priority;
2D26 10788 cst_p^.dual_state_prior_subpriority := tmv$dual_state_dispatch_prior
2D26 10789 [cst_p^.dispatching_priority];
2D26 10790
2D60 10791 IF ptle_p^.monitor_flags <> $syt$monitor_flags [] THEN
2D60 10792 xcb_p^.xp.user_condition_register := xcb_p^.xp.user_condition_register +
2D60 10793 $ost$user_conditions [osc$free_flag];
2D60 10794 xcb_p^.monitor_flags := xcb_p^.monitor_flags + ptle_p^.monitor_flags;
2D60 10795 ptle_p^.monitor_flags := $syt$monitor_flags [];
2D7C 10796 IFEND;
2D7C 10797 IF ptle_p^.system_flags <> $tmt$system_flags [] THEN
2D84 10798 xcb_p^.system_flags := xcb_p^.system_flags + ptle_p^.system_flags;
2D84 10799 ptle_p^.system_flags := $tmt$system_flags [];

```

TMP\$SWITCH_TASK

```

2D92 10799     IFEND;
2D92 10800
2D92 10801     IF (ptle_p^.ptl_flags.wait_inhibited = tmc$wi_wait_inhibited) THEN
2D9E 10802         xcb_p^.wait_inhibited := TRUE;
2DA2 10803     IFEND;
2DA2 10804         ptle_p^.ptl_flags.wait_inhibited := tmc$wi_null;
2DA2 10805
2DA2 10806 { Set keypoint mask and keypoint enable flag in job exchange package. Set the value for
2DA2 10807 { monitor's keypoint mask.
2DA2 10808
2DA2 10809         #WRITE_REGISTER (osc$pr_set_keypoint_enable, $integer (xcb_p^.keypoint_register_enable));
2DB8 10810
2DB8 10811 { Update the next time to ready a task from the timed wait queue.
2DB8 10812
2DB8 10813         IF (tmv$timed_wait_queue.head = 0) THEN
2DC0 10814             tmv$time_to_call_dispatcher := Offffffff(16);
2DCC 10815         ELSE
2DCC 10816             tmv$time_to_call_dispatcher := tmv$ptl_p^ [tmv$timed_wait_queue.head].end_of_wait_time;
2DDC 10817         IFEND;
2DDC 10818
2DDC 10819     IFEND;
2DDC 10820     tmp$clear_lock (tmv$ptl_lock);
2E14 10821
2E14 10822     #KEYPOINT (osk$mtr, osk$monitor_multiplier * ptlo, tmk$switch_task + osk$m);
2E1C 10823
2E1C 10824     PROCEND tmp$switch_task;
      0 10825

```

SOURCE LIST OF tmm\$dispatcher

TMP\$SET_UP_DEBUG_REGISTERS

```

      0 10827
      0 10828 { PURPOSE:
      0 10829 { This procedure is called during the first swapin for job recovery to straighten out the
      0 10830 { debug list and mask in the exchange package of recovering jobs.
      0 10831
      0 10832     PROCEDURE [XDCL] tmp$set_up_debug_registers
      0 10833     { ptlo: ost$task_index;
      0 10834       jle_p: ^jmt$initiated_job_list_entry;
      0 10835       xcb_p: ^ost$execution_control_block);
      0 10836
      0 10837     VAR
      0 10838         null_debug_mask: [STATIC, READ] ost$debug_mask := [FALSE, FALSE, [REP 5 of FALSE]];
      0 10839
      0 10840     IF (xcb_p^.xp.debug_list_pointer <> NIL) AND (#RING (xcb_p^.xp.debug_list_pointer) = 1) THEN
20 10841         xcb_p^.xp.debug_list_pointer := NIL;
20 10842         xcb_p^.xp.debug_mask_register := null_debug_mask;
20 10843
20 10844         xcb_p^.xp.debug_index := 0;
20 10845         IF osc$debug IN xcb_p^.xp.user_mask THEN
56 10846             xcb_p^.xp.user_mask := xcb_p^.xp.user_mask - $ost$user_conditions [osc$debug];
56 10847             xcb_p^.monitor_flags := xcb_p^.monitor_flags + $syt$monitor_flags [syc$mf_system_debugger];
56 10848             IF tmv$ptl_p^ [ptlo].ptl_flags.wait_inhibited <> tmc$wi_wait_selected_r3 THEN
88 10849                 xcb_p^.xp.user_condition_register := xcb_p^.xp.user_condition_register +
96 10850                 $ost$user_conditions [osc$free_flag];
96 10851             IFEND;
96 10852         IFEND;
96 10853     IFEND;
96 10854     PROCEND tmp$set_up_debug_registers;
      0 10855

```

TMP\$UPDATE_DEBUG_REGISTERS

```

O 10857
O 10858 { PURPOSE:
O 10859 { This procedure sets the debugger flag for all tasks. The next time a task executes, it will trap to
O 10860 { reset the debug list and mask in its exchange package. This procedure is called whenever a breakpoint
O 10861 { is selected, set, removed, or modified during a debugger session.
O 10862
O 10863 PROCEDURE tmp$update_debug_registers;
O 10864
O 10865 VAR
O 10866     pt1o: ost$task_index;
O 10867
O 10868 FOR pt1o := 1 TO UPPERBOUND (tmv$pt1_p^ ) DO
1A 10869     IF tmv$pt1_p^ [pt1o].status <> tmc$ts_null THEN
2A 10870         tmv$pt1_p^ [pt1o].monitor_flags := tmv$pt1_p^ [pt1o].monitor_flags +
3A 10871             $Syt$monitor_flags [syc$mf_system_debugger];
3B 10872     IFEND;
3C 10873 FOREND;
3C 10874
3C 10875 PROCEND tmp$update_debug_registers;
10876

```

SOURCE LIST OF tmm\$dispatcher

UPDATE_TIMED_WAIT_QUEUE

```

10878
10879 PROCEDURE [INLINE] update_timed_wait_queue;
10880
10881 { This procedure is called during task switch to remove tasks from the timed wait
10882 { queue if the wait time has expired.
10883 { NOTE: this routine is called with the PTL lock set.
10884
10885 VAR
10886     attempt_preselection: boolean;
10887     free_running_clock: integer;
10888     ijle_p: ^jmt$initiated_job_list_entry;
10889     xcb_p: ^ost$execution_control_block;
10890     pt1o: ost$task_index;
10891     pt1e_p: ^tmt$primary_task_list_entry;
10892
10893     free_running_clock := #FREE_RUNNING_CLOCK (0);
10894
10895 {UPDATE THE TIMED_WAIT_QUEUE
10896 WHILE (tmv$pt1_p^ [tmv$timed_wait_queue.head].end_of_wait_time <= free_running_clock) AND
10897     (tmv$timed_wait_queue.head <> 0) DO
10898     pt1o := tmv$timed_wait_queue.head;
10899     pt1e_p := ^tmv$pt1_p^ [pt1o];
10900     tmv$timed_wait_queue.head := pt1e_p.queue_link.head;
10901     tmv$pt1_p^ [pt1e_p.queue_link.head].queue_link.tail := 0;
10902     pt1e_p.queue_link.head := 0;
10903     IF (pt1e_p.status <= tmc$ts_last_status_in_dct) OR (pt1e_p.status > tmc$ts_last_status_in_wait_q) THEN
10904         mtp$error_stop ('TM36 - task not in wait queue');
10905     IFEND;
10906     jmp$get_ijle_p (pt1e_p.ijl_ordinal, ijle_p);
10907     IF pt1e_p.idle_status = tmc$sis_idled THEN
10908         pt1e_p.idle_status := tmc$sis_idled_sched_notified;
10909         IF pt1e_p.status = tmc$ts_timeout_reqexp_longvlong THEN
10910             ijle_p.statistics.tasks_not_in_long_wait := ijle_p.statistics.tasks_not_in_long_wait + 1;
10911         IFEND;
10912         pt1e_p.status := tmc$ts_ready_but_swapped;
10913         jmp$ready_task_in_swapped_job (pt1e_p.ijl_ordinal, ijle_p);
10914         ijle_p.long_wait_aging_complete := FALSE;
10915     ELSE
10916         IF pt1e_p.status = tmc$ts_timeout_reqexp_longvlong THEN
10917             ijle_p.statistics.tasks_not_in_long_wait := ijle_p.statistics.tasks_not_in_long_wait + 1;
10918             jmv$ajl_p^ [ijle_p.ajl_ordinal].job_is_good_swap_candidate := FALSE;
10919         IFEND;
10920         pt1e_p.status := tmc$ts_ready;
10921         xcb_p := #ADDRESS (1, mtc$job_fixed_segment + ijle_p.ajl_ordinal, pt1e_p.xcb_offset);
10922         xcb_p.timeslice.minor := 0;
10923         xcb_p.timeslice.major := 0;
10924         attempt_preselection := (ijle_p.multiprocessing_allowed) OR (ijle_p.executing_task_count = 0);
10925         tmp$dct_ready_task (xcb_p, ijle_p, pt1o, attempt_preselection);
10926     IFEND;
10927     ijle_p.statistics.ready_task_count := ijle_p.statistics.ready_task_count + 1;
10928 WHILEND;
10929
10930 { Update the queue's tail.
10931
10932 IF tmv$timed_wait_queue.head = 0 THEN
10933     tmv$timed_wait_queue.tail := 0;

```

UPDATE_TIMED_WAIT_QUEUE

```

10934     IFEND;
10935
10936     PROCEND update_timed_wait_queue;
10937

```

SOURCE LIST OF tmm\$dispatcher

TMPSJOB_RECOVERY_REQUESTS

```

10939 { monitor requests for job recovery
10940
10941     TYPE
10942     syt$rb_job_recovery = record
10943     reqcode: ALIGNED [0 MOD 8] syt$monitor_request_code,
10944     status: syt$monitor_status,
10945     ijlo: jmt$ijl_ordinal,
10946     case subreq: syt$job_recovery_subreq of
10947     = syc$recover_pt1 :
10948     count: integer,
10949     task_list_p: ^array [1 .. *] of syt$pt1_recovery_info,
10950     casend,
10951     recend,
10952
10953     syt$pt1_recovery_info = record
10954     xcb_offset: 0 .. 7fffffff(16),
10955     dispatching_priority: jmt$dispatching_priority,
10956     gtid: ost$global_task_id,
10957     recend,
10958
10959     syt$job_recovery_subreq = (syc$recover_pt1, syc$dummy_job_recovery);
10960
0 10963
0 10964     PROCEDURE [XDCL] tmp$job_recovery_requests
0 10965     (VAR rb: syt$rb_job_recovery;
0 10966     cst_p: ^ost$cpu_state_table);
0 10967
0 10968     VAR
0 10969     j,
0 10970     i: integer,
0 10971     gtid: ost$global_task_id,
0 10972     jmtr_ptlo,
0 10973     ptlo: ost$task_index,
0 10974     ijle_p: ^jmt$initiated_job_list_entry,
0 10975     pt1_p: ^tmt$primary_task_list_entry,
0 10976     service_class: jmt$service_class_index;
0 10977
0 10978     rb.status.normal := TRUE;
4 10979     CASE rb.subreq OF
16 10980     = syc$recover_pt1 :
16 10981     tmp$set_lock (tmv$pt1_lock);
52 10982
52 10983     FOR i := 1 TO rb.count DO
60 10984     IF tmv$pt1_p^ [rb.task_list_p^ [i].gtid.index].dispatching_priority <> jmc$null_dispatching_priority
7E 10985     THEN
7E 10986     rb.status.normal := FALSE;
7E 10987     rb.status.condition := tme$pt1_full;
7E 10988     tmp$clear_lock (tmv$pt1_lock);
C2 10989     RETURN;
C4 10990     IFEND;
C4 10991     FOREND;
C8 10992
C8 10993     jmtr_ptlo := jmv$ijl_p.block_p^ [rb.ijlo.block_number].index_p^ [rb.ijlo.block_index].
C8 10994     job_monitor_taskid.index;
C8 10995     FOR i := 1 TO rb.count DO
FE 10996     ptlo := rb.task_list_p^ [i].gtid.index;

```

TMP\$JOB_RECOVERY_REQUESTS

```

FE 10997      remove_task_from_free_queue (ptlo);
1D6 10998      ptl_p := Atmv$ptl_p^ [ptlo];
1D6 10999      ptl_p^.ptl_thread := 0;
1D6 11000      ptl_p^.status := tmc$ts_timeout_reqexp_inflong;
1D6 11001      ptl_p^.ijl_ordinal := rb.ijlo;
1D6 11002      ptl_p^.xcb_offset := rb.task_list_p^ [i].xcb_offset;
1D6 11003      ptl_p^.dispatching_priority := rb.task_list_p^ [i].dispatching_priority;
1D6 11004      ptl_p^.monitor_flags := $syt$monitor_flags [i];
1D6 11005      ptl_p^.system_flags := $tmt$system_flags [i];
1D6 11006      ptl_p^.sequence_number := rb.task_list_p^ [i].gtid.seqno;
1D6 11007      ptl_p^.idle_status := tmc$ts_idled;
1D6 11008      ptl_p^.queue_link.head := 0;
1D6 11009      ptl_p^.queue_link.tail := 0;
1D6 11010      ptl_p^.end_of_wait_time := 0;
1D6 11011
1D6 11012 { Emulate insert_ijl here - note special case for first task (jmtr).
1D6 11013
1D6 11014      IF ptlo <> jmtr_ptlo THEN
23E 11015          tmv$ptl_p^ [ptlo].ijl_thread := tmv$ptl_p^ [jmtr_ptlo].ijl_thread;
23E 11016          tmv$ptl_p^ [jmtr_ptlo].ijl_thread := ptlo;
24E 11017      IFEND;
24E 11018      gtid_index := ptlo;
24E 11019      gtid_seqno := ptl_p^.sequence_number;
24E 11020      tmp$set_task_ready (gtid, 0 {readying_task_priority}, tmc$rc_ready_conditional_wi);
27E 11021      FOREND;
280 11022      tmp$clear_lock (tmv$ptl_lock);
2BA 11023
2BA 11024 { Get an ijl pointer - we need to reset some ijl fields and straighten out some counts.
2BA 11025
2BA 11026      jmp$get_ijle_p (rb.ijlo, ijle_p);
2BA 11027
2BA 11028 { Set the swap status of the job to swapout complete; clear the swap queue link fields and
2BA 11029 { relink the job into the swapped_out swap queue.
2BA 11030
2BA 11031      ijle_p^.swap_status := jmc$iss_swapout_complete;
2BA 11032      ijle_p^.swap_queue_link.queue_id := jsc$isqi_null;
2BA 11033      ijle_p^.swap_queue_link.backward_link := jmv$null_ijl_ordinal;
2BA 11034      ijle_p^.swap_queue_link.forward_link := jmv$null_ijl_ordinal;
2BA 11035      jsp$relink_swap_queue (rb.ijlo, ijle_p, jsc$isqi_swapped_out);
31C 11036
31C 11037 { Increment the total task count by the number of tasks in the recovered job.
31C 11038
31C 11039      tmv$total_task_count := tmv$total_task_count + rb.count;
31C 11040
31C 11041 { Increment the scheduler initiated job count so that scheduler knows how many jobs of this class
31C 11042 { there are; this count is normally incremented in jmp$initiate_job_from_scheduler, but for job
31C 11043 { recovery we'll do it here.
31C 11044
31C 11045      service_class := ijle_p^.job_scheduler_data.service_class;
31C 11046      jmv$job_counts.service_class_counts [service_class].scheduler_initiated_jobs := jmv$job_counts.
31C 11047      service_class_counts [service_class].scheduler_initiated_jobs + 1;
31C 11048
31C 11049 { Increment swapped job count so that scheduler knows how many swapped jobs there are.
31C 11050
31C 11051      jmp$increment_swapped_job_count (ijle_p);
354 11052

```

TMP\$JOB_RECOVERY_REQUESTS

```

354 11053      ELSE
354 11054
354 11055 { unsupported subrequest
354 11056
354 11057      CASEND;
354 11058      PROCEND tmp$job_recovery_requests;
0 11059

```

[XDCL] tmp\$free_unrecovered_tasks

```

0 11061
0 11062 { PURPOSE:
0 11063 { This procedure frees the PTL entries for the tasks of a job that is being terminated
0 11064 { because it cannot be recovered. The total task count is decremented; it was incremented
0 11065 { earlier during job recovery when the PTL entries were made.
0 11066 { The caller of this procedure MUST set the PTL lock.
0 11067
0 11068 PROCEDURE [XDCL] tmp$free_unrecovered_tasks
0 11069 (
0 11070 ( ijob_p: Ajmt$initiated_job_list_entry);
0 11071
0 11072 VAR
0 11073 next_index: ost$task_index;
0 11074 task_index: ost$task_index;
0 11075
0 11076 task_index := ijob_p^.job_monitor_taskid.index;
0 11077 WHILE task_index <> 0 DO
16 11077 next_index := tmv$ptl_p^ [task_index].ijl_thread;
16 11078 IF tmv$ptl_p^ [task_index].ptl_thread <> 0 THEN
2A 11079 mtp$error_stop ('Unrecovered task--PTL thread not zero');
4A 11080 IFEND;
4A 11081
4A 11082 { The procedure free_ptl does not clear the ijl thread. Not clearing the field
4A 11083 { causes a potential problem for the next task using the ptl entry.
4A 11084
4A 11085 tmv$ptl_p^ [task_index].ijl_thread := 0;
4A 11086 free_ptl [task_index];
84 11087 tmv$total_task_count := tmv$total_task_count - 1;
84 11088 task_index := next_index;
84 11089 WHILEND;
9A 11090
9A 11091 PROCEND tmp$free_unrecovered_tasks;
0 11092

```

SOURCE LIST OF tmm\$dispatcher

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TMP\$SWITCH_TASK_FROM_FAILING_CP

```

0 11094
0 11095 PROCEDURE [XDCL] tmp$switch_task_from_failing_cp
0 11096 (VAR cst_p: Aost$cpu_state_table);
0 11097
0 11098 VAR
0 11099 next_task_ijkl_p: Ajmt$initiated_job_list_entry;
0 11100 next_task_xcb_p: Aost$execution_control_block;
0 11101 ptle_p: Aamt$primary_task_list_entry;
0 11102 ptlo: ost$task_index;
0 11103 xcb_p: Aost$execution_control_block;
0 11104
0 11105 xcb_p := cst_p^.xcb_p;
4 11106
4 11107 tmp$set_lock (tmv$ptl_lock);
4E 11108
4E 11109 IF xcb_p <> NIL THEN
58 11110 xcb_p^.timeslice := jmv$service_classes [cst_p^.ijle_p^.job_scheduler_data.service_class]^^.attributes.
58 11111 dispatching_control [cst_p^.ijle_p^.dispatching_control.dispatching_control_index].
58 11112 dispatching_timeslice;
58 11113 xcb_p^.last_lpid_for_task := cst_p^.cst_index;
58 11114 cst_p^.jcb_p^.last_lpid_for_job := cst_p^.cst_index;
58 11115 cst_p^.jcb_p^.last_execution_time := #FREE_RUNNING_CLOCK (0);
9C 11116 ptlo := cst_p^.taskid.index;
9C 11117 ptle_p := Aamt$ptl_p^ [ptlo];
9C 11118 cst_p^.ijle_p^.executing_task_count := cst_p^.ijle_p^.executing_task_count - 1;
9C 11119 IF ptle_p^.new_task_status > tmc$ts_last_status_in_dct THEN
C6 11120 ptle_p^.status := ptle_p^.new_task_status;
C6 11121 ptle_p^.new_task_status := tmc$ts_null;
C6 11122 cst_p^.ijle_p^.statistics.ready_task_count := cst_p^.ijle_p^.statistics.ready_task_count - 1;
C6 11123 IF ptle_p^.status <= tmc$ts_last_status_in_wait_q THEN
DE 11124 tmp$insert_timed_wait_queue (ptlo);
IA8 11125 IFEND;
IA8 11126 IF (ptle_p^.status = tmc$ts_timeout_reqexp_longvlong) OR (ptle_p^.status =
18C 11127 tmc$ts_timed_wait_not_queued) OR (ptle_p^.status = tmc$ts_timeout_reqexp_infvlong) THEN
18C 11128 cst_p^.ijle_p^.statistics.tasks_not_in_long_wait := cst_p^.ijle_p^.statistics.
18C 11129 tasks_not_in_long_wait - 1;
18C 11130 IF (cst_p^.ijle_p^.statistics.tasks_not_in_long_wait = 0) AND
1E4 11131 (cst_p^.ijle_p^.entry_status = jmc$ies_job_in_memory) AND
1E4 11132 (ptle_p^.idle_status = tmc$ts_not_idled) THEN
1E4 11133 cst_p^.xcb_p := NIL;
1E4 11134 tmp$set_swapout_candidate (cst_p^.ajlo);
1FC 11135 IFEND;
1FC 11136 ELSE
200 11137 ptle_p^.status := tmc$ts_ready;
200 11138 tmp$dct_ready_task (xcb_p, cst_p^.ijle_p, ptlo, TRUE);
AEA 11140 IFEND;
AEA 11141 IFEND;
AEA 11142
AEA 11143 { If a task has been pre-selected to run on this CPU, that task needs to be inserted in the DCT.
AEA 11144
AEA 11145 IF cst_p^.next_ptlo_to_dispatch <> 0 THEN
AF6 11146 jmp$get_ijkl_p (tmv$ptl_p^ [cst_p^.next_ptlo_to_dispatch].ijl_ordinal, next_task_ijkl_p);
AF6 11147 next_task_ijkl_p^.executing_task_count := next_task_ijkl_p^.executing_task_count - 1;
AF6 11148 tmv$ptl_p^ [cst_p^.next_ptlo_to_dispatch].status := tmc$ts_ready;
AF6 11149 tmp$get_xcb_p_from_ptlo (cst_p^.next_ptlo_to_dispatch, next_task_ijkl_p^.ajl_ordinal,

```


TMPSSWITCH_TASK_FROM_FAILING_CP

```

AF6 11150             next_task_xcb_p);
AF6 11151             tmp$dct_ready_task (next_task_xcb_p, next_task_ijle_p, cst_p^
1402 11152             cst_p^next_ptlo_to_dispatch := 0;
140A 11153             IFEND;
140A 11154
140A 11155             tmp$clear_lock (tmv$pt1_lock);
1442 11156
1442 11157             PROCEND tmp$switch_task_from_failing_cp;
O 11159             MODEND tmm$dispatcher;
**** I=$05578173AS0102D19890821T183254 L=ZZXXLIST B=LGD DA=NONE LO=R RC=NONE OPT=SCHED EL=F LF=CS612 PAD=0

```

ERROR LIST OF tmm\$dispatcher

ERROR	LINE	TEXT
WARNING CY 821	7866	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	7866	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	7866	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	7866	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	7898	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	8227	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	8227	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	8227	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	8227	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	8227	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	8227	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	8227	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	8227	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	8227	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	8739	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	9406	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	9406	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	9479	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	9479	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	9479	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	9479	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	9479	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	9479	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	9623	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	9623	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	9623	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	9623	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	9623	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	9623	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	9647	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	9647	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	10201	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	10201	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	10201	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	10201	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	10201	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	10466	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	10518	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	10518	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	10518	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	10518	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	10518	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	10518	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	10518	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	10518	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	10550	Code scheduling abandoned for this block due to register jamming.
WARNING CY 821	10550	Code scheduling abandoned for this block due to register jamming.

LEVEL SUMMARY

**** 45 warning diagnostics

IDENTIFIER-----	DEFINED-----	REFERENCES-----								
	ON LINE									
a0_dynamic_space_pointer	3104	7593								
accumulated_job_cptime	1161	8738/M 8738	8739	8739	8739	8739	8739	9688	9690	
		9703 9710	9929	9961/M	9961	10465	10466	10466		
		10466 10466								
accumulated_monitor_cptime	1162	8732/M 8739	8739	8739	8739	9686	9690	9700		
		9708 10466	10466	10466	10466					
activity	8763	8765								
activity	8772	8787	8802							
ajl_ordinal	1267	5252 5293	5493	5698	5716	7054	7054	7056		
		7056 7056	7106	7149	7218	7258	7320	7459		
		7463 7505	7509	7539	7543	7577	7581	7598		
		7668 7673	7685	7698	7702	7716	7810	7866		
		7866 7866	7866	7866	7896/S	7898	7898	7898		
		7898 7898	7978	7979	7979	7979	7979	7979		
		8170 8172	8172	8172	8172	8172	8218	8222		
		8226/S 8227	8227	8227	8227	8227	8232	8499		
		8499 8499	8499	8499	8630	8630	8630	8630		
		8630 9406	9406	9406	9406	9406	9479	9479		
		9479 9479	9479	9623	9623	9623	9623	9623		
		9647 9647	9647	9647	9647	10201	10201	10201		
		10201 10201	10615/P	10519	10519	10519	10519	10519		
		10550/S 10550	10550	10550	10550	10550	10550	10579		
		10579 10579	10579	10579	10637	10680	10918/S	10921		
		10925 10925	10925	10925	10925	10925	11139	11139		
		11139 11139	11149/P	11151	11151	11151	11151	11151		
ajl_ordinal	4828	7660/M 7666/S	7673/M	7702/M	7721					
ajl_ordinal	5676	5698/M 5698	5698/S	5698/S	5698					
ajl_ordinal	5713	5716/M 5717	5721/S	5721/S	5722					
ajl_ordinal	7416	7459/M 7459	7459/S	7459/S	7459					
ajl_ordinal	7472	7505/M 7505	7505/S	7505/S	7505					
ajl_ordinal	7517	7539/M 7539	7539/S	7539/S	7539					
ajl_ordinal	7562	7577/M 7577	7577/S	7577/S	7577					
ajl_ordinal	7609	7612								
ajl_ordinal	7618	7668/M 7668	7668/S	7668/S	7668	7698/M	7698	7698/S		
		7698/S 7698								
		8428/P 8433								
		9255								
ajl_ordinal	8416	9227 9231/S	9255/P	9268/P						
ajl_ordinal	9217	9287 9291/S	9302	9310/P						
ajl_ordinal	9218	9331/S 9333/P	9335/P							
ajl_ordinal	9279	9366/S 9368	9372/S	9385						
ajl_ordinal	9323	9458/S 9461/S	9471							
ajl_ordinal	9350	10516								
ajl_ordinal	9447	11150								
ajl_ordinal	10408	8425 8606	8754	9140	9887	10530	10571/P	10637/M		
ajl_ordinal	11095	10638 10639	10680/M	10681	10682	11134/P				
ajlo	1153	5493/M 5494/S	5497/S	5497/S						
ajlo	5491	5698/P 5698/M								
ajlo	5676	5698/P 5698/P								
ajlo	5691	5719/P 5722/M								
ajlo	5710	7459/P 7459/M								
ajlo	7416	7459/P 7459/P								
ajlo	7416	7505/P 7505/P								

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----								
	ON LINE									
ajlo	7472	7505/P 7505/M								
ajlo	7517	7539/P 7539/M								
ajlo	7517	7539/P 7539/M								
ajlo	7562	7577/P 7577/M								
ajlo	7562	7577/P 7577/M								
ajlo	7562	7598/M 7598/S	7598/S	7598/S						
ajlo	7618	7668/P 7668/M	7698/P	7698/M						
ajlo	7618	7685/M 7685/S	7685/S	7685/S	7716/M	7716/S	7716/S	7716/S		
ajlo	4989	8433/M 8437	8438/S	8450/M	8459	8475	8493/P			
amc\$file_byte_limit	3923	3926 3928								
amc\$max_file_id_ordinal	6030	6037								
amt\$file_byte_address	3926	3887								
amt\$file_id_ordinal	6037	6034								
amt\$file_id_sequence	6038	6035								
amt\$file_idenfier	6033	5997								
amt\$file_limit	3928	3891								
asid	2700	8427/P								
asti	8206	8230 8231/S	8232/S							
attempt_preselection	6867	6938								
attempt_preselection	7766	7866 7898								
attempt_preselection	7795	7865/M 7866/P	7897/M	7898/P						
attempt_preselection	7932	7979								
attempt_preselection	7950	7977/M 7979/P								
attempt_preselection	8110	8172								
attempt_preselection	8128	8171/M 8172/P								
attempt_preselection	8182	8227								
attempt_preselection	8381	8499								
attempt_preselection	8400	8498/M 8499/P								
attempt_preselection	8581	8630								
attempt_preselection	8598	8627/M 8630/P								
attempt_preselection	9349	9406								
attempt_preselection	9355	9404/M 9406/P								
attempt_preselection	9446	9479								
attempt_preselection	9450	9478/M 9479/P								
attempt_preselection	9584	9623 9647								
attempt_preselection	9589	9622/M 9623/P	9646/M	9647/P						
attempt_preselection	10157	10201								
attempt_preselection	10183	10200/M 10201/P								
attempt_preselection	10408	10519 10550	10579							
attempt_preselection	10408	10550/M 10550/P								
attempt_preselection	10423	10577/M 10579/P								
attempt_preselection	10879	10925								
attempt_preselection	10886	10924/M 10925/P								
attempt_preselection	11095	11139 11151								
attributes	6263	6884 7866	7898	7979	8172	8227	8496	8499		
		8624 8630	8198	9406	9479	9623	9647	10045		
		10056 10193	10201	10277	10330	10337	10373	10497		
		10519 10550	10579	10925	11110	11139	11151			
avc\$monitor_statistics_flag	3605	8739/P 9745/P	10466/P							
b	5286	5295 5295								
b	5330	5340 5341								

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED ON LINE	REFERENCES			
b	8424	8431	8432		
b	8464	8490	8490		
b	7416	7450	7450		
b	7472	7501	7501		
b	7517	7529	7529		
b	7562	7573	7573		
b	7618	7653	7653		
b	7730	7748	7748		
b	7766	7806	7806		
b	7932	7956	7956		
b	8011	8017	8017		
b	8044	8070	8070		
b	8110	8134	8134		
b	8182	8222	8222	8232	8232
b	8259	8265	8265		
b	8276	8282	8282		
b	8381	8444	8444		
b	8513	8532	8532		
b	8581	8607	8607		
b	8682	8727	8727		
b	8770	8783	8783		
b	8875	8897	8897		
b	8966	9004	9004		
b	9057	9100	9100		
b	9217	9247	9247		
b	9322	9329	9329		
b	9446	9469	9469		
b	9491	9504	9504		
b	9584	9607	9607		
b	9936	9951	9951		
b	10157	10170	10170		
b	10224	10233	10233		
b	10408	10461	10461	10593	10593
b	10964	10981	10981		
b	11095	11107	11107		
backward_link	2031	11033/M			
bc	8425	8428/M	8429	8433	
bc	8464	8490/M	8490	8490	
bc	7416	7450/M	7450	7450	
bc	7472	7501/M	7501	7501	
bc	7517	7529/M	7529	7529	
bc	7562	7573/M	7573	7573	
bc	7618	7653/M	7653	7653	
bc	7730	7748/M	7748	7748	
bc	7766	7806/M	7806	7806	
bc	7932	7956/M	7956	7956	
bc	8011	8017/M	8017	8017	
bc	8044	8070/M	8070	8070	
bc	8110	8134/M	8134	8134	
bc	8259	8265/M	8265	8265	
bc	8276	8282/M	8282	8282	
bc	8381	8444/M	8444	8444	
bc	8513	8532/M	8532	8532	

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED ON LINE	REFERENCES							
bc	8581	8607/M	8607	8607					
bc	8682	8727/M	8727	8727					
bc	8770	8783/M	8783	8783					
bc	8875	8897/M	8897	8897					
bc	8966	9004/M	9004	9004					
bc	9057	9100/M	9100	9100					
bc	9217	9247/M	9247	9247					
bc	9322	9329/M	9329	9329					
bc	9446	9469/M	9469	9469					
bc	9491	9504/M	9504	9504					
bc	9584	9607/M	9607	9607					
bc	9936	9951/M	9951	9951					
bc	10157	10170/M	10170	10170					
bc	10224	10233/M	10233	10233					
bc	10408	10461/M	10461	10461	10593/M	10593	10593		
bc	10964	10981/M	10981	10981					
bc	11095	11107/M	11107	11107					
block_index	1243	5389/S	6612/S	6966/S	7458/S	7503/S	7537/S	7575/S	7665/S
		7688	7696/M	7809/S	7866/S	7898/S	7959/S	7979/S	8140/S
		8172/S	8227/S	8439/S	8445/S	8499/S	8608/S	8630/S	8837/S
		9196/S	9406/S	9458/S	9479/S	9596/S	9623/S	9647/S	9969/S
		10201/S	10512/S	10519/S	10550/S	10550/S	10579/S	10636/S	10679/S
block_number	1242	10906/S	10925/S	10993/S	11026/S	11139/S	11146/S	11151/S	
		5389/S	6612/S	6966/S	7458/S	7503/S	7537/S	7575/S	7665/S
		7691	7697/M	7809/S	7866/S	7898/S	7959/S	7979/S	8140/S
		8172/S	8227/S	8439/S	8445/S	8499/S	8608/S	8630/S	8837/S
		9196/S	9406/S	9458/S	9479/S	9596/S	9623/S	9647/S	9969/S
		10201/S	10512/S	10519/S	10550/S	10550/S	10579/S	10636/S	10679/S
block_p	4063	10906/S	10925/S	10993/S	11026/S	11139/S	11146/S	11151/S	
		5389	6612	6966	7458	7503	7537	7575	7665
		7692	7694	7809	7866	7898	7959	7979	8140
		8172	8227	8439	8445	8499	8608	8630	8837
		9196	9406	9458	9479	9596	9623	9647	9969
		10201	10512	10519	10550	10550	10579	10636	10679
		10906	10925	10993	11026	11139	11146	11151	
calculate_circular_service	10279	10279	10288	10291					
calculate_service_used	10262	9996	10063	10297					
call_dispatcher	3724	8290/M	7758/M	7666/M	7898/M	7979/M	8024/M	8092/M	8172/M
		8227/M	8499/M	8559/M	8630/M	8746/M	8815/M	8825/M	8933/M
		9049/M	9174/M	9406/M	9479/M	9623/M	9647/M	10084/M	10201/M
		10519/M	10550/M	10579/M	10925/M	11139/M	11151/M		
circular_service	10267	10276/M	10285/M	10285	10292	10293			
clear	4741	8453/M	6494/M	6502/M	7454/M	7466/M	7512/M	7547/M	7599/M
		7725/M	7757/M	7927/M	7983/M	8039/M	8104/M	8176/M	8270/M
		8292/M	8451/M	8508/M	8553/M	8610/M	8677/M	8756/M	8870/M
		8961/M	9010/M	9028/M	9048/M	9104/M	9211/M	9250/M	9257/M
		9271/M	9339/M	9486/M	9539/M	9573/M	9631/M	9655/M	9975/M
		10137/M	10141/M	10210/M	10246/M	10523/M	10589/M	10820/M	10988/M
		11022/M	11155/M						
cmc\$on	1190	10481							
cmt\$element_state	1190	1149	1150	1180					
condition	275	6551/M	6496/M	6534/M	6604/M	7452/M	7461/M	8445/M	8608/M

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED ON LINE	REFERENCES
		8704/M 9102/M 9373/M 9412/M 9433/M 9543/M 9577/M 9976/M
condition	5646	10987/M
condition	6464	5651
condition	6505	6496
condition	6567	6534
condition	7416	6604
condition	8381	7452 7461
condition	8581	8445
condition	8682	8508
condition	9057	8704
condition	9349	9102
condition	9491	9373 9412 9433
condition	9536	9543 9577
controls	4790	9576
controls_defined	4786	8739 9694 9762 10466 10774
count	4738	8739 9694 9762 10466 10774
		8435/M 6435 6450 6451/M 6451 6490/M 6490 6494
		6494/M 6494 6502 6502/M 6502 7450/M 7450 7454
		7454/M 7454 7466 7466/M 7466 7501/M 7501 7512
		7512/M 7512 7529/M 7529 7547 7547/M 7547 7573/M
		7573 7599 7599/M 7599 7653/M 7653 7725 7725/M
		7725 7748/M 7748 7757 7757/M 7757 7806/M 7806
		7927 7927/M 7927 7956/M 7956 7983 7983/M 7983
		8017/M 8017 8039 8039/M 8039 8070/M 8070 8104
		8104/M 8104 8134/M 8134 8176 8176/M 8176 8265/M
		8265 8270 8270/M 8270 8282/M 8282 8292 8292/M
		8292 8444/M 8444 8451 8451/M 8451 8508 8508/M
		8508 8532/M 8532 8553 8553/M 8553 8607/M 8607
		8610 8610/M 8610 8677 8677/M 8677 8727/M 8727
		8756 8756/M 8756 8783/M 8783 8870 8870/M 8870
		8897/M 8897 8961 8961/M 8961 9004/M 9004 9010
		9010/M 9010 9028 9028/M 9028 9048 9048/M 9048
		9100/M 9100 9104 9104/M 9104 9211 9211/M 9211
		9247/M 9247 9250 9250/M 9250 9257 9257/M 9257
		9271 9271/M 9271 9329/M 9329 9339 9339/M 9339
		9469/M 9469 9486 9486/M 9486 9504/M 9504 9538
		9539/M 9539 9573 9573/M 9573 9607/M 9607 9631
		9631/M 9631 9655 9655/M 9655 9951/M 9951 9975
		9975/M 9975 10137 10137/M 10137 10141 10141/M 10141
		10170/M 10170 10210 10210/M 10210 10233/M 10233 10246
		10246/M 10246 10461/M 10461 10466 10466/M 10466 10523
		10589/M 10589 10593/M 10593 10620 10620/M 10620 10820
		10881 10881/M 10881 10988 10988/M 10988 11022 11022/M
		11107 11155 11155/M 11155 11022 11022/M 11022 11107/M
count	10948	10983 10985 11039
cp_service_at_class_switch	1454	10275
cp_time	1642	8739/M 8739 8739/M 8739 8739 8739 9685/M 9685
		9687/M 9687 9742 9743 10274 10275 10466/M 10466
		10466/M 10466 10466 10466 10466
cp_time	2541	8739/M 8739 8739/M 8739 9707/M 9707 9709/M 9709
ptime_signal_last_sent	2251	9921 10466/M 10466
cpu_idle_statistics	1174	8739 8739/M 9744 9746/M 10466 10466/M 10766
		10734 10735/M 10737/M 10739/M 10765 10766 10767 10768/M

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED ON LINE	REFERENCES
cpu_state	1156	10768 10771/M 10771 10775/M 10776/M 10776
cst_index	1148	10480 10587
		6993 7866 7898 7979 8172 8227 8499 8630
		8744 9406 9478 9623 9647 10201 10469 10470
		10519 10550 10579 10642 10648 10685 10687 10694
cst_index	6870	10925 11113 11114 11139 11151
		6942/M 6947/S 6948 6949 6952 6953/S 6955/M 6955
cst_index	7766	6956
		7866/M 7866/S 7866 7866 7866/S 7866/M 7866
		7866 7898/M 7898/S 7898 7898 7898 7898/S 7898/M
cst_index	7932	7898 7898
		7979/M 7979/S 7979 7979 7979 7979/S 7979/M 7979
cst_index	8110	7979
		8172/M 8172/S 8172 8172 8172 8172/S 8172/M 8172
cst_index	8182	8172
		8227/M 8227/S 8227 8227 8227 8227/S 8227/M 8227
cst_index	8381	8227
		8499/M 8499/S 8499 8499 8499 8499/S 8499/M 8499
cst_index	8581	8499
		8630/M 8630/S 8630 8630 8630 8630/S 8630/M 8630
cst_index	9349	8630
		9406/M 9406/S 9406 9406 9406 9406/S 9406/M 9406
cst_index	9446	9406
		9479/M 9479/S 9479 9479 9479 9479/S 9479/M 9479
cst_index	9584	9479
		9623/M 9623/S 9623 9623 9623 9623/S 9623/M 9623
		9623 9647/M 9647/S 9647 9647 9647 9647/S 9647/M
cst_index	10157	9647
		10201/M 10201/S 10201 10201 10201 10201/S 10201/M 10201
cst_index	10408	10201
		10519/M 10519/S 10519 10519 10519 10519/S 10519/M 10519
		10519 10550/M 10550/S 10550 10550 10550 10550/S 10550/M
		10550 10550 10579/M 10579/S 10579 10579 10579 10579/S
cst_index	10879	10579/M 10579 10579
		10925/M 10925/S 10925 10925 10925 10925/S 10925/M 10925
cst_index	11095	10925
		11139/M 11139/S 11139 11139 11139 11139/S 11139/M 11139
		11139 11151/M 11151/S 11151 11151 11151 11151/S 11151/M
		11151 11151
cst_p	5633	5635/M
cst_p	5863	6941/M
cst_p	6871	6941/P 6993
cst_p	7354	7369/S 7370/S
cst_p	7377	7399
cst_p	7562	7585/M
cst_p	7568	7585/P 7587
cst_p	7720	7747/M
cst_p	7744	7747/P 7749/S 7756/S 7758/M 7759 7760/M
cst_p	7766	7820/M 7866/M 7898/M
cst_p	7766	7866/P 7866 7898/P 7898
cst_p	7786	7820/P 7821/P
cst_p	7832	7979/P 7979
cst_p	7832	7979/M

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES
cst_p	7888	8004/M
cst_p	8002	8004/P 8006/M 8006
cst_p	8011	8016/M
cst_p	8014	8016/P 8018/S 8019/S 8022/S 8023/S 8024/M 8025 8026/M
cst_p	8044	8091/M
cst_p	8064	8091/P 8092/M 8093 8094/M 8096
cst_p	8110	8172/P 8172
cst_p	8110	8172/M
cst_p	8182	8227/P 8227
cst_p	8182	8227/M
cst_p	8381	8499/P 8499
cst_p	8381	8499/M
cst_p	8383	8425 8425 8493/P
cst_p	8515	8533/S 8536/S 8543/M 8543 8544/P 8549/S 8550/S 8557
cst_p	8581	8558/M 8559/M 8560 8561/M 8571/P 8575/P 8576/P 8576/P
cst_p	8581	8630/P 8630
cst_p	8583	8635/S 8635/S
cst_p	8682	8606 8608/P 8614/S 8617/P 8617/P 8619 8620/M 8623
cst_p	8682	8624/S 8625/S 8627 8628 8630/P 8635/P 8642/M 8642
cst_p	8682	8643/M 8643 8644/M 8647 8650 8675/P 8675/P
cst_p	8682	8739/M 8739 8739/S 8739/M 8739 8739/S 8739/S 8739
cst_p	8682	8739/P 8739/S 8739/S 8739/S 8739/S 8739/S 8739/S 8739/S
cst_p	8682	8739/M 8739/M 8739 8739 8739 8739/M 8739/M
cst_p	8682	8739/M 8739 8739 8739 8739 8739/P 8739/M
cst_p	8684	8742 8754 8754/P 8703 8708 8713/P 8723/P 8724 8728/M 8729/M 8729
cst_p	8684	8730/M 8730 8731/M 8732/M 8733 8733 8738/M 8738
cst_p	8684	8739/P 8740/M 8740 8741/P 8742/P 8742/P 8743/M 8744/M
cst_p	8771	8744 8745/M 8746/M 8747 8748/M 8753/S 8754/P
cst_p	8877	8803 8815/M 8816 8817/M 8825/M 8826 8827/M
cst_p	8877	8896 8900 8901 8902 8903/M 8904 8905 8905
cst_p	8877	8907/M 8908/M 8913 8916 8917/P 8917/P 8925/M 8926/M
cst_p	8968	8933/M 8934 8935/M 9000/M 9006 9007 9009/M 9018/S 9021/M 9021 9023/M
cst_p	9059	9023 9024/M 9024 9033 9041 9048/M 9050 9051/M
cst_p	9059	9113 9121 9134/S 9139 9140 9140 9142 9144/P
cst_p	9059	9145/M 9155/M 9155 9157/M 9157 9158/M 9158 9162
cst_p	9059	9168 9174/M 9175 9176/M 9179/M 9188 9189/S 9190
cst_p	9349	9192/S 9198/S 9199/M 9202 9203/M 9206/M
cst_p	9349	9406/P 9406
cst_p	9349	9406/M 9406
cst_p	9446	9479/P 9479
cst_p	9446	9479/M 9479
cst_p	9584	9623/P 9623 9647/P 9647
cst_p	9584	9623/M 9647/M 9685/M 9685 9687/M 9687 9688 9690 9690
cst_p	9662	9685/M 9685 9686 9687/M 9687 9688 9690 9690
cst_p	9662	9694/P 9698/S 9699/S 9699/S 9701/S 9702/S 9702 9707/M
cst_p	9662	9707 9707 9709/M 9709 9709 9712 9716 9717/P
cst_p	9662	9717/P 9723/M 9723 9725 9726 9728/M 9730/M 9733/M
cst_p	9662	9735/M 9742 9742 9744 9744 9745/P 9746/M

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES
cst_p	9875	9882 9887 9894/P
cst_p	9902	9921 9924 9924 9929 9930
cst_p	9938	9956 9956 9961/M 9961 9962/M 9962 9963/M 9964/M
cst_p	10157	10080/S 10081/P 10084/M 10128/M 10129/M 10129 10130/S 10131/M
cst_p	10157	10131 10132/S 10133/S 10134/S
cst_p	10408	10201/P 10201 10201/M 10466/M 10466 10466 10466/M 10466 10466 10466 10466
cst_p	10408	10466/P 10466/S 10466/S 10466/S 10466/S 10466 10466 10466/M
cst_p	10408	10466 10466 10466/M 10466 10466 10466 10466/P 10466/M
cst_p	10408	10466/P 10466/M 10466 10466 10466 10466 10466/P 10466/M
cst_p	10408	10519/P 10519 10550/P 10550 10579/P 10579
cst_p	10408	10519/M 10550/M 10579/M
cst_p	10408	10530 10530 10530/P
cst_p	10410	10444/M 10459 10465 10466/P 10469 10470/M 10470 10471/M
cst_p	10410	10472 10480 10481 10482/M 10488/M 10494/M 10495/S 10496/M
cst_p	10410	10496/S 10497/S 10510 10511 10517/M 10521/M 10529/M 10530/P
cst_p	10410	10539/M 10539 10540 10541/M 10559/M 10559 10565/M 10565
cst_p	10410	10567 10568 10570/M 10571/P 10577 10578 10578 10579/P
cst_p	10410	10583/P 10583/P 10587 10584 10594 10595 10595 10597/M
cst_p	10410	10631 10632 10634/M 10635/M 10636/P 10636/P 10637/M 10637
cst_p	10410	10638/M 10638 10639 10641 10642 10648 10648 10678/M
cst_p	10410	10679/P 10679/P 10680/M 10680 10681/M 10681 10682 10685
cst_p	10410	10686 10687 10688 10689 10694 10724/M 10726/M 10726
cst_p	10410	10734 10735/M 10737/M 10739/M 10742/M 10743/M 10744/M 10745/M
cst_p	10410	10746/M 10759/M 10765 10766 10767 10768/M 10768 10771/M
cst_p	10410	10771 10775/M 10776/M 10776 10776 10783/M 10784/M 10785/M 10786/M
cst_p	10879	10787/M 10788/M 10789/S
cst_p	10879	10825/P 10825
cst_p	11095	10825/M 11139/P 11139 11151/P 11151
cst_p	11095	11139/M 11151/M
cst_p	11096	11105 11110/S 11111/S 11113 11114/M 11114 11115/M 11116
cst_p	11096	11118/M 11118 11122/M 11122 11128/M 11128 11130 11131
cst_p	11096	11133/M 11134/P 11139/P 11145 11146/S 11148/S 11149/P 11151/P
cst_p	11096	11152/M
curr_pt lo	7394	7399/M 7402 7402 7403 7406 7407/M 7410/S 7411/S
curr_pt lo	8682	8742/M 8742 8742 8742 8742/M 8742 8742/S 8742/S
current_pit_value	9918	9924/M 9926 9927/M 9927 9929 9929
current_pit_value	9942	9956/M 9958 9959/M 9959 9961 9962
dct_placement	6873	6894/M 6896/M 6899/M 6922 7019 7033 7040 7053
dct_placement	7766	7055 7057 7866/M 7866/M 7866/M 7866 7866 7866 7866
dct_placement	7766	7866 7866 7866/M 7866/M 7866/M 7866 7866 7866 7866
dct_placement	7932	7898 7898 7898 7898 7898 7898 7898 7898
dct_placement	7932	7979/M 7979/M 7979/M 7979 7979 7979 7979 7979
dct_placement	8110	7979 7979 8172/M 8172/M 8172/M 8172 8172 8172 8172
dct_placement	8182	8172 8172 8172/M 8172/M 8172/M 8172 8172 8172 8172
dct_placement	8182	8227/M 8227/M 8227/M 8227 8227 8227 8227 8227

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
dcte	10879	10925 10925/M 10925 10925/M 10925 10925/M 10925/M 10925/M 10925/M 10925
dcte	10879	10925/M 10925 10925/M 10925 10925/M 10925/M 10925/M 10925/M
dcte	10864	10925 10925 10925/M 10925 10925/M 10925 10925/M 10925/M 10925/M
dcte	11095	10997/M 10997 10997 10997/M 10997 10997/M 10997/M 10997/M 10997/M 10997/M
		11139/M 11139 11139/M 11139 11139/M 11139/M 11139/M 11139/M 11139/M 11139/M
		11139 11139/M 11139 11139/M 11139/M 11139/M 11139/M 11139/M 11139/M 11139/M
		11139/S 11139/S 11139 11139 11139/M 11139 11139/M 11139/M 11139/M 11139/S
		11139/S 11139/M 11139/M 11139/S 11139/M 11139/P 11139/P 11139/P 11139/P
		11139 11151/M 11151 11151/M 11151 11151/M 11151/M 11151/M 11151/M 11151
		11151/M 11151 11151/M 11151 11151/M 11151 11151/M 11151/M 11151/M 11151/M
		11151/S 11151/S 11151/M 11151/S 11151 11151 11151/M 11151/M 11151/M 11151/P
		11151/P 11151 11139 11139/M 11139/M 11139 11139 11139/M 11139/M 11139/M 11139/M
dcte	11095	11139 11139/M 11139 11139/M 11139 11139/M 11139/M 11139/M 11139/M 11139/M
		11151/M 11151/M 11151 11151 11151/M 11151/M 11151/M 11151/M 11151/M 11151
dcte	11095	11139 11139 11139/M 11139/M 11139 11139/M 11139/M 11139/M 11139/M 11139/M
		11139/M 11139 11139/M 11139 11139/M 11139/M 11139/M 11139/M 11139/M 11139/M
		11151/M 11151 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M 11151
dcte	11095	11151/M 11151 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M
		11139 11139 11139/M 11139/M 11139 11139/M 11139/M 11139/M 11139/M 11139/M
		11151 11151 11151/M 11151/M 11151 11151/M 11151 11151/M 11151/M 11151/M
debug_index	3146	10844/M
debug_list_pointer	3149	10840
debug_mask_register	3148	10842/M
delayed_swapin_work	1297	9460 9598 10173/M 10173 10236/M 10236
dest	5600	5610
dest	9936	10106
determine_dispatching_priority	10310	10019 10046 10070 10346 10392
dfc\$command_record_bytes	1367	1375
dfc\$division_overwrite_words	1354	1382
dfc\$esm_command_record_size	1375	1383
dfc\$esm_header_record_size	1376	1383
dfc\$esm_maintenance_buf_size	1355	1386
dfc\$esm_memory_base_shift	1361	1383 1384 1384
dfc\$header_record_bytes	1366	1376
dfc\$max_esm_memory_size	1358	1385
dfc\$max_number_of_mainframes	1363	1348
dfc\$min_data_record_bytes	1371	1382
dfc\$min_esm_division_size	1381	1385
dft\$mainframe_set	1348	1298 1299 1437 1438
dispatch_control	1159	6990/M 7758/M 7866/M 7898/M 7979/M 8024/M 8092/M 8172/M
		8227/M 8499/M 8559/M 8630/M 8746/M 8815/M 8825/M 8933/M
		9049/M 9174/M 9406/M 9479/M 9623/M 9647/M 10084/M 10201/M
dispatching_control	1280	10519/M 10550/M 10579/M 10925/M 11139/M 11151/M
		6885/S 7819 7822 7866/S 7898/S 7979/S 8172/S 8227/S 8497 8499
		8497/S 8499/S 8625/S 8630/S 8739 8739 8739/M 8739
		8838 8916 9406/S 9479/S 9623/S 9647/S 9712 9716
		9723/M 9723 9994/M 10000/M 10002/M 10004/M 10008 10010
		10017/M 10027/M 10029/M 10030/M 10031/M 10032/M 10043/M 10044/M

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
		10066/M 10068/M 10179 10182 10194/S 10201/S 10275 10321
		10323 10324/M 10324 10327 10328 10330/S 10332/M 10332
		10335/M 10337/S 10343 10379 10389/M 10391/M 10466 10466
		10466/M 10466 10466/S 10519/S 10519/S 10550/S 10579/S 10925/S 11111/S
dispatching_control	2582	6885 7866 7898 7979 8172 8227 8497 8499
		8625 8630 9406 9479 9623 9647 10045 10057
		10194 10201 10278 10330 10337 10374 10497 10519
dispatching_control_index	1449	10550 10579 10925 11111 11139 11151
		6885/S 7866/S 7898/S 7979/S 8172/S 8227/S 8497/S 8499/S
		8625/S 8630/S 9406/S 9479/S 9623/S 9647/S 10027/M 10043/M
		10066/M 10194/S 10201/S 10330/S 10337/S 10379 10391/M 10498/S
		10519/S 10550/S 10579/S 10925/S 11111/S 11139/S 11151/S
dispatching_control_index	4903	10057/S 10067
dispatching_control_index	10269	10280 10282/S 10283/S 10285/S
dispatching_control_info	4891	7818/M 9991 10000 10002 10017 10057/S 10059 10060
		10066 10068
dispatching_control_p	10288	10277/M 10282 10283 10285
dispatching_control_p	10369	10373/M 10386 10388 10389
dispatching_controls	9943	10056/M 10058 10060
dispatching_priority	1145	8739/P 8739/S 8739/S 8739/S 8739/S 8739/S 8739/S 8739/S
		9701/S 9702/S 10466/P 10466/S 10466/S 10466/S 10466/S 10466/S
		10488/M 10495/S 10782/M 10787/M 10787/S 10787/S 10787/S 10787/S
dispatching_priority	1450	7819 7822 8838 8916 10002/M 10008 10010 10029/M
		10179 10182 10321 10324/M 10332/M 10335/M 10343
dispatching_priority	1464	10060 10331 10338
dispatching_priority	2530	6826 6912 6915 7866 7866 7898 7898 7979
		7979 8172 8172 8227 8227 8227 8445 8499 8499
		8608 8623/M 8623 8630 8630 8813 8818/S 8828/S
		8834 8913 9188 9190 9406 9406 9479 9479
		9623 9623 9647 9647 10190/M 10199 10201 10201
		10203 10519 10519 10550 10550 10579 10579 10925
		10925 11139 11139 11139 11151 11151 11151 11151
dispatching_priority	4904	9991 10001 10003 10018 10061
dispatching_priority	4942	6626/M 6649/M 6682/S 6693 6724/S 6913/M 6915/M 6919
		6920/M 6929 6930/M 6940/S 6969/S 6978 6984/S 6989/S
		7004/S 7013 7015/S 7062/S 7864/S 7864 7864/S 7866/M
		7866/M 7866 7866/M 7866 7866/M 7866/S 7866/S 7866
		7866/S 7866/S 7866/S 7866 7866/S 7866/S 7898/M 7898/M
		7898 7898/M 7898 7898/M 7898/S 7898/S 7898 7898/S
		7898/S 7898/S 7898 7898/S 7898/S 7898/S 7898/S 7898/S
		7979/M 7979 7979/M 7979/S 7979/S 7979 7979/S 7979/S
		8172 8172/M 8172/S 8172/S 8172 8172/S 8172/S 8172/S
		8172 8172/S 8172/S 8172/S 8227/M 8227/M 8227/S 8227/S
		8227/M 8227/S 8227/S 8227 8227/S 8227/S 8227/S 8227/S
		8499/M 8499/S 8445/M 8499/M 8499 8499/S 8499/S 8499/S
		8499/S 8499/S 8499/S 8499 8499/S 8499/S 8499/S 8499/S
		8630 8630/M 8630/S 8608/M 8630/M 8630/M 8630/S 8630/S
		8630 8630/S 8630/S 8741/M 8813/M 8834/S 8834/M 8406/M
		8630 8630/S 8630/S 8741/M 8813/M 8834/S 8834/M 8406/M
		9406/M 9406 9406/M 9406 9406/S 9406/S 9406/S 9406/S
		9406/S 9406/S 9406/S 9406 9406/S 9406/S 9417/S 9417

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table with columns for IDENTIFIER, DEFINED ON LINE, and REFERENCES. Includes entries like dispatching_priority, dispatching_priority_time, and various numeric identifiers.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table with columns for IDENTIFIER, DEFINED ON LINE, and REFERENCES. Includes entries like dispatching_timeslice, dmt\$system_file_id, dual_state_prior_subpriority, and various numeric identifiers.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table listing identifiers and their references. Columns include identifier name, defined on line, and multiple reference numbers. Identifiers include entry_status, environment, expected_wait_time, and various file and task management variables.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table listing identifiers and their references. Columns include identifier name, defined on line, and multiple reference numbers. Identifiers include gfc\$fde_table_base, global_task_id, hash, and various system parameters.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

IDENTIFIER	DEFINED ON LINE	REFERENCES
ijl_ordinal	8381	8445/P 8445
ijl_ordinal	8413	8438/M 8439/P 8445/P 8464
ijl_ordinal	8581	8608/P 8608
ijl_ordinal	8581	8608/S 8608/S 8630/S 8630/S
ijl_ordinal	8770	8837/S 8837/S
ijl_ordinal	9057	9196/S 9196/S
ijl_ordinal	9349	9406/S 9406/S
ijl_ordinal	9446	9458/S 9458/S 9479/S 9479/S
ijl_ordinal	9584	9596/S 9596/S 9623/S 9623/S 9647/S 9647/S
ijl_ordinal	9936	9969/S 9969/S
ijl_ordinal	10157	10201/S 10201/S
ijl_ordinal	10158	10171/P
ijl_ordinal	10226	10234/P
ijl_ordinal	10364	10393/P
ijl_ordinal	10408	10512/S 10512/S 10519/S 10519/S 10550/S 10550/S 10550/S 10550/S
ijl_ordinal	10879	10579/S 10579/S 10636/S 10636/S 10679/S 10679/S
ijl_ordinal	10964	10906/S 10906/S 10925/S 10925/S
ijl_ordinal	11095	11026/S 11026/S
ijl_p	6863	11139/S 11139/S 11146/S 11146/S 11151/S 11151/S
ijl_p	6863	7054 7054
ijl_p	7071	7056 7056
ijl_p	7183	7106 7149
ijl_p	7766	7218 7258
ijl_p	7766	7866 7866 7898 7898
ijl_p	7932	7866 7866 7898 7898
ijl_p	7932	7979 7979
ijl_p	8110	7979 7979
ijl_p	8182	8172 8172
ijl_p	8182	8227 8227
ijl_p	8381	8227 8227
ijl_p	8381	8499 8499
ijl_p	8581	8499 8499
ijl_p	9349	8630 8630
ijl_p	9349	8630 8630
ijl_p	9446	9406 9406
ijl_p	9446	9406 9406
ijl_p	9584	9479 9479
ijl_p	9584	9479 9479
ijl_p	10157	9623 9623 9647 9647
ijl_p	10157	9623 9623 9647 9647
ijl_p	10408	10201 10201
ijl_p	10408	10201 10201
ijl_p	10879	10519 10519 10550 10550 10579 10579
ijl_p	10879	10519 10519 10550 10550 10579 10579
ijl_p	10925	10879 10879
ijl_p	10925	10879 10879
ijl_p	11095	10925 10925
ijl_p	11095	11139 11139 11151 11151
ijl_p	11095	11139 11139 11151 11151
ijl_thread	4944	7369/M 7369 7370/M 7407 7410/M 7410 7411/M 7723
		8536 8635/M 8635 8635/M 8742 8742/M 8742 8742/M
		8754 9264 9299 9410 9429 9483 9536 9570
		9628 9652 9884 10530 11015/M 11015 11016/M 11077

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

IDENTIFIER	DEFINED ON LINE	REFERENCES
ijle_p	1173	11085/M
		7369/S 7370/S 7399 8543/M 8543 8549/S 8550/S 8571/P
		8575/P 8576/P 8617/P 8620/M 8624/S 8625/S 8627 8628
		8630/P 8635/S 8635/S 8642/M 8642 8643/M 8643 8644/M
		8675/P 8728/M 8729/M 8729 8730/M 8730 8731/M 8739/M
		8739 8739/M 8739 8739 8739/P 8739/M 8739
		8739 8739 8740/M 8740 8742 8754 8916 8917/P
		8925/M 9139 9140 9144/P 9145/M 9198/S 9199/M 9202
		9203/M 9206/M 9685/M 9685 9687/M 9687 9712 9716
		9717/P 9723/M 9723 9742 9742 9882 10081/P 10466/M
		10466 10466/M 10466 10466 10466 10466/P 10466/M 10466
		10466 10466 10496/S 10497/S 10530 10539/M 10539 10559/M
		10559 10565/M 10565 10567 10568 10578 10578 10579/P
		10583/P 10636/P 10637 10641 10679/P 10680 10686 10693
		10726/M 10726 11110/S 11111/S 11118/M 11118 11122/M 11122
		11128/M 11128 11130 11131 11139/P
		8463/M
ijle_p	2246	7666 9231 9291 9331 9366 9372 9503
ijle_p	4690	7665/P 7676/M 7685/P 7703/M 7716/P
ijle_p	4830	5252
ijle_p	5235	5286
ijle_p	5286	5293
ijle_p	5287	5293/P
ijle_p	5358	5364 5368/M 5373/P 5377/P
ijle_p	5386	5389/M
ijle_p	5488	5493 5495/P
ijle_p	5676	5698 5698/P
ijle_p	5677	5693 5694 5698/P
ijle_p	5708	5716 5718/P
ijle_p	6567	6612/M
ijle_p	6593	6612/P 6613
ijle_p	6863	6966/M
ijle_p	6863	7058
ijle_p	6865	6884/S 6885/S 6992/M 6992 7051 7051 7054/P 7056/P
		7058/P 7320
ijle_p	7292	7320
ijle_p	7416	7458/M
ijle_p	7416	7459 7459 7459/P
ijle_p	7416	7459 7459/P
ijle_p	7419	7458/P 7459/P 7463
ijle_p	7472	7503/M
ijle_p	7472	7505 7505 7505/P
ijle_p	7472	7505 7505/P
ijle_p	7475	7503/P 7505/P 7508
ijle_p	7517	7537/M
ijle_p	7517	7539 7539 7539/P
ijle_p	7517	7539 7539/P
ijle_p	7520	7535/M 7537/P 7539/P 7543
ijle_p	7562	7575/M
ijle_p	7562	7577 7577 7577/P
ijle_p	7562	7577 7577/P
ijle_p	7562	7598 7598/P
ijle_p	7569	7575/P 7577/P 7581 7598/P
ijle_p	7618	7665/M

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES
ijle_p	7618	7668
ijle_p	7618	7668
ijle_p	7618	7685
ijle_p	7620	7668/P
ijle_p	7766	7809/M
ijle_p	7766	7866/S
ijle_p		7866/P
ijle_p		7898/P
ijle_p	7766	7866
ijle_p	7798	7809/P
ijle_p		7827/M
ijle_p		7879
ijle_p		7896/S
ijle_p	7832	7959/M
ijle_p	7932	7979/S
ijle_p		7979/P
ijle_p	7932	7979
ijle_p	7951	7959/P
ijle_p		7978
ijle_p	8110	8140/M
ijle_p	8110	8172/S
ijle_p		8172/P
ijle_p	8110	8172
ijle_p	8129	8140/P
ijle_p		8171
ijle_p	8182	8222/P
ijle_p	8182	8222
ijle_p	8182	8227/S
ijle_p		8227/P
ijle_p	8182	8227/M
ijle_p	8182	8227
ijle_p	8184	8218
ijle_p		8250
ijle_p	8381	8439/M
ijle_p	8381	8445/P
ijle_p	8381	8471
ijle_p	8381	8499/S
ijle_p		8499/P
ijle_p	8381	8499
ijle_p	8414	8439/P
ijle_p		8469/M
ijle_p		8499/P
ijle_p	8581	8608/P
ijle_p	8581	8608/M
ijle_p	8581	8630/S
ijle_p		8630/P
ijle_p	8581	8630
ijle_p	8770	8837/M
ijle_p	8776	8837/P
ijle_p	9057	9196/M
ijle_p	9221	9231/M
ijle_p	9285	9291/M
ijle_p	9326	9331/M

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES
ijle_p	9349	9406/S
ijle_p		9406/P
ijle_p	9349	9406/M
ijle_p	9357	9406
ijle_p	9446	9366/M
ijle_p	9446	9458/M
ijle_p		9479/S
ijle_p		9479/P
ijle_p	9446	9479
ijle_p	9452	9458/P
ijle_p	9499	9503/M
ijle_p	9584	9596/M
ijle_p	9584	9623/S
ijle_p		9623/P
ijle_p		9647/P
ijle_p	9584	9623
ijle_p	9593	9596/P
ijle_p		9646
ijle_p	9936	9959/M
ijle_p	9936	10081
ijle_p	9945	10081/P
ijle_p		9970
ijle_p		10004/M
ijle_p		10020/P
ijle_p		10041/M
ijle_p		10064/P
ijle_p	10157	10201/S
ijle_p		10201/P
ijle_p	10157	10201/M
ijle_p	10157	10201
ijle_p	10158	10171/P
ijle_p		10200
ijle_p	10225	10234/P
ijle_p	10263	10271
ijle_p	10311	10321
ijle_p		10330/S
ijle_p	10363	10373/S
ijle_p	10408	10512/M
ijle_p	10408	10519/S
ijle_p		10519/P
ijle_p		10550/P
ijle_p	10408	10579/P
ijle_p	10408	10519
ijle_p		10550/P
ijle_p		10550/M
ijle_p	10879	10906/M
ijle_p	10879	10925/S
ijle_p		10925/P
ijle_p	10879	10825
ijle_p	10888	10806/P
ijle_p		10821
ijle_p	10964	11026/M
ijle_p	10974	11026/P
ijle_p	11069	11075

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
ijle_p	11095	11139/S 11139/S 11139/M 11139 11139 11139 11139/P 11139/P
		11139/P 11151/S 11151/S 11151/M 11151 11151 11151 11151/P 11151/P
ijle_p	11095	11139/M 11146/M 11151/M
ijle_p	11095	11139 11151
ijlo	4990	8427/P
ijlo	5676	5698/P
ijlo	5709	5718/P
ijlo	7416	7459/P
ijlo	7472	7505/P
ijlo	7517	7539/P
ijlo	7562	7577/P
ijlo	7618	7666/P
ijlo	10945	7698/P 7698/P
in_use	2095	10993/S 10993/S 11001 11026/P 11035/P
in_use	4688	8231
		5494 5497/M 5497 5698/M 5698 5721/M 5721 7459/M
		7459 7505/M 7505 7539/M 7539 7577/M 7577 7596
		7598/M 7598 7663/M 7663 7668/M 7668 7685 7685/M
		7685 7698/M 7698 7716 7716/M 7716
in_use_incremented	4831	7662/M 7669/M 7686/M 7699/M 7715 7717/M
index	102	8491 8492/S 8493/S 6530 6531/S 6532/S 6549 6550/S
		6551/S 6562/S 6634/M 7369/S 7369/S 7370 7399
		7403 7452 7452/S 7452/S 7458/S 7458/S 7463/S 7502
		7502/S 7502/S 7503/S 7505/S 7509/S 7531 7532/S 7533/S
		7537/S 7538/S 7543/S 7574 7574/S 7574/S 7575/S 7577/S
		7581/S 7587/S 7666 7674 7701 7749/S 7756/S 7807
		7807/S 7807/S 7808/S 7864/P 7866/P 7898/P 7957 7957/S
		7957/S 7958/S 7979/P 8018/S 8019/S 8022/S 8023/S 8068/S
		8072 8072/S 8072/S 8075 8083 8084 8086 8088
		8135/M 8136/S 8138 8138/S 8138/S 8172/P 8227/P 8235
		8266 8266/S 8266/S 8267/S 8268/M 8445/M 8499/P 8533/S
		8536/S 8544/P 8608/M 8614/S 8615/S 8616/S 8630/P 8635/S
		8635/S 8635/S 8635 8741/P 8742 8742 8753/S 8754
		8803 8837/S 8839/S 8842/S 8844/S 8896 8919/S 8921/S
		9018/S 9102 9102/S 9102/S 9134/S 9189/S 9192/S 9196/S
		9232 9295 9380 9397 9464 9508 9602 9882
		10080/S 10130/S 10132/S 10133/S 10134/S 10191 10472 10530
		10746/M 10783/M 10984/S 10994 10996 11018/M 11075 11116
index_p	4076	5389 6612 6866 7458 7503 7537 7575 7665
		7692 7694 7809 7866 7898 7959 7979 8140
		8172 8227 8439 8445 8499 8608 8630 8837
		9196 9406 9458 9479 9596 9623 9647 9969
		10201 10512 10519 10550 10550 10579 10636 10679
		10906 10925 10993 11026 11139 11146 11151
inhibit_access	5679	5695/M 5697/M
inhibit_access	7416	7459/M 7459/M
inhibit_access	7447	7459/P 7460
inhibit_access	7472	7505/M 7505/M
inhibit_access	7499	7505/P 7506
inhibit_access	7517	7539/M 7539/M
inhibit_access	7527	7539/P 7540
inhibit_access	7562	7577/M 7577/M
inhibit_access	7570	7577/P 7578

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
inhibit_access	7618	7668/M 7668/M 7698/M 7698/M
inhibit_access	7649	7669 7670 7698/P 7699 7700
initiate_swap_if_possible	9874	8754 9897 10530
insert_ijl	7352	7372 8635
insert_ijle_p	6875	6966/P 6967/M 6967
insert_ijle_p	7766	7866/P 7866/M 7866 7898/P 7898/M 7898
insert_ijle_p	7932	7979/P 7979/M 7979
insert_ijle_p	8110	8172/P 8172/M 8172
insert_ijle_p	8182	8227/P 8227/M 8227
insert_ijle_p	8381	8499/P 8499/M 8499
insert_ijle_p	8581	8630/P 8630/M 8630
insert_ijle_p	9349	9406/P 9406/M 9406
insert_ijle_p	9446	9479/P 9479/M 9479
insert_ijle_p	9584	9623/P 9623/M 9623 9647/P 9647/M 9647
insert_ijle_p	10157	10201/P 10201/M 10201
insert_ijle_p	10408	10519/P 10519/M 10519 10550/P 10550/M 10550 10579/P 10579/M
		10579
insert_ijle_p	10879	10925/P 10925/M 10925
insert_ijle_p	11095	11139/P 11139/M 11139 11151/P 11151/M 11151
insert_ptle_p	6874	6965/M 6966/P 6968/M 6969/S 6971/M 6978 6981/M 6984/S
insert_ptle_p	7766	7866/M 7866/P 7866/M 7866/S 7866/M 7866 7866/M 7866/S 7866/S 7866/S
		7898/M 7898/P 7898/M 7898/S 7898/M 7898 7898/M 7898/S
insert_ptle_p	7932	7979/M 7979/P 7979/M 7979/S 7979/M 7979 7979/M 7979/S
insert_ptle_p	8110	8172/M 8172/P 8172/M 8172/S 8172/M 8172 8172/M 8172/S
insert_ptle_p	8182	8227/M 8227/P 8227/M 8227/S 8227/M 8227 8227/M 8227/S
insert_ptle_p	8381	8499/M 8499/P 8499/M 8499/S 8499/M 8499 8499/M 8499/S
insert_ptle_p	8581	8630/M 8630/P 8630/M 8630/S 8630/M 8630 8630/M 8630/S
insert_ptle_p	9349	9406/M 9406/P 9406/M 9406/S 9406/M 9406 9406/M 9406/S
insert_ptle_p	9446	9479/M 9479/P 9479/M 9479/S 9479/M 9479 9479/M 9479/S
insert_ptle_p	9584	9623/M 9623/P 9623/M 9623/S 9623/M 9623 9623/M 9623/S
		9647/M 9647/P 9647/S 9647/M 9647
insert_ptle_p	10157	10201/M 10201/P 10201/M 10201/S 10201/M 10201 10201/M 10201/S
insert_ptle_p	10408	10519/M 10519/P 10519/M 10519/S 10519/M 10519 10519/M 10519/S
		10550/M 10550/P 10550/M 10550/S 10550/M 10550 10550/M 10550/S
		10579/M 10579/P 10579/M 10579/S 10579/M 10579 10579/M 10579/S
insert_ptle_p	10879	10925/M 10925/P 10925/M 10925/S 10925/M 10925 10925/M 10925/S
insert_ptle_p	11095	11139/M 11139/P 11139/M 11139/S 11139/M 11139 11139/M 11139/S
		11151/M 11151/P 11151/M 11151/S 11151/M 11151 11151/M 11151/S
insert_ptlo	6863	7054/M 7054/S 7054/M 7054 7054 7054/S 7054/S 7054
		7054 7054/M 7054/M 7054/S 7056/M 7056 7056/S 7056/S
insert_ptlo	6863	7056/M 7056/S 7056/M 7056 7056/S 7056/S
		7056/M 7056/S 7058/M 7058
insert_ptlo	6863	7092/M 7114/S 7115/S 7116/M 7126 7131 7133/S 7134
insert_ptlo	7081	7136 7145/M 7152/M 7158 7165 7169/S 7170/S
		7204/M 7229/S 7230/S 7231/M 7241 7242 7243/S 7244
insert_ptlo	7193	7254/M 7262/M 7267 7278/S 7279/S
		7314/M 7336/S 7337/M 7347
insert_ptlo	7301	7866/M 7866/S 7866/M 7866 7866 7866/S 7866/S 7866
insert_ptlo	7766	7866 7866/M 7866/S 7866 7866 7866/S 7866/S 7866/M
		7898/S 7898/S 7898/M 7898 7898 7898/S 7898/S 7898
		7898/M 7898/M 7898 7898 7898/S 7898/S
insert_ptlo	7766	7866/M 7866/S 7866/S 7866/M 7866 7866 7866/S 7866

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table with columns for identifier, defined on line, and references. Includes entries like insert_ptlo, integer_dp_sets, interactive_task_gtid, etc.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table with columns for identifier, defined on line, and references. Includes entries like insert_ptlo, integer_dp_sets, interactive_task_gtid, etc.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table with columns for identifier, defined on line, and references. Includes entries like jmc\$dpb_absolute, jmc\$ssw_executing, jmc\$smx_active_jobs, etc.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table with columns for identifier, defined on line, and references. Includes entries like jmc\$min_ecc, jmc\$null_dispatching_priority, jmc\$priority_aging_interval_max, etc.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED	REFERENCES
ON LINE		
jmt\$maximum_active_jobs	2599	2576
jmt\$smtr_serv_class_stat_entry	6268	6264
jmt\$priority_aging_interval	2614	2584
jmt\$queue_file_ijkl_information	1723	1307
jmt\$sc_cp_stat	6286	6275 6276
jmt\$sc_pf_stat	6287	6280 6281 6282
jmt\$sc_swap_count	6289	6286 6287 6291 6293
jmt\$sc_swap_stat	6288	6288 6289 6290 6292
jmt\$sc_scheduling_data	1313	1291
jmt\$sc_scheduling_priority	2590	2583
jmt\$service_accumulator	1748	1315 1316 1317 2574 2575
jmt\$service_class_attributes	2562	6263 9094
jmt\$service_class_count	6250	6249
jmt\$service_class_counts	6249	6219
jmt\$service_class_cp_time	6274	6269
jmt\$service_class_entry	6262	6258
jmt\$service_class_index	1795	1326 2567 2577 6249 6258 6314 10976
jmt\$service_class_name	2632	2569 2570
jmt\$service_class_page_faults	6279	6270
jmt\$service_class_swap_stats	6285	6271
jmt\$service_factor_value	2640	2578
jmt\$service_factors	2636	2578
jmt\$swap_data	1329	1293
jmt\$swapout_reasons	1798	1321 5444 9351
jmt\$swapped_job_entry	1813	1338 2059 2266
jmt\$system_supplied_name	2013	1264 2241 4890 5991
jmt\$task_time_slice	1502	1482 1483
jmt\$time_slice_values	1481	1466 2543 6879
jmt\$user_dispatchering_priority	645	4800
jmt\$user_supplied_name	2295	2242
jmt\$working_set_size	2309	2253
jmr_ptlo	10972	10993/M 11014 11015/S 11016/S
jmr_taskid	4888	8445/P 8454 8461 8470 8499/P
jmv\$ajl_p	6356	5494 5497/M 5497 5898/M 5898 5721/M 5721 7459/M
		7459 7505/M 7505 7539/M 7539 7577/M 7577 7598
		7598/M 7598 7663/M 7663 7666/M 7666 7685/M 7685
		7685/M 7685 7698/M 7698 7716/M 7716 7896/M 7896
		8226/M 8438 8231 8291 8331 9366 9372 9458/P
		9461/P 9503 9596/P 10550/M 10918/M
jmv\$idle_dispatchering_controls	6173	8739/M 8739 9694/M 9694 9798/M 9798 10466/M 10466
		10774/M 10774
jmv\$ijl_entry_status_statistics	6196	5366/M 5367 8471/M 8471 10081/M 10081
jmv\$ijl_p	6357	5389 6612 6966 7458 7503 7537 7575 7665
		7691 7692 7694 7809 7866 7898 7959 7979
		8140 8172 8227 8439 8445 8499 8608 8630
		8837 9186 9406 9458 9479 9596 9623 9647
		9969 10201 10512 10519 10550 10550 10579 10636
		10679 10906 10925 10993 11026 11139 11146 11151
jmv\$job_counts	6209	8504/M 8505 8549/M 8550 11046/M 11046
jmv\$null_ijkl_ordinal	6308	8446/P 8923/P 10207/P 10243/P 11033 11034
jmv\$service_classes	6257	6884 7866 7898 7979 8172 8227 8496 8499
		8624 8630 9198 9406 9479 9623 9647 10044
		10056 10193 10201 10277 10329 10336 10373 10496

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED	REFERENCES
ON LINE		
jmv\$subsystem_priority_changes	6314	10519 10550 10579 10925 11110 11139 11151
jmv\$swap_jobs_in_long_wait	6359	7824/M
jmv\$system_ajl_ordinal	6360	9227 9287 7663/S 7663/S 9227 9287 9503/S 9515 9549
		9596/S 9612 9636
jmv\$system_ijkl_ordinal	6361	7661 7665/P 8619 9599/P
job_cp_time	4870	9929/M
job_fixed_asid	1276	5898/P
job_fixed_contiguous_pages	1302	8467/M
job_is_good_swap_candidate	4891	7896/M
job_mode	1282	8226/M 10550/M 10918/M
job_monitor_id	2245	10271
job_monitor_taskid	1281	8461/M 8739/P 9745/P 10466/P
		7369/S 7370/S 7399 7666 7674 7701 8470/M 8635/S
		8635/S 8742 8754 9232 9295 9380 9397 9464
		9508 9602 9882 10530 10994 11075
job_scheduler_data	1291	6884/S 7824/S 7866/S 7898/S 7979/S 8172/S 8227/S 8496/S
		8499/S 8504/S 8505/S 8549/S 8551/S 8620/M 8624/S 8630/S
		9198/S 9199/M 9201 9202 9202 9203/M 9203 9205
		9206/M 9206 9406/S 9478/S 9623/S 9647/S 10045/S 10056/S
		10193/S 10201/S 10277/S 10329/S 10336/S 10373/S 10496/S 10519/S
		10550/S 10579/S 10925/S 11045 11110/S 11139/S 11151/S
job_segnum	8411	8426/M 8427/S 8493/P
jsc\$isqi_null	2035	11032
jsc\$isqi_swapped_io_completed	2036	2038
jsc\$isqi_swapped_io_not_init	2035	2038
jsc\$isqi_swapped_out	2036	11035/P
jsc\$min_ecc	4290	4291
jsc\$min_ecc_js	4291	4294 4297 4300 4303 4306 4309 4312 4315
		4318 4321 4324 4327
jse\$job_executing_non_swappable	4312	9373/P
jse\$unable_to_idle_all_tasks	4303	9412/P 9433/P 9543/P 9577/P
jsp\$idle_tasks_complete	5511	8754 8894 10530
jsp\$long_wait_aging	5516	9144 9238
jsp\$relink_swap_queue	5521	11035
jst\$changed_asid_entry	2081	2072
jst\$ijl_swap_queue_id	2035	2030
jst\$ijl_swap_queue_link	2029	1275
jst\$io_control_information	2043	1294
jst\$swap_file_descriptor	2057	1295
jst\$swapped_page_descriptor	2066	2064
jst\$swapped_page_descriptors	2063	2060
keypoint_enable	2552	6621/M 8445/M 8608/M 8650/M 8650 8658
keypoint_mask	3121	8488/M 8673/M
keypoint_register_enable	2553	6622/M 8445/M 8481/M 8608/M 8655/M 8661/M 8665/M 10809
ktt	8402	8473/M 8474 8475
last_execution_time	2249	8462/M 10471/M 11115/M
last_pid_for_job	2237	8744/M 10470/M 10848 10694 11114/M
last_pid_for_task	2512	10469/M 10642 10687 11113/M
last_ptlo	7393	7398/M 7406/M 7407/S 7410/S
last_ptlo	8682	8742/M 8742/M 8742/S 8742/S
length	5601	5608 5611/M 5611

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

Table with columns: IDENTIFIER, DEFINED ON LINE, REFERENCES. Contains entries for low_priority, major, and major_priority with various reference codes like 9446, 9584, 10157, etc.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

Table with columns: IDENTIFIER, DEFINED ON LINE, REFERENCES. Contains entries for major_time_slice_remaining, major_timeslice_insert, max_block_in_use, etc.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIIER-----DEFINED-----REFERENCES
ON LINE

Table with columns for identifier, defined values, and references. Includes entries like 'maximums_exceeded' (6180), 'maxws_ao_slowdown_display' (1305), 'memory_port_mask' (1147), 'minimum' (2591), 'minimum_time' (4803), 'minimums_to_satisfy' (4787), and 'minimums_to_satisfy' (4816). Lists various numerical values and their abbreviations (M, A, S, I, R, W, P).

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIIER-----DEFINED-----REFERENCES
ON LINE

Table with columns for identifier, defined values, and references. Includes entries like 'minor' (1482) and 'minor_priority' (5101). Lists various numerical values and their abbreviations (M, A, S, I, R, W, P).

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES
mmc\$spq_wired	1832	1875
mmc\$segment_fault_processor_id	3383	3437
mmc\$sequence_pointer	2874	2881
mmc\$ssk_none	2975	2947
mmc\$ssk_segment_number	2976	2945
mmp\$create_job	5552	8493
mmp\$create_task	5540	8675
mmp\$exit_job	5561	8567
mmp\$exit_task	5547	8723
mmp\$get_sdtx_entry_p	5566	8221
mmp\$set_sdtx_entry_p	5568	5570/M 8221/M
mmt\$active_segment_table	2106	8321
mmt\$active_segment_table_entry	2091	2069 2107 2140
mmt\$ast_index	2123	1337 2084 2669 3886 8206
mmt\$attribute_keyword	2796	2811
mmt\$seoi_state	4022	3888
mmt\$global_page_queue_index	1888	2213
mmt\$global_page_queue_list_ent	2203	2213
mmt\$hardware_attribute_set	2865	2831
mmt\$hardware_attributes	2853	2865
mmt\$job_page_queue_index	1889	1815 2214
mmt\$job_page_queue_list	2214	1292
mmt\$link	2114	2092 2130 2131 2200
mmt\$lock_segment_status	2955	2740
mmt\$locked_page	2152	2136
mmt\$lus_lock_type	2905	4718
mmt\$lus_page_disposition	2907	4720
mmt\$max_sdt	2679	2683
mmt\$max_sdt_p	2683	8401
mmt\$max_sdtx	2764	2768
mmt\$memory_reserve_request	2181	1285
mmt\$page_age	2159	2139 2163 2163
mmt\$page_frame_index	2052	2044 2046 2047 2048 2116 2116 2183 2184
		5968 5970 5972
mmt\$page_frame_queue_id	1890	2045 2100 2134
mmt\$page_frame_table_entry	2129	2087 2145
mmt\$page_queue_list_entry	2199	2204 2214
mmt\$rb_lock_unlock_segment	4710	8213
mmt\$sdtx_stream_data	2747	2743
mmt\$segment_access_condition	3410	3438
mmt\$segment_access_rights	2919	2739
mmt\$segment_access_state	2925	2734
mmt\$segment_descriptor	2666	2676 2680 5924
mmt\$segment_descriptor_extended	2732	2761 2765 5568 5571 8211 8221
mmt\$segment_inheritance	2782	2736 2839
mmt\$segment_pointer_kind	2874	2878
mmt\$segment_reservation_state	2965	2737
mmt\$shadow_info	2940	2741
mmt\$shadow_reference_info	2988	2556
mmt\$shadow_segment_kind	2975	2944
mmt\$software_attribute_set	2867	2738 2833
mmt\$software_attributes	2861	2867
mmt\$xcb_page_wait_info	2999	2542

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES
mmv\$ast_p	6321	8230 8230 8231 8232/P
monitor_cptime	4869	9821/M
monitor_flags	2510	9023/M 9023 9157/M 9157 10129/M 10129 10793/M 10793
		10847/M 10847
monitor_flags	4939	6614/M 6616/M 8445/M 8445/M 8608/M 8608/M 9020 9023
		9026/M 9141 9154 9157 9159/M 10130 10134/M 10790
monitor_lock	3876	10793 10794/M 10870/M 10870 11004/M
mtc\$job_fixed_segment	4122	5295/P 8222/P 8232/P
		5252 5293 7054 7054 7056 7056 7058 7106
		7149 7218 7258 7320 7463 7509 7543 7581
		7612 7721 7810 7866 7866 7866 7866 7866
		7898 7898 7898 7898 7898 7898 7898 7898
		7979 7979 7979 8170 8172 8172 8172 8172
		8172 8218 8222 8227 8227 8227 8227 8227
		8232 8425 8437 8459 8499 8499 8499 8499
		8499 8606 8630 8630 8630 8630 8630 8630
		9255 9302 9368 9385 9406 9406 9406 9406
		9406 9471 9479 9479 9479 9479 9479 9479
		9549 9612 9623 9623 9623 9623 9623 9623
		9647 9647 9647 9647 9647 9647 10201 10201
		10201 10201 10201 10516 10519 10519 10519
		10519 10530 10550 10550 10550 10550 10550 10550
		10579 10579 10579 10579 10579 10579 10638 10638
		10682 10921 10925 10925 10925 10925 10925 11139
		11139 11139 11139 11139 11150 11151 11151 11151
		11151 11151
mtc\$scb_max_hardware_status	5889	5839
mtp\$scst_p	5633	6941 7585 7747 7820 7866 7898 7979 8004
		8016 8091 8172 8227 8499 8630 9406 9479
		9623 9647 10201 10519 10550 10579 10925 11139
		11151
mtp\$error_stop	5323	6499 6537 6552 6686 6709 6756 6769 7045
		7452 7502 7574 7579 7751 7807 7864 7864
		7866 7898 7957 7965 7979 8020 8072 8076
		8079 8097 8138 8158 8172 8227 8246 8266
		8288 8334 8499 8534 8537 8630 8710 8725
		8789 8793 8796 9102 9406 9417 9417 9479
		9528 9528 9562 9562 9623 9647 10138 10198
		10198 10201 10519 10550 10550 10561 10579 10590
		10725 10725 10904 10925 10997 10997 11079 11124
		11139 11151
mtp\$interrupt_processor	5528	6986 7898 7979 8172 8227 8499 8630
		8406 8479 8623 9647 10201 10519 10550 10579
		10925 11139 11151
mtp\$set_interlock	5327	5295 5348 8232 8232
mtp\$set_status_abnormal	5645	5652 6486 6534 6604 7452 7461 8445 8608
		8704 9102 9373 9412 9433 9543 9577 9976
mtt\$idle_status_block	5817	5804
mtt\$monitor_interlock	4029	3876 5327
mtt\$scb_i80_status	5793	5784
mtt\$scb_hardware_status	5841	5779 5864
mtt\$scb_hardware_status_count	5839	5842
mtt\$scb_hardware_status_msg	5845	5851

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

Table with columns: IDENTIFIER, DEFINED (ON LINE), and REFERENCES. Rows include identifiers like mtt\$scb hardware status, mtv\$monitor segment table, nat\$received message descriptor, etc., with corresponding line numbers and reference codes.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

Table with columns: IDENTIFIER, DEFINED (ON LINE), and REFERENCES. Rows include identifiers like new_entry_status, next_avail_ptlo, next_index, next_processor_state, next_ptlo_to_dispatch, etc., with corresponding line numbers and reference codes.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
next_ptlo_to_dispatch	10425	11152/M
next_ready_time	9222	10511/M 10512/S 10514/S 10515/P 10518/P
next_search_ptlo	6863	9233/M 9261 9262/M 9267
next_search_ptlo	6863	7054/M 7054
next_search_ptlo	6863	7056/M 7056
next_search_ptlo	6863	7058/M 7058
next_search_ptlo	7082	7104/M 7123
next_search_ptlo	7194	7216/M 7238
next_search_ptlo	7302	7318/M 7344
next_search_ptlo	7765	7866/M 7866 7898/M 7898
next_search_ptlo	7765	7866/M 7866 7898/M 7898
next_search_ptlo	7765	7866/M 7866 7898/M 7898
next_search_ptlo	7932	7979/M 7979
next_search_ptlo	7932	7979/M 7979
next_search_ptlo	7932	7979/M 7979
next_search_ptlo	8110	8172/M 8172
next_search_ptlo	8110	8172/M 8172
next_search_ptlo	8110	8172/M 8172
next_search_ptlo	8182	8227/M 8227
next_search_ptlo	8182	8227/M 8227
next_search_ptlo	8182	8227/M 8227
next_search_ptlo	8381	8499/M 8499
next_search_ptlo	8381	8499/M 8499
next_search_ptlo	8381	8499/M 8499
next_search_ptlo	8581	8630/M 8630
next_search_ptlo	8581	8630/M 8630
next_search_ptlo	8581	8630/M 8630
next_search_ptlo	9349	9406/M 9406
next_search_ptlo	9349	9406/M 9406
next_search_ptlo	9349	9406/M 9406
next_search_ptlo	9446	9479/M 9479
next_search_ptlo	9446	9479/M 9479
next_search_ptlo	9446	9479/M 9479
next_search_ptlo	9584	9623/M 9623 9647/M 9647
next_search_ptlo	9584	9623/M 9623 9647/M 9647
next_search_ptlo	9584	9623/M 9623 9647/M 9647
next_search_ptlo	10157	10201/M 10201
next_search_ptlo	10157	10201/M 10201
next_search_ptlo	10157	10201/M 10201
next_search_ptlo	10408	10518/M 10518 10550/M 10550 10579/M 10579
next_search_ptlo	10408	10518/M 10518 10550/M 10550 10579/M 10579
next_search_ptlo	10408	10518/M 10518 10550/M 10550 10579/M 10579
next_search_ptlo	10879	10925/M 10925
next_search_ptlo	10879	10925/M 10925
next_search_ptlo	10879	10925/M 10925
next_search_ptlo	11095	11139/M 11139 11151/M 11151
next_search_ptlo	11095	11139/M 11139 11151/M 11151
next_search_ptlo	11095	11139/M 11139 11151/M 11151
next_state	2489	10480 10587
next_task_jjle_p	10426	10512/P 10513/M 10513 10515/P 10518/P
next_task_jjle_p	11099	11146/P 11147/M 11147 11149/P 11151/P
next_task_xcb_p	10427	10516/P 10518/P
next_task_xcb_p	11100	11150/P 11151/P

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
n1c\$cc_connect_confirm	3047	3038
n1c\$cc_connect_request	3046	3036
n1c\$cc_expedited_data	3052	3038
n1c\$cc_max_pdu_kind	3054	3057
n1c\$channel_connection_pdu	3070	3022
n1c\$channelnet_pdu	3070	3024
n1t\$cc_pdu_kind	3057	3035
n1t\$cc_seq#_or_connect_time	3034	3023
n1t\$cc_sequence_number	3060	3039
n1t\$device_identifier	3067	3018
n1t\$pdu_type	3070	3021
normal	274	5650/M 6489/M 6496/M 6529/M 6534/M 6601/M 6604/M 7449/M
		7452/M 7452/M 7453 7461/M 8429 8445/M 8445/M 8446
		8608/M 8608/M 8609 8702/M 8704/M 8718 9102/M 9102/M
		9103 9334 9365/M 9373/M 9412/M 9433/M 9502/M 9543/M
		9577/M 9595/M 9920/M 9950/M 9976/M 10978/M 10986/M
null_debug_mask	10838	10842
null_dispatcher_info	7799	7818
null_pva	6326	8448
offset	1999	7595 8006/M 8006
offset	5243	5248/M 5255/M 5258
offset	5286	5293/M 5293/M 5293
offset	8182	8222/M 8222/M 8222 8232/M 8232/M 8232
offset	8601	8605/M 8606
old_dispatchering_priority	7797	7819/M 7822
old_entry_status	5362	5364/M 5366/S 5367/S 5371 5375
old_entry_status	8381	8471/M 8471/S 8471/S 8471 8471
old_entry_status	9936	10081/M 10081/S 10081/S 10081 10081
old_max_ptlo	9947	10104/M 10107 10114 10116
old_te	5579	5594/M
old_te	5617	5630
operator_set_dispatchering_prio	1452	9994/M 10000/M 10031/M 10323 10325
option	6486	6495
option	6507	6533
option	7416	7452
option	9057	9102
osc\$aging_interval_maximum	2333	2336
osc\$base_exception	5036	5038 6094
osc\$call_instruction	3250	3258
osc\$cpu_stepped	2492	10480 10587
osc\$data_read	3249	3258
osc\$debug	3087	10845
osc\$free_flag	3085	9022 9156 10792 10850
osc\$free_running_clock_maximum	1524	1521
osc\$idle_no_io_active	2484	10739
osc\$idle_with_io_active	2483	10737 10767
osc\$invaltid_ring	1945	1985
osc\$keypoint_base	6094	6096 6100 6104 6107 6111 6115 6119 6122
		6126 6130 6134 6138 6141 6144 6148 6151
		6155 6158 6163
osc\$keypoint_enable	3155	8484 8486 8669 8671
osc\$max_fault_contents	3450	3444

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

IDENTIFIER	DEFINED	REFERENCES
	ON LINE	
osc\$max_idle_count	2471	2479
osc\$max_kpt_pages	5962	5968 5970 5972 6008
osc\$max_name_size	2299	2303 2306
osc\$max_number_of_processors	716	1140 5931 5937 5997 5998
osc\$max_page_frames	1895	1332 1333 1814 1816 2052 2093 2201 2207
osc\$max_page_size	2432	2428
osc\$max_page_table_entries	1896	1899
osc\$max_ring	1944	1985 1986
osc\$max_segment_length	1968	1991 2744 2775
osc\$max_status_condition_code	240	236 252
osc\$max_status_condition_number	5655	5646
osc\$max_string_size	256	259 262 267
osc\$max_tasks	110	107 9361 9500
osc\$maximum_offset	1967	1968 1988 1988
osc\$maximum_processor_id	3275	3271
osc\$maximum_processor_number	2455	2450
osc\$maximum_processors	720	716 2455
osc\$maximum_segment	1966	1987
osc\$min_ecc	5035	5036
osc\$min_page_size	2431	2428
osc\$min_ring	1943	1986
osc\$not_idle	2483	10754
osc\$pr_base_constant	3811	10765 10775 10775 6494 6502 7450 7454 7466
		6428 6447 6490 6494 6502 7450 7454 7466
		7501 7512 7529 7547 7573 7599 7653 7725
		7748 7757 7806 7927 7956 7983 8017 8039
		8070 8104 8134 8176 8265 8270 8282 8292
		8444 8451 8508 8532 8553 8607 8610 8677
		8727 8756 8783 8870 8897 8961 9004 9010
		9028 9048 9100 9104 9211 9247 9250 9257
		9271 9329 9339 9469 9486 9504 9539 9573
		9607 9631 9655 9951 9975 10137 10141 10170
		10210 10233 10246 10461 10523 10589 10593 10820
		10981 10988 11022 11107 11155
osc\$pr_element_id	3800	4883
osc\$pr_process_interval_timer	3837	4884 8732 9922 9955
osc\$pr_set_keypoint_enable	5948	10809
osc\$pr_user_mask_reg	3846	4883
osc\$purge_all_cache	3852	10644 10649 10689 10695
osc\$purge_all_page_seg_map	3861	8448 10645 10650 10690 10696
osc\$system_keypoints	6040	8478 8652
osc\$system_sample_keypoints	6041	8479 8653
osc\$system_table_lock_set	728	6910 7862 7866 7898 7979 8172 8227 8499
		8630 9386 9406 9479 9519 9553 9623 9647
		10201 10482 10519 10550 10579 10925 11139 11151
osc\$task_time_slice_maximum	1513	1516
osc\$tsrv_ring	1948	9188
osc\$vl_invalid_entry	2694	8447
osk\$base	833	735 739 743 747 751 755 759 763
		767 771 775 779 783 787 792 795
		798
osk\$m	904	8474 8475 10524 10822
osk\$monitor_multiplier	903	10441 10524 10822
osk\$mtr	868	10441 10524 10822

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

IDENTIFIER	DEFINED	REFERENCES
	ON LINE	
osk\$performance	869	8474 8475
osk\$system_class	879	863 864 865 866 867 868 869
osp\$update_job_keypoint_mask	5659	9451 9599
ost\$aging_interval	2336	2255 2256
ost\$asid	1931	1276 1927 2071 2082 2083 2098 2700 2837
		5460 5731
ost\$binary_unique_name	4043	3878
ost\$class_15_keypoint	6058	6015
ost\$cp_time	1629	1597 1642 2541 4781
ost\$cp_time_value	1627	1318 1630 1631 2250 2251 2554
ost\$cpu_element_id	2447	1171
ost\$cpu_idle_statistics	2474	1174
ost\$cpu_memory_port_mask	2449	1147 5528
ost\$cpu_running_or_stepped	2492	2489 2489
ost\$cpu_state	2487	1156
ost\$cpu_state_reason	2498	1177
ost\$cpu_state_table	1143	1140 5633 6871 7354 7377 7568 7744 7796
		8002 8014 8064 8383 8515 8583 8684 8771
		8877 8968 9059 9451 9662 9875 9902 9938
		10410 10966 11096
ost\$cs_lock	3289	2522
ost\$cs_trace_control	3685	1175
ost\$date_time	6077	6080
ost\$debug_code	3249	3237
ost\$debug_list	3245	3149
ost\$debug_list_entry	3236	3245
ost\$debug_mask	3255	3148 4755 10838 10838
ost\$exchange_package	3098	2509
ost\$execute_privilege	2713	2695 2708
ost\$execution_control_block	2508	1157 2534 4987 5006 5540 5541 5547 5554
		5555 5561 5567 5764 6568 6864 7085 7197
		7305 7418 7474 7519 7571 7610 7623 7802
		7952 8130 8214 8415 8529 8602 8762 8773
		8781 8889 9224 9363 9454 9497 9592 10166
		10230 10427 10434 10835 10889 11100 11103
ost\$external_interrupt_request	3873	1163
ost\$family_name	2346	2341
ost\$flags	3155	3105 8484 8486 8669 8671
ost\$frame_descriptor	3213	3228
ost\$free_running_clock	1521	1287 1288 1289 1290 1324 1334 1335 1336
		1453 1465 2097 2249 2258 2540 3894 4692
		4796 4803 4804 4844 4845 4857 4858 4905
		6189 6190 9032 9112 1281 1310 2245 2519 2520
ost\$global_task_id	101	96 583 1152 1281 1310 2245 2519 2520
		3334 3537 3896 3984 4859 4979 4988 4998
		5008 5016 5024 5744 5751 5757 5918 5918
		6353 6353 6380 6465 6506 6545 6559 6570
		7353 7376 7417 7473 7518 7563 7745 7767
		7933 8045 8112 8183 8260 10956 10971
ost\$halfword	2004	2460
ost\$idle_type	2483	2478
ost\$key_lock	1974	2701 2822
ost\$key_lock_value	1980	1977 3172 3174

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

Table with columns: IDENTIFIER, DEFINED ON LINE, REFERENCES. Lists identifiers like ost\$keypoint, ost\$logical_processor_id, etc., with their corresponding line numbers and reference numbers.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

Table with columns: IDENTIFIER, DEFINED ON LINE, REFERENCES. Lists identifiers like ost\$task_time_slice, ost\$stop_of_stack_pointer, osv\$cpus_physiically_configured, etc., with their corresponding line numbers and reference numbers.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

IDENTIFIER	DEFINED ON LINE	REFERENCES
		10925 10925 10925 10925 10925 10925/M 10925 10925/M
		10925 10925/M 10925 10925 10925 10925 10925 10925/M 10925/M 10925/M
		10925 10925/M 10925/M 10925/M 10925/M 10925/M 10925/M 10925/M 10925/M 10925/M
		11078 11086/M 11086/M 11139/M 11139/M 11139/M 11139/M 11139/M 11139/M 11139/M
		11139/M 11139 11139/M 11139 11139/M 11139/M 11139/M 11139/M 11139/M 11139/M
		11139 11139 11139/M 11139 11139 11139/M 11139 11139/M 11139 11139/M
		11139 11139 11139 11139 11139 11139/M 11139 11139 11139/M 11139/M
		11139/M 11139 11139/M 11139 11139/M 11139/M 11139/M 11139/M 11139/M
		11139 11139 11139 11139/M 11139 11139/M 11139/M 11139/M 11139/M
		11151/M 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M
		11151 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M
		11151 11151/M 11151 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M
		11151 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M
		11151 11151 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M
		11151 11151 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M
		11151/M 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M 11151/M
ptle_p	6679	6681/M 6682/S 6693 6696 6698 6701 6723/M 6724/S
ptle_p	6749	6751/M 6761
ptle_p	6878	6881/M 6912 6913/M 6913 6915/M 6919 6920/M 6929
		6930/M 6940/S 6989/S 6991/M 7004/S 7007/M 7013 7015/S
		7020/M 7030 7034/M 7037 7042/M 7062/S
ptle_p	7766	7864/M 7864/S 7864 7864 7864 7864 7864/M 7864/S
ptle_p	7766	7866/M 7866 7866/M 7866 7866/M 7866 7866/M 7866/M 7866
		7866/M 7866/S 7866/S 7866/M 7866/S 7866/M 7866/S 7866/S
		7866/M 7866 7866/M 7866 7866/M 7866/S 7866/M 7866/S
		7898/M 7898 7898/M 7898 7898/M 7898 7898/M 7898/M 7898/S
		7898/S 7898/M 7898/S 7898/M 7898/S 7898 7898/S 7898/M 7898
ptle_p	7766	7898/M 7898 7898/M 7898/S 7877 7877 7877/S 7877
ptle_p	7932	7877/M 7877/M 7979 7979 7979/M 7979 7979/M 7979
		7979/M 7979/S 7979/S 7979/M 7979/S 7979/S 7979/M 7979/S
ptle_p	8110	7979/M 7979 7979/M 7979 7979/S 7979/S 7979/S 7979/S
		8172/M 8172 8172/M 8172 8172/M 8172 8172/M 8172/S
		8172/M 8172/S 8172/M 8172/M 8172/M 8172/M 8172/M 8172/S
ptle_p	8182	8223 8223 8223/S 8223 8223 8223 8223/S 8223/S 8223
		8223/M 8223/M 8249 8249 8249/S 8249 8249/S 8249
ptle_p	8182	8249/S 8249 8249/M 8249/M 8227/M 8227 8227/M 8227
		8227/M 8227/S 8227/S 8227/M 8227/S 8227/M 8227/S 8227/S
ptle_p	8359	8227/M 8227 8227/M 8227/M 8227/S 8227/M 8227/S 8227/S
		8362 8363 8365/S 8365 8368 8368 8371/S 8371
ptle_p	8381	8374/M 8375/M 8499/M 8499 8499/M 8499 8499/M 8499
		8499/M 8499/S 8499/S 8499/M 8499/S 8499/M 8499/S 8499/S
ptle_p	8581	8499/M 8499 8499/M 8499 8499/M 8499/S 8499/S 8499/S
		8630/M 8630 8630/M 8630 8630/M 8630/M 8630/M 8630/M
		8630/M 8630/S 8630/S 8630/M 8630/M 8630/S 8630/M 8630/S

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

IDENTIFIER	DEFINED ON LINE	REFERENCES
ptle_p	8997	9018/M 9020 9023 9024 9026/M 9027/M 9034/M 9036/M
ptle_p	9091	9042/M 9044/M 9047/M 9134/M 9135/M 9141 9143 9153 9154 9157 9158
ptle_p	9349	9159/M 9160/M 9163 9169/M 9171/M 9173/M 9180/M 9181/M
		9406/M 9406 9406/M 9406 9406/M 9406/M 9406/M 9406/S
ptle_p	9349	9406/M 9406/S 9406/S 9406/M 9406 9406/M 9406/S 9406/S
ptle_p	9446	9417/M 9417/S 9417 9417 9417 9417 9417/M 9417/S
		9479/M 9479 9479/M 9479 9479/M 9479 9479/M 9479
		9479/M 9479/S 9479/S 9479/M 9479/S 9479/M 9479/S 9479/S
ptle_p	9491	9479/M 9479 9479/M 9479 9479/M 9479/S 9479/S 9479/S
		9528/M 9528/S 9528 9528 9528 9528 9528/M 9528/S
ptle_p	9584	9562/M 9562/S 9562 9562 9562 9562 9562/M 9562/S
		9623/M 9623 9623/M 9623/M 9623/M 9623/M 9623/M 9623/S
		9623/M 9623/S 9623/S 9623/M 9623/S 9623/S 9623/S 9623/S
		9647/M 9647 9647/M 9647 9647/M 9647 9647/M 9647/S
		9647/S 9647/M 9647/S 9647/M 9647/M 9647/S 9647/M 9647
ptle_p	10157	9647/M 9647 9647/M 9647/S 9647/S 9647/M 9647/S 9647/S
ptle_p	10157	10198/M 10198/S 10198 10198 10198 10198 10198/M 10198/S
		10201/M 10201 10201/M 10201/M 10201/M 10201/M 10201/M 10201/S
		10201/M 10201/S 10201/S 10201/M 10201/S 10201/M 10201/S 10201/S
ptle_p	10165	10201/M 10201 10201/M 10201/M 10201/M 10201/M 10201/S 10201/S
ptle_p	10408	10192/M 10197 10199/M 10203/M 10519/M 10519 10519/M 10519
		10519/M 10519 10519/M 10519 10519/M 10519 10519/M 10519/S
		10519/M 10519/S 10519/S 10519/M 10519 10519/M 10550/M 10550
		10550/M 10550 10550/M 10550 10550/M 10550/M 10550 10550/S
		10550/S 10550/M 10550/S 10550/M 10550/M 10550/S 10550/M 10550
		10550/M 10550 10550/M 10550/S 10579/M 10579 10579/M 10579
		10579/M 10579/S 10579/S 10579/M 10579/S 10579/S 10579/M 10579/M
ptle_p	10408	10579/S 10579/M 10579 10579/S 10579/M 10579 10579/M 10579
		10579/M 10579/S 10579/S 10550/M 10550/S 10550/M 10550 10550/P 10550
ptle_p	10408	10550/M 10550 10550/M 10550/P 10550 10550/M 10550/M 10550
ptle_p	10435	10725/M 10725/S 10725 10725 10725 10725 10725/M 10725/S
		10473/M 10480 10527 10528/M 10556 10557/M 10557 10558/M
		10560 10563 10563 10564 10569 10575 10576/M 10581/M
		10582/M 10633/M 10635 10639 10677/M 10678 10682 10781/M
		10784 10787 10790 10793 10794/M 10796 10797 10798/M
ptle_p	10879	10801 10804/M 10925/M 10925 10925/M 10925 10925/M 10925
		10925/M 10925/S 10925/S 10925/M 10925/S 10925/M 10925/S
		10925/M 10925 10925/M 10925/M 10925/S 10925/M 10925/S
ptle_p	10891	10899/M 10900 10901/S 10902/M 10903 10903 10906/P 10907
		10908/M 10909 10912/M 10913/P 10916 10920/M 10921
ptle_p	10964	10987/M 10987 11139/M 11139 11139/M 11139 11139/M 11139
ptle_p	11095	11139/M 11139 11139/S 11139/M 11139/S 11139/M 11139/S
		11139/M 11139 11139/M 11139/M 11139/M 11139/S 11151/M 11151
		11151/M 11151 11151/M 11151 11151/M 11151/M 11151/M 11151/S
		11151/S 11151/M 11151/S 11151/M 11151/M 11151/M 11151/M 11151

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFI	DEFIN	REFERENCES
ER	ON	
	LINE	
ptle_p	11101	11117/M 11119 11120/M 11120 11121/M 11123 11126 11126
ptlo	6646	11127 11132 11138/M
ptlo	6661	6648/S 6649/S 6650/S 6652 6654 6656
		6681/S 6685 6695 6697 6700 6705 6705 6712/S
		6713 6716 6719
ptlo	6731	6751/S 6755 6760 6764 6764 6771/S 6772
ptlo	6863	7054 7054 7054/S 7054 7054 7054/S 7054/S 7054
		7054/S 7054
ptlo	6863	7056 7056 7056/S 7056 7056 7056/S 7056/S 7056
		7056/S 7056 7056/S 7056
ptlo	6863	7058 7058 7058/S 7058
ptlo	6866	6881/S 6987 7008 7009 7010 7011 7021 7027
		7029 7031 7035 7036 7038 7041 7043 7054/P
ptlo	7070	7056/P 7058/P
		7092 7103 7103 7105/S 7126 7146/S 7148/S 7159
		7160/S 7182 7186 7183/S 7186 7168/S 7169/S 7170 7171/S
		7172
ptlo	7182	7204 7215 7215 7217/S 7241 7255/S 7257/S 7268
		7268/S 7271 7272/S 7274 7275/S 7277/S 7278/S 7279
		7280/S 7281
ptlo	7291	7314 7317 7317 7319/S
ptlo	7608	7612/S
ptlo	7766	7864/S 7864 7864 7864 7864 7864 7864/S
		7864 7864 7864
ptlo	7766	7866/S 7866 7866 7866 7866 7866 7866 7866
		7866 7866 7866 7866 7866 7866 7866
		7866/P 7866/P 7898/S 7898 7898 7898 7898
		7898 7898 7898 7898 7898 7898 7898
ptlo	7766	7898 7898/P 7898/P 7898/P 7898/P
		7866 7866 7866/S 7866 7866 7866/S 7866/S 7866
		7866/S 7866 7866/S 7866 7866/S 7866/S 7866/S 7866/S
		7866 7898 7898 7898/S 7898 7898 7898/S 7898/S
ptlo	7766	7898 7898/S 7898 7898/S 7898 7898/S 7898/S 7898
		7898/S 7898
		7866 7866 7866/S 7866 7866 7866/S 7866 7866
		7866/S 7866 7866/S 7866 7866/S 7866/S 7866/S 7866
		7898/S 7898 7898/S 7898 7898/S 7898/S 7898/S 7898/S
ptlo	7766	7898/S 7898 7898/S 7898 7898/S 7898/S 7898/S 7898
ptlo	7766	7866 7866 7866/S 7866 7866 7866/S 7866 7866
ptlo	7932	7898/S 7898 7898/S 7898 7898/S 7898/S 7898/S 7898
		7898 7898/S 7898 7898/S 7898 7898/S 7898/S 7898
		7898/S 7898 7898/S 7898 7898/S 7898/S 7898/S 7898
ptlo	7932	7898/S 7898 7898/S 7898 7898/S 7898/S 7898/S 7898
ptlo	7932	7898/S 7898 7898/S 7898 7898/S 7898/S 7898/S 7898
ptlo	8110	8172/S 8172 8172 8172 8172 8172 8172 8172

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFI	DEFIN	REFERENCES
ER	ON	
	LINE	
ptlo	8110	8172 8172 8172 8172 8172 8172 8172 8172/P
		8172/P 8172/P
		8172/S 8172 8172/S 8172 8172 8172/S 8172/S 8172
		8172/S 8172 8172/S 8172 8172/S 8172/S 8172/S 8172
ptlo	8110	8172 8172 8172/S 8172 8172 8172/S 8172/S 8172
ptlo	8182	8172/S 8172 8172/S 8172 8172 8172/S 8172/S 8172
		8172/S 8172
ptlo	8182	8227/S 8227 8227/S 8227 8227 8227 8227 8227
		8227/S 8227 8227/S 8227 8227 8227/S 8227/S 8227/P
		8227 8227/P 8227/P
ptlo	8182	8227 8227 8227/S 8227 8227 8227/S 8227/S 8227
		8227/S 8227 8227/S 8227 8227 8227/S 8227/S 8227
ptlo	8182	8227/S 8227 8227/S 8227 8227 8227/S 8227/S 8227
ptlo	8276	8227/S 8227 8227/S 8227 8227 8227/S 8227/S 8227
		8227/S 8227 8227/S 8227 8227 8227/S 8227/S 8227
ptlo	8280	8288/S 8288 8288/S 8288 8288 8288 8288
ptlo	8299	8288 8288/S 8288 8288 8288 8288/S 8288
		8284 8285/S 8285/S 8287/S 8288/P 8288 8333 8337/S 8339
		8321/S 8324 8325 8327 8328 8341 8343/S 8345 8347
ptlo	8381	8499/S 8499 8499/S 8499 8499 8499 8499 8499
		8499 8499 8499/S 8499 8499 8499 8499 8499/P
ptlo	8381	8499/P 8499/P
		8499 8499 8499/S 8499 8499 8499/S 8499 8499/S
		8499/S 8499 8499/S 8499 8499/S 8499/S 8499 8499/S
ptlo	8381	8499/S 8499 8499/S 8499 8499 8499/S 8499/S 8499
		8499 8499 8499/S 8499 8499/S 8499/S 8499/S 8499
ptlo	8513	8499 8499 8499/S 8499 8499 8499/S 8499 8499
ptlo	8581	8544/S 8544/S 8544/S 8544 8544 8544 8544
		8630/S 8630 8630 8630 8630 8630 8630 8630
		8630 8630 8630 8630 8630 8630 8630 8630/P
ptlo	8581	8630/P 8630/P
		8630 8630 8630/S 8630 8630 8630/S 8630/S 8630
		8630/S 8630 8630/S 8630 8630 8630/S 8630/S 8630
ptlo	8581	8630 8630 8630/S 8630 8630 8630/S 8630/S 8630
		8630/S 8630 8630/S 8630 8630/S 8630/S 8630/S 8630
ptlo	8581	8630/S 8630 8630/S 8630 8630 8630/S 8630/S 8630
ptlo	8682	8630 8630 8630/S 8630 8630 8630/S 8630/S 8630
ptlo	8682	8741/S 8741/S 8741/S 8741 8741 8741 8741
ptlo	8776	8754/M 8754 8754/S 8754/M 8754/S 8754 8754/S 8754
ptlo	8894	8803/M 8813/S 8834/S 8835/S 8850/S
ptlo	8911	8913/S 8914/S 8927/S 8929/S 8930/S
ptlo	9217	9255/S
ptlo	9223	9232/M 9254 9254 9255/P 9259/S 9260/S 9261/S 9262/S
		9264/M 9264/S
ptlo	9283	9295/M 9297 9297 9298/S 9299/M 9299/S

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES
pt 10	9349	9406/S 9406 9406 9406 9406 9406 9406 9406 9406/P
pt 10	9349	9406/P 9406/P 9406 9406 9406/S 9406/S 9406/S 9406/S 9406/S 9406/S
pt 10	9349	9406 9406 9406/S 9406 9406 9406/S 9406/S 9406/S 9406/S 9406
pt 10	9349	9406/S 9406 9406/S 9406 9406/S 9406/S 9406/S 9406/S 9406
pt 10	9349	9406 9406 9406/S 9406 9406/S 9406/S 9406/S 9406/S 9406
pt 10	9349	9417/S 9417 9417 9417 9417 9417 9417 9417 9417/S
pt 10	9359	9380/M 9384 9384 9384 9385/S 9387/S 9388/S 9389/S 9390/S
pt 10	9446	9394/S 9396 9396 9397/M 9398 9398 9399/S 9400/S 9401/S
pt 10	9446	9403/S 9406/P 9409/S 9410/M 9410/S 9416/S 9417/P 9418/S
pt 10	9446	9420/S 9422/S 9425/S 9425/M 9429/S 9429 9429 9429 9429/P
pt 10	9446	9479/S 9479 9479 9479 9479 9479 9479 9479 9479/P
pt 10	9446	9479/P 9479/P 9479/S 9479 9479 9479/S 9479/S 9479/S 9479
pt 10	9446	9479/S 9479 9479/S 9479 9479/S 9479/S 9479/S 9479/S 9479/S
pt 10	9446	9479 9479 9479/S 9479 9479 9479/S 9479/S 9479/S 9479
pt 10	9455	9479/S 9479 9479/S 9479 9479/S 9479/S 9479/S 9479/S 9479
pt 10	9491	9464/M 9470 9470 9471/S 9472/S 9473/S 9474/S 9477/S
pt 10	9496	9479/P 9482/S 9483/M 9483/S 9528/S 9528 9528 9528 9528/S 9528 9528 9528 9528/S
pt 10	9584	9528 9528 9528 9528 9528/S 9528 9528 9528 9528/S
pt 10	9584	9562/S 9562 9562/S 9562 9562 9562 9562 9562/S 9562/S 9562/S 9562/S
pt 10	9584	9508/M 9514 9514 9515/S 9520/S 9521/S 9522/S 9523/S
pt 10	9584	9526/S 9527/S 9528/P 9529/S 9531/S 9533/S 9536/M 9536/S
pt 10	9584	9548 9548 9549/S 9554/S 9555/S 9557/S 9570/M 9570/S
pt 10	9584	9561/S 9562/P 9563/S 9565/S 9567/S 9623 9623 9623 9623
pt 10	9584	9623/S 9623 9623 9623 9623 9623 9623 9623 9623/P
pt 10	9584	9623/P 9623/P 9647/S 9647 9647 9647 9647
pt 10	9584	9647 9647/P 9647/P 9647/P 9623 9623/S 9623/S 9623
pt 10	9584	9623/S 9623 9623/S 9623 9623/S 9623/S 9623/S 9623/S
pt 10	9584	9623 9647 9647 9647/S 9647 9647 9647/S 9647/S 9647/S
pt 10	9584	9647/S 9647 9647/S 9647 9647/S 9647/S 9647/S 9647/S 9647/S
pt 10	9584	9623 9623 9623/S 9623 9647 9647 9647/S 9647
pt 10	9590	9602/M 9611 9611 9612/S 9616/S 9617/S 9618/S 9621/S
pt 10	9590	9623/P 9626/S 9628/M 9628/S 9635 9635 9635 9635

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER	DEFINED ON LINE	REFERENCES
pt 10	9880	9641/S 9642/S 9645/S 9647/P 9650/S 9652/M 9652/S
pt 10	10157	9882/M 9883 9883/S 9883 9883/S 9884/M 9884/S 9886
pt 10	10157	10198/S 10198 10198 10198 10198 10198 10198 10198 10198/S
pt 10	10157	10198 10198 10198 10198 10198 10198 10198 10198 10198
pt 10	10157	10201/S 10201 10201 10201 10201 10201 10201 10201 10201/P
pt 10	10157	10201 10201 10201/P 10201/P 10201/S 10201 10201 10201/S 10201/S 10201
pt 10	10157	10201/S 10201 10201/S 10201 10201/S 10201/S 10201/S 10201/S
pt 10	10157	10201 10201 10201/S 10201 10201 10201/S 10201/S 10201/S
pt 10	10164	10201/S 10201 10201/S 10201 10201 10201/P 10201/P
pt 10	10408	10191/M 10192/S 10198/P 10201/P 10519/S 10519 10519 10519 10519 10519
pt 10	10408	10519/S 10519 10519 10519 10519 10519 10519 10519 10519
pt 10	10408	10519 10519 10519/P 10550/S 10550 10550 10550 10550 10550 10550/P
pt 10	10408	10550 10550 10550 10550 10550 10550 10550 10550 10550
pt 10	10408	10550/S 10550/P 10550/P 10550/P 10579/S 10579 10579 10579
pt 10	10408	10579 10579 10579 10579 10579 10579 10579 10579 10579
pt 10	10408	10579 10579 10579/P 10579/P 10579/P 10579/P 10519 10519
pt 10	10408	10519/S 10519 10519/S 10519 10519/S 10519/S 10519/S 10519/S
pt 10	10408	10519 10550 10550 10550/S 10550 10550 10550/S 10550/S
pt 10	10408	10550/S 10550 10550/S 10550 10550/S 10550/S 10550/S 10550/S
pt 10	10408	10579/S 10579 10579/S 10579 10579/S 10579 10579/S 10579/S
pt 10	10408	10579 10579/S 10579 10519/S 10519 10519 10519/S 10519/S
pt 10	10408	10519 10519 10519/S 10519 10519/S 10519/S 10519/S 10519
pt 10	10408	10519/S 10519 10519/S 10519 10550/S 10550 10550/S 10550/S
pt 10	10408	10550/S 10550 10550/S 10550 10550/S 10550/S 10550/S 10550/S
pt 10	10408	10550/S 10550 10550/S 10550 10579 10579/S 10579 10579/S
pt 10	10408	10579/S 10579 10579/S 10579 10579/S 10579 10579/S 10579/S
pt 10	10408	10579 10579/S 10579 10579/S 10579 10579/S 10579 10579/S
pt 10	10408	10519 10519 10519/S 10519 10550 10550/S 10550/S 10550
pt 10	10408	10579 10579 10579/S 10579 10579/S 10579 10550 10550/S
pt 10	10408	10530/M 10530 10530/S 10530/P 10530/M 10530/S 10530/S 10530
pt 10	10408	10550/M 10550/S 10550/P 10561 10561 10561 10561
pt 10	10408	10561/S 10561 10561/S 10561 10561 10561 10561/S 10561
pt 10	10408	10561 10561/S 10561 10561 10561 10561 10561 10561
pt 10	10436	10725/S 10725 10725 10725 10725 10725 10725 10725/S
pt 10	10436	10725 10725 10725 10725 10725 10725 10725 10725/S
pt 10	10833	10472/M 10473/S 10524 10561/P 10579/P 10588/M 10598/M 10632/M
pt 10	10866	10633/S 10671/M 10674/M 10676 10676 10677/S 10704/M 10704/S
pt 10	10878	10723 10725/P 10733 10783 10822 10822 10822 10822
pt 10	10833	10848/S 10848/S 10870/S 10870/S 10870/S 10870/S 10870/S 10870/S
pt 10	10866	10868 10868/S 10870/S 10870/S 10870/S 10870/S 10870/S 10870/S
pt 10	10878	10825/S 10825 10825 10825 10825 10825 10825 10825
pt 10	10878	10825 10825 10825 10825 10825 10825 10825 10825/P
pt 10	10878	10825/P 10825/P 10825/P 10825/P 10825/P 10825/P 10825/P 10825/P

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table with columns for identifier, defined on line, and references. Includes entries for pt lo, queue, pva, and queue_head with various alphanumeric codes.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table with columns for identifier, defined on line, and references. Includes entries for queue_id, queue_link, and various alphanumeric codes.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table with columns for identifier, defined on line, and references. Includes entries like 'readying_task_priority' with value 4943 and 'relative_insert_major_dct' with value 7180.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table with columns for identifier, defined on line, and references. Includes entries like 'relative_priority' with value 6863 and 'relative_task_priority' with value 2544.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

		8172/P	8172	8172	8172/P	8172	8227/P	8227	8227
		8227/P	8227	8227	8227/P	8227	8499/P	8499	8499
		8499/P	8499	8499	8499/P	8499	8499/P	8630	8630
		8630/P	8630	8630	8630/P	8630	9406/P	9406	9406
		9406/P	9406	9406	9406/P	9406	9479/P	9479	9479
		9479/P	9479	9479	9479/P	9479	9623/P	9623	9623
		9623/P	9623	9623	9623/P	9623	9647/P	9647	9647
		9647/P	9647	9647	9647/P	9647	10201/P	10201	10201
		10201/P	10201	10201	10201/P	10201	10519/P	10519	10519
		10519/P	10519	10519	10519/P	10519	10550/P	10550	10550
		10550/P	10550	10550	10550/P	10550	10579/P	10579	10579
		10579/P	10579	10579	10579/P	10579	10925/P	10925	10925
		10925/P	10925	10925	10925/P	10925	11139/P	11139	11139
		11139/P	11139	11139	11139/P	11139	11151/P	11151	11151
		11151/P	11151	11151	11151/P	11151			
remove_ijn	7375	7413	8742						
remove_task_from_free_queue	6730	6778	10997						
remove_task_from_seg_lock_q	8182	7914	8256						
req	83	7902	7903	9005	9006	10452/P			
reqcode	4876	7813/M							
request_origin	4888	7815/M	9980	9993	10040				
requested_wait_time	4844	9032	9047						
requested_wait_time	4857	9112	9173						
residence	1403	5246	5293	8222	8232				
residence	5244	5246/M	5250	5251					
residence	5286	5293/M	5293	5293					
residence	8182	8222/M	8222	8222	8232/M	8232	8232		
rindex	43	54	55						
ring	1997	7590	7592	9007	9168				
ring	7564	7588/S	7590	7592	7595/S				
s	8405	8473/M							
save_ptlo	6678	6706/M	6712/S	6714	6717	6720			
save_ptlo	6747	6785/M	6771/S	6773					
save_ptlo	8863	7054/M	7054/S	7054/M	7054/M	7054/S			
save_ptlo	8863	7056/M	7056/S	7056/M	7056/M	7056/S			
save_ptlo	8863	7058/M	7058/S	7058/M	7058/M	7058/S			
save_ptlo	7083	7094/M	7112/S	7118/M	7121/M	7168/S			
save_ptlo	7195	7206/M	7224/S	7233/M	7236/M	7277/S			
save_ptlo	7303	7313/M	7332	7333/S	7339/M	7342/M			
save_ptlo	7766	7854/M	7864/S	7864	7864	7864			
save_ptlo	7766	7866/M	7866/S	7866/M	7866/M	7866/S	7898/M	7898/S	7898/M
save_ptlo	7766	7898/M	7898/S						
save_ptlo	7766	7866/M	7866/S	7866/M	7866/M	7866/S	7898/M	7898/S	7898/M
save_ptlo	7766	7898/M	7898/S						
save_ptlo	7932	7866/M	7866	7866/S	7866/M	7866/M	7898/M	7898	7898/S
save_ptlo	7932	7898/M	7898/S						
save_ptlo	7932	7979/M	7979/S	7979/M	7979/M	7979/S			
save_ptlo	8110	7979/M	7979/S	7979/M	7979/M	7979/S			
save_ptlo	8110	8172/M	8172/S	8172/M	8172/M	8172/S			
save_ptlo	8110	8172/M	8172/S	8172/M	8172/M	8172/S			
save_ptlo	8110	8172/M	8172	8172/S	8172/M	8172/M			

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

save_ptlo	8182	8227/M	8227/S	8227/M	8227/M	8227/S			
save_ptlo	8182	8227/M	8227/S	8227/M	8227/M	8227/S			
save_ptlo	8182	8227/M	8227	8227/S	8227/M	8227/M			
save_ptlo	8381	8499/M	8499/S	8499/M	8499/M	8499/S			
save_ptlo	8381	8499/M	8499/S	8499/M	8499/M	8499/S			
save_ptlo	8381	8499/M	8499	8499/S	8499/M	8499/M			
save_ptlo	8581	8630/M	8630/S	8630/M	8630/M	8630/S			
save_ptlo	8581	8630/M	8630/S	8630/M	8630/M	8630/S			
save_ptlo	8581	8630/M	8630	8630/S	8630/M	8630/M			
save_ptlo	9349	9406/M	9406/S	9406/M	9406/M	9406/S			
save_ptlo	9349	9406/M	9406/S	9406/M	9406/M	9406/S			
save_ptlo	9349	9406/M	9406	9406/S	9406/M	9406/M			
save_ptlo	9349	9417/M	9417/S	9417	9417	9417			
save_ptlo	9446	9479/M	9479/S	9479/M	9479/M	9479/S			
save_ptlo	9446	9479/M	9479/S	9479/M	9479/M	9479/S			
save_ptlo	9446	9479/M	9479	9479/S	9479/M	9479/M			
save_ptlo	9491	9528/M	9528/S	9528	9528	9528	9562/M	9562/S	9562
save_ptlo	9584	9562	9562						
save_ptlo	9584	9623/M	9623/S	9623/M	9623/M	9623/S	9647/M	9647/S	9647/M
save_ptlo	9584	9647/M	9647/S						
save_ptlo	9584	9623/M	9623/S	9623/M	9623/M	9623/S	9647/M	9647/S	9647/M
save_ptlo	9584	9647/M	9647/S						
save_ptlo	9584	9623/M	9623	9623/S	9623/M	9623/M	9647/M	9647	9647/S
save_ptlo	10157	9647/M	9647/M						
save_ptlo	10157	10198/M	10198/S	10198	10198	10198			
save_ptlo	10157	10201/M	10201/S	10201/M	10201/M	10201/S			
save_ptlo	10157	10201/M	10201/S	10201/M	10201/M	10201/S			
save_ptlo	10157	10201/M	10201	10201/S	10201/M	10201/M			
save_ptlo	10408	10519/M	10519/S	10519/M	10519/M	10519/S	10550/M	10550/S	10550/M
save_ptlo	10408	10550/M	10550/S	10579/M	10579/S	10579/M	10579/M	10579/S	
save_ptlo	10408	10519/M	10519/S	10519/M	10519/S	10519/M	10550/M	10550/S	10550/M
save_ptlo	10408	10550/M	10550/S	10579/M	10579/S	10579/M	10579/M	10579/S	
save_ptlo	10408	10519/M	10519	10519/S	10519/M	10519/M	10550/M	10550	10550/S
save_ptlo	10408	10550/M	10550/M	10579/M	10579	10579/S	10579/M	10579/M	
save_ptlo	10879	10725/M	10725/S	10725	10725	10725			
save_ptlo	10879	10925/M	10925/S	10925/M	10925/M	10925/S			
save_ptlo	10879	10925/M	10925/S	10925/M	10925/M	10925/S			
save_ptlo	10879	10925/M	10925	10925/S	10925/M	10925/M			
save_ptlo	10964	10997/M	10997/S	10997					
save_ptlo	11095	11139/M	11139/S	11139/M	11139/M	11139/S	11151/M	11151/S	11151/M
save_ptlo	11095	11151/M	11151/S						
save_ptlo	11095	11139/M	11139/S	11139/M	11139/M	11139/S	11151/M	11151/S	11151/M
save_ptlo	11095	11151/M	11151/S						
save_ptlo	11095	11139/M	11139	11139/S	11139/M	11139/M	11151/M	11151	11151/S
save_ptlo	11095	11151/M	11151/S						
scan_ptlo	6677	11151/M	11151						
scan_ptlo	6748	6704/M	6705	6705	6706	6707/M	6707/S	6708	
scan_ptlo		6763/M	6764	6764	6764	6764	6765	6766/M	6766/S
scan_ptlo		6768							
scan_ptlo	7766	7864/M	7864	7864	7864/M	7864/S	7864	7864	
scan_ptlo	8349	9417/M	9417	9417	9417/M	9417/S	9417	9417	
scan_ptlo	9491	9528/M	9528	9528	9528/M	9528/S	9528	9528	9562/M
scan_ptlo		9562	9562	9562/M	9562/S	9562	9562		
scan_ptlo	10157	10198/M	10198	10198	10198/M	10198/S	10198	10198	

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table with columns for IDENTIFIER, DEFINED ON LINE, and REFERENCES. Rows include search_pt 10 entries for values 9349, 9446, 9448, 9446, 9446, 9584, 9584, 9584, 10157, 10157, 10157, 10408, and search_xcb entries for values 8863, 8863, 8863, 7085, 7197.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

Table with columns for IDENTIFIER, DEFINED ON LINE, and REFERENCES. Rows include search_pt 10 entries for values 10408, 10879, 10879, 10879, 11095, 11095, 11095, 8863, 8863, 8863, 7085, 7197, and search_xcb entries for values 7054, 7054, 7054, 7108, 7197.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

IDENTIFIER	DEFINED ON LINE	REFERENCES
statistics	1300	7051 7866 7879/M 7879 7884/M 7884 7889/M 7889 7894/M 7894 7898 7970/M 7970 7975/M 7975 7979 8163/M 8163 8168/M 8168 8172 8224/M 8224 8227 8250/M 8250 8468/M 8469/M 8499 8630 8642/M 8642 8643/M 8643 8729/M 8729 8739/M 8739 8730/M 8739 8739/M 8739 8739 8739 9139 9249 9406 9479 9623 9647 9685/M 9685 9687/M 9687 9742 10201 10274 10274 10466/M 10466 10466/M 10466 10466 10466 10519 10550/M 10550 10550/M 10550 10550/M 10550 10559/M 10559 10565/M 10565 10567 10578 10579 10910/M 10910 10917/M 10917 10925 10927/M 10927 11122/M 11122 11128/M 11128 11130 11139 11151
status	4856	9102/P 9103
status	4868	9920/M
status	4877	9950/M
status	4935	6493 6532 6551 8623/M 8648/M 8668/M 6991/M 7452 7502 7533 7574 7587 7807 7848 7848 7853 7866/M 7866/M 7869 7878 7878 7882 7882 7890/M 7895/M 7898/M 7898/M 7909 7913 7957 7963 7964 7971/M 7976/M 7979/M 7979/M 8072 8138 8151 8151 8156 8157 8164/M 8169/M 8172/M 8172/M 8225/M 8227/M 8227/M 8251/M 8266 8285 8287/M 8445/M 8499/M 8544/M 8608/M 8616/M 8630/M 8630/M 8741/M 9102 9259 9260 9387 9388 9389 9390 9400 9401/M 9403 9406/M 9406/M 9416 9418/M 9473 9474/M 9477 9479/M 9479/M 9520 9521 9522 9523 9527 9529/M 9554 9555 9556 9557 9561 9563/M 9617 9618/M 9621 9623/M 9623/M 9641 9642/M 9645 9647/M 10197 10201/M 10201/M 10514/M 10519/M 10519/M 10550 10550 10550 10550/M 10550/M 10550/M 10550/M 10560 10563 10563 10564 10576/M 10579/M 10579/M 10581/M 10781/M 10869 10903 10903 10909 10912/M 10916 10920/M 10925/M 11000/M 11086/M 11120/M 11123 11126 11126 11127 11138/M 11139/M 11139/M 11148/M 11151/M
status	4986	8428/P 8429 8445/P 8446
status	5005	8608/P 8609
status	5023	8702/M 8704/P 8717/P 8718
status	5647	5650/M 5651/M
status	5676	5698/P
status	5714	5719/P
status	5854	6496/M 6496/M
status	6467	6489/M 6489/P
status	6505	6534/M 6534/M
status	6508	6529/M 6534/P
status	6567	6604/M 6604/M
status	6571	6601/M 6604/P
status	7416	7452/M 7452/P
status	7416	7452/M 7452/M 7461/M 7461/M
status	7416	7459/P
status	7420	7449/M
status	7472	7452/P 7453 7461/P
status	7517	7505/P
status	7562	7539/P 7577/P

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

IDENTIFIER	DEFINED ON LINE	REFERENCES
status	7618	7668/P 7698/P
status	8381	8445/M 8445/P
status	8381	8445/M 8445/M
status	8581	8608/M 8608/P
status	8581	8608/M 8608/M
status	8682	8704/M 8704/M
status	8682	8739/P
status	8780	8855/P
status	9057	9102/M 9102/P
status	9057	9102/M 9102/M
status	9327	9333/P 9334
status	9349	9373/M 9373/M 9412/M 9412/M 9433/M 9433/M
status	9352	9365/M 9373/P 9412/P 9433/P
status	9491	9543/M 9543/M 9577/M 9577/M
status	9493	9502/M 9543/P 9577/P
status	9586	9595/M
status	9678	9745/P
status	9936	9976/M
status	10408	10466/P
status	10944	10878/M 10886/M 10887/M
str	2667	8427/P 8447/M
stlc_allocation	2517	8853 8854/M
str1	5604	5609/M 5611
str1	9936	10106/M 10106
str2	5605	5610/M 5611/M 5612
str2	9936	10106/M 10106/M 10106
subcode	4878	7814/M 9952
subpriority	3792	10597/M
subreq	10946	10979
subsystem_give_up_cpu	2528	6917/M 6925/M 7866/M 7866/M 7898/M 7898/M 7979/M 7979/M 8172/M 8172/M 8227/M 8227/M 8499/M 8499/M 8630/M 8630/M 8821 8823/M 8905 8907/M 9406/M 9406/M 9479/M 9479/M 9623/M 9623/M 9647/M 9647/M 10201/M 10201/M 10490/M 10519/M 10519/M 10550/M 10550/M 10579/M 10579/M 10925/M 10925/M 11139/M 11139/M 11151/M 11151/M
subsystem_lock_priority_count	2529	6911 6923/M 6923 7863 7866 7866/M 7866 7898 7898/M 7898 7979 7979/M 7979 8172 8172/M 8172 8227 8227/M 8227 8499 8499/M 8499 8622/M 8630 8630/M 8630 8849/M 8926/M 9406 9406/M 9406 9479 8479/M 9479 9623 9623/M 9623 9647 9647/M 9647 10201 10201/M 10201 10486 10487/M 10487 10519 10519/M 10925/M 10925 11139 11139 11139/M 11139 11151 11151/M
subsystem_locks_set	4955	7836 8850/M 8927/M 9426/M
swap_queue_link	1275	11032/M 11033/M 11034/M
swap_status	1269	5693 7459 7505 7539 7577 7668 7698 8465/M 11031/M
swapout_reason	9351	9392
swapping_keypoints	4771	8472
sysmf_system_debugger	3364	6614 8445 8608 10847 10871
sysSrc_update_job_task_enviro	170	7813
sys\$recovery_pt1	10959	10947 10980
sys\$ucr_condition	3455	3466

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

Table with columns: IDENTIFIER, DEFINED ON LINE, REFERENCES. Rows include system_breakpoint_selected, system_flags, system_give_up_cpu, system_locks_set, system_task_id, etc.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

Table with columns: IDENTIFIER, DEFINED ON LINE, REFERENCES. Rows include tail_timeslice_insert, task_created_after_last_swap, task_has_terminated, task_id, task_index, task_is_terminating, task_list_p, task_priority_changed, task_queue, task_rb, task_status, task_time_slice_used, taskid, etc.

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
taskid	8112	8135/M 8136/S 8137/M 8138/P 8172/P
taskid	8183	8227/P 8235
taskid	8259	8266 8266 8266/S 8266 8266/S
taskid	8260	8266/P 8267/S 8268/M 8269/M
taskid	8381	8445/M 8445/M
taskid	8581	8608/M 8608/M
taskid	8581	8635/S 8635
taskid	8682	8742
taskid	9057	9102 9102 9102/S 9102 9102/S
tasks_not_in_long_wait	1647	7879/M 7879 7884/M 7884 8469/M 8643/M 8643 8730/M
		8730 9139 10550/M 10550 10550/M 10550 10566/M 10566/M 10566
		10567 10910/M 10910 10917/M 10917 11128/M 11129 11130
tasks_not_swappable_count	9361	9382/M 9393/M 9393 9432
tasks_not_swappable_count	9500	9510/M 9525/M 9525 9541 9559/M 9559 9575
temp_next_cyclic_aging	8682	8754/M 8754
temp_next_cyclic_aging	9282	9303/M 9304 9307
temp_next_cyclic_aging	9362	9435/M 9436 9439
temp_next_cyclic_aging	9879	9888/M 9889 9892
temp_next_cyclic_aging	10408	10530/M 10530 10530
temp_xcb_p	8781	8840/P 8841 8841 8842/S 8844/S 8846/P
time	10433	10748/M 10750/M 10752 10753/M 10753 10754 10755/M 10756
		10757/M 10759
time_left_in_interval	4796	8739 8739/M 8739 8694 8694/M 8694 8761 9771/M
		9772 10466 10466/M 10466 10774 10774/M 10774
time_next_scan_wait_not_queued	8277	8286
time_spent_in_job_mode	1630	8739/M 8739 8739/M 8739 8739/M 8739 8739 8687/M
		8888 9701/M 9702 9709/M 9709 9742 9930 10274
		10466/M 10466 10466/M 10466 10466/M 10466 10466 10466
time_spent_in_mtr_mode	1631	8739/M 8739 8739/M 8739 8739/M 8739 8739 8685/M
		8686 8698/M 8699 9707/M 9707 9743 9921 10275
		10466/M 10466 10466/M 10466 10466/M 10466 10466 10466
time_used	8682	8739 8739 8739 8739 8739 8739 8739
time_used	9661	9694 9694 9694 9694 9694 9694
time_used	9754	9761 9772 9776 9786 9793 9811
time_used	10408	10466 10466 10466 10466 10466 10466 10774 10774
		10774 10774 10774
timeslice	2543	6893 6895 6897/M 6900/M 6901/M 7866 7866 7866/M
		7866/M 7866/M 7898/M 7898 7898/M 8172 8172 8172/M 8172/M
		7979 7979/M 7979/M 7979/M 8172 8172 8172/M 8496/M 8499
		8172/M 8227 8227/M 8227/M 8227/M 8227/M 8227/M 8499
		8499 8499/M 8499/M 8499/M 8499/M 8624/M 8630 8630 8630/M
		8630/M 8630/M 8739 8739 8739/M 8739/M 8739/M 8739/M 8739/M
		9406 9406 9406/M 9406/M 9406/M 9479 9479 9479/M 9479/M
		9479/M 9479/M 9479/M 9623 9623/M 9623/M 9623/M 9647
		9647 9647/M 9647/M 9647/M 9725 9726 9726/M 9730/M
		9733/M 9735/M 10193/M 10201 10201 10201/M 10201/M 10201/M
		10466 10466 10466/M 10466/M 10466/M 10519 10519
		10519/M 10519/M 10519/M 10550/M 10550/M 10550/M 10550/M
		10550/M 10550/M 10579 10579 10579/M 10579/M 10786
		10922/M 10923/M 10925 10925 10925/M 10925/M 10925/M 11110/M
		11139 11139 11139/M 11139/M 11139/M 11151 11151 11151/M
		11151/M 11151/M
timeslice	6879	6884/M 6893 6895 6897 6900 6901

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----	DEFINED-----	REFERENCES-----
	ON LINE	
timeslice	7766	7866/M 7866 7866 7866 7866 7866 7898/M 7898
		7898 7898 7898 7898
timeslice	7932	7979/M 7979 7979 7979 7979 7979
timeslice	8110	8172/M 8172 8172 8172 8172 8172
timeslice	8182	8227/M 8227 8227 8227 8227 8227
timeslice	8381	8499/M 8499 8499 8499 8499 8499
timeslice	8581	8630/M 8630 8630 8630 8630 8630
timeslice	9349	9406/M 9406 9406 9406 9406 9406
timeslice	9446	9479/M 9479 9479 9479 9479 9479
timeslice	9584	9623/M 9623 9623 9623 9623 9647/M 9647
		9647 9647 9647 9647
timeslice	10157	10201/M 10201 10201 10201 10201 10201 10201
timeslice	10408	10519/M 10519 10519 10519 10519 10519 10550/M 10550
		10550 10550 10550 10550 10579/M 10579 10579 10579
		10579 10579
timeslice	10879	10925/M 10925 10925 10925 10925 10925
timeslice	11095	11139/M 11139 11139 11139 11139 11139 11139 11151/M 11151
		11151 11151 11151
tmc\$	5038	5044 5047 5050 5053 5056 5059 5062 5065
		5068 5071 5074 5077 5080 5083 5086 5089
tmc\$broken_task_fault_id	3383	3433
tmc\$btc_inValid_a0	3481	3502
tmc\$btc_inValid_p	3481	3502
tmc\$btc_mcr_traps_disabled	3482	3503
tmc\$btc_mf_traps_disabled	3481	3501
tmc\$btc_mtr_fault_buffer_full	3480	3501
tmc\$btc_system_error	3483	3497
tmc\$btc_ucr_traps_disabled	3482	3503
tmc\$cpo_interactive_command	4914	7815 10035 10040
tmc\$cpo_interrupt_restore	4915	10048
tmc\$cpo_operator	4910	9981 9993
tmc\$cpo_recovery	4916	9981
tmc\$cpo_save_swap_file_sfId	4912	10035
tmc\$cpo_set_job_unswappable	4913	10022
tmc\$cpo_user	4911	10013
tmc\$cycle_reason	619	613 6376
tmc\$dummy_fault	3384	3439
tmc\$flag_available_31	3618	3622
tmc\$fnx_continue	4824	7657 8846/P 8923/P 10207/P 10243/P
tmc\$fnx_job	4824	7670 8839/P 8917/P 10005/P 10020/P 10033/P 10047/P 10071/P
		10393/P
tmc\$fnx_system	4824	7659 7683
tmc\$ir_dm_system_tasks	4837	9512 9609 10094/P 10097/P
tmc\$ir_other_system_tasks	4837	10088/P 10091/P
tmc\$is_idle_initiated	3779	8614 8753 9394 9526 9560 10527
tmc\$is_idled	3779	7837 7888 7893 7969 7974 8162 8167 8217
		8533 8754 8754 9298 9399 9422 9472 9533
		9567 9616 9640 9883 9883 10528 10530 10530
		10550 10575 10907 11007
tmc\$is_idled_sched_notified	3780	7891 7973 8165 8252 8615 9420 9531 9565
		10550 10582 10908
tmc\$is_not_idled	3779	8629 8445 8808 9409 9482 9626 9650 10080
		10569 11132

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IDENTIFIER	DEFINED ON LINE	REFERENCES
tmc\$maximum_monitor_faults	3388	3379
tmc\$maximum_pt1	4922	6338 6344
tmc\$maximum_signals	3598	3595
tmc\$maximum_system_task_id	3631	3634
tmc\$smcr_fault	3383	3435
tmc\$opt_return	4950	6495 6533 7452/P 7452 9102/P 9102
tmc\$src_ready_conditional_wi	3782	7921 9195/P 10453/P 11020/P
tmc\$signal_available_63	3580	3591
tmc\$stid_administer_top	3640	9516 9550 9613 9637
tmc\$stid_dm_split_a1	3841	9517 9551 9614 9638
tmc\$stid_null_task	3637	3634
tmc\$stid_volume_space_managemnt	3842	9518 9552 9615 9639
tmc\$sts_executing	3770	7587 7848 7964 8157 9387 9520 9554 10781
tmc\$sts_first_external_queue	3757	7749 8078 8156
tmc\$sts_first_ready_uncond	3756	7849
tmc\$sts_first_status_in_wait_q	3753	9259
tmc\$sts_io_wait_not_queued	3774	3756 7753 7960
tmc\$sts_io_wait_queued	3777	8099 8151 8152
tmc\$sts_last_status_in_dct	3752	9403 9416 9477 9527 9561 9621 9645 10197
tmc\$sts_last_status_in_wait_q	3754	10550 10556 10903 11119 9523 9557 10550 10560 10903 11123
tmc\$sts_null	3761	6493 8532 8551 6648 7452 7533 7574
		7750 7807 7850 7957 7967 8018 8072 8078
		8138 8160 8266 8544 8741 9102 9154 9161
tmc\$sts_page_wait	3776	9164 9167 9181 10480 10558 10869 11086 11121
tmc\$sts_ready	3761	3757 8099 8151 8152
		3752 6623 6968 7853 7866 7895 7898 7976
		7979 8169 8172 8225 8227 8445 8499 8608
		8630 9401 9406 9474 9479 9518 9623 9642
		9647 10201 10514 10519 10550 10576 10579
tmc\$sts_ready_and_selected	3763	10920 10925 11138 11139 11148 11151
		6991 7848 7866 7898 7979 8172 8227 8499
		8630 9388 9406 9479 9521 9555 9623 9647
tmc\$sts_ready_but_swapped	3772	10201 10519 10550 10579 10925 11139 11151
		7869 7890 7909 7971 8164 8251 8616 9400
		9418 9473 9529 9563 9617 9641 10550 10581
tmc\$sts_segment_lock_wait	3776	10912 7913
tmc\$sts_timed_wait_not_queued	3768	3755 7883 8285 9038 9118 9138 10564 11127
tmc\$sts_timeout_reqexp_inflong	3771	9042 9122 11000
tmc\$sts_timeout_reqexp_inflong	3771	7882 8044 9124 9136 9391 10564 11127
tmc\$sts_timeout_reqexp_longlong	3766	9034 8114 9390
tmc\$sts_timeout_reqexp_longvlong	3766	3754 7878 8287 9036 9116 9137 10550 10550
tmc\$sts_timeout_reqexp_shortshrt	3765	10563 10909 10916 11126
tmc\$ujte_dispatchering_priority	4878	3753 8019 8022 8929 9523 9557
tmc\$ujte_expand_pt1	4878	4887 7814 9868
tmc\$ujte_idle_dm_sys_tasks	4880	4892 10099
tmc\$ujte_idle_other_sys_tasks	4879	4895 10093
tmc\$ujte_restart_dm_systasks	4880	4894 10087
tmc\$ujte_restart_other_systasks	4879	4895 10096
tmc\$ujte_set_non_swappable	4879	4894 10090
tmc\$ujte_set_task_terminating	4881	4894 10079

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IDENTIFIER	DEFINED ON LINE	REFERENCES
tmc\$ujte_update_debug_masks	4880	4895 10120
tmc\$ujte_xp_register	4878	4882 9953
tmc\$wi_null	4961	9180 10804
tmc\$wi_wait_inhibited	4961	7924 9143 9163 10801
tmc\$wi_wait_selected	4962	7922 9171
tmc\$wi_wait_selected_r3	4962	7923 9169 10848
tme\$invalid_global_taskid	5044	6496/P 6534/P 7452/P 8704/P 9102/P
tme\$job_swapped_out	5056	7461/P
tme\$pt1_full	5047	6604/P 8445/P 8608/P 10987
tmk\$monitor_base	839	5121 5124 5127 5130 5133 5136 5142
		5145 5148 5151 5154 5157 5160 5164 5167
		5170 5173 5177 5181 5184 5187 5190 5193
		5196 5199 5202 5205 5208 5211 5214 5217
		5220 5223 5226
tmp\$switch_task	5173	10441 10524 10822
tmp\$assign_pt1	6567	6637 8445 8808
tmp\$calculate_dct_priority_int	9852	6694 6979 7014 7864 7866 7866 7898 7898
		7979 8172 8227 8227 8499 8499
		8630 8630 8739 8739 8406 9406 9417
		9479 9479 9528 9562 9623 9623 9647 9647
		9694 9694 9894 9766 9781 9806 9868 10198
		10201 10201 10466 10466 10466 10519 10519 10550
		10550 10579 10579 10725 10774 10774 10774 10925
		10925 11139 11139 11151 11151
tmp\$cause_task_switch	8011	8041
tmp\$check_for_swapout_candidate	9217	9273
tmp\$check_pt1_lock	5668	5673
tmp\$check_taskid	6464	6503
tmp\$check_taskid_with_lock_set	6505	6540
tmp\$check_timed_wait_not_queued	8276	8294 7452 9102
tmp\$clear_lock	6444	6457 6494 6502 7454 7466 7512 7547 7599
		7725 7757 7927 7983 8039 8104 8176 8270
		8292 8451 8508 8553 8610 8677 8756 8870
		8961 9010 9028 9048 9104 9211 9250 9257
		9271 9339 9486 9539 9573 9631 9655 9975
		10137 10141 10210 10246 10523 10589 10820 10988
		11022 11155
tmp\$create_job	8381	8510
tmp\$create_task	8581	8679
tmp\$cycle	8875	8963
tmp\$dct_ready_task	6863	7064 7866 7898 7979 8172 8227 8499 8630
		8406 9479 9623 9647 10201 10518 10550 10579
		10925 11139 11151
tmp\$delay	8986	9055
tmp\$dequeue_task	8110	8178
tmp\$exit_job	8513	8578
tmp\$fetch_task_statistics	9900	9932
tmp\$find_next_queued_task	8259	8272
tmp\$find_next_xcb	7618	7727 8839 8846 8917 8923 10171 10207 10234
		10243
tmp\$find_xcb	7416	7468
tmp\$free_unrecovered_tasks	11068	11091
tmp\$get_top_of_stack	7562	7601

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES

IDENTIFIER	DEFINED	REFERENCES
tmp\$get_xcb_access_status	5701	7459 7505 7539 7577 7668 7698
tmp\$get_xcb_p	7472	7514
tmp\$get_xcb_p_from_pt10	7607	7614 9255 10515 11149
tmp\$idle_non_dispatchable_job	9322	9341
tmp\$idle_tasks_in_job	9349	9333 9443
tmp\$idle_tasks_in_system_job	9491	9581 10088 10094
tmp\$insert_timed_wait_queue	8298	8288 8351 11124
tmp\$job_recovery_requests	10984	11058
tmp\$smtr_begin_lock_activity	8761	8767
tmp\$smtr_end_lock_activity	8770	8872
tmp\$smtr_update_job_task_enviro	9936	7821 10143
tmp\$smtr_wait	9057	9214
tmp\$obtain_ijkl_ordinal_from_pt1	6558	6564
tmp\$queue_task	8044	8106
tmp\$reissue_monitor_request	7988	8008 10083
tmp\$remove_task_from_dct	6660	6726 7864 9417 9528 9562 10198 10725
tmp\$remove_task_from_q	8358	7877 8223 8249 8377
tmp\$reset_dispatching_control	10382	8739 9717 9997 10064 10404 10466
tmp\$restart_idled_tasks	9446	9488
tmp\$restart_tasks_in_system_job	9584	9658 10091 10097
tmp\$send_signal	5757	8717
tmp\$set_lock	6421	6439 6490 7450 7501 7529 7573 7653 7748
		7806 7956 8017 8070 8134 8265 8282 8444
		8532 8607 8727 8783 8897 9004 9100 9247
		9329 9469 9504 9607 9951 10170 10233 10461
		10593 10981 11107
tmp\$set_monitor_flag	5751	8855
tmp\$set_swapout_candidate	9278	9268 9312 10571 11134
tmp\$set_system_flag	5744	8739 9745 10466
tmp\$set_task_ready	7766	7929 9194 10452 11020
tmp\$set_task_ready_uncond	7932	7985
tmp\$set_task_wait	7730	7763
tmp\$set_up_debug_registers	10832	10854
tmp\$stop_if_bad_taskid	6544	6555 7502 7574 7807 7957 8072 8138 8266
tmp\$switch_task	10408	10824
tmp\$switch_task_from_failing_cp	11095	11157
tmp\$task_exit	8682	8758
tmp\$test_get_xcb_p	7517	7549
tmp\$update_debug_registers	10863	10121 10875
tmp\$update_job_task_cpu_selects	10224	10248
tmp\$update_job_task_environment	10157	10005
tmp\$update_system_task_list	5764	8713 10020 10033 10047 10071 10212 10393
tmp\$broken_task_condition	3480	3496
tmp\$broken_task_monitor_fault	3494	3434
tmp\$change_priority_origin	4910	4888
tmp\$cpu_execution_statistics	4780	6329
tmp\$dct_entry	5099	5106 6676 6746 6872 7072 7184 7293
tmp\$dispatch_control	3723	1159
tmp\$dispatch_control_table	5106	6366
tmp\$dispatching_control_sets	4814	6333 9830 9855 10429
tmp\$dispatching_controls	4785	6332
tmp\$dispatching_prio_controls	4795	4790 6334
tmp\$dispatching_priority_time	4800	4797

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IDENTIFIER-----DEFINED-----REFERENCES

IDENTIFIER	DEFINED	REFERENCES
tmt\$dual_state_dispatch_prio	3786	6335 6335
tmt\$dual_state_priority_entry	3790	1146 3787
tmt\$find_next_xcb_state	4826	7622 8779 8891 10168 10231
tmt\$fnx_search_type	4824	4827 7619 10160 10227
tmt\$idle_resume_sys_task_kind	4837	9492 9585
tmt\$idle_status	3779	4937
tmt\$smcr_faults	3519	3436
tmt\$monitor_fault_buffer	3373	2547
tmt\$monitor_fault_buffers	3379	3374 3375 3376
tmt\$monitor_fault_identifiers	3382	3432 3508
tmt\$option	4950	6466 6507
tmt\$primary_task_list	4948	4893 6355
tmt\$primary_task_list_entry	4930	4948 6595 6679 6749 6874 6878 7801 7953
		8065 8131 8185 8263 8359 8997 9091 10165
		10435 10881 10975 11101
tmt\$pt1_flags	4954	4941
tmt\$pt1_lock	4734	6352 6352 6421 6444
tmt\$rb_cycle	611	8876
tmt\$rb_delay	4841	8600 8967
tmt\$rb_exit_job	4995	8514
tmt\$rb_fetch_task_statistics	4866	9901
tmt\$rb_initiate_job	4984	8382
tmt\$rb_initiate_task	5003	8582
tmt\$rb_ready_task	93	83
tmt\$rb_task_exit	5021	8683
tmt\$rb_update_job_task_enviro	4875	7800 9937
tmt\$rb_wait	4854	4862
tmt\$rb_wait_signal	4862	9058
tmt\$ready_condition	3782	7769
tmt\$signal	3536	3531
tmt\$signal_buffer	3528	2548
tmt\$signal_buffers	3595	3529 3530 3531
tmt\$system_flags	3601	2523 4940 6628 8445 8608 9027 9160 10133
		10796 10798 11005
tmt\$system_task_id	3634	2514
tmt\$task_queue_link	2169	2138 4011 4938 6343 6343 8047 8111 8209
		8210 8360
tmt\$task_status	3761	3726 4935 4936 6377 7731 7934 8046 9090
tmt\$time_limits	4802	4800
tmt\$wait_inhibited	4961	4956
tmt\$xcb_offset_size	4952	4933
tmv\$cpu_execution_statistics	6328	8739/M 8739/M 8739/M 8739/M 9698/M 9699 9701/M 9702
		10466/M 10466 10466/M 10466
tmv\$cycle_delay_time	6347	8023 8928 8930
tmv\$dct	6366	6602 6608/M 6651 6652/M 6654/S 6656/M 6682 6724/M
		6752 6776/M 6969 6984/M 7004 7015/M 7062/M 7664
		7864/M 7866 7866/M 7866 7866/M 7898 7898/M 7898/M
		7898 7898/M 7898/M 7979 7979/M 7979 7979/M 7979/M
		8172 8172/M 8172 8172/M 8227 8227/M 8227
		8227/M 8227/M 8445 8445/M 8499 8499/M 8499 8499/M
		8499/M 8544 8544/S 8544/M 8544/M 8608 8608/M 8630
		8630/M 8630 8630/M 8630/M 8741 8741/M 8741/S 8741/M
		9406 9406/M 9406 9406/M 9406/M 9417 9417/M 9479

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IDENTIFIER-----	DEFINED ON LINE	REFERENCES								
xcb_p	9224	9255/P	9256							
xcb_p	9349	9406	9406	9406/M	9406/M	9406/M	9406	9406	9406	9406
		9406/P	9406/P	9406/M	9406/M	9406	9406/M	9406/M	9406	
xcb_p	9363	9385/M	9386	9388	9406/P	9425				
xcb_p	9446	9479	9479	9479/M	9479/M	9479/M	9479	9479	9479	9479
		9479/P	9479/P	9479/M	9479/M	9479	9479/M	9479/M	9479	
xcb_p	9454	9471/M	9478/P							
xcb_p	9497	9515/M	9516	9517	9518	9519	9522	9549/M	9550	
		9551	9552	9553						
xcb_p	9584	9623	9623	9623/M	9623/M	9623/M	9623	9623	9623	9623
		9623/P	9623/P	9623/M	9623/M	9623	9623/M	9623/M	9623	
		9647	9647	9647/P	9647	9647	9647/M	9647/M	9647/M	9647
xcb_p	9592	9647/M	9647/M	9647	9647/P	9647/P	9647/P			
		9612/M	9613	9614	9615	9623/P	9636/M	9637	9638	
		9639	9647/P							
xcb_p	10157	10201	10201	10201/M	10201/M	10201/M	10201	10201	10201	10201
		10201/P	10201/P	10201/M	10201/M	10201	10201/M	10201/M	10201	
xcb_p	10166	10171/P	10172	10176	10176	10177	10178	10180	10183	
		10190/M	10191	10193/M	10196	10199	10201/P	10203	10207/P	
xcb_p	10230	10234/P	10235	10239	10239	10240	10241/M	10243/P		
xcb_p	10408	10516/M								
xcb_p	10408	10519	10519	10519/M	10519/M	10519/M	10519	10519	10519	10519
		10519/P	10519/P	10519/M	10519/M	10519	10519/M	10519/M	10519	
		10550/M	10550/M	10550/P	10550	10550	10550/M	10550/M	10550/M	10550
		10579/M	10579/M	10579/M	10579	10579	10579	10579	10579	10579
xcb_p	10408	10579/M	10579/M	10579	10579/M	10579/M	10579	10579/P	10579/P	
xcb_p	10434	10459/M	10463	10469/M	10479	10482	10485/M	10486	10487/M	
		10487	10490/M	10554	10579/P	10642	10639/M	10644	10645	
		10649	10650	10682/M	10685	10687	10689	10690	10695	
		10696	10785	10786	10791/M	10791	10793/M	10793	10797/M	
xcb_p	10835	10797	10802/M	10809						
		10840	10840	10841/M	10842/M	10844/M	10845	10846/M	10846	
xcb_p	10879	10847/M	10847	10849/M	10849					
		10925	10925	10925/M	10925/M	10925/M	10925	10925/M	10925	
		10925/P	10925/P	10925/M	10925/M	10925	10925/M	10925/M	10925	
xcb_p	10889	10921/M	10922/M	10923/M	10925/P					
xcb_p	11095	11139	11139	11139/M	11139/M	11139/M	11139	11139	11139	11139
		11139/P	11139/P	11139/M	11139/M	11139	11139/M	11139/M	11139	
		11151	11151	11151	11151	11151	11151/M	11151/M	11151/M	11151
		11151/M	11151/M	11151	11151/P	11151/P	11151/P			
xcb_p	11095	11150/M								
xcb_p	11103	11105/M								
xcb_search	10160	10171/P	11109	11110/M	11113/M	11139/P				

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IDENTIFIER-----	DEFINED ON LINE	REFERENCES								
xcb_search	10227	10234/P								
xp	2509	7590	7592	7593	7595	8006/M	8006	8219	8484/M	
		8484	8486/M	8486	8488/M	8669/M	8669	8671/M	8671	
		8673/M	8733	8733	9007	9021/M	9021	9155/M	9155	
		9168	9924	9924	9956	9956	9963/M	9964/M	10791/M	
		10791	10840	10840	10841/M	10842/M	10844/M	10845	10846/M	
		10846	10849/M	10849						

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```

2 MODULE tmm$mtr_flag_signal_functions;
3
4 {
5 { PURPOSE:
6 {   This module processes flag and signal handling job mode monitor requests
7 {   and monitor mode requests.
8 {
9
o 1025 { Define arbitrary constant that is used to check if there is enough space in SFSA of current
o 1026 { task to terminate it.
o 1027
o 1028   CONST
o 1029     task_termination_stack_area = 30000;
o 1030
o 1031
o 1032 {Define constants for recognizing hung tasks.
o 1033
o 1034   VAR
o 1035     tmv$halt_on_hung_task: [XDCL, #GATE] boolean := FALSE,
o 1036     tmv$system_debug_ring: [XDCL, #GATE] integer := 0,
o 1037     tmv$system_debug_segment: [XDCL, #GATE] integer := 0,
o 1038     tmv$job_deBug_ring_p: [XDCL, #GATE] Aost$ring := NIL,
o 1039     tmv$system_error_hang_count: [XDCL, #GATE] 0..0ffffff(16) := 6;
o 1040
o 1041 {External procedures called by tmp$mtr_task_manager_functions.
o
o 1043
o 1044   PROCEDURE [XREF] dpp$display_error
o 1045   (   line: string ( * (<= dpc$top_line_message_size));
o 1046
o 1058
o 1059   PROCEDURE [INLINE] jmp$unlock_ajl
o 1060   (   ijle_p: ^ajmt$initiated_job_list_entry);
o 1061
o 1062   VAR
o 1063     ajlo: jmt$ajl_ordinal;
o 1064
o 1065     tmp$set_lock (tmv$ptl_lock);
o 1066     ajlo := ijle_p^.ajl_ordinal;
o 1067     IF [jmv$ajl_p^ [ajlo].in_use = jmc$lock_ajl] THEN
o 1068       jmp$free_ajl_with_lock (ijle_p, jmc$lock_ajl);
o 1069     ELSE
o 1070       jmv$ajl_p^ [ajlo].in_use := jmv$ajl_p^ [ajlo].in_use - jmc$lock_ajl;
o 1071     IFEND;
o 1072     tmp$clear_lock (tmv$ptl_lock);
o 1073
o 1074   PROCEND jmp$unlock_ajl;
o 1075
o 2238
o 2240   PROCEDURE [INLINE] jmp$unlock_ajl_with_lock
o 2241   (   ijle_p: ^ajmt$initiated_job_list_entry);
o 2242
o 2243   VAR

```

```

o 2244     ajlo: jmt$ajl_ordinal;
o 2245
o 2246     ajlo := ijle_p^.ajl_ordinal;
o 2247     IF [jmv$ajl_p^ [ajlo].in_use = jmc$lock_ajl] THEN
o 2248       jmp$free_ajl_with_lock (ijle_p, jmc$lock_ajl);
o 2249     ELSE
o 2250       jmv$ajl_p^ [ajlo].in_use := jmv$ajl_p^ [ajlo].in_use - jmc$lock_ajl;
o 2251     IFEND;
o 2252
o 2253   PROCEND jmp$unlock_ajl_with_lock;
o 2254
o 2258
o 2259   PROCEDURE [XREF] tmp$delay (VAR rb: tmt$rb_delay;
o 2260     cst_p: ^ost$cpu_state_table);
o 2261
o 3587
o 3588   PROCEDURE [XREF] tmp$check_taskid (taskid: ost$global_task_id;
o 3589     option: tmt$option;
o 3590     VAR status: syt$monitor_status);
o 3591
o 3641
o 3642   PROCEDURE [XREF] tmp$check_taskid_with_lock_set
o 3643   (   taskid: ost$global_task_id;
o 3644     option: tmt$option;
o 3645     VAR status: syt$monitor_status);
o 3646
o 3649
o 3650   PROCEDURE [XREF] tmp$set_task_ready (task_id: ost$global_task_id;
o 3651     readying_task_priority: jmt$dispatching_priority;
o 3652     ready_condition: tmt$ready_condition);
o 3653
o 3656
o 3657   PROCEDURE [XREF] tmp$find_xcb (taskid: ost$global_task_id;
o 3658     VAR xcb_p: ^ost$execution_control_block;
o 3659     VAR ijle_p: ^ajmt$initiated_job_list_entry;
o 3660     VAR status: syt$monitor_status);
o 3661
o 3666   PROCEDURE [XREF] tmp$get_xcb_p (task_id: ost$global_task_id;
o 3667     VAR xcb_p: ^ost$execution_control_block;
o 3668     VAR ijle_p: ^ajmt$initiated_job_list_entry);
o 3669
o 3672   PROCEDURE [XREF] mmp$fetch_stack_segment_info (xcb_p: ^ost$execution_control_block;
o 3673     ring: ost$valid_ring;
o 3674     set_length_to_zero: boolean;
o 3675     VAR stack_segment_number: ost$segment;
o 3676     VAR maximum_segment_length: ost$segment_length;
o 3677     VAR found: boolean);
o 3680   PROCEDURE [INLINE] mtp$scst_p (VAR cst_p: ^ost$cpu_state_table);
o 3691
o 3692
o 3693 { PURPOSE:   procedure mtp$error_stop
o 3694 {   Prefixes 'ERR:VE0S1000-' to the string and calls mtp$step_unstep_system to write string and step system}
o 3695
o 3696   PROCEDURE [XREF] mtp$error_stop (text: string(*=<=63) );
o 3697
o 3698   PROCEDURE [INLINE] mtp$set_status_abnormal (identifier: string (2);
o 3699     condition: osc$max_status_condition_number + 1 .. 0xffffffff(16);

```

```

3700     VAR status: syt$monitor_status);
3701
0 3711
0 3712 { PURPOSE: procedure mtp$step_unstep_system
0 3713 {   Writes a line of text to the Top Line of the console. If the idle code specified indicates an error
0 3714 {   (i.e. not an operator command or a recovery), then 'ERR' must be first 4 characters or it will be
0 3715 {   prefixed. Then the system is Terminated, Aborted, Stopped, or Idled per the idle code specified.
0 3716 {   All messages sent to the Top Line must have a VEDSxxxx code in the text. Guidelines for using
0 3717 {   VEDSxxxx codes and a list of all VEDSxxxx codes are documented in the comments found in the proc
0 3718 {   dpp$display_error which is in the deck dpm$system_console_monitor.
0 3719
0 3720 PROCEDURE [XREF] mtp$step_unstep_system
0 3721     (term_code: syt$180_idle_code; text: string(*<=76) );
0 3722

3726 {External variables.
3727
3728 {If a MCR fault occurs in a task executing in a ring <= the value
3729 {of this variable, Monitor will halt the CPU. The variable is located
3730 {in Monitor and is intended to be used for debug only.
3731
3732     VAR
3733     mtv$system_haltring: [XREF] 0 .. 255,
3734     mtv$halt_cpu_ring_number: [XREF] 0 .. 255;
3735
3736     VAR
3737     tmv$pt1_p: [XREF] ^tmt$primary_task_list;
3738
0 3741 {Define SMU Communications Block (SCB).
0 3742
0 3743     VAR
0 3744     mtv$scb: [XREF] mtt$smu_communications_block;
0 3880

```

```

0 3883
0 3884 PROCEDURE [XDCL] tmp$set_monitor_flag
0 3885     ( task_id {input} : ost$global_task_id;
0 3886     flag_id {input} : syt$monitor_flag;
0 3887     VAR status {output} : syt$monitor_status);
0 3888
0 3889
0 3890     VAR
0 3891     cst_p: ^ost$cpu_state_table;
0 3892
0 3893     #KEYPOINT (osk$debug, osk$m * task_id.index, tmk$set_monitor_flag);
0 3894
10 3895     status.normal := TRUE;
10 3896     mtp$cst_p (cst_p);
2A 3897
2A 3898 { If it is the current task, set the free flag in UCR.
2A 3899
2A 3900     tmp$set_lock (tmv$pt1_lock);
70 3901
70 3902     tmp$check_taskid_with_lock_set (task_id, tmc$opt_return, status);
8C 3903     IF NOT status.normal THEN
94 3904         tmp$clear_lock (tmv$pt1_lock);
CC 3905         RETURN;
CE 3906     IFEND;
CE 3907
CE 3908     IF task_id = cst_p^.taskid THEN
DE 3909         cst_p^.xcb_p^.xp.user_condition_register := cst_p^.xcb_p^.xp.
DE 3910         user_condition_register + $ost$user_conditions [osc$free_flag];
DE 3911         cst_p^.xcb_p^.monitor_flags := cst_p^.xcb_p^.monitor_flags + $syt$monitor_flags [flag_id];
DE 3912         IF tmv$pt1_p [task_id.index].new_task_status < tmc$ts_first_ready_uncond THEN
124 3913             tmv$pt1_p [task_id.index].new_task_status := tmc$ts_null;
128 3914             IFEND;
128 3915             tmp$clear_lock (tmv$pt1_lock);
15E 3916             RETURN;
160 3917             IFEND;
160 3918
160 3919     IF flag_id IN tmv$pt1_p [task_id.index].monitor_flags THEN
184 3920         tmp$clear_lock (tmv$pt1_lock);
18C 3921         RETURN;
18E 3922         IFEND;
18E 3923
18E 3924 { Set monitor flag in PTL.
18E 3925
18E 3926     tmv$pt1_p [task_id.index].monitor_flags := tmv$pt1_p [task_id.index].
18E 3927     monitor_flags + $syt$monitor_flags [flag_id];
18E 3928     tmp$set_task_ready (task_id, 0 [reading_task_priority], tmc$rc_ready_conditional);
200 3929     tmp$clear_lock (tmv$pt1_lock);
236 3930
236 3931 PROCEND tmp$set_monitor_flag;

```

```

0 3933
0 3934 PROCEDURE [XDCL] tmp$set_system_flag
0 3935 ( task_id [input] : ost$global_task_id;
0 3936 flag_id [input] : ost$system_flag;
0 3937 VAR status [output] : syt$monitor_status);
0 3938
0 3939 {
0 3940 { The purpose of this request is to set a system flag in the XCB of the task
0 3941 { specified. System flags can be set in the XCB of any valid task including
0 3942 { tasks currently swapped out. If the specified task is swapped out, swapin
0 3943 { will be initiated by this request.
0 3944 {
0 3945 { TMP$SET_SYSTEM_FLAG (TASKID, FLAGID, STATUS)
0 3946 {
0 3947 { TASKID: (input) This parameter specifies the taskid of the task to receive the
0 3948 { flag.
0 3949 {
0 3950 { FLAGID: (input) This parameter specifies the flag id to be set.
0 3951 {
0 3952 { STATUS: (output) This parameter specifies the request status.
0 3953 {
0 3954 {
0 3955 {
0 3956 VAR
0 3957 cst_p: ^ost$cpu_state_table,
0 3958 ijle_p: ^jmt$initiated_job_list_entry,
0 3959 xcb_p: ^ost$execution_control_block;
0 3960
0 3961 #KEYPOINT (osk$debug, osk$m * task_id.index, tmk$set_system_flag);
10 3962
10 3963 status.normal := TRUE;
10 3964 tmp$cst_p (cst_p);
2A 3965
2A 3966 { If it is the current task, set the free flag in UCR.
2A 3967
2A 3968 tmp$set_lock (tmv$pt1_lock);
70 3969
70 3970 tmp$check_taskid_with_lock_set (task_id, tmc$opt_return, status);
8C 3971 IF NOT status.normal THEN
9A 3972 tmp$clear_lock (tmv$pt1_lock);
CC 3973 RETURN;
CE 3974 IFEND;
CE 3975
CE 3976 IF task_id = cst_p^.taskid THEN
DE 3977 IF cst_p^.xcb_p^.task_is_terminating THEN
EA 3978 tmp$set_status_abnormal ('TM', tme$invalid_global_taskid, status);
100 3979 ELSE
100 3980 cst_p^.xcb_p^.xp.user_condition_register := cst_p^.xcb_p^.xp.
100 3981 user_condition_register + $ost$user_conditions [osc$free_flag];
100 3982 cst_p^.xcb_p^.system_flags := cst_p^.xcb_p^.system_flags + $tmt$system_flags [flag_id];
100 3983 cst_p^.xcb_p^.monitor_flags := cst_p^.xcb_p^.monitor_flags +
100 3984 $syt$monitor_flags [tmc$mf_cause_job_free_flag_trap];
100 3985 cst_p^.xcb_p^.wait_inhibited := TRUE;
100 3986 IF tmv$pt1_p^ [task_id.index].new_task_status < tmc$ts_first_ready_uncond THEN
15A 3987 tmv$pt1_p^ [task_id.index].new_task_status := tmc$ts_null;
15A 3988 IFEND;
15A 3989

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```

15A 3990 tmp$clear_lock (tmv$pt1_lock);
190 3991 RETURN;
192 3992 IFEND;
192 3993
192 3994 IF flag_id IN tmv$pt1_p^ [task_id.index].system_flags THEN
1B6 3995 tmp$clear_lock (tmv$pt1_lock);
1EE 3996 RETURN;
1FO 3997 IFEND;
1FO 3998
1FO 3999 tmp$get_xcb_p (task_id, xcb_p, ijle_p);
214 4000 IF (xcb_p <> NIL) AND xcb_p^.task_is_terminating THEN
22A 4001 status.normal := FALSE;
22A 4002 status.condition := tme$invalid_global_taskid;
22A 4003 jmp$unlock_adj_with_lock (ijle_p);
282 4004 tmp$clear_lock (tmv$pt1_lock);
288 4005 RETURN;
28A 4006 IFEND;
28A 4007 IF (xcb_p <> NIL) THEN
2CA 4008 jmp$unlock_adj_with_lock (ijle_p);
30A 4009 IFEND;
30A 4010
30A 4011 { Set system flag in PTL.
30A 4012
30A 4013 tmv$pt1_p^ [task_id.index].system_flags := tmv$pt1_p^ [task_id.index].
30A 4014 system_flags + $tmt$system_flags [flag_id];
30A 4015 IF (tmv$pt1_p^ [task_id.index].ptl_flags.wait_inhibited <> tmc$wi_wait_selected_r3) THEN
33E 4016 tmv$pt1_p^ [task_id.index].monitor_flags := tmv$pt1_p^ [task_id.index].
34C 4017 monitor_flags + $syt$monitor_flags [tmc$mf_cause_job_free_flag_trap];
34C 4018 IFEND;
34C 4019 tmp$set_task_ready (task_id, 0 [readying_task_priority], tmc$src_ready_conditional_wi);
368 4020 tmp$clear_lock (tmv$pt1_lock);
39E 4021
39E 4022 PROCEND tmp$set_system_flag;

```

```

O 4024
O 4025 PROCEDURE [XDCL] tmp$flag_all_tasks
O 4026 {   flag_id {input} : ost$system_flag;
O 4027   VAR status {output} : syt$monitor_status};
O 4028
O 4029 {
O 4030 {   The purpose of this request is to set a system flag in the XCB of every task
O 4031 {   in the system. System flags can be set in the XCB of any valid task including
O 4032 {   tasks currently swapped out. If the specified task is swapped out, swapin
O 4033 {   will be initiated by this request.
O 4034 {
O 4035 {   TMP$FLAG_ALL_TASKS (FLAG_ID, STATUS)
O 4036 {
O 4037 {   FLAG_ID: {input} This parameter specifies the flag id to be set.
O 4038 {
O 4039 {   STATUS: {output} This parameter specifies the request status.
O 4040 {
O 4041
O 4042   VAR
O 4043     i: ost$task_index,
O 4044     task_id: ost$global_task_id,
O 4045     cst_p: ^ost$cpu_state_table;
O 4046
O 4047     status.normal := TRUE;
O 4048
O 4049     tmp$set_lock (tmv$ptl_lock);
46 4050     FOR i := 1 TO UPPERBOUND (tmv$ptl_p^ ) DO
5A 4051       IF tmv$ptl_p^ [i].status <> tmc$ts_null THEN
6A 4052
6A 4053         task_id.index := i;
6A 4054         task_id.seqno := tmv$ptl_p^ [i].sequence_number;
6A 4055
6A 4056 {   If it is the current task, set the free flag in UCR.
6A 4057
6A 4058         tmp$cst_p (cst_p);
6A 4059         IF task_id = cst_p^.taskid THEN
A8 4060           cst_p^.xcb_p^.xp.user_condition_register := cst_p^.xcb_p^.xp.
A8 4061           user_condition_register + $est$user_conditions [osc$free_flag];
A8 4062           cst_p^.xcb_p^.system_flags := cst_p^.xcb_p^.system_flags + $tmt$system_flags [flag_id];
A8 4063           cst_p^.xcb_p^.monitor_flags := cst_p^.xcb_p^.monitor_flags +
A8 4064           $syt$monitor_flags [tmc$mf_cause_job_free_flag_trap];
A8 4065           cst_p^.xcb_p^.wait_inhibited := TRUE;
A8 4066           IF tmv$ptl_p^ [task_id.index].new_task_status < tmc$ts_first_ready_uncond THEN
F8 4067             tmv$ptl_p^ [task_id.index].new_task_status := tmc$ts_null;
FC 4068           IFEND;
FC 4069           IFEND;
FC 4070
FC 4071 { Set system flag in PTL.
FC 4072
FC 4073         tmv$ptl_p^ [task_id.index].system_flags := tmv$ptl_p^ [task_id.index].
FC 4074         system_flags + $tmt$system_flags [flag_id];
FC 4075         IF (tmv$ptl_p^ [task_id.index].ptl_flags.wait_inhibited <> tmc$wi_wait_selected_r3) THEN
130 4076           tmv$ptl_p^ [task_id.index].monitor_flags := tmv$ptl_p^ [task_id.index].
13E 4077           monitor_flags + $syt$monitor_flags [tmc$mf_cause_job_free_flag_trap];
13E 4078           IFEND;
13E 4079           tmp$set_task_ready (task_id, 0 {readying_task_priority}, tmc$rc_ready_conditional_wi);
15A 4080           IFEND;

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```

15A 4081   FOREND;
15E 4082   tmp$clear_lock (tmv$ptl_lock);
19A 4083
19A 4084   PROCEND tmp$flag_all_tasks;

```

```

O 4086
O 4087 PROCEDURE [XDCL] tmp$monitor_flag_job_tasks
O 4088 [   monitor_flag_id: syt$monitor_flag;
O 4089   ijle_p: ^jmt$initiated_job_list_entry);
O 4090
O 4091 VAR
O 4092   taskid: ost$global_task_id;
O 4093   status: syt$monitor_status;
O 4094
O 4095   tmp$set_lock (tmv$ptl_lock);
3E 4096   taskid := ijle_p^.job_monitor_taskid;
3E 4097
3E 4098 WHILE taskid.index <> 0 DO
4E 4099   taskid.seqno := tmv$ptl_p^ [taskid.index].sequence_number;
4E 4100   tmp$set_monitor_flag (taskid, monitor_flag_id, status);
86 4101   taskid.index := tmv$ptl_p^ [taskid.index].ijl_thread;
86 4102 WHILEND;
AO 4103
AO 4104   tmp$clear_lock (tmv$ptl_lock);
DC 4105 PROCEND tmp$monitor_flag_job_tasks;

```

```

O 4107
O 4108 PROCEDURE [XDCL] tmp$mtr_ready_task
O 4109 [VAR rb {input, output} : tmt$rb_ready_task);
O 4110
O 4111
O 4112 {
O 4113 { The purpose of this procedure is to process the job mode
O 4114 { request to change the status of a specified task to ready
O 4115 { and to set the WAIT INHIBITED flag in the task's PTL entry.
O 4116 {
O 4117 { TMSMTR_READY_TASK (RB)
O 4118 {
O 4119 { RB : (INPUT,OUTPUT) This parameter specifies the request block.
O 4120 {
O 4121 {
O 4122 { The purpose of this request is to change the status of a specified task
O 4123 { to ready and to set the WAIT_INHIBITED flag in the task's PTL entry.
O 4124 {
O 4125 { TYPE
O 4126 {   TMT$RB_READY_TASK = RECORD
O 4127 {     REQCDE,
O 4128 {     STATUS,
O 4129 {     TASK_ID,
O 4130 {     RECEND;
O 4131 {
O 4132 { REQCDE: (input) The value of this parameter is SYC$RC_READY_TASK.
O 4133 {
O 4134 { STATUS: (output) This parameter specifies the standard monitor status.
O 4135 {
O 4136 { TASK_ID: (input) This parameter specifies the global task id of the task to
O 4137 { ready.
O 4138 {
O 4139 VAR
O 4140   readying_task_priority: jmt$dispatching_priority;
O 4141   cst_p: ^ost$cpu_state_table;
O 4142
O 4143   tmp$check_taskid (rb.task_id, tmc$opt_return, rb.status);
2A 4144   IF rb.status.normal = FALSE THEN
32 4145     RETURN;
34 4146   IFEND;
34 4147
34 4148   mtp$cst_p (cst_p);
44 4149
44 4150   IF cst_p^.xcb_p^.dispatching_priority >=
76 4151     tmv$ptl_p^ [cst_p^.xcb_p^.global_task_id.index].readying_task_priority THEN
76 4152     readying_task_priority := cst_p^.xcb_p^.dispatching_priority;
7A 4153   ELSE
7A 4154     readying_task_priority := tmv$ptl_p^ [cst_p^.xcb_p^.global_task_id.index].readying_task_priority;
7C 4155   IFEND;
7C 4156
7C 4157   tmp$set_task_ready (rb.task_id, readying_task_priority, tmc$rc_ready_conditional_wi);
9A 4158
9A 4159 PROCEND tmp$mtr_ready_task;

```



```

O 4161
O 4162 PROCEDURE [XDCL] tmp$send_signal
O 4163 ( task_id {input} : ost$global_task_id;
O 4164 signal {input} : pmt$signal;
O 4165 VAR status {output} : syt$monitor_status);
O 4166
O 4167
O 4168 {
O 4169 { The purpose of this procedure is to place the signal in the
O 4170 { specified task's XCB. This procedure is callable only internal
O 4171 { to monitor.
O 4172 {
O 4173 { TMP$SEND_SIGNAL (TASKID, SIGNAL, STATUS)
O 4174 {
O 4175 { TASKID: {input} This parameter specifies the task in whose XCB
O 4176 { the signal is to be placed.
O 4177 {
O 4178 { SIGNAL: {input} This parameter specifies the signal block.
O 4179 {
O 4180 { STATUS: {output} This parameter is the standard monitor status.
O 4181 {
O 4182
O 4183 VAR
O 4184 cst_p: ^ost$cpu_state_table,
O 4185 i {signal array index} : 1 .. tmc$maximum_signals,
O 4186 ijle_p: ^jmt$initiated_job_list_entry,
O 4187 xcb_p: ^ost$execution_control_block;
O 4188
O 4189 #KEYPOINT (osk$debug, osk$m * task_id.index, tmk$send_signal);
10 4190
O 4191 tmp$find_xcb (task_id, xcb_p, ijle_p, status);
3C 4192 IF status.normal = FALSE THEN
44 4193 RETURN;
46 4194 IFEND;
46 4195
46 4196 /access_xcb/
46 4197 BEGIN
46 4198 IF xcb_p^.task_is_terminating THEN
52 4199 status.normal := FALSE;
52 4200 status.condition := tme$invalid_global_taskid;
52 4201 EXIT /access_xcb/;
68 4202 IFEND;
68 4203
68 4204 IF xcb_p^.system_error_count >= tmv$system_error_hang_count THEN
78 4205 status.normal := FALSE;
78 4206 status.condition := pme$hung_recipient_task;
78 4207 EXIT /access_xcb/;
8E 4208 IFEND;
8E 4209
8E 4210 /find_free_signal_buffer/
8E 4211 BEGIN
8E 4212
8E 4213 /free_buffer_loop/
8E 4214 FOR i := 1 TO tmc$maximum_signals DO
98 4215 IF xcb_p^.signals.reserved [i] = FALSE THEN
AC 4216 EXIT /find_free_signal_buffer/;
BO 4217 IFEND;

```

```

BO 4218 FOREND /free_buffer_loop/;
B6 4219 #KEYPOINT (osk$unusual, 0, tmk$signal_buffers_full);
BA 4220 status.normal := FALSE;
BA 4221 status.condition := tme$mtr_signal_buffers_full;
BA 4222 EXIT /access_xcb/;
D2 4223 END /find_free_signal_buffer/;
D2 4224
D2 4225 { Place signal in free signal buffer.
D2 4226
D2 4227 xcb_p^.signals.reserved [i] := TRUE;
D2 4228 xcb_p^.signals.present [i] := TRUE;
D2 4229 mtp$cst_p (cst_p);
F2 4230 xcb_p^.signals.buffer [i].originator := cst_p^.taskid;
F2 4231 xcb_p^.signals.buffer [i].signal := signal;
128 4232
128 4233 IF tmv$pt1_p^ [task_id.index].pt1_flags.wait_inhibited <> tmc$wi_wait_selected_r3 THEN
146 4234
146 4235 { Set task status to ready and set free flag in specified tasks user condition
146 4236 { register to invoke trap handler when the task gets the CPU.
146 4237
146 4238 xcb_p^.wait_inhibited := TRUE;
146 4239 tmp$set_monitor_flag (task_id, tmc$mf_cause_job_free_flag_trap, status);
16C 4240 ELSE
16C 4241 tmp$set_task_ready (task_id, 0 [readying_task_priority], tmc$rc_ready_conditional);
18A 4242 IFEND;
18A 4243
18A 4244 END /access_xcb/;
18C 4245
18C 4246 jmp$unlock_a1 (ijle_p);
242 4247
242 4248 PROCEND tmp$send_signal;

```

```

O 4250
O 4251 PROCEDURE [XDCL] tmp$mtr_set_system_flag
4 4252 (VAR rb {input, output} : tmt$rb_set_system_flag);
4 4253
4 4254
4 4255 {
4 4256 { The purpose of this procedure is to process the job mode
4 4257 { request to set the specified system flag in the specified
4 4258 { task's PTL entry.
4 4259 {
4 4260 { TMSMTR_SET_SYSTEM_FLAG (RB)
4 4261 {
4 4262 { RB : (INPUT,OUTPUT) This parameter specifies the request block.
4 4263 {
4 4264 { The purpose of this request is to set a system flag in the XCB
4 4265 { (execution control block) of the specified task.
4 4266 {
4 4267 { TYPE
4 4268 { TMT$RB_SET_SYSTEM_FLAG = RECORD
4 4269 { REOCODE,
4 4270 { STATUS,
4 4271 { TASK_ID,
4 4272 { FLAG_ID,
4 4273 { recend;
4 4274 {
4 4275 { reqcode: (input) The value of this parameter is SYC$RC_MTR_SET_SYSTEM_FLAG.
4 4276 {
4 4277 { task_id: (input) This parameter is the global task id of the task in
4 4278 { which the system flag is set.
4 4279 {
4 4280 { flag_id: (input) This parameter specifies which system flag is to be set.
4 4281 {
4 4282 { status: (output) This parameter is the standard monitor status.
4 4283 {
4 4284 tmp$set_system_flag (rb.task_id, rb.flag_id, rb.status);
2C 4285
2C 4286 PROCEND tmp$mtr_set_system_flag;

```

```

O 4288
O 4289 PROCEDURE [XDCL] tmp$mtr_send_signal
4 4290 (VAR rb {input,output} : tmt$rb_send_signal);
4 4291
4 4292
4 4293 {
4 4294 { The purpose of this procedure is to process the job mode
4 4295 { request to send signals to a specified task's XCB.
4 4296 {
4 4297 { TMSMTR_SEND_SIGNAL (RB)
4 4298 {
4 4299 { RB : (INPUT,OUTPUT) This parameter specifies the request block.
4 4300 {
4 4301 { TYPE
4 4302 { TMT$RB_SEND_SIGNAL = RECORD
4 4303 { REOCODE,
4 4304 { STATUS,
4 4305 { TASK_ID,
4 4306 { SIGNAL,
4 4307 { RECEND;
4 4308 {
4 4309 { REOCODE: (input) The value of this parameter is SYC$RC_SEND_SIGNAL.
4 4310 { STATUS: (output) This parameter specifies standard system status.
4 4311 { TASK_ID: (input) This parameter specifies the task in whose XCB the
4 4312 { signal is to be placed.
4 4313 { SIGNAL: (input) This parameter specifies the signal block.
4 4314 {
4 4315 {
4 4316 tmp$send_signal (rb.task_id, rb.signal, rb.status);
36 4317
36 4318 PROCEND tmp$mtr_send_signal;
O 4319

```

```

0 4321
0 4322 PROCEDURE [XDCL] tmp$process_task_mcr_fault;
0 4323
0 4324 { Purpose:
0 4325 { This procedure is called by the monitor interrupt processor to process
0 4326 { an MCR fault from a task if the MCR fault was selected by the task
0 4327 { to be processed in job mode.
0 4328
0 4329 VAR
0 4330 cst_p: ^ost$cpu_state_table,
0 4331 fault: ost$monitor_fault,
0 4332 mcr_fault_p: ^tmt$mcr_faults,
0 4333 xcb_p: ^ost$execution_control_block,
0 4334 zero_pva: [STATIC] ost$pva := [0, 0, 0];
0 4335
0 4336
0 4337 {Copy the fault information for the task's XCB to the signal record.
0 4338
0 4339 mtp$cst_p (cst_p);
14 4340 xcb_p := cst_p^.xcb_p;
14 4341 #KEYPOINT (osk$sdebug, xcb_p^.global_task_id.index * osk$m, tmk$process_task_mcr_fault);
38 4342 fault.identifier := tmc$mcr_fault;
38 4343 mcr_fault_p := #LOC (fault.contents);
42 4344 mcr_fault_p^.faults := xcb_p^.xp.monitor_condition_register;
42 4345 mcr_fault_p^.untranslatable_pointer := xcb_p^.xp.untranslatable_pointer;
42 4346
42 4347
42 4348 {Send the Monitor Fault to the task.
42 4349
42 4350 send_monitor_fault (xcb_p, ^fault, 'Job mode MCR fault', TRUE);
7A 4351
7A 4352
7A 4353 {Reset the XCB. Clear the UTP and MCR.
7A 4354
7A 4355 xcb_p^.xp.untranslatable_pointer := zero_pva;
7A 4356
7A 4357 PROCEND tmp$process_task_mcr_fault;
0 4358

```

```

0 4360
0 4361 PROCEDURE [XDCL] tmp$process_unknown_req_fault;
0 4362
0 4363 {
0 4364 { This procedure is called by the monitor to process an unknown
0 4365 { system request fault issued by the current task.
0 4366 {
0 4367 { TMP$PROCESS_UNKNOWN_REQ_FAULT
0 4368 {
0 4369
0 4370 VAR
0 4371 cst_p: ^ost$cpu_state_table,
0 4372 fault: ost$monitor_fault,
0 4373 xcb_p: ^ost$execution_control_block;
0 4374
0 4375 { Set up the fault information in the signal block.
0 4376
0 4377 mtp$cst_p (cst_p);
14 4378 xcb_p := cst_p^.xcb_p;
14 4379 fault.identifier := tmc$unknown_system_req_fault;
14 4380
14 4381 { Send the monitor fault to the task.
14 4382
14 4383 send_monitor_fault (xcb_p, ^fault, 'invalid monitor request', TRUE);
5A 4384
5A 4385 PROCEND tmp$process_unknown_req_fault;
0 4386

```

```

0 4388 {
0 4389 PROCEDURE tmp$send_monitor_fault;
0 4390 { PURPOSE:
0 4391 {   The purpose of this procedure is to place a monitor fault in the
0 4392 {   monitor fault buffer of the specified task.
0 4393 { NOTE:
0 4394 {   The first monitor fault buffer is reserved for 'broken_task_monitor_fault'.
0 4395 {   This procedure will start with the second monitor fault buffer when
0 4396 {   searching for a free buffer.
0 4397 {
0 4398 ??FMT(FORMAT:=DN)??
0 4399
0 4400 PROCEDURE [XDCL] tmp$send_monitor_fault
0 4401 {   task_id {input} : ost$global_task_id;
0 4402 {   monitor_fault_p {input} : ^ost$monitor_fault;
0 4403 {   check_traps_enabled {input} : boolean;
0 4404
0 4405 VAR
0 4406 {   ijle_p: ^jmt$initiated_job_list_entry;
0 4407 {   xcb_p: ^ost$execution_control_block;
0 4408
0 4409 tmp$get_xcb_p (task_id, xcb_p, ijle_p);
28 4410 send_monitor_fault (xcb_p, monitor_fault_p, 'monitor fault', check_traps_enabled);
52 4411 jmp$unlock_ajl (ijle_p);
10A 4412
10A 4413 PROCEND tmp$send_monitor_fault;

```

```

0 4415
0 4416 PROCEDURE [XDCL] tmp$mr_process_system_error
0 4417 {   rb: ost$rb_system_error;
0 4418
0 4419 {
0 4420 {   This request is used by DSPSSYSTEM_ERROR to tell monitor that a
0 4421 {   fatal system error occurred.
0 4422 {
0 4423 {   TYPE
0 4424 {     OST$SRB_SYSTEM_ERROR = RECORD
0 4425 {       REQCODE,
0 4426 {       FATAL,
0 4427 {       STATUS,
0 4428 {       CALLER_P_REGISTER,
0 4429 {       STATUS_P,
0 4430 {       TEXT_P,
0 4431 {       CONDITION,
0 4432 {       TEXT,
0 4433 {       RECOND;
0 4434 {
0 4435 {   REQCODE: {input} The value of this parameter is SYC$SRC_SYSTEM_ERROR.
0 4436 {
0 4437 {   FATAL: {input} This parameter specifies whether or not to halt the
0 4438 {   system.
0 4439 {
0 4440 {   STATUS: {output} This specifies standard monitor status.
0 4441 {
0 4442 {   CALLER_P_REGISTER: {input} This parameter specifies the P register
0 4443 {   where the problem occurred.
0 4444 {
0 4445 {   STATUS_P: {input} This parameter specifies a pointer to a status
0 4446 {   variable of possible importance to the error.
0 4447 {
0 4448 {   TEXT_P: {input} This parameter specifies a pointer to a text message
0 4449 {   related to the failure.
0 4450 {
0 4451 {   CONDITION: {input} This parameter specifies the condition code found
0 4452 {   in the status variable.
0 4453 {
0 4454 {   TEXT: {input} This parameter specifies the actual error message related
0 4455 {   to the failure.
0 4456 {
0 4457 VAR
0 4458 {   cst_p: ^ost$cpu_state_table;
0 4459 {   fault: ost$monitor_fault;
0 4460 {   error_message: string (80);
0 4461 {   broken_task_fault_p: ^tmt$broken_task_monitor_fault;
0 4462
0 4463 mtp$cst_p (cst_p);
14 4464
14 4465 IF NOT mtv$sys_core_init_complete OR (cst_p^.xcb_p^.xp_p_register.pva.ring = 1) AND rb.fatal THEN
50 4466 error_message (1,10) := 'VE0S1100-';
5E 4467 error_message (11,*) := rb.text;
6C 4468 mtp$step_unstep_system (syc$ic_fatal_software_error, error_message(1,72));
8E 4469 IFEND;
8E 4470
8E 4471 fault.identifier := tmc$broken_task_fault_id;

```

```

8E 4472 broken_task_fault_p := #LOC (fault.contents);
9E 4473 broken_task_fault_p^.broken_task_condition := tmc$btoc_system_error;
9E 4474 broken_task_fault_p^.trap_enable := cst_p^.xcb_p^.xp.trap_enable;
9E 4475 broken_task_fault_p^.status_p := rb.status_p;
9E 4476 broken_task_fault_p^.text_p := rb.text_p;
9E 4477 broken_task_fault_p^.caller_p_register := rb.caller_p_register;
9E 4478 error_message (1, *) := rb.text;
DE 4479
DE 4480 send_monitor_fault (cst_p^.xcb_p, ^fault, error_message, TRUE);
100 4481
100 4482
100 4483 PROCEND tmp$mtr_process_system_error;

```

```

O 4485
O 4486 PROCEDURE send_monitor_fault
O 4487 [ xcb_p {input} : ^ost$execution_control_block;
O 4488 monitor_fault_p {input} : ^ost$monitor_fault;
O 4489 mtrflt_message {input} : string (*);
O 4490 check_traps_enabled {input} : boolean;
O 4491
O 4492 {
O 4493 { PURPOSE:
O 4494 { The purpose of this procedure is to place the monitor fault into free
O 4495 { monitor fault buffer of specified task. The free flag is set to preempt
O 4496 { specified task execution and process the monitor fault next time task executes.
O 4497 {
O 4498 { NOTE:
O 4499 { The first monitor fault buffer is reserved for sending monitor fault to
O 4500 { task to inform it that it is considered a broken task. This is to ensure
O 4501 { that a buffer full condition will never occur when a task is broken.
O 4502 { It is assumed the specified task is in ready status.
O 4503 {
O 4504 { If broken task processing aborts (in job mode), we will hang the task
O 4505 { unless it is a critical task or has system tables locked, in which case
O 4506 { we will halt the system. We will not recurse back through here.
O 4507 {
O 4508 { If a task is broken tmv$system_error_hang_count different times it
O 4509 { will be considered a hung task. It will be processed as if broken
O 4510 { task processing had aborted. (See above.)
O 4511 {
O 4512 {
O 4513 { VAR
O 4514 { cst_p: ^ost$cpu_state_table,
O 4515 { i: 1 .. tmc$maximum_monitor_faults + 1,
O 4516 { fault_contents_p: ^tm$broken_task_monitor_fault,
O 4517 { broken_task: boolean,
O 4518 { fault: ^ost$monitor_fault,
O 4519 { halt_message: string (72),
O 4520 { jdr_p: ^ost$ring,
O 4521 { status: syt$monitor_status;
O 4522 {
O 4523 { #KEYPOINT (osk$debug, xcb_p^.global_task_id.index + osk$m, tmk$send_monitor_fault);
10 4524 mtp$scst_p (cst_p);
20 4525
20 4526 IF NOT mtv$sys_core_init_complete OR (cst_p^.ajlo = 0) AND
58 4527 (cst_p^.taskid = cst_p^.ijle_p^.job_monitor_taskid) THEN
58 4528 halt_message (1,10) := 'VEDS1100-';
66 4529 halt_message (11,*) := mtrflt_message;
84 4530 mtp$step_unstep_system (syc$ic_fatal_software_error, halt_message(1,72));
A6 4531 IFEND;
A6 4532
A6 4533 IF [(xcb_p^.xp.p_register.pva.ring <= mtv$halt_cpu_ring_number) OR
DE 4534 (xcb_p^.xp.p_register.pva.ring <= mtv$system_halt_ring) AND (cst_p^.ajlo = 0)]
DE 4535 AND (check_traps_enabled) THEN
DE 4536 halt_message (1,10) := 'VEDS9920-';
E4 4537 halt_message (11, *) := mtrflt_message;
102 4538 dpp$display_error ('Software Err Below Halt Ring, initiating Software Breakpoint');
11E 4539 mtv$scb_nos_180_status.system_status.step_status_block.requested_status := mtc$stepped_system;
11E 4540 mtp$step_unstep_system (syc$ic_software_breakpoint, halt_message);
148 4541 IFEND;

```

```

148 4542
148 4543 { Set up the broken task fault.
148 4544
148 4545 fault.identifier := tmc$broken_task_fault_id;
148 4546 fault.pva := xcb_p^xp.p_register.pva;
148 4547 fault.a0 := xcb_p^xp.a0_dynamic_space_pointer;
148 4548 fault.a1 := xcb_p^xp.a1_current_stack_frame;
148 4549 fault.a2 := xcb_p^xp.a2_previous_save_area;
148 4550 fault_contents_p := #LOC (fault.contents);
170 4551 fault_contents_p^p := xcb_p^xp.p_register;
170 4552 fault_contents_p^a0 := xcb_p^xp.a0_dynamic_space_pointer;
170 4553 fault_contents_p^trap_enable := xcb_p^xp.trap_enable;
170 4554 fault_contents_p^monitor_condition_register := xcb_p^xp.monitor_condition_register;
170 4555 fault_contents_p^user_condition_register := xcb_p^xp.user_condition_register;
170 4556 fault_contents_p^monitor_fault_id := monitor_fault_p^identifier;
170 4557
170 4558 check_repair_trap_mechanism (xcb_p, check_traps_enabled, broken_task, fault_contents_p^
1C2 4559 broken_task_condition);
1C2 4560
1C2 4561 i := 2;
1C2 4562 WHILE (i <= tmc$maximum_monitor_faults) AND (xcb_p^monitor_faults.present [i]) DO
1DC 4563 i := i + 1;
1DC 4564 WHILEND;
1F4 4565
1F4 4566 IF i <= tmc$maximum_monitor_faults THEN
1FA 4567 xcb_p^monitor_faults.buffer [i] := monitor_fault_p^;
20A 4568 xcb_p^monitor_faults.buffer [i].pva := xcb_p^xp.p_register.pva;
20A 4569 xcb_p^monitor_faults.buffer [i].a0 := xcb_p^xp.a0_dynamic_space_pointer;
20A 4570 xcb_p^monitor_faults.buffer [i].a1 := xcb_p^xp.a1_current_stack_frame;
20A 4571 xcb_p^monitor_faults.buffer [i].a2 := xcb_p^xp.a2_previous_save_area;
20A 4572 xcb_p^monitor_faults.present [i] := TRUE;
23E 4573 ELSE
23E 4574 IF broken_task = FALSE THEN
23E 4575 broken_task := TRUE;
23E 4576 fault_contents_p^broken_task_condition := tmc$btc_mntr_fault_buffer_full;
23E 4577 xcb_p^xp.trap_enable := osc$traps_enabled;
254 4578 IFEND;
25E 4579 IFEND;
25E 4580
25E 4581 IF broken_task OR (monitor_fault_p^identifier = tmc$broken_task_fault_id) THEN
26E 4582
26E 4583 xcb_p^system_error_count := xcb_p^system_error_count + 1;
26E 4584
26E 4585 IF xcb_p^system_error_count > (tmv$system_error_hang_count + 4) THEN
280 4586 dpp$display_error ('Broken Task, System Error Count exceeds limit; Terminating System');
2A0 4587 halt_message (1,10) := 'VE0S2020-';
2AA 4588 halt_message (11,*) := mtrflt_message;
2C8 4589 mtp$step_unstep_system (syc$ic_fatal_software_error, halt_message);
2EE 4590
2EE 4591 ELSEIF (xcb_p^system_error_count = tmv$system_error_hang_count) OR
2FC 4592 (xcb_p^monitor_faults.present [i]) THEN
2FC 4593
2FC 4594 { HUNG TASK
2FC 4595
2FC 4596 IF (xcb_p^system_table_lock_count >= 256) OR (xcb_p^critical_task) THEN
310 4597 halt_message (1,10) := 'VE0S2010-';
31E 4598 halt_message (11,*) := mtrflt_message;

```

```

33C 4599 mtp$step_unstep_system (syc$ic_fatal_software_error, halt_message);
3E2 4600 ELSE
3E2 4601 IF tmv$halt_on_hung_task THEN
36A 4602 mtv$scb.nos_T80_status.system_status.step_status.requested_status := mtc$stepped_system;
36A 4603 halt_message (1,10) := 'VE0S9910-';
380 4604 halt_message (11,*) := mtrflt_message;
39E 4605 dpp$display_error ('Task hung, initiating software breakpoint');
3BA 4606 mtp$step_unstep_system (syc$ic_software_breakpoint, halt_message);
3DC 4607 IFEND;
3DC 4608 tmp$set_monitor_flag (xcb_p^global_task_id, syc$mf_hang_task, status);
3FC 4609 cst_p^ijle_p^hung_task_in_job := TRUE;
40E 4610 IFEND;
40A 4611
40A 4612 IFEND;
40A 4613 IFEND;
40A 4614
40A 4615 IF broken_task AND (xcb_p^monitor_faults.present [i] = FALSE) THEN
41C 4616 xcb_p^monitor_faults.buffer [i] := fault;
42E 4617 xcb_p^monitor_faults.present [i] := TRUE;
42A 4618 IFEND;
42A 4619
42A 4620 tmp$set_monitor_flag (xcb_p^global_task_id, tmc$mf_cause_job_free_flag_trap, status);
44C 4621 IF xcb_p^xp.p_register.pva.ring <= tmv$system_debug_ring THEN
460 4622 IF (tmv$system_debug_segment = 0) OR (xcb_p^xp.p_register.pva.seg <= tmv$system_debug_segment) THEN
474 4623 tmp$set_monitor_flag (xcb_p^global_task_id, syc$mf_invoke_sysdebug, status);
49E 4624 IFEND;
49E 4625 ELSE
49E 4626 IF tmv$job_debug_ring_p <> NIL THEN
4A6 4627 jdr_p := #address (1, #segment (xcb_p), #offset (tmv$job_debug_ring_p));
4A6 4628 IF xcb_p^xp.p_register.pva.ring <= jdr_p^ THEN
4C2 4629 tmp$set_monitor_flag (xcb_p^global_task_id, syc$mf_invoke_sysdebug, status);
4E0 4630 IFEND;
4E0 4631 IFEND;
4E0 4632 IFEND;
4E0 4633
4E0 4634 PROCEND send_monitor_fault;

```

```

0 4636
0 4637 PROCEDURE check_repair_trap_mechanism
0 4638 ( xcb_p: ^ost$execution_control_block;
0 4639   check_traps_enabled: boolean;
0 4640   VAR broken: boolean;
0 4641   VAR fault_id: tmt$broken_task_condition);
0 4642
0 4643 VAR
0 4644   found: boolean,
0 4645   stack_segnum: ost$segment,
0 4646   stack_length: ost$segment_length,
0 4647   status: syt$monitor_status;
0 4648
0 4649   broken := FALSE;
4 4650
4 4651
4 4652 { Make sure that traps are enabled.
4 4653
4 4654   IF (check_traps_enabled = TRUE) AND (xcb_p^.xp.trap_enable <> osc$straps_enabled) THEN
1E 4655     broken := TRUE;
1E 4656     fault_id := tmc$btc_mf_traps_disabled;
1E 4657     xcb_p^.xp.trap_enable := osc$straps_enabled;
2E 4658   IFEND;
2E 4659
2E 4660
2E 4661 {Validate AO.
2E 4662
2E 4663   mmp$fetch_stack_segment_info (xcb_p, xcb_p^.xp.p_register.pva.ring, { set_length_to_zero } FALSE,
66 4664     stack_segnum, stack_length, found);
66 4665   IF NOT found THEN
6E 4666     mtp$error_stop ('BTC - lost the stack segment');
8E 4667   IFEND;
8E 4668   IF (#RING (xcb_p^.xp.a0_dynamic_space_pointer) <> xcb_p^.xp.p_register.pva.ring) OR
C2 4669     (#SEGMENT (xcb_p^.xp.a0_dynamic_space_pointer) <> stack_segnum) OR
C2 4670     (#OFFSET (xcb_p^.xp.a0_dynamic_space_pointer) < 0) OR
C2 4671     (#OFFSET (xcb_p^.xp.a0_dynamic_space_pointer) + 37 + 8 > stack_length) THEN
C2 4672     broken := TRUE;
C2 4673     fault_id := tmc$btc_invalid_a0;
C2 4674     xcb_p^.xp.a0_dynamic_space_pointer := #ADDRESS (xcb_p^.xp.p_register.pva.ring, stack_segnum,
C2 4675       mmc$string_crossing_offset);
C2 4676     xcb_p^.xp.a2_previous_save_area := NIL;
C2 4677     mmp$fetch_stack_segment_info (xcb_p, xcb_p^.xp.p_register.pva.ring, { set_length_to_zero } TRUE,
126 4678       stack_segnum, stack_length, found);
126 4679   IFEND;
126 4680
126 4681
126 4682 PROCEND check_repair_trap_mechanism;
0 4683 MODEND tmm$mtr_flag_signal_functions;

```

**** I=\$05578173AS0102D19890821T183254 L=ZXXLIST B=LGD DA=NONE LD=R RC=NONE OPT=SCHED EL=F LF=CS612 PAD=0
**** NO DIAGNOSTICS

IDENTIFIER-----DEFINED-----REFERENCES

IDENTIFIER	ON LINE	REFERENCES
a0	622	4552/M
a0	689	4547/M 4569/M
a0_dynamic_space_pointer	3192	4547 4552 4569 4668 4669 4670 4671 4674/M
a1	690	4548/M 4570/M
a1_current_stack_frame	3196	4548 4570
a2	691	4549/M 4571/M
a2_previous_save_area	3198	4549 4571 4676/M
access_xcb	4196	4196 4201 4207 4222 4244
aj1_ordinal	1103	1066 2246 4003 4008 4246 4411
ajlo	1063	1066/M 1067/S
ajlo	2244	2246/M 2247/S 2250/S
ajlo	2296	4526 4534
ajlo	3934	4003/M 4003/S 4003/S 4003/S 4008/M 4008/S 4008/S 4008/S
ajlo	4162	4246/M 4246/S 4246/S 4246/S
ajlo	4400	4411/M 4411/S 4411/S 4411/S
b	1059	1065 1065
b	2187	2194 2195
b	3884	3900 3900
b	3934	3968 3968
b	4025	4049 4049
b	4087	4095 4095
b	4162	4246 4246
b	4400	4411 4411
bc	1059	1065/M 1065 1065
bc	2188	2191/M 2192 2196
bc	3884	3900/M 3900 3900
bc	3934	3968/M 3968 3968
bc	4025	4049/M 4049 4049
bc	4087	4095/M 4095 4095
bc	4162	4246/M 4246 4246
bc	4400	4411/M 4411 4411
broken	4640	4648/M 4655/M 4672/M
broken_task	4517	4558/P 4574 4575/M 4581 4615
broken_task_condition	613	4473/M 4559/P 4576/M
broken_task_fault_p	4461	4472/M 4473/M 4474/M 4475/M 4476/M 4477/M
buffer	765	4567/M 4568/M 4569/M 4570/M 4571/M 4616/M
buffer	3399	4230/M 4231/M
caller_p_register	43	4477
caller_p_register	615	4477/M
check_repair_trap_mechanism	4637	4558 4682
check_traps_enabled	4403	4410/P
check_traps_enabled	4480	4535 4558/P
check_traps_enabled	4639	4654
clear	2127	1072/M 2096/M 3904/M 3915/M 3920/M 3929/M 3972/M 3990/M
cmt\$element_state	2333	3995/M 4004/M 4020/M 4082/M 4104/M 4246/M 4411/M
condition	213	2292 2293 2323
condition	3699	3704/M 3978/M 4002/M 4200/M 4206/M 4221/M
condition	3934	3704
condition	700	3978
contents	2124	4343 4472 4550
count	2124	1065/M 1065 1072 1072/M 1072 2093 2094/M 2094

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

IDENTIFIER	DEFINED ON LINE	REFERENCES
		2188/M 2188 3900/M 3900 3904 3904/M 3904 3915
		3915/M 3915 3920 3920/M 3920 3929/M 3929
		3968/M 3968 3972 3972/M 3972 3990 3990/M 3990
		3995 3995/M 3995 4004 4004/M 4004 4020 4020/M
		4020 4049/M 4049 4082 4082/M 4082 4095/M 4095
		4104 4104/M 4104 4246/M 4246 4246 4246/M 4246
		4411/M 4411 4411 4411/M 4411
critical_task	2824	4596
cst_p	3850	3882/M
cst_p	3884	3896/M
cst_p	3891	3896/P 3908 3909/M 3909 3911/M 3911
cst_p	3934	3964/M
cst_p	3957	3964/P 3976 3977 3980/M 3980 3982/M 3982 3983/M
		3983 3985/M
cst_p	4025	4058/M
cst_p	4045	4058/P 4059 4060/M 4060 4062/M 4062 4063/M 4063
		4065/M
cst_p	4108	4148/M
cst_p	4141	4148/P 4150 4151/S 4152 4154/S
cst_p	4162	4229/M
cst_p	4184	4229/P 4230
cst_p	4322	4339/M
cst_p	4330	4339/P 4340
cst_p	4361	4377/M
cst_p	4371	4377/P 4378
cst_p	4416	4463/M
cst_p	4458	4463/P 4465 4474 4480/P
cst_p	4486	4524/M
cst_p	4514	4524/P 4526 4527 4527 4534 4609/M
dfc\$command_record_bytes	1203	1211
dfc\$division_overwrite_words	1190	1218
dfc\$esm_command_record_size	1211	1219
dfc\$esm_header_record_size	1212	1219
dfc\$esm_maintenance_buf_size	1191	1222
dfc\$esm_memory_base_shift	1197	1219
dfc\$header_record_bytes	1202	1220 1220
dfc\$max_esm_memory_size	1192	1221
dfc\$max_number_of_mainframes	1199	1184
dfc\$min_data_record_bytes	1207	1218
dfc\$min_esm_division_size	1217	1221
dft\$mainframe_set	1184	1134 1135 1311 1312
dispatching_priority	2639	4150 4152
dmf\$system_file_id	1233	1166
dpc\$console_row_size	1056	1050
dpc\$stop_line_message_size	1050	1045 3845
dpp\$display_error	1044	4538 4586 4605
dpt\$stop_line_message	3845	3820
error_message	4460	4466/M 4467/M 4468/P 4478/M 4480/P
fatal	41	4465
fault	4331	4342/M 4343 4350/P

*** REFERENCE ABBREVIATIONS : M=modify, A=attribute, S=subscript, I=I/O ref, R=read, W=write, P=parameter

IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

IDENTIFIER	DEFINED ON LINE	REFERENCES
fault	4372	4379/M 4383/P
fault	4459	4471/M 4472 4480/P
fault	4518	4545/M 4546/M 4547/M 4548/M 4549/M 4550 4556/M 4558/P
fault_contents_p	4516	4550/M 4551/M 4552/M 4553/M 4554/M 4555/M 4556/M 4558/P
		4576/M
fault_id	4641	4656/M 4673/M
faults	847	4344/M
find_free_signal_buffer	4210	4210 4216 4223
flag_id	940	4284/P
flag_id	3886	3911 3919 3927
flag_id	3936	3982 3994 4014
flag_id	4026	4062 4074
found	4644	4664/P 4665 4678/P
free_buffer_loop	4213	4213 4218
gft\$file_descriptor_index	1248	1238
gft\$system_file_identifier	1237	1233
gft\$stable_residence	1251	1239
global_task_id	2628	4151/S 4154/S 4341 4523 4608/P 4620/P 4623/P 4629/P
halt_message	4519	4528/M 4529/M 4530/P 4536/M 4537/M 4540/P 4587/M 4588/M
		4589/P 4597/M 4598/M 4599/P 4603/M 4604/M 4606/P
hung_task_in_job	1139	4609/M
i	4043	4050
i	4185	4214 4215/S 4227/S 4228/S 4230/S 4231/S
i	4515	4561/M 4562 4562/S 4562/S 4563/M 4563 4566
		4567/S 4568/S 4568/S 4570/S 4571/S 4572/S
i#program_error	2104	1072 3904 3915 3920 3929 3972 3990
		3995 4004 4020 4082 4104 4246 4411
id	2125	1065 1065/M 1072 2090 2192 2196/M 3900 3900/M
		3904 3915 3920 3929 3968 3968/M 3972 3990
		3995 4004 4020 4049 4049/M 4082 4085 4095/M
		4104 4246 4246/M 4246 4411 4411/M 4411
identifier	892	4342/M 4379/M 4471/M 4545/M 4556
ijl_thread	3620	4101
ijle_p	1060	1066 1068/P
ijle_p	2241	2246 2248/P
ijle_p	2316	4527 4609/M
ijle_p	3934	4003 4003/P
ijle_p	3958	3998/P 4003/P 4008/P 4008/P
ijle_p	4089	4096
ijle_p	4162	4246 4246/P
ijle_p	4186	4191/P 4246/P
ijle_p	4400	4411 4411/P
ijle_p	4406	4409/P 4411/P
in_use	2224	1067 1070/M 1070 2247 2250/M 2250 4003 4003/M
		4003 4008 4008/M 4008 4246 4246/M 4246 4411
		4411/M 4411
index	863	3893 3912/S 3913/S 3919/S 3926/S 3926/S 3961 3986/S
		3987/S 3994/S 4013/S 4013/S 4015/S 4016/S 4016/S 4053/M
		4066/S 4067/S 4073/S 4073/S 4075/S 4075/S 4082 4085 4098
		4098 4099/S 4101/M 4101/S 4151/S 4154/S 4189 4233/S

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IDENTIFIER-----	DEFINED-----	REFERENCES-----							
	ON LINE								
iot\$io_error	2044	4341	4523						
iot\$transfer_count	3140	1167	2010						
		3126							
jdr_p	4520	4627/M	4628						
jmc\$detached_job_wait_time_max	2394	2391							
jmc\$highest_det_job_wait_time	2404	2394	2405						
jmc\$highest_pri_age_interval	2735	2726	2736						
jmc\$highest_service_accumulator	1721	1722							
jmc\$highest_service_factor_valu	2759	2752							
jmc\$highest_working_set_size	2430	2421	2431	2433	2435	2437			
jmc\$ies_job_swapped	1479	1488							
jmc\$ies_swapin_in_progress	1478	1487							
jmc\$iss_idle_tasks_initiated	1494	1521							
jmc\$iss_swapin_io_complete	1519	1522							
jmc\$iss_swapin_requested	1515	1522							
jmc\$iss_swapout_complete	1514	1521							
jmc\$iss_swapped_io_cannot_init	1505	1532							
jmc\$iss_swapped_no_io	1496	1531							
jmc\$keyword_offset_maximum	1738	2422	2727						
jmc\$skj1_maximum_entries	1272	1265	1266	1673					
jmc\$skol_maximum_entries	1282	1267							
jmc\$lock_aj1	1080	1067	1068/P	1070	2247	2248/P	2250	4003	4003/P
		4003	4008	4008/P	4008	4246	4246/P	4246	4411
		4411/P	4411						
jmc\$max_active_jobs	1263	2708	2716	2717					
jmc\$max_aj1_ord	1264	1257	1263						
jmc\$max_dispatching_control	1436	1442							
jmc\$max_dispatching_priority	1360	1320	1323	1324					
jmc\$maximum_job_classes	1651	1654							
jmc\$maximum_job_count	1279	1272							
jmc\$maximum_output_count	1289	1282							
jmc\$maximum_service_classes	1754	1757							
jmc\$min_dispatching_control	1437	1441							
jmc\$null_service_class	1747	1748							
jmc\$priority_aging_interval_max	2726	2723							
jmc\$priority_p1	1374	1321	3577						
jmc\$priority_p10	1383	1322							
jmc\$priority_p14	1387	1322	3577						
jmc\$priority_p8	1381	1321							
jmc\$required_offset	1736	2436							
jmc\$reserved_aj1s	1268	1263							
jmc\$service_accumulator_maximum	1713	1710							
jmc\$service_factor_value_max	2752	2749							
jmc\$system_default_offset	1737	1738	2438						
jmc\$system_supplied_name_size	1885	1882							
jmc\$unlimited_offset	1734	1723	2395	2406	2432	2737			
jmc\$unspecified_offset	1735	2434							
jmc\$working_set_size_maximum	2421	2418							
jmp\$free_aj1_with_lock	1083	1068	2248	4003	4008	4246	4411		
jmp\$unlock_aj1	1059	1074	4246	4411					
jmp\$unlock_aj1_with_lock	2240	2253	4003	4008					
jmt\$active_job_list	2231	2209							

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IDENTIFIER-----	DEFINED-----	REFERENCES-----							
	ON LINE								
jmt\$active_job_list_entry	2223	2231							
jmt\$aj1_ordinal	1257	1063	1103	2244	2296				
jmt\$delayed_swapin_work	1304	1133	1308						
jmt\$detached_job_wait_time	2391	2376							
jmt\$dispatching_control	1408	2691	1408						
jmt\$dispatching_control_index	1441	1398	1408						
jmt\$dispatching_controls	1411	1115	1399	1400	1401	1413	2288	2639	2641
jmt\$dispatching_priority	1320	3618	3619	3651	4140				
		1547							
jmt\$ij1_block_index	1551	1547							
jmt\$ij1_block_number	1550	1546							
jmt\$ij1_dispatching_control	1397	1116							
jmt\$ij1_entry_status	1474	1102							
jmt\$ij1_ordinal	1545	1122	1150	1900	1901	1963	2002	2225	2315
		2366	3610						
jmt\$ij1_page_fault_count	1570	1565	1566	1567					
jmt\$ij1_page_stats	1564	1560							
jmt\$ij1_service_class_stats	1558	1137							
jmt\$ij1_statistics	1603	1136							
jmt\$ij1_swap_count	1579	1575	1576						
jmt\$ij1_swap_counts	1574	1156	1561						
jmt\$ij1_swap_status	1482	1105	1106	1107					
jmt\$initiated_job_list_entry	1099	1060	1084	1927	2226	2241	2316	2365	3659
		3668	3958	4089	4186	4406			
jmt\$input_file_location	1693	1688							
jmt\$job_abort_disposition	1702	1686							
jmt\$job_class	1654	1161							
jmt\$job_control_block	2347	2298							
jmt\$job_mode	1657	1118							
jmt\$job_priority	1682	1158							
jmt\$job_recovery_disposition	1705	1687							
jmt\$job_system_id	2410	2362							
jmt\$skj1_index	1673	1104	2410						
jmt\$maximum_active_jobs	2708	2685							
jmt\$priority_aging_interval	2723	2693							
jmt\$queue_file_ij1_information	1685	1143							
jmt\$scheduling_data	1149	1127							
jmt\$scheduling_priority	2699	2692							
jmt\$service_accumulator	1710	1151	1152	1153	2683	2684			
jmt\$service_class_index	1757	1162	2676	2686					
jmt\$service_class_name	2741	2678	2679						
jmt\$service_factor_value	2749	2687							
jmt\$service_factors	2745	2687							
jmt\$swap_data	1165	1129							
jmt\$swapout_reasons	1760	1157							
jmt\$swapped_job_entry	1775	1174	1928	2385					
jmt\$system_supplied_name	1882	1100	2360						
jmt\$task_time_slice	1451	1431	1432						
jmt\$time_slice_values	1430	1415	2652						
jmt\$user_supplied_name	2414	2361							
jmt\$working_set_size	2418	2372	2373						
jmv\$aj1_p	2209	1067	1070/M	1070	2247	2250/M	2250	4003	4003/M
		4003	4008	4008/M	4008	4246	4246/M	4246	4411

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IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

		4411/M	4411						
job_monitor_taskid	1117	4096	4527						
jsc\$isi_swapped_io_completed	1905	1907							
jsc\$isi_swapped_io_not_init	1904	1907							
jst\$changed_asid_entry	1950	1941							
jst\$ijl_swap_queue_id	1904	1899							
jst\$ijl_swap_queue_link	1912	1898							
jst\$io_control_information	1912	1130							
jst\$swap_file_descriptor	1926	1131							
jst\$swapped_page_descriptor	1935	1933							
jst\$swapped_page_descriptors	1932	1929							
lock	1059	1065	1065	1065/M	1065/M	1065			
lock	1059	1072	1072	1072/M	1072	1072/M			
lock	2087	2090	2093	2094/M	2094	2096/M			
lock	2184	2192	2194	2196/M	2198/M	2198			
lock	3884	3900	3900	3900/M	3900/M	3900			
lock	3884	3904	3904	3904/M	3904	3904/M	3915	3915	3915/M
		3915	3915/M	3920	3920	3920/M	3920	3920/M	3929
		3929	3929/M	3929	3929/M				
lock	3934	3968	3968	3968/M	3968/M	3968			
lock	3934	3972	3972	3972/M	3972	3972/M	3990	3990	3990/M
		3990	3990/M	3995	3995	3995/M	3995	3995/M	4004
		4004	4004/M	4004	4004/M	4020	4020	4020/M	4020
		4020/M							
lock	4025	4049	4049	4049/M	4049/M	4049			
lock	4025	4082	4082	4082/M	4082	4082/M			
lock	4087	4095	4095	4095/M	4095/M	4095			
lock	4087	4104	4104	4104/M	4104	4104/M			
lock	4182	4246	4246	4246/M	4246/M	4246			
lock	4182	4246	4246	4246/M	4246	4246/M			
lock	4400	4411	4411	4411/M	4411/M	4411			
lock	4400	4411	4411	4411/M	4411	4411/M			
locked	2123	1065	2194	3900	3968	4049	4095	4246	4411
mcr_fault_p	4332	4343/M	4344/M	4345/M					
mmc\$assign_active_null	2884	2885							
mmc\$cell_pointer	2983	2988							
mmc\$heap_pointer	2984	2992							
mmc\$kw_asid	2908	2945							
mmc\$kw_clear_space	2907	2932							
mmc\$kw_current_segment_length	2906	2926							
mmc\$kw_error_exit_procedure	2908	2936							
mmc\$kw_gl_key	2908	2930							
mmc\$kw_hardware_attributes	2910	2939							
mmc\$kw_inheritance	2910	2947							
mmc\$kw_max_segment_length	2907	2928							
mmc\$kw_preset_value	2909	2934							
mmc\$kw_ps_transfer_size	2911	2955							
mmc\$kw_ring_numbers	2905	2921							
mmc\$kw_segment_access_control	2909	2943							
mmc\$kw_segment_number	2906	2924							
mmc\$kw_shadow_segment	2911	2949							

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IDENTIFIER-----DEFINED-----REFERENCES
ON LINE

mmc\$kw_software_attributes	2908	2941							
mmc\$kw_wired_segment	2911	2952							
mmc\$pd_avail	1792	1838							
mmc\$pd_free	1791	1850							
mmc\$pd_job_fixed	1832	1839	1851						
mmc\$pd_job_working_set	1834	1851	1852						
mmc\$pd_shared_first_site	1842	1846							
mmc\$pd_shared_num_sites	1843	1846							
mmc\$pd_shared_other	1801	1841							
mmc\$pd_shared_site_01	1803	1842							
mmc\$pd_shared_site_25	1827	1847							
mmc\$pd_shared_task_service	1796	1840							
mmc\$pd_swapped_io_error	1830	1850							
mmc\$pd_wired	1794	1837							
mmc\$ring_crossing_offset	32	4675							
mmc\$segment_fault_processor_id	772	697							
mmc\$sequence_pointer	2983	2990							
mmc\$ssk_none	3084	3056							
mmc\$ssk_segment_number	3085	3054							
mmp\$fetCh_stack_segment_info	3672	4663	4677						
mnt\$active_segment_table_entry	1960	1938	1976	2009					
mnt\$ast_index	1992	1173	1953	2778					
mnt\$attribute_keyword	2905	2920							
mnt\$global_page_queue_index	1850	2082							
mnt\$global_page_queue_list_ent	2072	2082							
mnt\$hardware_attribute_set	2974	2940							
mnt\$hardware_attributes	2962	2974							
mnt\$job_page_queue_index	1851	1777	2083						
mnt\$job_page_queue_list	2083	1128							
mnt\$link	1983	1961	1999	2000	2069				
mnt\$lock_segment_status	3064	2849							
mnt\$locked_page	2021	2005							
mnt\$max_sdt	2788	2792							
mnt\$max_sdtx	2873	2877							
mnt\$memory_reserve_request	2050	1121							
mnt\$page_age	2028	2008							
mnt\$page_frame_index	1921	1913	1915	1916	1917	1985	1985	2052	2053
mnt\$page_frame_queue_id	1852	1914	1969	2003					
mnt\$page_frame_table_entry	1998	1936	2014						
mnt\$page_map_offsets_ord	17	20							
mnt\$page_queue_list_entry	2068	2073	2083						
mnt\$sdtx_stream_data	2856	2852							
mnt\$segment_access_condition	799	698							
mnt\$segment_access_rights	3028	2848							
mnt\$segment_access_state	3034	2843							
mnt\$segment_descriptor	2775	2785	2788						
mnt\$segment_descriptor_extended	2841	2870	2874						
mnt\$segment_inheritance	2891	2845	2848						
mnt\$segment_pointer_kind	2983	2987							
mnt\$segment_reservation_state	3074	2846							
mnt\$shadow_info	3049	2850							
mnt\$shadow_reference_info	3097	2665							
mnt\$shadow_segment_kind	3084	3053							

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IDENTIFIER-----	DEFINED-----	REFERENCES								
	ON LINE									
mnt\$software_attribute_set	2376	2847	2942							
mnt\$software_attributes	2970	2976								
mnt\$xcb_page_wait_info	3108	2651								
monitor_condition_register	823	4554/M								
monitor_condition_register	3203	4344	4554							
monitor_fault_id	825	4556/M								
monitor_fault_p	4402	4410/P								
monitor_fault_p	4488	4556	4567	4581						
monitor_faults	2656	4562	4562	4567/M	4568/M	4569/M	4570/M	4571/M	4572/M	
		4592	4615	4616/M	4617/M					
monitor_flag_id	4088	4100/P								
monitor_flags	2619	3911/M	3911	3983/M	3983	4063/M	4063			
monitor_flags	3615	3919	3926/M	3927	4016/M	4017	4076/M	4077		
mtc\$scb_max_hardware_status	3853	3812								
mtc\$stepped_system	3857	4539	4602							
mtp\$scst_p	3680	3896	3964	4058	4148	4229	4339	4377	4463	
		4524								
mtp\$error_stop	3696	4666								
mtp\$set_status_abnormal	3698	3705	3978							
mtp\$step_unstep_system	3720	4468	4530	4540	4589	4599	4606			
mtr_flt_message	4489	4529	4537	4588	4598	4604				
mtt\$idle_status_block	3790	3777								
mtt\$scb_180_status	3766	3757								
mtt\$scb_hardware_status	3814	3752	3837							
mtt\$scb_hardware_status_count	3812	3815								
mtt\$scb_hardware_status_msg	3818	3824								
mtt\$scb_hardware_status_msgs	3823	3760								
mtt\$scb_hardware_status_options	3804	3814	3823							
mtt\$smu_communications_block	3751	3744								
mtt\$step_status_block	3785	3778								
mtt\$system_idle_update_request	3858	3792								
mtt\$system_status_block	3776	3767								
mtt\$system_step_update_request	3857	3787	3787							
mtv\$scsto	3686	3682	3896	3964	4058	4148	4229	4339	4377	
		4463	4524							
mtv\$halt_cpu_ring_number	3734	4533								
mtv\$scb	3744	4539/M	4602/M							
mtv\$sys_core_init_complete	1022	4465	4526							
mtv\$system_haltring	3733	4534								
nat\$received_message_descriptor	3124	3117	3126							
nat\$received_message_list	3116	2633								
new_task_status	3612	3912	3913/M	3986	3987/M	4066	4067/M			
nlc\$cc_connect_confirm	3156	3147								
nlc\$cc_connect_request	3155	3145								
nlc\$cc_expedited_data	3181	3147								
nlc\$cc_max_pdu_kind	3183	3166								
nlc\$channel_connection_pdu	3179	3131								
nlc\$channelnet_pdu	3178	3133								
nlt\$cc_pdu_kind	3166	3144								
nlt\$cc_seq#_or_connect_time	3143	3132								
nlt\$cc_sequence_number	3169	3148								
nlt\$device_idenfifier	3176	3127								

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IDENTIFIER-----	DEFINED-----	REFERENCES								
	ON LINE									
nlt\$pdu_type	3179	3130								
normal	212	3703/M	3895/M	3903	3963/M	3971	3978/M	4001/M	4047/M	
		4144	4192	4199/M	4205/M	4220/M				
nos_180_status	3757	4539/M	4602/M							
originator	3405	4230/M								
osc\$aging_interval_maximum	2442	2445								
osc\$base_exception	319	321								
osc\$call_instruction	3273	3281								
osc\$data_read	3272	3281								
osc\$free_flag	652	3910	3981	4061						
osc\$free_running_clock_maximum	1009	1006								
osc\$invalid_ring	228	268								
osc\$max_fault_contents	710	704								
osc\$max_idle_count	2580	2588								
osc\$max_name_size	809	813	816							
osc\$max_number_of_processors	2112	2107	2283							
osc\$max_page_frames	1857	1168	1169	1776	1778	1921	1962	2070	2076	
osc\$max_page_size	2541	2537								
osc\$max_page_table_entries	1858	1861								
osc\$max_ring	227	268	269							
osc\$max_segment_length	251	274	2853	2884						
osc\$max_status_condition_code	67	63	79							
osc\$max_status_condition_number	3708	3699								
osc\$max_string_size	83	86	89	94						
osc\$max_tasks	871	868								
osc\$maximum_offset	250	251	271	271	272					
osc\$maximum_processor_id	3288	3294								
osc\$maximum_processor_number	2564	2559								
osc\$maximum_processors	2116	2112	2564							
osc\$maximum_segment	249	270								
osc\$min_ecc	318	319								
osc\$min_page_size	2540	2537								
osc\$min_ring	226	268								
osc\$pr_base_constant	2146	1065	1072	2090	2191	3900	3904	3915	3920	
		3929	3968	3972	3990	3995	4004	4020	4049	
		4082	4095	4104	4246	4411	4411			
osc\$task_time_slice_maximum	1462	1465								
osc\$traps_enabled	676	4577	4654	4657						
osk\$debug	563	3893	3961	4189	4341	4523				
osk\$m	600	3893	3961	4189	4341	4523				
osk\$system_class	575	559	560	561	562	563	564	565		
osk\$unusual		4219								
ost\$aging_interval	2445	2374	2375							
ost\$asid	998	994	1112	1940	1951	1952	1967	2809	2946	
ost\$cp_time	1591	1559	1604	2650						
ost\$cp_time_value	1589	1154	1592	1593	2369	2370	2663			
ost\$cpu_element_id	2556	2314								
ost\$cpu_idle_statistics	2558	2317								
ost\$cpu_memory_port_mask	2558	2290								
ost\$cpu_running_or_stepped	2601	2598	2598							
ost\$cpu_state	2598	2299								
ost\$cpu_state_reason	2607	2320								

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IDENTIFIER-----	DEFINED-----	REFERENCES							
	ON LINE								
ost\$cpu_state_table	2286	2260	2283	3680	3691	3957	4045	4141	4184
		4330	4371	4458	4514				
ost\$cs_lock	3312	2631							
ost\$scst_trace_control	3476	2318							
ost\$debug_code	3272	3260							
ost\$debug_list	3268	3237							
ost\$debug_list_entry	3259	3268							
ost\$debug_mask	3278	3236							
ost\$exchange_package	3186	2618							
ost\$execute_privilege	2822	2604	2817						
ost\$execution_control_block	2517	2300	2643	3658	3667	3672	3959	4187	4333
		4373	4407	4487	4638				
ost\$external_interrupt_request	3464	2306							
ost\$family_name	2455	2450							
ost\$flags	3243	3193							
ost\$frame_descriptor	730	745							
ost\$free_running_clock	1006	976	977	1123	1124	1125	1126	1160	1170
		1171	1172	1402	1414	1966	2228	2268	2269
		2368	2377	2649					
ost\$global_task_id	862	857	877	939	978	1019	1019	1117	1146
		2295	2364	2628	2629	3357	3405	3588	3643
		3650	3657	3666	3885	3935	4044	4092	4163
		4401							
ost\$halfword	1012	2569							
ost\$idle_type	2592	2587							
ost\$key_lock	257	2810	2931						
ost\$key_lock_value	263	260	667	669					
ost\$keypoint_class	3254	3206	3256						
ost\$keypoint_mask	3256	3209							
ost\$logical_processor_id	2559	2291							
ost\$minimum_save_area	740	691	715	3198					
ost\$monitor_condition	841	648							
ost\$monitor_conditions	848	623	720	847	3199	3203			
ost\$monitor_fault	687	765	4331	4372	4402	4459	4488	4518	
ost\$monitor_fault_contents	704	700							
ost\$name	816	833	2414	2453	2455	2677	2741		
ost\$register	665	43	615	621	741	3187			
ost\$page_id	1863	1873							
ost\$page_size	2537	2518							
ost\$page_table_entry	1868	1877	1937						
ost\$page_table_index	1861	1877	2006						
ost\$paging_statistics	1627	1605	2658						
ost\$parcel	1014	2312	2313						
ost\$pre_processed_for_reconfig	3472	2321							
ost\$processor_element_id	2568	2556							
ost\$processor_element_number	2577	2570							
ost\$processor_id	3294	2621	3288						
ost\$processor_id_set	3288	2620	3754	3755	3867				
ost\$processor_model_number	2467	2461	2571						
ost\$processor_serial_number	2545	2462	2572						
ost\$pva	279	670	688	848	3231	3249	4334	4334	
ost\$rb_system_error	39	4417							
ost\$read_privilege	2825	2805	2818						

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IDENTIFIER-----	DEFINED-----	REFERENCES							
	ON LINE								
ost\$real_memory_address	986	2311							
ost\$register_number	661	726	734	735	736	3228			
ost\$string	268	280	1038	2807	2808	2842	2922	2923	3248
		4520							
ost\$string_termination_reason	3306	2654							
ost\$segment	270	281	2925	3055	3226	3262	3675	4645	
ost\$segment_access_control	2815	2944							
ost\$segment_descriptor	2802	2776							
ost\$segment_length	274	2927	2929	2951	2953	2956	3676	4646	
ost\$segment_offset	271	282	995	2857	3263	3265			
ost\$stack_frame_save_area	714	748	828						
ost\$state_tables	2283	3686							
ost\$status	51	44	2938						
ost\$status_condition	75	46	213						
ost\$status_condition_code	79	54	75						
ost\$string	92	55							
ost\$string_size	86	93							
ost\$system_flag	944	940	3417	3936	4026				
ost\$system_virtual_address	993	2011							
ost\$task_index	868	863	2039	2040	2325	3607	3620	4043	
ost\$task_time_slice	1465	1451							
ost\$top_of_stack_pointer	3246	3238							
ost\$trap_enable	675	612	3195						
ost\$user_condition	651	658							
ost\$user_conditions	658	624	718	747	831	3197	3201	3910	3981
		4061							
ost\$user_identification	2448	2363							
ost\$user_name	2453	2449							
ost\$valid_relative_pointer	277	2647	2648						
ost\$valid_ring	269	3238	3673						
ost\$vector_simulation_control	3885	3756							
ost\$virtual_machine_identifier	753	742	3189	3191					
ost\$write_privilege	2828	2806	2819						
ost\$x_register	662	726	3228						
osv\$cpus_logically_on	2107	1065	1072	2089	2190	3900	3904	3915	3920
		3929	3968	3972	3990	3995	4004	4020	4049
		4082	4095	4104	4246	4246	4411	4411	
p_register	621	4551/M							
p_register	3187	4465	4533	4534	4546	4551	4568	4621	4622
		4628	4663/P	4668	4674	4677/P			
pmc\$internal_base_exception	309	313							
pmc\$kill_task_flag	944	860							
pmc\$max_signal_contents	929	923							
pmc\$max_task_id	3366	3363							
pmc\$min_ecc	301	297							
pmc\$pc_base_exception	297	309							
pme\$hung_recipient_task	313	4206							
pmt\$binary_mainframe_id	2480	2367							
pmt\$condition_identifier	806	800							
pmt\$cpu_model_number	2527	2516	2523						
pmt\$cpu_serial_number	2530	2517	2522						
pmt\$initialization_value	3000	2935							

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IDENTIFIER-----	DEFINED-----	REFERENCES
	ON LINE	
pmt\$sense_switches	2550	2378
pmt\$signal	885	878 3406 4164
pmt\$signal_contents	923	887
pmt\$signal_id	890	886
pmt\$task_id	3363	2645 3358
pmt\$vector_simulation	3872	3866
present	763	4562 4562 4572/M 4592 4615 4617/M
present	3397	4228/M
ptl_flags	3617	4015 4075 4233
pva	670	4465 4533 4534 4546 4568 4621 4622 4628
		4663/P 4668 4674 4677/P
pva	688	4546/M 4568/M
rb	4109	4143/P 4143/P 4144 4157/P
rb	4252	4284/P 4284/P 4284/P
rb	4290	4316/P 4316/P 4316/P
rb	4417	4465 4467 4475 4476 4477 4478
reading_task_priority	3619	4151 4154
reading_task_priority	4140	4152/M 4154/M 4157/P
requested_status	3786	4539/M 4602/M
reserved	3398	4215 4227/M
ring	280	4465 4533 4534 4621 4628 4663/P 4668 4674
		4677/P
seg	281	4622
send_monitor_fault	4486	4350 4383 4410 4480 4634
seqno	864	4054/M 4095/M
sequence_number	3608	4054 4095
sft\$counter	1637	1605 1607 2379 2381 2382 2384
sft\$file_space_limit_kind	3093	2851
signal	878	4316/P
signal	3406	4231/M
signal	4164	4231
signals	2657	4215 4227/M 4228/M 4230/M 4231/M
stack_length	4646	4664/P 4671 4678/P
stack_segnum	4645	4664/P 4669 4674 4678/P
status	856	4143/P 4144
status	876	4316/P
status	938	4284/P
status	3611	4051
status	3700	3703/M 3704/M
status	3887	3895/M 3902/P 3903
status	3934	3978/M 3978/M
status	3937	3963/M 3970/P 3971 3978/P 4001/M 4002/M
status	4027	4047/M
status	4093	4100/P
status	4165	4191/P 4192 4199/M 4200/M 4205/M 4206/M 4220/M 4221/M
		4239/P 4620/P 4623/P 4629/P
status	4521	4608/P 4620/P
status_p	44	4475
status_p	616	4475/M
step_status_block	3778	4539/M 4602/M
syc\$ic_fatal_software_error	3490	4466/P 4530/P 4589/P 4599/P

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IDENTIFIER-----	DEFINED-----	REFERENCES
	ON LINE	
syc\$ic_software_breakpoint	3503	4540/P 4606/P
syc\$mf_hang_task	3385	4608/P
syc\$mf_invoke_sysdebug	3366	4623/P 4629/P
syc\$ucr_condition	819	830
syc\$user_defined_condition	820	832
system_error_count	2642	4204 4583/M 4583 4585 4591
system_flags	2632	3982/M 3982 4062/M 4062
system_flags	3616	3994 4013/M 4014 4073/M 4074
system_status	3767	4539/M 4602/M
system_table_lock_count	2631	4596
syt\$180_idle_code	3484	2307 3721 3768 3770
syt\$monitor_flag	3385	3370 3886 4088
syt\$monitor_flags	3370	2619 3615 3911 3927 3984 4017 4064 4077
syt\$monitor_request_code	104	40 855 875 937 974 2266
syt\$monitor_status	211	42 856 876 938 975 2267 3590 3645
		3660 3700 3887 3937 4027 4093 4165 4521
		4647
task_id	857	4143/P 4157/P
task_id	877	4316/P
task_id	839	4284/P
task_id	3885	3893 3902/P 3908 3912/S 3913/S 3919/S 3926/S 3926/S
		3928/P
task_id	3935	3961 3970/P 3976 3986/S 3987/S 3994/S 3999/P 4013/S
		4013/S 4015/S 4016/S 4016/S 4019/P
task_id	4044	4053/M 4054/M 4058 4066/S 4067/S 4073/S 4073/S 4075/S
		4076/S 4076/S 4078/P
task_id	4163	4188 4191/P 4233/S
task_id	4401	4409/P
task_is_terminating	2634	3977 4000 4198
taskid	2295	3908 3976 4059 4230 4527
taskid	4082	4096/M 4098 4098 4099/M 4099/S 4100/P 4101/M 4101/S
text	47	4467 4478
text_p	45	4476
text_p	617	4476/M
tmc\$	321	327 330 333 336 339 342 345 348
		351 354 357 360 363 366 369 372
tmc\$broken_task_fault_id	772	693 4471 4545 4581
tmc\$btc_invalid_a0	635	619 4673
tmc\$btc_invalid_p	635	619
tmc\$btc_mcr_traps_disabled	636	620
tmc\$btc_mf_traps_disabled	635	618 4656
tmc\$btc_mntr_fault_buffer_full	634	618 4576
tmc\$btc_system_error	637	614 4473
tmc\$btc_uwr_traps_disabled	636	620
tmc\$dummy_fault	773	699
tmc\$flag_available_31	957	961
tmc\$maximum_monitor_faults	777	768 4515 4562 4562 4566
tmc\$maximum_signals	3414	3411 4185 4214
tmc\$maximum_system_task_id	3422	3425
tmc\$mcr_fault	772	695 4342
tmc\$mf_cause_job_free_flag_trap	3365	3984 4017 4064 4077 4239/P 4620/P
tmc\$opt_return	3626	3902/P 3970/P 4143/P

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IDENTIFIER	DEFINED ON LINE	REFERENCES	REFERENCES	REFERENCES	REFERENCES	REFERENCES	REFERENCES	REFERENCES	REFERENCES
tmc\$rc_ready_conditional	3573	3928/P	4241/P						
tmc\$rc_ready_conditional_wi	3573	4018/P	4079/P	4157/P					
tmc\$signal_available_63,	921	932							
tmc\$stid_null_task	3428	3425							
tmc\$st_first_ready_uncond	3547	3912	3986	4066					
tmc\$st_io_wait_not_queued	3565	3547							
tmc\$st_null	3552	3913	3987	4051	4067				
tmc\$st_page_wait	3567	3548							
tmc\$st_ready	3552	3543							
tmc\$st_timed_wait_not_queued	3559	3546							
tmc\$st_timeout_reqexp_longvlong	3557	3545							
tmc\$st_timeout_reqexp_shortshrt	3556	3544							
tmc\$unknown_system_req_fault	773	4379							
tmc\$wi_wait_selected_r3	3638	4015	4075	4233					
tme\$invalid_global_taskid	327	3978/P	4002	4200					
tme\$mr_signal_buffers_full	345	4221							
tmk\$monitor_base	535	388	391	394	397	400	403	406	409
		412	415	418	421	424	427	431	434
		437	440	444	448	451	454	457	460
		463	466	469	472	475	478	481	484
		487	490	493					
tmk\$process_task_mcr_fault	484	4341							
tmk\$send_monitor_fault	472	4523							
tmk\$send_signal	489	4189							
tmk\$set_monitor_flag	490	3893							
tmk\$set_system_flag	466	3961							
tmk\$signal_buffers_full	481	4219							
tmp\$check_taskid	3588	4143							
tmp\$check_taskid_with_lock_set	3642	3902	3970						
tmp\$clear_lock	2087	1072	2100	3904	3915	3920	3929	3972	3990
		3995	4004	4020	4082	4104	4246	4411	
tmp\$find_xcb	3657	4181							
tmp\$flag_all_tasks	4025	4084							
tmp\$get_xcb_p	3666	3999	4409						
tmp\$monitor_flag_job_tasks	4087	4105							
tmp\$mr_process_system_error	4416	4483							
tmp\$mr_ready_task	4108	4159							
tmp\$mr_send_signal	4289	4318							
tmp\$mr_set_system_flag	4251	4286							
tmp\$process_task_mcr_fault	4322	4357							
tmp\$process_unknown_req_fault	4361	4385							
tmp\$send_monitor_fault	4400	4413							
tmp\$send_signal	4162	4248	4316						
tmp\$set_lock	2184	1065	2202	3900	3968	4048	4095	4246	4411
tmp\$set_monitor_flag	3884	3931	4100	4239	4608	4620	4623	4629	
tmp\$set_system_flag	3934	4022	4284						
tmp\$set_task_ready	3650	3928	4019	4079	4157	4241			
tmt\$broken_task_condition	634	613	4641						
tmt\$broken_task_monitor_fault	611	694	4461	4516					
tmt\$dispatch_control	3514	2302							
tmt\$dual_state_priority_entry	3581	2289	3578						
tmt\$idle_status	3570	3613							
tmt\$mcr_faults	846	696	4332						

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IDENTIFIER	DEFINED ON LINE	REFERENCES	REFERENCES	REFERENCES	REFERENCES	REFERENCES	REFERENCES	REFERENCES	REFERENCES
tmt\$monitor_fault_buffer	762	2656							
tmt\$monitor_fault_buffers	768	763	764	765					
tmt\$monitor_fault_identifiers	771	625	692						
tmt\$option	3626	3589	3644						
tmt\$primary_task_list	3624	3737							
tmt\$primary_task_list_entry	3606	3624							
tmt\$pt1_flags	3630	3617							
tmt\$pt1_lock	2120	2087	2184	2235					
tmt\$rb_delay	2265	2258							
tmt\$rb_ready_task	854	4109							
tmt\$rb_send_signal	874	4290							
tmt\$rb_set_system_flag	936	4252							
tmt\$rb_wait	973	981							
tmt\$ready_condition	3573	3652							
tmt\$signal	3404	3399							
tmt\$signal_buffer	3396	2657							
tmt\$signal_buffers	3411	3397	3398	3399					
tmt\$system_flags	3417	2632	3616	3982	4014	4062	4074		
tmt\$system_task_id	3425	2623							
tmt\$task_queue_link	2038	2007	3614						
tmt\$task_status	3552	3517	3611	3612					
tmt\$wait_inhibited	3637	3632							
tmt\$xcb_offset_size	3628	3609							
tmv\$halt_on_hung_task	1035	4601							
tmv\$job_debug_ring_p	1038	4626	4627						
tmv\$pt1_lock	2235	1065/P	1072/P	3900/P	3904/P	3915/P	3920/P	3929/P	3988/P
		3972/P	3990/P	3995/P	4004/P	4020/P	4049/P	4082/P	4095/P
		4104/P	4246/P	4246/P	4411/P	4411/P			
tmv\$pt1_p	3737	3912	3913/M	3919	3926/M	3928	3986	3987/M	3994
		4013/M	4013	4015	4016/M	4016	4050	4051	4054
		4066	4067/M	4073/M	4073	4075	4076/M	4076	4099
		4101	4151	4154	4233				
tmv\$system_debug_ring	1036	4621							
tmv\$system_debug_segment	1037	4622							
tmv\$system_error_hang_count	1039	4204	4585	4591					
trap_enable	612	4474/M	4553/M						
trap_enable	3195	4474	4553	4577/M	4654	4657/M			
untranslatable_pointer	848	4345/M							
untranslatable_pointer	3231	4345	4355/M						
user_condition_register	624	4555/M							
user_condition_register	3201	3909/M	3910	3980/M	3981	4060/M	4061	4555	
wait_inhibited	2630	3985/M	4065/M	4238/M					
wait_inhibited	3632	4015	4075	4233					
xcb_p	2300	3909/M	3909	3911/M	3911	3977	3980/M	3980	3982/M
		3982	3983/M	3983	3985/M	4060/M	4060	4062/M	4062
		4063/M	4063	4065/M	4150	4151/S	4152	4154/S	4340
		4378	4465	4474	4480/P				
xcb_p	3959	3989/P	4000	4000	4007				
xcb_p	4187	4191/P	4198	4204	4215	4227/M	4228/M	4230/M	4231/M
		4236/M							

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IDENTIFIER	DEFINED ON LINE	REFERENCES											
xcb_p	4333	4340/M	4341	4344	4345	4350/P	4355/M						
	4373	4378/M	4383/P										
	4407	4409/P	4410/P										
	4487	4523	4522	4534	4546	4547	4548	4549	4551				
xcb_p	4638	4552	4553	4554	4555	4558/P	4562	4562	4567/M				
		4568/M	4568	4569/M	4569	4570/M	4570	4571/M	4571				
		4572/M	4577/M	4583/M	4583	4585	4591	4592	4596				
		4596	4608/P	4615	4616/M	4617/M	4620/P	4621	4622				
		4623/P	4627	4628	4629/P								
		4654	4657/M	4663/P	4663/P	4668	4668	4669	4670				
		4671	4674/M	4674	4676/M	4677/P	4677/P						
		xp	2618	3909/M	3909	3980/M	3980	4060/M	4060	4344	4345		
				4355/M	4465	4474	4533	4534	4546	4547	4548		
				4549	4551	4552	4553	4554	4555	4568	4569		
4570	4571			4577/M	4621	4622	4628	4654	4657/M				
zero_pva	4334	4663/P	4668	4668	4669	4670	4671	4674/M	4674				
		4676/M	4677/P										

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