



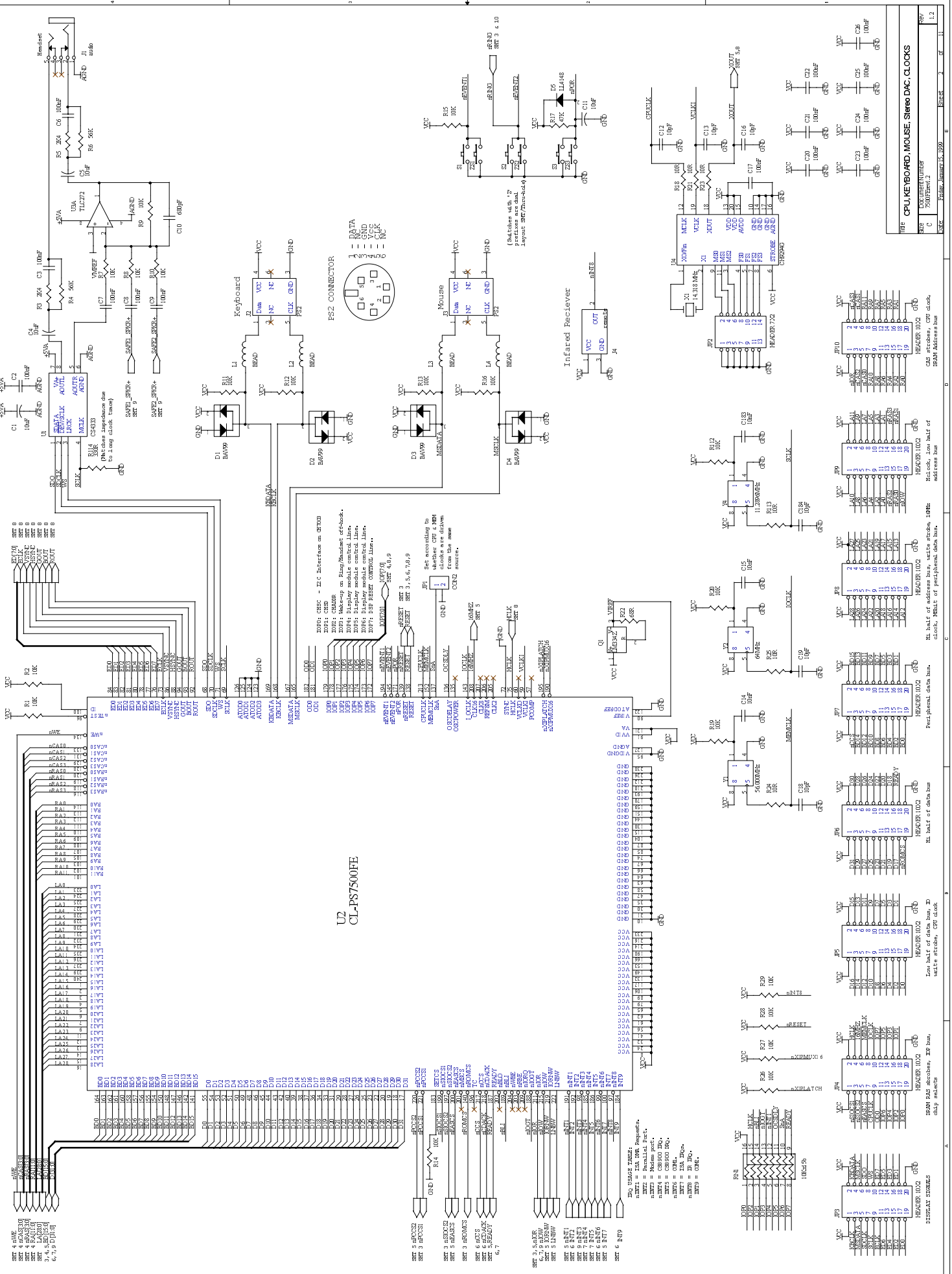
CL-PS7500FE Development Kit

Board Schematics

Embedded Processors Division

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This document provides information to enable you to quickly setup and use the CL-PS7500FE evaluation board. Please refer to either the Hardware User's Guide or the Software User's Guide for more detailed information. The information contained in this document is subject to change without notice.



U2-CL-PS7500FE

Legend:
RES = Resistor
CAP = Capacitor
DI = Diode
IC = IC
SW = Switch
CONN = Connector
IND = Inductor

Pin Functions Legend:
#102 = Bank 1/2
#104 = Bank 3/4
#106 = Bank 5/6
#108 = Bank 7/8
#110 = Bank 9/10
#112 = Bank 11/12
#114 = Bank 13/14
#116 = Bank 15/16
#118 = Bank 17/18
#120 = Bank 19/20

Revision History:
REV 1.0 - Initial Design
REV 1.1 - Added Mouse Support
REV 1.2 - Final Production

Table with 2 columns: Pin, Function

Pin	Function
1	VCC
2	DATA
3	CLK
4	VCC
5	NC
6	NC
7	NC
8	NC
9	NC
10	NC
11	NC
12	NC
13	NC
14	NC
15	NC
16	NC
17	NC
18	NC
19	NC
20	NC

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1	VCC
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3	CLK
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13	NC
14	NC
15	NC
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17	NC
18	NC
19	NC
20	NC

Table with 2 columns: Pin, Function

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7	NC
8	NC
9	NC
10	NC
11	NC
12	NC
13	NC
14	NC
15	NC
16	NC
17	NC
18	NC
19	NC
20	NC

Other technical notes and component footprints.

Component footprints for headers JP1 through JP8, with pin numbers and labels.

Component footprints for headers JP9 through JP12, with pin numbers and labels.

Component footprints for headers JP13 through JP16, with pin numbers and labels.

Additional technical notes, including a note about the mouse connector: '(Switches 14, 15, 17, 19 are pulled up by a 10K resistor)'. A table of component footprints for headers JP1 through JP16 is provided.

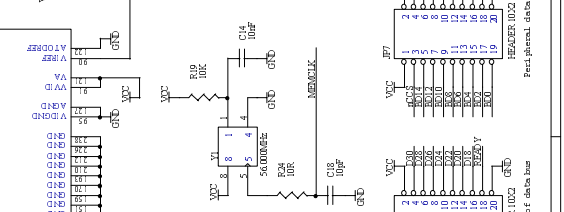
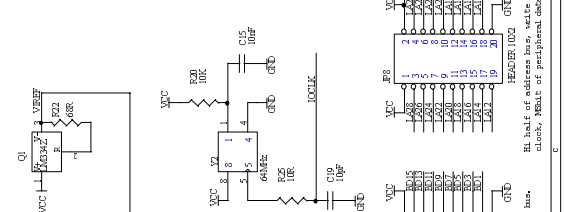
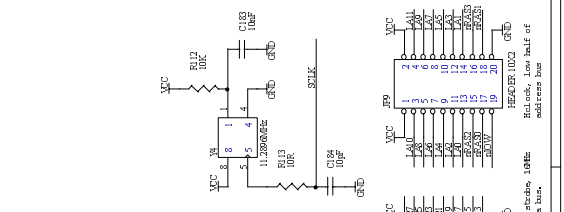
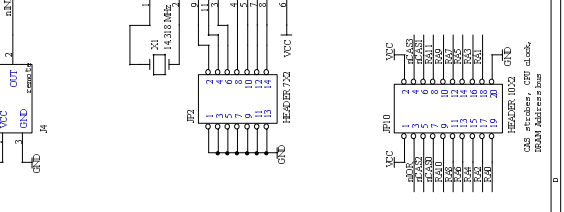
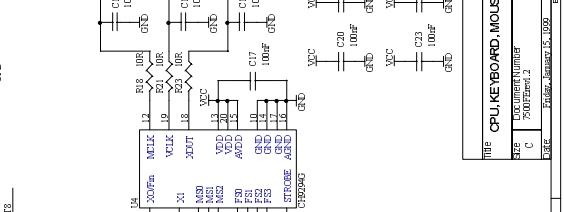
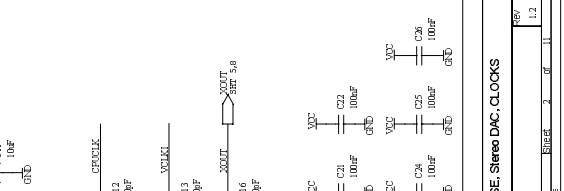


Table with 2 columns: Component, Value

C1	10uF
C2	100nF
C3	100nF
C4	100nF
C5	100nF
C6	100nF
C7	100nF
C8	100nF
C9	100nF
C10	100nF
C11	100nF
C12	100nF
C13	100nF
C14	100nF
C15	100nF
C16	100nF
C17	100nF
C18	100nF
C19	100nF
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C21	100nF
C22	100nF
C23	100nF
C24	100nF
C25	100nF
C26	100nF

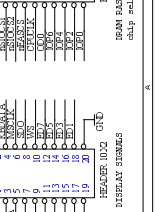
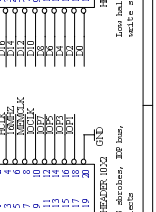
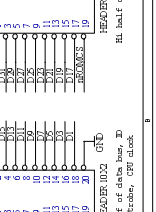
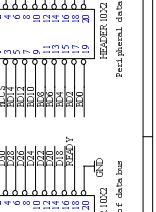
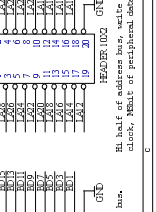
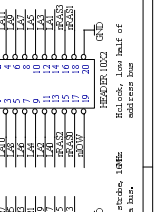
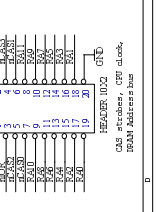
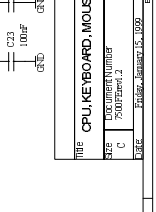
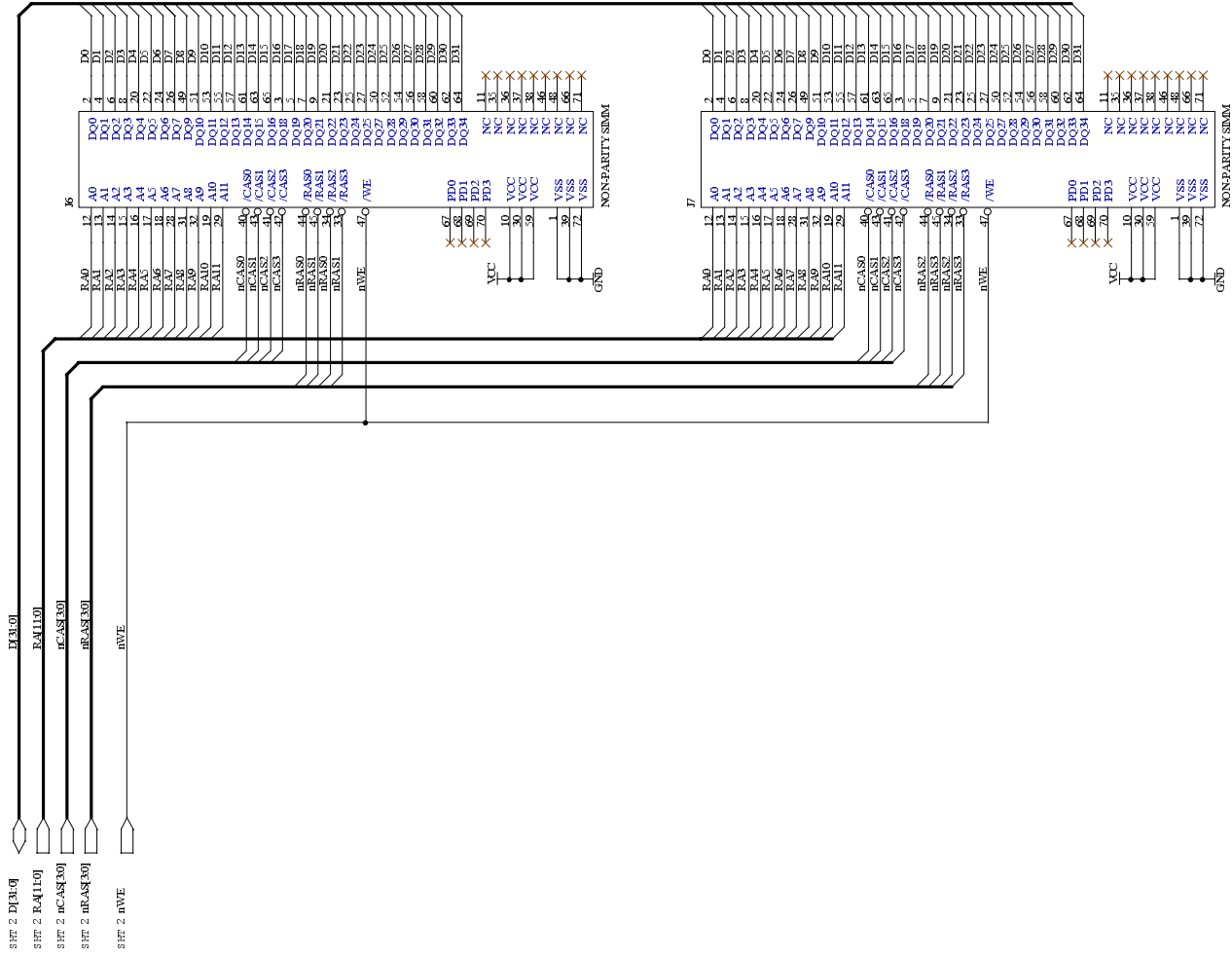
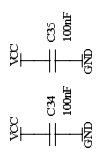


Table with 2 columns: Pin, Function

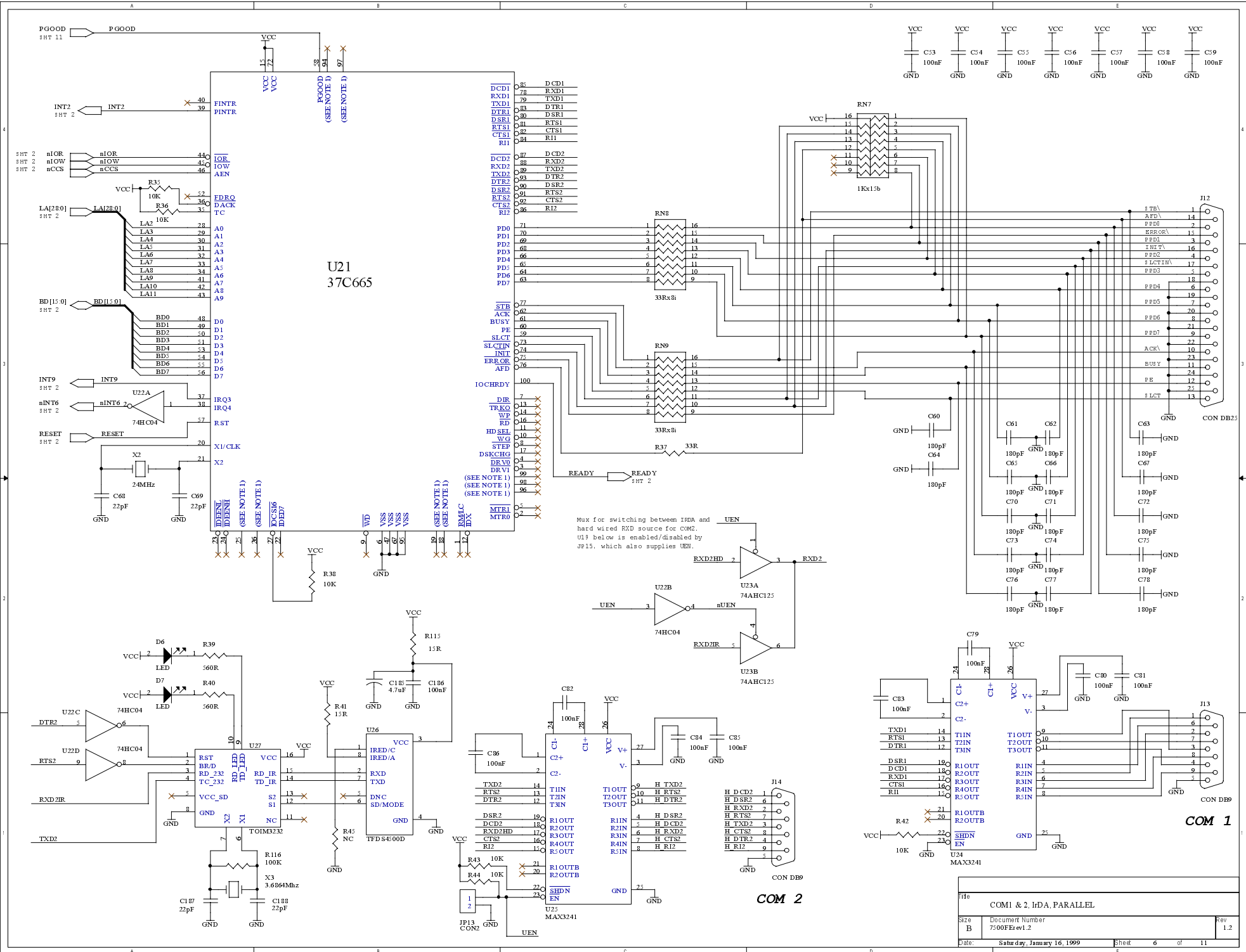
Pin	Function
1	VCC
2	DATA
3	CLK
4	VCC
5	NC
6	NC
7	NC
8	NC
9	NC
10	NC
11	NC
12	NC
13	NC
14	NC
15	NC
16	NC
17	NC
18	NC
19	NC
20	NC



DRAM SIMMs do not generally require any external bypassing, but it never hurts...

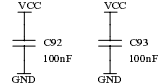
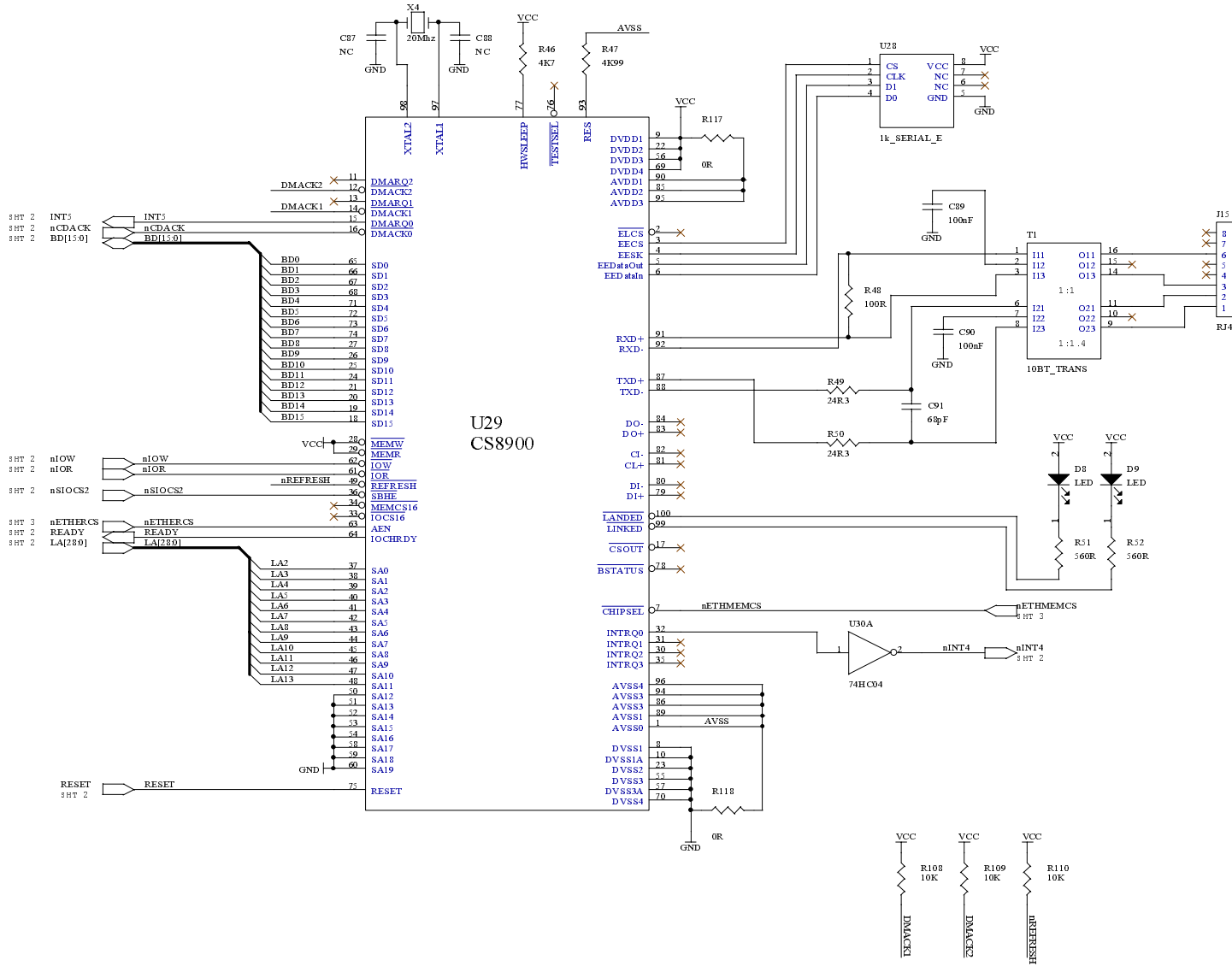


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Size	Document Number	Rev	
B	7000Rev4.2	1.2	
Date:	Friday, January 13, 1999	Sheet	4 of 11

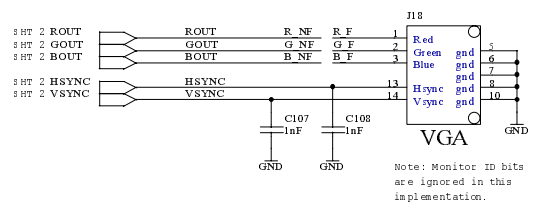
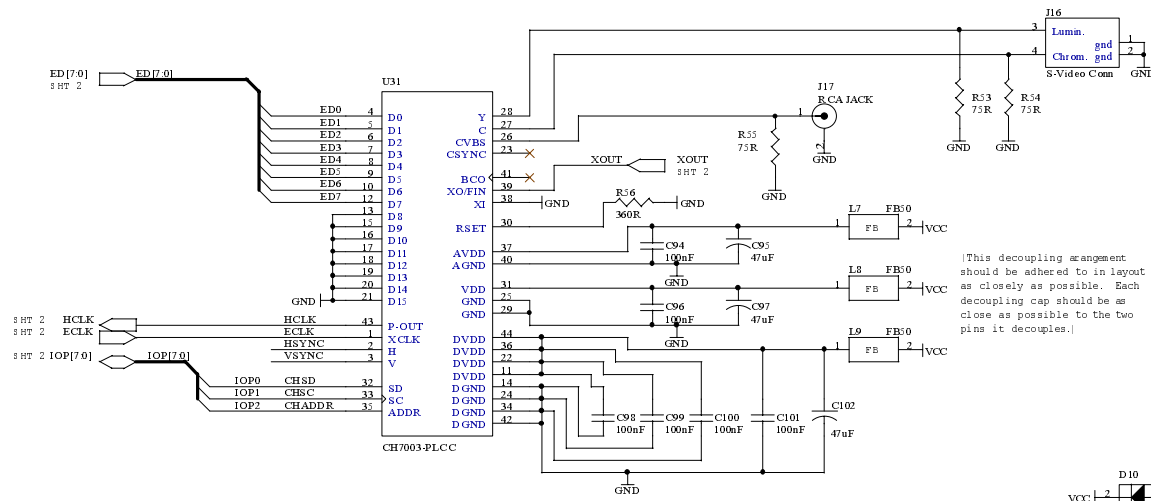


Mux for switching between IPDA and hard wired RXD source for COM2. U29 below is enabled/disabled by JP15, which also supplies IEM.

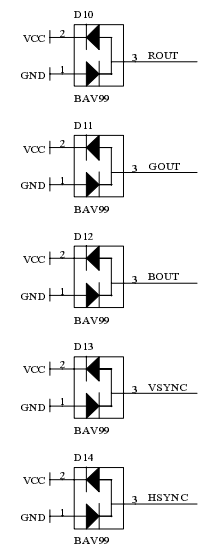
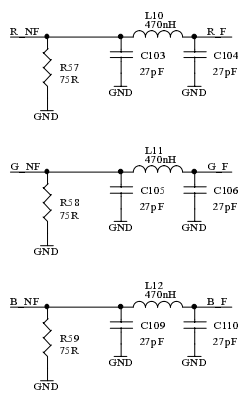
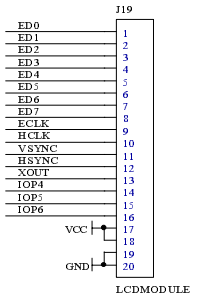
COM1 & 2, I/O, PARALLEL		
Size B	Document Number 7500FEv1.2	Rev 1.2
Date: Saturday, January 16, 1999	Sheet 6	of 11



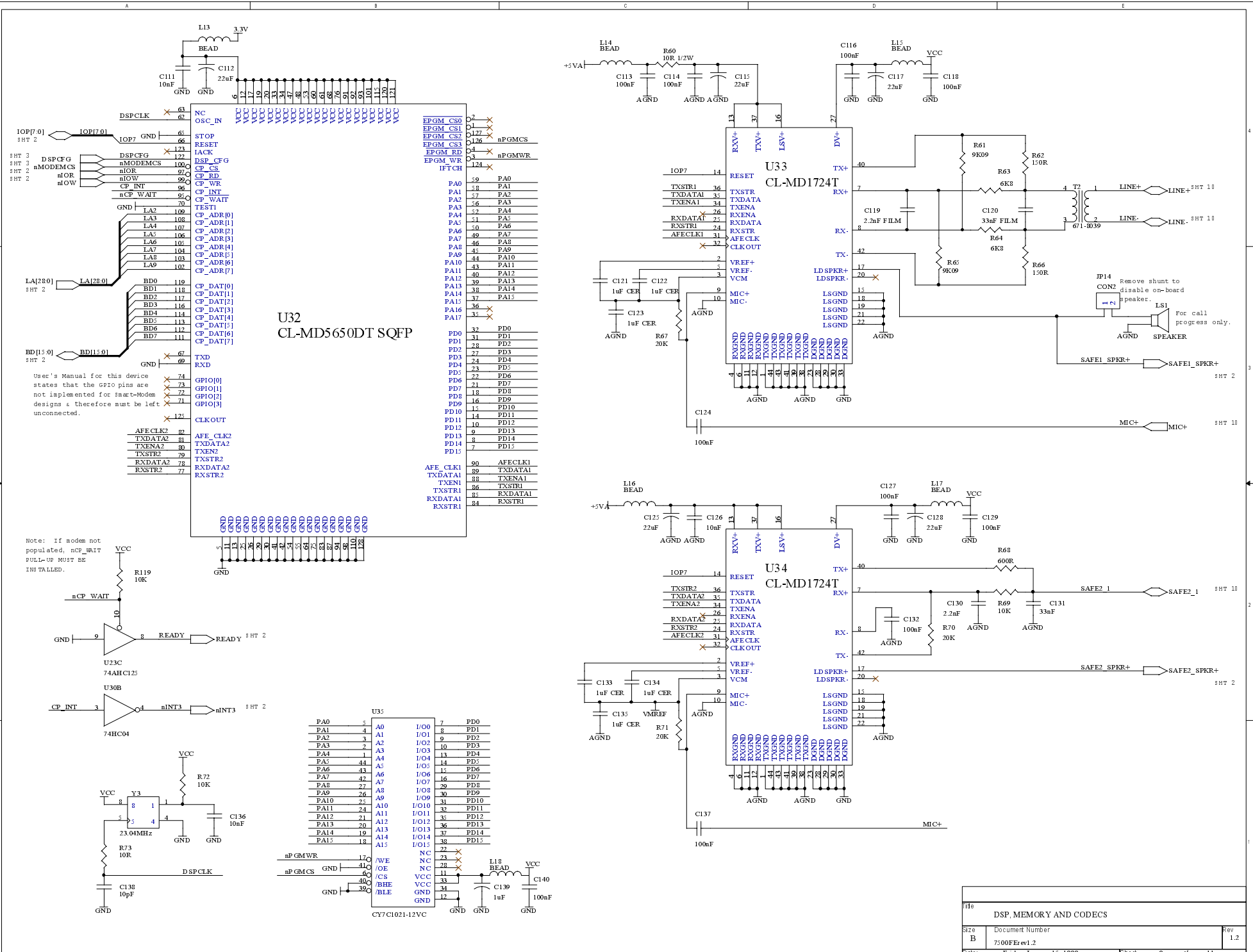
Title		
ETHERNET INTERFACE		
Size	Document Number	Rev
B	7500Erev1.2	1.2
Date:	Friday, January 15, 1999	Sheet 7 of 11



LCD Control board module part.



Title		
VGA, SVIDEO, LCD MODULE CONNECTOR		
Size	Document Number	Rev
B	7500Erev1.2	1.2
Date:	Friday, January 15, 1999	Sheet 8 of 11



U32
CL-MD5650DT SQFP

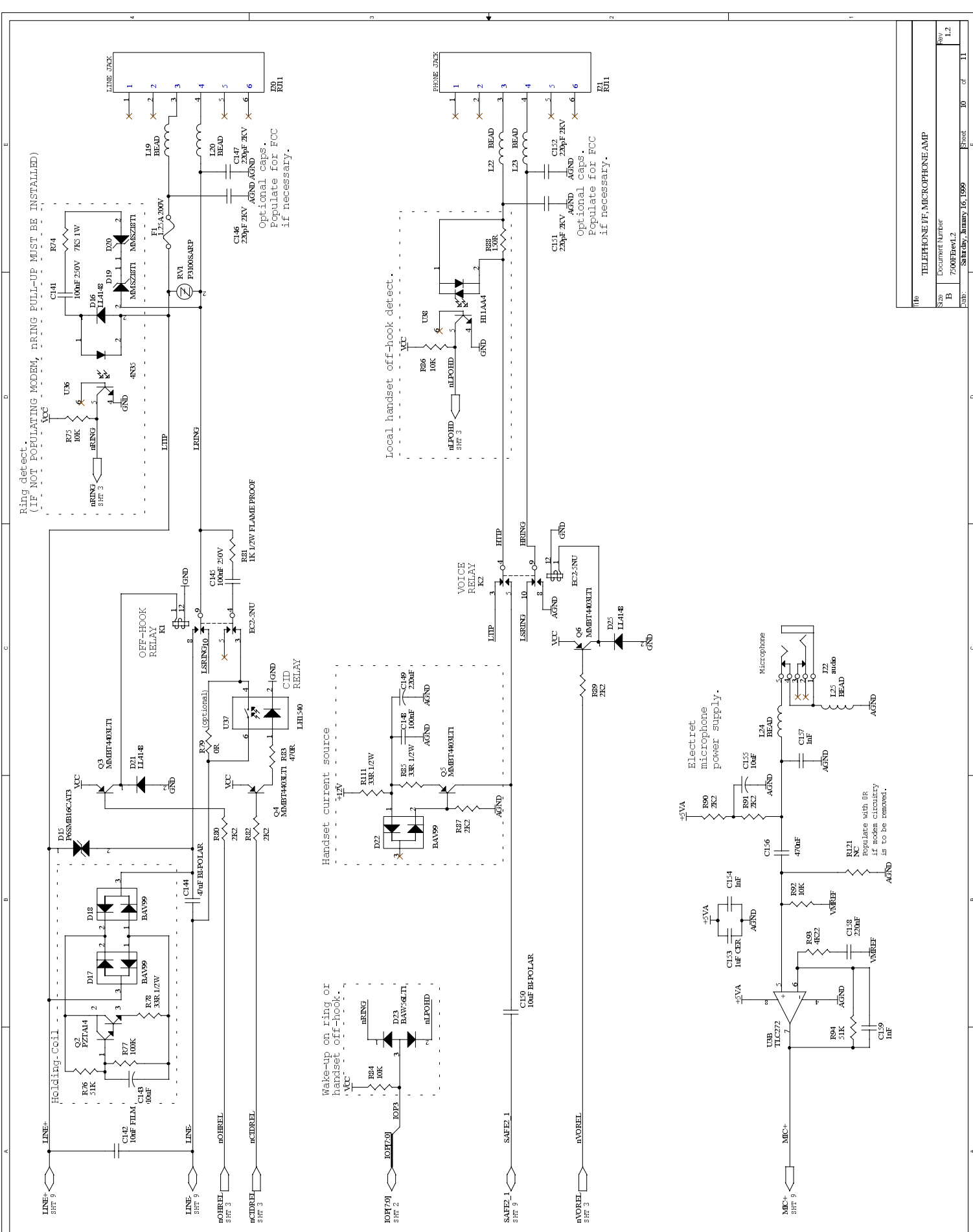
U33
CL-MD1724T

U34
CL-MD1724T

PA0	PA1	PA2	PA3	PA4	PA5	PA6	PA7	PA8	PA9	PA10	PA11	PA12	PA13	PA14	PA15	PD0	PD1	PD2	PD3	PD4	PD5	PD6	PD7	PD8	PD9	PD10	PD11	PD12	PD13	PD14	PD15	
5	4	3	2	1	44	43	42	27	26	25	24	21	20	19	18	7	8	9	10	13	14	12	16	17	15	11	3	2	6	5		
A0	A1	A2	A3	A4	A5	A6	A7	A8	A9	A10	A11	A12	A13	A14	A15	I/00	I/01	I/02	I/03	I/04	I/05	I/06	I/07	I/08	I/09	I/10	I/11	I/12	I/13	I/14	I/15	
17	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	
12	11	10	9	8	7	6	5	4	3	2	1	0	31	30	29	W/E	OE	CS	VCC	VCC	VCC	VCC	VCC	VCC	VCC	VCC	VCC	VCC	VCC	VCC	VCC	
12	11	10	9	8	7	6	5	4	3	2	1	0	31	30	29	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND

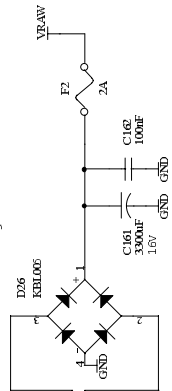
CY7C1021-12VC

Ring detect.
(IF NOT POPULATING MODEM, RING PULL-UP MUST BE INSTALLED)



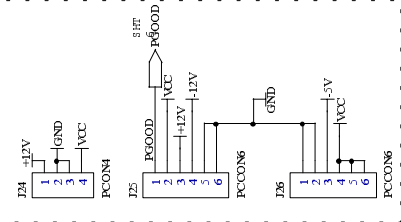
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Size	Document Number	Rev	
B	7700Rev4.2	1.2	
Date	Sheet No.	Quantity	of
1999	16	10	11

12VAC Power Plug

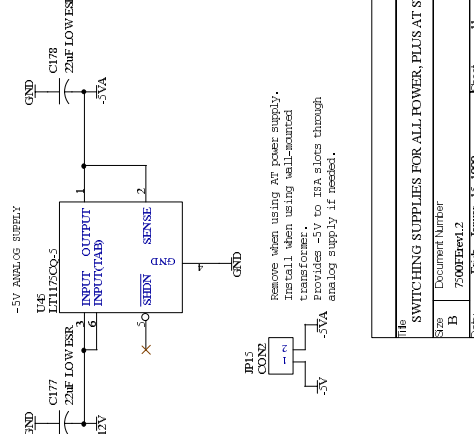
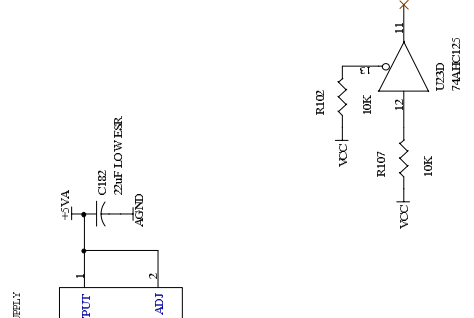
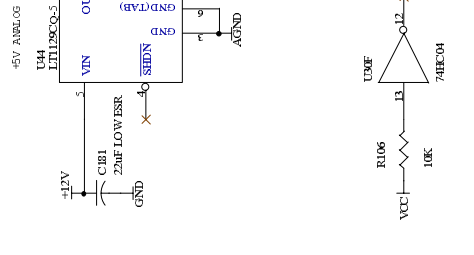
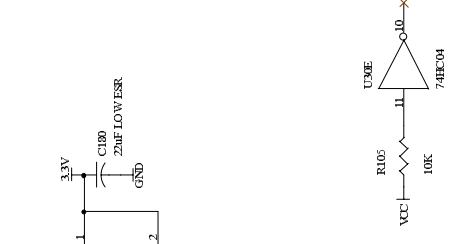
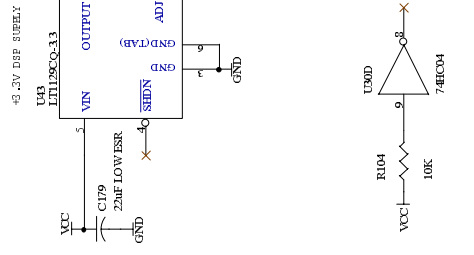
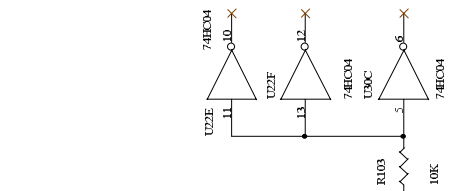
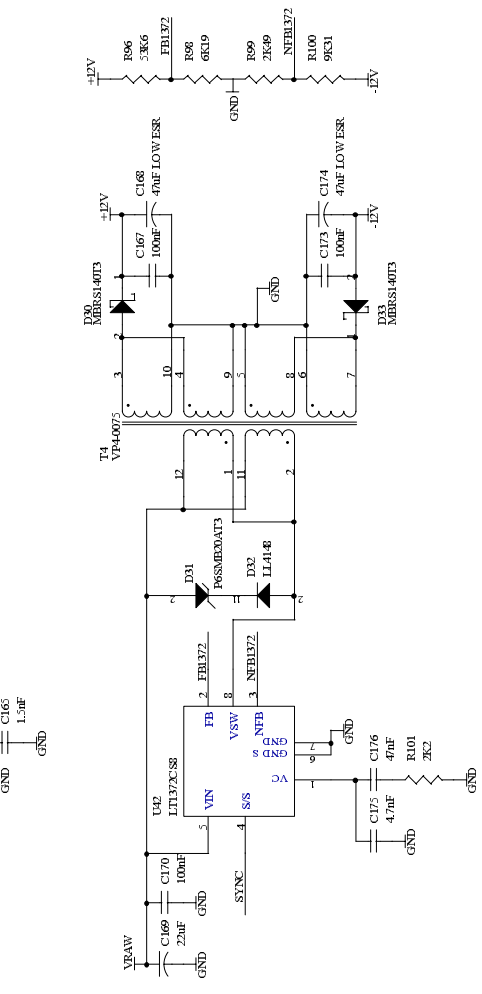
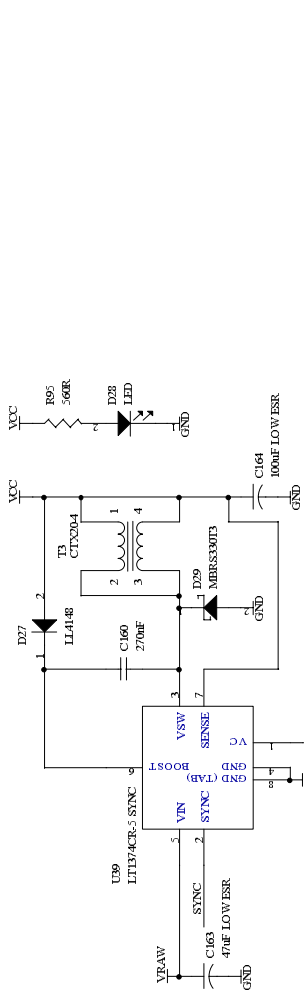
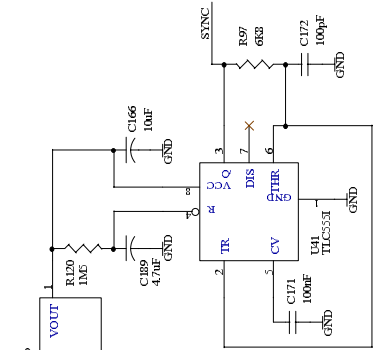


A suitable power supply for North America is a Magnetek MD012-1200

Standard AT Power Plug, connectors



-5VDC clock generator for synchronizing switching supplies. This is done to avoid beat frequency noise. Important only in the sense that a majority of these voltages are used by the analog sections like the DMA.



Remove when using AT power supply. Install when using wall-mounted transformer. Provides -5V to ISA slots through analog supply if needed.

File	SWITCHING SUPPLIES FOR ALL POWER, PLUS AT SUPPLY INPUTS		
Size	Document Number	Rev	1,2
B	7700FF04.2		
Date:	Friday, January 15, 1999	Sheet	11 of 11